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A Novel Dead-Time Vector Approach to Analysis of DC Link Current in PWM Inverter Drives

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Abstract—An analysis on the DC link current of space-vector PWM drives using novel dead-time vector (DTV) concept has been presented. By employing the DTV, a bi-value function to quantify the polarities of three phase winding currents, inverter output voltages and DC link current at dead time state can be expressed in close vectored forms. Thus, the entire drive system is considered as a whole to investigate the characteristics of DC link current at both normal and dead time states during steady and transient operations. Increasingly, a phenomenon of negative spikes on DC link current caused by dead time effect is newly observed and highlighted. This phenomenon can be accurately predicted and mathematically characterized by using the proposed DTV approach. Both computer simulation and experimental results have been used for verification.

I. INTRODUCTION

The knowledge of DC link current in PWM inverter-fed AC motor drives has been of utmost importance for system protection, efficiency optimization and state identification [1]-[3]. The available approaches, however, are unnecessarily complicated and impractical for the modern drive systems using space vector PWM schemes [4]. Even so, these approaches have been developed mainly for steady state analysis, while the characteristics of DC link current during transient operation, which are of much significance, have seldom been analyzed.

On the other hand, it is well known that "dead time" is inevitably equipped to protect inverter devices from short-through the power source in an inverter. Since dead time has great influence on the performance of drive systems [5], [6], it is urgently need to know the precise information of dead time in a practical drive system. Because of the time delay in the gate driver circuit, however, the duration of dead time in the main circuit is not the same as that set in the control board, especially at high power rating. And it is difficult to directly measure dead time at the main power modules when the drive system is operating. Fortunately, the DC link current can easily be non-invasively measured. It has been observed that there are both positive and negative spikes on the instantaneous DC link current waveform. It is apt to consider these spikes as the switching transients of power devices, such as reverse recovery effect of freewheeling diodes, and omit them as EMI issue. But, are all the current spikes exclusively caused by the switching transients? Does the existence of dead time also cause any spikes on the

instantaneous DC link current waveforms? Or can the information of dead time be deduced from DC link current? And how? To the knowledge of authors, the available studies on dead time effect are mainly focus on the inverter output side and the analysis of dead time effect on DC link current is poorly inadequate.

In this paper, a comprehensive analysis on DC link current of space vector PWM induction motor drives in the presence of dead time is presented. With the introduction of a novel concept - dead time vector (DTV), which is a bi-value function to quantify the polarities of three-phase winding currents, the inverter output voltages and DC link current at dead time can be expressed in close vectored forms. Thus, the entire drive system, including PWM strategy, inverter and motor, is considered as a whole to investigate the characteristics of DC link current at both normal and dead time states during steady and transient operations. Moreover, the phenomenon of spikes caused by dead time effect on the DC link current is not only descriptively discussed and explained, but also mathematically predicted and derived using the proposed DTV approach. Theoretical analysis and computer simulation are verified by experimental results.

II. UNIFIED APPROACH

A voltage source inverter-induction motor drive system is shown in Fig. 1, where I_{dc} is the instantaneous DC link current, I_u , I_v and I_w are the instantaneous three phase upper leg currents, I_a , I_b and I_c are the instantaneous three phase winding currents. And the nominal current directions are indicated as the arrow signs.

Generally, the current flows inside the inverter are affected by the diode recovery and snubber action. When a switch is being turned-on and the conducting diode at the same leg is being blocked off by this turn-on, because of the reverse recovery effect of diode, this leg is in fact shorted through at this moment such that a positive current spike will appear at the DC link side. And the snubbers are for stray-inductance clamping and turn-off switching loss reduction. The main concern here is to establish the relationships between DC link current, windings currents and device switching patters. So it is assumed that the inverter switches are "ideal": both on-state losses and switching times are negligible, the diode recovery effect and snubber action are not considered either.

A. Space Vectors for Normal States

During normal states, there are eight switching states, which can be expressed as the space voltage vector $SVV(S_a, S_b, S_c)$, such as (0,0,0), (0,0,1), (0,1,0), ..., (1,1,1), where $S_p = 0$ ($p = a, b, c$) means that the upper leg switch of phase p is off while the lower one is on, and vice versa. The three phase winding terminal voltage potentials are

$$\begin{cases} U_a = \frac{1}{2}U_{dc}(2S_a - 1) \\ U_b = \frac{1}{2}U_{dc}(2S_b - 1) \\ U_c = \frac{1}{2}U_{dc}(2S_c - 1) \end{cases} \quad (1)$$

The switching vectors describe the inverter output voltages as shown in Fig. 2. Projecting voltage vectors onto a two-dimension α, β plane, the α, β components can be derived as:

$$\begin{bmatrix} U_\alpha \\ U_\beta \end{bmatrix} = \frac{\sqrt{2}}{\sqrt{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} S_a \\ S_b \\ S_c \end{bmatrix} U_{dc} \quad (2)$$

Once the voltage vectors are derived and the load property is known from mathematical model of the motor, the winding currents can be calculated out.

The motor winding currents flow via either the positive or negative supply rail depending on the switching states. Although under different cases, the conducting devices may be different, their effects are the same – for instance, current flowing in switch S_1 or freewheeling diode D_1 will result in the winding terminal point A being connected to the positive supply rail.

Phase A is considered first. When $S_a = 1$, the winding terminal point A is connected to the positive supply rail, and the current flows through the upper leg of the inverter (S_1 conducts if $I_a > 0$ and D_1 conducts if $I_a < 0$), thus $I_u = I_a$; when $S_a = 0$, the winding terminal point A is connected to the negative supply rail, and the current flows through the lower leg of the inverter (D_4 conducts if $I_a > 0$ and S_4 conducts if $I_a < 0$), there is no current flowing through the upper leg of the inverter, thus $I_u = 0$. The situations for phase B and C are similar.

From the analysis above, the relation between leg currents and switching states are derived as

$$\begin{cases} I_u = I_a S_a \\ I_v = I_b S_b \\ I_w = I_c S_c \end{cases} \quad (3)$$

As the instantaneous DC link current is the sum of inverter three leg currents, that is

$$I_{dc} = I_u + I_v + I_w \quad (4)$$

Substituting (3) into (4) yields

$$I_{dc} = I_a S_a + I_b S_b + I_c S_c \quad (5)$$

Equation (5) is the relation between I_{dc} and three phase winding currents derived from the view of switching state transitions. Actually, it can also be derived from the view of power flow inside the inverter. A deeper insight on inverter operation will be exposed by means of the derivation.

Inverter output instantaneous three phase power is always equal to DC input power to the inverter, thus

$$U_{dc} I_{dc} = U_{an} I_a + U_{bn} I_b + U_{cn} I_c \quad (6)$$

where U_{an} , U_{bn} and U_{cn} are the three phase voltages of motor windings respectively. Let U_n be the voltage potential of the neutral point N, considering the terminal voltage potentials as in (1), we get

$$\begin{aligned} & U_{an} I_a + U_{bn} I_b + U_{cn} I_c \\ &= (U_a - U_n) I_a + (U_b - U_n) I_b + (U_c - U_n) I_c \\ &= U_{dc} (S_a I_a + S_b I_b + S_c I_c) - \left(\frac{1}{2} U_{dc} + U_n\right) (I_a + I_b + I_c) \end{aligned} \quad (7)$$

Because of the constraint of $I_a + I_b + I_c = 0$, by comparing (7) with (6), the DC link current is again derived as

$$I_{dc} = I_a S_a + I_b S_b + I_c S_c \quad (8)$$

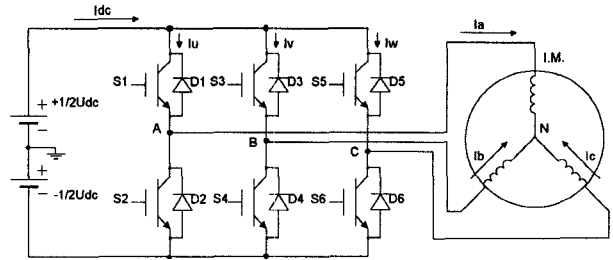


Fig. 1. Three-phase PWM inverter with an induction motor load.

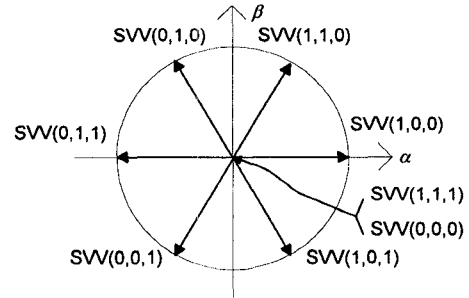


Fig. 2. Voltage vectors and space sectors.

B. Dead-Time Vectors

During dead time period, both two series-connected switches on the same leg are Off and the winding currents are flowing through the freewheeling diodes, as determined by the polarities of winding currents but independent of the switching states – upper diodes if currents flow back to supply and lower diodes if currents flow forward to load.

Dead time state of phase A is illustrated as Fig. 3. If the switching state of phase A is scheduled to be changed, i.e. from the state of S_1 On and S_2 Off to the state of S_1 Off and S_2 On or vice versa, a dead time is inserted before the scheduled switching action happens. At this interval, both S_1 and S_2 are Off and there are two modes for the current flow.

- **Mode 1:** $I_a > 0$.

The current flows forward to load through the diode D_2 at the lower leg of the inverter. Terminal point A is connected to the negative supply rail, and there is no current flowing through the upper leg. Thus the inverter leg current and terminal potential are

$$I_u = 0 \quad (9)$$

$$\begin{aligned} U_a &= \left(-\frac{1}{2}\right)U_{dc} \\ &= \frac{1}{2}U_{dc}(0-1) \end{aligned} \quad (10)$$

- **Mode 2:** $I_a < 0$.

The current flows back to the supply through the diode D_1 at the upper leg of the inverter. Terminal point A is connected to the positive supply rail, and there is no current flowing through the lower leg. Thus the inverter leg current and terminal potential are

$$I_u = I_a \quad (11)$$

$$\begin{aligned} U_a &= \left(+\frac{1}{2}\right)U_{dc} \\ &= \frac{1}{2}U_{dc}(2-1) \end{aligned} \quad (12)$$

Obviously, during dead time interval, the currents and voltages greatly depend on the polarities of winding currents, which are often expressed as a function defined as [6]

$$\text{Sign}(I_p) = \begin{cases} 1 & \text{when } I_p > 0 \\ -1 & \text{when } I_p < 0 \end{cases} \quad \text{where } p = a, b, c \quad (13)$$

However, it is inconvenient to directly use $\text{Sign}(I_p)$ to express the currents and voltages at dead time intervals into simple and clear forms. Thus a novel concept – dead time vector is proposed. For three phases A, B and C, $DTV(D_a, D_b, D_c)$ is defined as

$$D_p = \frac{1}{2}[1 - \text{Sign}(I_p)] \quad \text{where } p = a, b, c \quad (14)$$

Under this definition, the physical meaning of DTV is

$$D_p = \begin{cases} 0 & \text{when } I_p > 0 \\ 1 & \text{when } I_p < 0 \end{cases} \quad \text{where } p = a, b, c \quad (15)$$

Thus DTV is a bi-value [0,1] function similar to SVV in form. By introducing DTV to (9) – (12), the inverter upper leg current and terminal potential are derived from the analysis above as

$$I_u = I_a D_a \quad (16)$$

$$U_a = \frac{1}{2}U_{dc}(2D_a - 1) \quad (17)$$

Similarly, if phase B and phase C are at dead time state respectively (not necessarily simultaneous), we have

$$I_v = I_b D_b \quad (18)$$

$$U_b = \frac{1}{2}U_{dc}(2D_b - 1) \quad (19)$$

and

$$I_w = I_c D_c \quad (20)$$

$$U_c = \frac{1}{2}U_{dc}(2D_c - 1) \quad (21)$$

Totally, there are $3^3 - 8 = 19$ combinations of dead time states for three phase inverters which can be categorized into three cases to express output voltages and DC link currents at dead time interval:

- **Case 1:** change in switching state of only one phase (phase A changes while both B and C remain unchanged, thus phase A is at dead time state and phase B, C are at normal switching states).

The three phase terminal voltage potentials are

$$\begin{cases} U_a = \frac{1}{2}U_{dc}(2D_a - 1) \\ U_b = \frac{1}{2}U_{dc}(2S_b - 1) \\ U_c = \frac{1}{2}U_{dc}(2S_c - 1) \end{cases} \quad (22)$$

The output voltage vector is

$$\begin{bmatrix} U_\alpha \\ U_\beta \end{bmatrix} = \frac{1}{\sqrt{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} D_a \\ S_b \\ S_c \end{bmatrix} U_{dc} \quad (23)$$

The instantaneous DC link current is

$$\begin{aligned} I_{dc} &= I_u + I_v + I_w \\ &= I_a D_a + I_b S_b + I_c S_c \end{aligned} \quad (24)$$

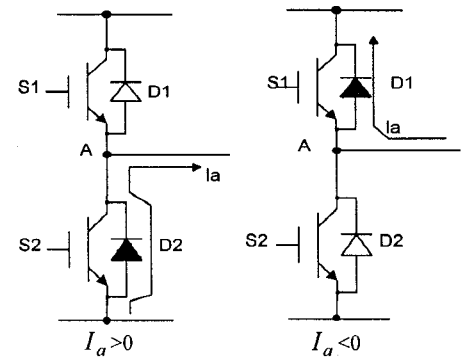


Fig. 3. Current flow at dead time interval for phase A.

- **Case 2:** change in switching states of two phases (both phase A and B change while C remains unchanged, thus phase A and B are at dead time states and phase C is at normal state).

The three phase terminal voltage potentials are

$$\begin{cases} U_a = \frac{1}{2}U_{dc}(2D_a - 1) \\ U_b = \frac{1}{2}U_{dc}(2D_b - 1) \\ U_c = \frac{1}{2}U_{dc}(2S_c - 1) \end{cases} \quad (25)$$

The output voltage vector is

$$\begin{bmatrix} U_\alpha \\ U_\beta \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} D_a \\ D_b \\ S_c \end{bmatrix} U_{dc} \quad (26)$$

The instantaneous DC link current is

$$\begin{aligned} I_{dc} &= I_u + I_v + I_w \\ &= I_a D_a + I_b D_b + I_c S_c \end{aligned} \quad (27)$$

- **Case 3:** change in switching states of all phases (but usually absent in normal switching schemes for induction motor drive so not be discussed here).

Therefore, thanks to the introduction of *DTV*, the voltage vectors (23), (26) and instantaneous DC link currents (24), (27) for dead time intervals are expressed in the similar form of their counterparts (1) and (8) for normal switching intervals which are derived from *SVV*. So the inverter, no matter operating at either normal or dead time interval, can be analyzed in a unified form as a whole instead of looking at the individual phase separately. It is convenient and useful for performance prediction, digital simulation and controller design. However, it should be addressed that although *DTV* has the similar form with *SVV*, they inherently are different in physical meanings. *DTV* reflects the polarities or directions of winding currents while *SVV* reflects the switching states of the inverter.

III. FORMATION OF NEGATIVE SPIKES

In this section, the newly proposed *DTV* and expressions for DC link currents will be used to discuss the question raised at Introduction section, that is, whether the existence of dead time causes some spikes at DC link current and how. Here current spike means that the DC link current at dead time interval is much smaller or larger than those at its adjacent normal switching subcycles. Since dead time duration is much shorter than switching period, the current spikes are seen on DC link current waveforms.

The transition of switching states at two adjacent subcycles is illustrated by Fig. 4. In subcycle T_n , the applied voltage vector is SVV_n ; in the next subcycle T_{n+1} , the applied voltage vector is SVV_{n+1} . Between T_n and T_{n+1} , dead time T_d is inserted. For the reason of simplicity and clarity, it is assumed that winding currents I_a , I_b and I_c keep unchanged at dead time interval because of winding inductance and high switching frequency.

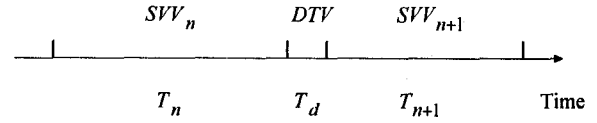


Fig. 4. Transition of switching states.

General space vector PWM switching schemes for induction motor control ensure that the case for three phase switching states changing simultaneously does not happen, so there are two cases for the change of I_{dc} at dead time interval which are analyzed as follows:

- **Case 1:** change in switching state of only one phase (phase A change while phase B, C remain unchanged).

Voltage vector changes from $SVV_n(S_{a,n}, S_{b,n}, S_{c,n})$ to $SVV_{n+1}(S_{a,n+1}, S_{b,n}, S_{c,n})$.

$$\text{Before } T_d, I_{dc,n} = I_a S_{a,n} + I_b S_{b,n} + I_c S_c \quad (28)$$

$$\text{During } T_d, I_{dc,dead} = I_a D_a + I_b S_{b,n} + I_c S_c \quad (29)$$

$$\text{After } T_d, I_{dc,n+1} = I_a S_{a,n+1} + I_b S_{b,n} + I_c S_c \quad (30)$$

Since both *DTV* and *SVV* are bi-value [0,1] functions, obviously there are only two results

$$I_{dc,dead} = I_{dc,n} \quad \text{when } D_a = S_{a,n}$$

$$I_{dc,dead} = I_{dc,n+1} \quad \text{when } D_a = S_{a,n+1}$$

As I_{dc} at dead time interval is equal to that at either one of its adjacent normal switching subcycles, there is definitely no spikes on DC link current caused by dead time at case 1.

- **Case 2:** change in switching states of two phases (phases A and B change while phase C remain unchanged).

Voltage vector changes from $SVV_n(S_{a,n}, S_{b,n}, S_{c,n})$ to $SVV_{n+1}(S_{a,n+1}, S_{b,n+1}, S_{c,n})$.

$$\text{Before } T_d, I_{dc,n} = I_a S_{a,n} + I_b S_{b,n} + I_c S_c \quad (31)$$

$$\text{During } T_d, I_{dc,dead} = I_a D_a + I_b D_b + I_c S_c \quad (32)$$

$$\text{After } T_d, I_{dc,n+1} = I_a S_{a,n+1} + I_b S_{b,n+1} + I_c S_c \quad (33)$$

First of all, $I_{dc,dead}$ is compared with $I_{dc,n}$ and $I_{dc,n+1}$. Obviously it is only needed to compare $(I_a D_a + I_b D_b)$ with $(I_a S_{a,n} + I_b S_{b,n})$ and $(I_a S_{a,n+1} + I_b S_{b,n+1})$ respectively.

When $[D_a \ D_b] = [S_{a,n} \ S_{b,n}]$, $I_{dc,dead} = I_{dc,n}$; when $[D_a \ D_b] = [S_{a,n+1} \ S_{b,n+1}]$, $I_{dc,dead} = I_{dc,n+1}$, hence there is definitely no spikes on DC link current caused by dead time.

However, when $[D_a \ D_b] \neq [S_{a,n} \ S_{b,n}]$ and $[D_a \ D_b] \neq [S_{a,n+1} \ S_{b,n+1}]$, things will be different. For example, if $[D_a \ D_b] = [0 \ 0]$, $[S_{a,n} \ S_{b,n}] = [0 \ 1]$, $[S_{a,n+1} \ S_{b,n+1}] = [1 \ 0]$, according to the physical meaning of *DTV* as (15), we obtain $I_a > 0$, $I_b > 0$. Since

$$I_a S_{a,n} + I_b S_{b,n} = I_b > 0 \quad (34)$$

$$I_a S_{a,n+1} + I_b S_{b,n+1} = I_a > 0 \quad (35)$$

$$I_a D_a + I_b D_b = 0 \quad (36)$$

we obtain $I_{dc,dead} < I_{dc,n}$ and $I_{dc,dead} < I_{dc,n+1}$.

There are totally eight combinations of DTV and SVV for this situation ($[D_a \ D_b] \neq [S_{a,n} \ S_{b,n}]$ and $[D_a \ D_b] \neq [S_{a,n+1} \ S_{b,n+1}]$), the comparison between $I_{dc,dead}$ and $I_{dc,n}$, $I_{dc,n+1}$ is analyzed and presented in the same method above in Table I. Comparisons for the eight combinations all lead to $I_{dc,dead} < I_{dc,n}$ and $I_{dc,dead} < I_{dc,n+1}$.

Secondly, the value of $I_{dc,dead}$ itself is checked also, which can be calculated by (32). There are totally eight combinations of D_a, D_b and $S_{c,n}$. Referring to the physical meaning of DTV and the constraint of $I_a + I_b + I_c = 0$, $I_{dc,dead}$ are calculated and listed in Table II. It is shown that under all of the eight combinations, no matter the polarities of winding currents I_a, I_b and I_c themselves might be, we always get $I_{dc,dead} \leq 0$. Therefore, under the condition of $[D_a \ D_b] \neq [S_{a,n} \ S_{b,n}]$ and $[D_a \ D_b] \neq [S_{a,n+1} \ S_{b,n+1}]$, DC link current at dead time interval is definitely smaller than those at its adjacent normal switching subcycles and the value itself of DC link current at dead time interval is negative or zero. Since dead time duration is much shorter than switching period, there are *negative* current spikes occurring on DC link current waveforms.

From the discussion above, it can be concluded that if and only if two phase switching states change simultaneously, say, SVV changing from (1,0,0) to (0,1,0), negative spikes may appear on DC link current at dead time interval, and the inverter operation also determine the occurrence of negative spikes. The criterion for the formation of negative spikes is presented precisely and clearly by the introduction of DTV . Increasingly dead time only causes negative current spikes and their amplitudes are equal to either one of three phase winding currents (see Table II). These negative spikes inherently represent the regenerative operating mode at dead time interval and will significantly influence the overall system stability.

TABLE I

DTV AND SVV SATISFYING ($I_{dc,dead} < I_{dc,n}$ AND $I_{dc,dead} < I_{dc,n+1}$)								
$[S_{a,n}$ $S_{b,n}]$	$[S_{a,n+1}$ $S_{b,n+1}]$	$[D_a$ $D_b]$	I_a	I_b	$I_a S_{a,n}$ +	$I_a S_{a,n+1}$ +	$I_a D_a$ +	OK?
					$I_b S_{b,n}$	$I_b S_{b,n+1}$	$I_a D_b$	
[0,0]	[1,1]	[0,1]	>0	<0	0	$I_a + I_b$	I_b	Y
[0,0]	[1,1]	[1,0]	<0	>0	0	$I_a + I_b$	I_a	Y
[1,1]	[0,0]	[0,1]	>0	<0	$I_a + I_b$	0	I_b	Y
[1,1]	[0,0]	[1,0]	<0	>0	$I_a + I_b$	0	I_a	Y
[0,1]	[1,0]	[1,1]	<0	<0	I_b	I_a	$I_a + I_b$	Y
[0,1]	[1,0]	[0,0]	>0	>0	I_b	I_a	0	Y
[1,0]	[0,1]	[1,1]	<0	<0	I_a	I_b	$I_a + I_b$	Y
[1,0]	[0,1]	[0,0]	>0	>0	I_a	I_b	0	Y

TABLE II

D_a, D_b and $S_{c,n}$ SATISFYING $I_{dc,dead} \leq 0$						
$S_{c,n}$	D_a	D_b	I_a	I_b	$I_{dc,dead}$	$I_{dc,dead} < 0?$
0	0	0	>0	>0	0	
0	0	1	>0	<0	I_b	Y
0	1	0	<0	>0	I_a	Y
0	1	1	<0	<0	$I_a + I_b$	Y
1	0	0	>0	>0	$-(I_a + I_b)$	Y
1	0	1	>0	<0	$-I_a$	Y
1	1	0	<0	>0	$-I_b$	Y
1	1	1	<0	<0	0	

IV. RESULTS AND DISCUSSION

Digital simulation on the whole inverter-induction motor drive system in the presence of dead time has been made and verified with experimental results. The flux-control space vector PWM scheme is used as in [7] and all the parameters and variables are presented in per unit (see Appendix).

A. Simulation Waveforms

The waveforms of I_{dc} and I_a at 50 Hz for one fundamental operation period (20 ms) are shown as Fig. 5. At one operation period, there are six pulses in the waveform of I_{dc} . It is because that the whole 360° plane is divided into six sectors by the voltage vectors as shown in Fig. 2, each spanning 60°. There are two selective voltage vectors as one group for each sector and the control strategy makes the inverter switching actions alike at each sector. Seen from DC link side, the waveforms at different sectors are similar.

The waveforms of instantaneous I_a and I_{dc} when the inverter-fed induction motor is being started-up from standstill at 50 Hz are shown in Fig. 6. There are both positive and negative values at the instantaneous DC link current during transient process because of the relatively low and changing power factor of the induction motor when it is being started-up from standstill.

Fig. 7 is the waveform of average DC link current during start-up process, which is responsible for real power transfer. It can be found that the average DC link current is very close to the output torque T_e of the induction motor during start-up process. The reasons are due to the fact that the real power transferred from supply to induction motor is $P_{real} = U_{dc} I_{dc,avg}$, where U_{dc} is the DC supply voltage and kept constant; the motor output power across air gap is $P_{gap} = T_e \omega_e$, where ω_e is the synchronous speed and is always equal to unity p.u.; and the stator copper and core losses are relatively small and usually changing during the start-up process.

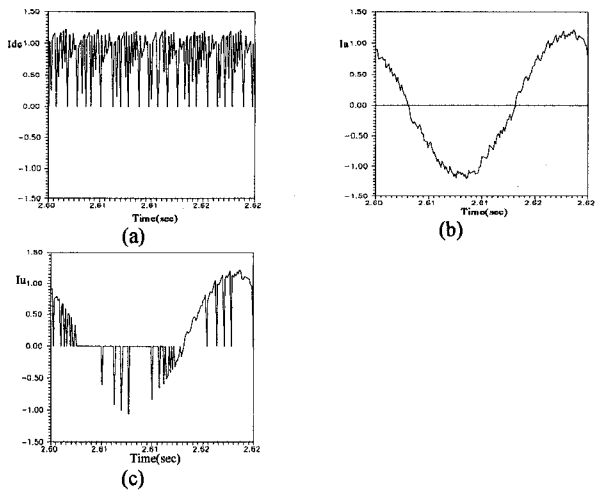


Fig. 5. Steady state waveforms without considering dead time. (a) DC link current. (b) Winding current. (c) Leg current.

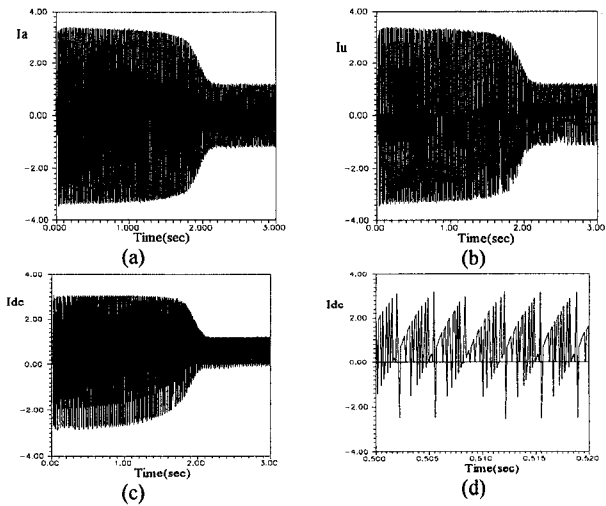


Fig. 6. Start-up transient waveforms without considering dead time. (a) Winding current. (b) Leg current. (c) DC link current. (d) DC link current zoomed to 20 ms.

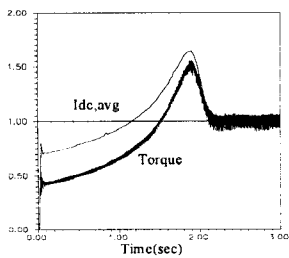


Fig. 7. Start-up average DC link current and output torque.

To illustrate the effect of dead time, waveforms of I_{dc} at one fundamental operation period during steady state are shown at Fig. 8, with and without dead time being considered. If dead time is not considered, there is definitely

no negative spike at I_{dc} . However, if dead time is considered as $T_d = 10 \mu s$, significant negative spikes are found at I_{dc} . The time-zoomed waveforms of the same I_{dc} are shown in Fig. 8 (c) and (d) so that the negative current spikes can be seen clearly. It is found that the time width of these negative spikes is equal to $10 \mu s$, which is the dead time duration. Also it is noticed that not in every subcycle there exists this kind of negative spikes. From the above figures, it can be concluded that the negative spikes on the DC link current are caused by dead time effect and influenced by inverter operation.

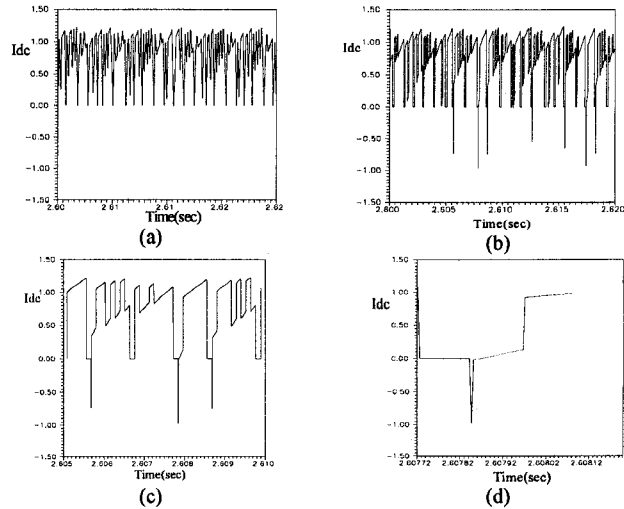


Fig. 8. Negative spikes at DC link current. (a) Dead time is not considered (20 ms). (b) Dead time is considered (20ms). (c) Dead time is considered (zoomed to 5 ms). (d) Dead time is considered (zoomed to one subcycle).

B. Experimental Verification

The experimental waveform of DC link current for one fundamental period (20ms) is shown in Fig. 9. The agreement between the simulation and measurement results verifies the correctness of the simulation.

Fig. 10 shows the measured waveforms of DC link current and its corresponding inverter upper leg current. It can be observed that there are both negative spike and positive spikes at the measured DC link current waveform while there is only positive spike on the measured leg current, which is mainly caused by the reverse recovery effect of diode. Moreover, the measured negative spike shows that its duration closely agrees with the dead time interval of $10 \mu s$ and it happens at the beginning of this subcycle, which is usually dead time interval. When the duration of dead time is changed, which is software-controlled inside of the inverter, the duration of the negative spike is changed correspondingly also. Furthermore not in every subcycle there exists this kind of negative spike (Fig. 11). Thus, it can be verified that the negative spike is caused by dead time and

influenced by the system operation. It should be pointed out that since the negative spike is with very short time duration and there is no regularity in practical system, the oscilloscope should be worked at trigger mode to capture the negative spikes. And the idealization of switching devices in theory analysis also causes a little difference on the shapes of current spikes between simulation and experimental ones.

V. CONCLUSION

With the introduction of novel concept *DTV*, DC link current of PWM inverter drives has been investigated deeply and comprehensively. Unified expressions for DC link current are derived and some new knowledge on DC link current are acquired for both steady and transient operations. And the phenomenon of negative spikes caused by dead time on DC link current has been confirmed by means of theoretical analysis, computer simulation and experimental results. Hence the information of dead time can be easily acquired from the DC link current. *DTV* has been verified as a useful tool for inverter analysis. The further utilization of *DTV*, such as system stability analysis and compensation of dead time effect, will be investigated.

APPENDIX

Parameters of the induction motor (per unit):

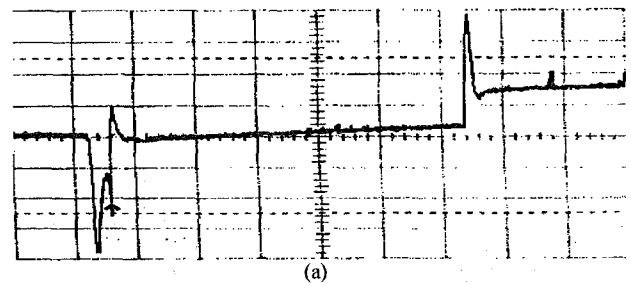
inertial constant $H = 300$, stator resistance $R_s = 0.03002$, rotor resistance $R_r = 0.02686$, stator reactance $L_s = 3.6775$, rotor reactance $L_r = 3.6775$, mutual reactance $L_m = 3.5775$.

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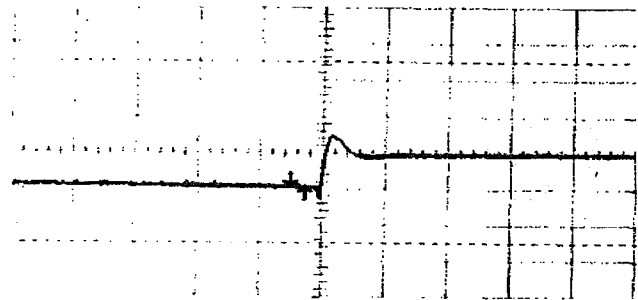
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Fig. 9. Measured waveform of DC link current over 20ms.



(a)



(b)

Fig 10. Measured current waveforms with occurrence of negative spikes. (a) DC link current (20 $\mu\text{s}/\text{div}$). (b) Upper leg current (10 $\mu\text{s}/\text{div}$).

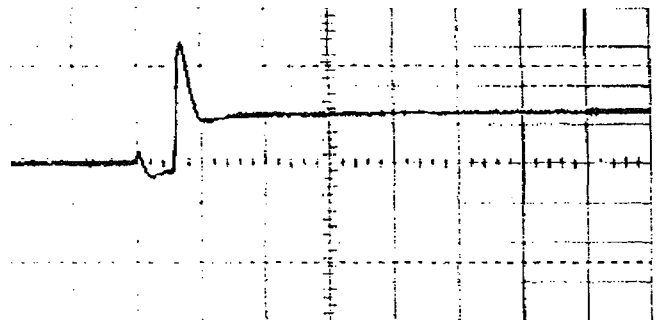


Fig. 11. Measured DC link current waveform without occurrence of negative spikes(10 $\mu\text{s}/\text{div}$).