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Resonant Tunnelling Diode Terahertz Sources for Broadband Wireless Communications

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ABSTRACT

This paper will discuss resonant tunnelling diode (RTD) sources being developed on a European project iBROW (ibrow.project.eu) to enable short-range multi-gigabit wireless links and microwave-photonics interfaces for seamless links to the optical fibre backbone network. The practically relevant output powers are at least 10 mW at 90 GHz, 5 mW at 160 GHz and 1 mW at 300 GHz and simulation and some experimental results show that these are feasible in RTD technology. To date, 75 - 315 GHz indium phosphide (InP) based RTD oscillators with relatively high output powers in the 0.5 - 1.1 mW range have been demonstrated on the project. They are realised in various circuit topologies including those that use a single RTD device, 2 RTD devices and up to 4 RTD devices for increasingly higher output power. The oscillators are realised using only photolithography by taking advantage of the large micron-sized but broadband RTD devices. The paper will also describe properties of RTD devices as photo-detectors which makes this a unified technology that can be integrated into both ends of a wireless link, namely consumer portable devices and fibre-optic supported base-stations (since integration with laser diodes is also possible).

Keywords: Terahertz sources, resonant tunneling diodes, wireless/photonic interfaces

1. INTRODUCTION

Terahertz (THz) radiation, whose frequency range lies between millimetre-waves and infrared light in the electromagnetic spectrum, has many potential applications in different scientific fields such as medical diagnostics, security imaging, and wireless communication^{1,2}. However, because of the lack of efficient and practical THz radiation sources this part of the electromagnetic spectrum has been known as the THz gap. Most of the current THz electronic sources used today are based on low frequency sources with multiplier chains^{3,4}. The efficiency of these architectures is low and the sources are not compact. There are also photonics-based THz sources: quantum cascade lasers (QCLs) and far infrared (FIR) gas lasers can operate in the THz gap⁵⁻⁷ producing tens of milliwatt power, but they are bulky with QCLs requiring cryogenic cooling. Recently, QCL sources operating at room temperature and limited to frequencies above 1 THz have been demonstrated⁷, but require high power to operate.

To realise compact and efficient THz sources, especially in the 0.1 - 3 THz range, a number of electronic devices including Gunn diodes, resonant tunneling diodes (RTDs) etc. have been considered. Of these, the InP-based resonant tunnelling diode (RTD) is the fastest electronic device and has the potential to construct compact oscillators that can operate up to 2.5 THz and at room temperature⁸. A number of demonstrations of the large RTD bandwidth have been reported recently⁹⁻¹⁴. Demonstrations of wireless transmission using RTD oscillators at 300 GHz¹⁵, 500 GHz¹⁶, and 350 GHz with intensity modulation up to 30 GHz bandwidth¹⁷ have also been reported. These individual RTD oscillators, however, exhibit low output power in the microwatt range, partly due to using sub-micron RTD device sizes chosen to reduce the RTD self-capacitance¹⁸. For example, 150 μ W output power was reported at 271 GHz¹⁹, 0.4 μ W was reported at 1.92 THz⁹, and 1.9 μ W was reported at 1.52 THz¹². Also, higher powers \sim 400 μ W at 530 - 590 GHz from a single oscillator and combined output powers of 610, 270, and 180 μ W at 620, 770, and 810 GHz in a two-element array have been reported²⁰. However, all prior work on THz RTD sources use integrated antennas and so the power measurements have to account for free space loss. As the antenna patterns are not well known with respect to measurement locations, the errors in reported output powers are difficult to quantify. For arrays of these free running

oscillators²⁰, the power may add up or subtract in free space and so the measurements can be very inaccurate. Nevertheless, high power levels in the milliwatt range are desirable. To provide a perspective for this, practically relevant output powers of at least 10 mW at 90 GHz, 5 mW at 160 GHz and 1 mW at 300 GHz are required²¹, for example, for the future wireless indoor communications in femtocell scenarios²², and these are yet to be demonstrated as single compact electronic sources in integrated circuit form.

To increase the output power of RTD oscillators, we have developed designs that employ the largest possible sized devices and also developed oscillator circuit topologies that employ two RTD devices. Using this approach, 28 GHz and 76 GHz InP-based RTD oscillators with around 1 mW were realized^{23,24}. Thereafter, D-band oscillators operating at 125 GHz, 156 GHz and 166 GHz, with output power of 0.34 mW, 0.24 mW, 0.17 mW, respectively, were also demonstrated²⁵. These oscillators used shorted coplanar waveguide (CPW) lines of characteristic impedance (Z_0) 50 Ω to realise the inductances required to resonate with the device self-capacitance for a given target oscillation frequency. However, for very high frequencies from the J-band, the length of the 50 Ω CPW line (shorted stub) becomes extremely small and so limits that maximum oscillation frequency. In this paper, we extend the previous work by investigating large RTD devices together with lower Z_0 CPW shorted stubs for oscillators with higher oscillation frequencies and higher power. Calculations show that using this approach the performance of RTD oscillators could be further improved and extended deeper into the THz regime.

The rest of the paper is organized as follows. In section 2, a discussion on RTD bandwidth, sizing and power considerations is presented first. Then a review of the conventional RTD device design for THz oscillators is provided and contrasted with the approach taken here. Details of the fabrication of RTD devices and their DC characteristics are also described. In section 3, the design and fabrication of RTD oscillators is described. The measurement setup for characterising the RTD oscillators is introduced in section 4, followed by discussion on the experimental results. Optoelectronic properties of RTDs are summarized in section 5, while conclusions with future work are given at the end of the paper in Section 6.

2. RTD DEVICE DESIGN, FABRICATION & CHARACTERISATION

2.1 Bandwidth, Device Sizing and Output Power Considerations

The device cut-off frequency (f_{max}) of an RTD can be estimated from (assuming $R_S \ll R_n$)²⁶:

$$f_{max} \approx \frac{1}{2\pi C_n \sqrt{R_S R_n}} \quad (1)$$

where C_n is the self-capacitance of the device; R_n is absolute value of the (minimum) negative differential resistance of the device; and R_S accounts for the contact and access parasitic resistances. Note from (1) that f_{max} can be increased through contradictory requirements, either as is usually done by reducing C_n (through decreasing device size) or as proposed here by lowering R_n and R_S (through increasing device size).

The resistance R_n can be estimated from $R_n = \frac{2\Delta V}{3\Delta I}$ where $R_n = 1/G_n$ is the negative differential conductance (NDC), ΔI and ΔV are the peak-to-valley current and voltage differences, respectively. A figure-of-merit for RTDs is the peak current density (J_p), the (maximum) current at which the device starts to exhibit NDC. Since ΔV is usually fixed by the epitaxial layer structure, to realise a device of a given NDC (which can be defined by G_n), the device has to be sized accordingly. G_n is usually chosen under 50 mS. Beyond about 100 mS, the NDC impedance level drops to a few Ohms ($< -10 \Omega$) and this region is difficult/impossible to discern or measure. As such high J_p structures necessarily require small RTD devices sizes. Further, to avoid oscillations of the bias circuit/lines when the device is biased in the NDC region, a shunt resistance R_e (with $R_n > 1/G_n$) can be connected across the device, which for a given R_e establishes the maximum device size²³, A_{max} , as

$$A_{max} R_e < \frac{2\Delta V}{3\Delta J} \quad (2)$$

where ΔJ is the peak-to-valley current density difference. R_e usually chosen to be in the 10-20 Ω range²⁷.

The RTD model, and in particular the device capacitance C_n , is extracted from DC and S-parameter measurements^{28,29},

while the theoretical maximum generated power (P_{max}) of a single RTD device oscillator oscillating at frequency f_{osc} can be estimated from the device's I-V characteristics and f_{max} using the formula²⁶:

$$P_{max}(f_{osc}) \approx \frac{3}{16} \Delta V \Delta I \left(1 - \frac{f_{osc}^2}{f_{max}^2} \right) \quad (3)$$

RTDs for use in high power oscillators should therefore be designed to maximize both ΔV and ΔI , with the epitaxial layer structure designed to maximize ΔV while the use of larger area (with low C_n) devices adopted to maximise ΔI .

2.2 Device design considerations

The RTD device consists of a narrow bandgap material (4.5 - 6 nm thick $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ quantum well) sandwiched between two thin wide bandgap materials (1 - 3 nm AlAs barriers), making up the double barrier quantum well (DBQW) structure. The structure is completed by un-doped or lightly doped $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ spacer layers, n-type emitter/collector layers and highly doped $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ contact layers on either side of the DBQW.

High peak current density (J_P) ($> 6 \text{ mA}/\mu\text{m}^2$) device designs have conventionally been used for THz RTDs since they require the use of submicron device sizes with correspondingly small device self-capacitances^{12-14, 18}, but they are actually best suited for switching applications³⁰. They typically employ $\sim 1 \text{ nm}$ AlAs barriers. However, such thin barrier thicknesses reduce the peak-to-valley current ratio (PVCR) to less than two (2) through the increased electron transmission probability through the DBQW. Therefore, the peak-to-valley current difference reduces and so does the potential output power of an oscillator using such a device. Besides, the accurate growth of such ultra-thin AlAs barriers is difficult/challenging leading to potential non-uniformity of the epi-layer structure and so limiting the repeatability, reproducibility, and manufacturability of the devices^{31,32}. Other issues that may be attributed to high J_P RTDs include reduced thermal stability^{18,30} and the need for ultra-low Ohmic specific contact resistances for the small-sized devices to keep the device contact resistance low³³, since high resistance limits the oscillator output power significantly^{26,27}. Last but not least, small devices in the sub-micrometer range require electron-beam lithography, which is more costly compared to fabrication of larger devices with photolithography.

Lower J_P designs as those used in mm-wave oscillators²³⁻²⁵, on the other hand, employ large micro-sized RTD devices and so can provide high RF power in oscillator circuits due to the increased ΔI , do not suffer thermal stability issues and can be fabricated with photolithography. Such devices employ thicker barrier layers ($> 1.4 \text{ nm}$). The epitaxial growth requirements are therefore less demanding and more accurate benefiting a more reproducible technology. PVCR is also higher (> 3), and since for a given specific contact resistance, the larger size RTD devices exhibit low Ohmic contact resistance, these factors benefit higher output power in oscillator circuits²⁶. Crucially, though, is that both the lower J_P , e.g. Ref. [23]-[25], and higher J_P , e.g. Ref. [14], [18], have low and comparable device self-capacitances in the 2-4 fF/ μm^2 range so lower J_P structures can also enable the realisation of THz oscillators.

2.3 Device fabrication and characterisation

The epi-layer structure of the RTD wafer that was used in the oscillators reported here was grown by molecular beam epitaxy (MBE) by IQE Ltd on a semi-insulating InP substrate. It employs a 4.5 nm $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ quantum well, 1.4 nm AlAs barriers and 25 nm spacers. This DBQW design offers current density J_P of $\sim 3 \text{ mA}/\mu\text{m}^2$ but large PVCR (> 3.5). The collector and emitter layers are made of highly doped $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$ material ($3 \times 10^{19} \text{ cm}^{-3}$) doped with Si. The RTD devices were fabricated using optical lithography techniques. Two RTD with two different mesa sizes, $4 \mu\text{m} \times 4 \mu\text{m}$ and $3 \mu\text{m} \times 3 \mu\text{m}$ chosen according to (2), were fabricated. Chemical wet etching ($\text{H}_3\text{PO}_4:\text{H}_2\text{O}_2:\text{H}_2\text{O} = 1:1:38$) was used to define the RTD mesa. This recipe has an etching rate of around 100 nm/min. Polyimide PI-2545 was used for device passivation. The epitaxial structure has an estimated capacitance per unit area of 3.56 fF/ μm^2 . Therefore, the self-capacitances for $3 \mu\text{m} \times 3 \mu\text{m}$ and $4 \mu\text{m} \times 4 \mu\text{m}$ were 32 fF and 57 fF, respectively, and are low enough to allow the realization of mm-wave/THz oscillators. The measured specific contact resistance ρ was $52 \Omega\text{-}\mu\text{m}^2$ giving f_{max} of 340 GHz and 390 GHz for the $3 \mu\text{m} \times 3 \mu\text{m}$ and the $4 \mu\text{m} \times 4 \mu\text{m}$ devices, respectively.

3. OSCILLATORS DESIGN AND FABRICATION

As already noted, for each RTD device, there is a limitation on the maximum mesa size (A_{max}) for which bias oscillations can be eliminated with a shunt resistor, R_e . The used oscillator topology in this work employs two individually biased and stabilised RTD devices and a single RF load as shown in Fig. 1(a). The circuit potentially provides double the RF output power of a single RTD device oscillator using an identical RTD device. Each RTD device is also connected to a bypass capacitor C_e which is placed to short-circuit the RF signal to ground and avoid dissipating the RF power over R_e . This capacitor also provides a short-circuit termination for a CPW transmission line used to realise inductance L which is designed to resonate with the RTD self-capacitance C_n to obtain the desired frequency. A shorted transmission line acts as an inductance when its electrical length is less than 90° and the natural/lowest oscillation frequency of the circuit is such that this is always the case. The resistance R_L represents the load, 50Ω in our case, which is introduced by the spectrum analyser or power meter during measurement. Capacitors (DC-block) are introduced in the circuit to prevent any DC from reaching the spectrum analyzer during on-wafer measurements. The inductor L_b denotes the bias cable inductance.

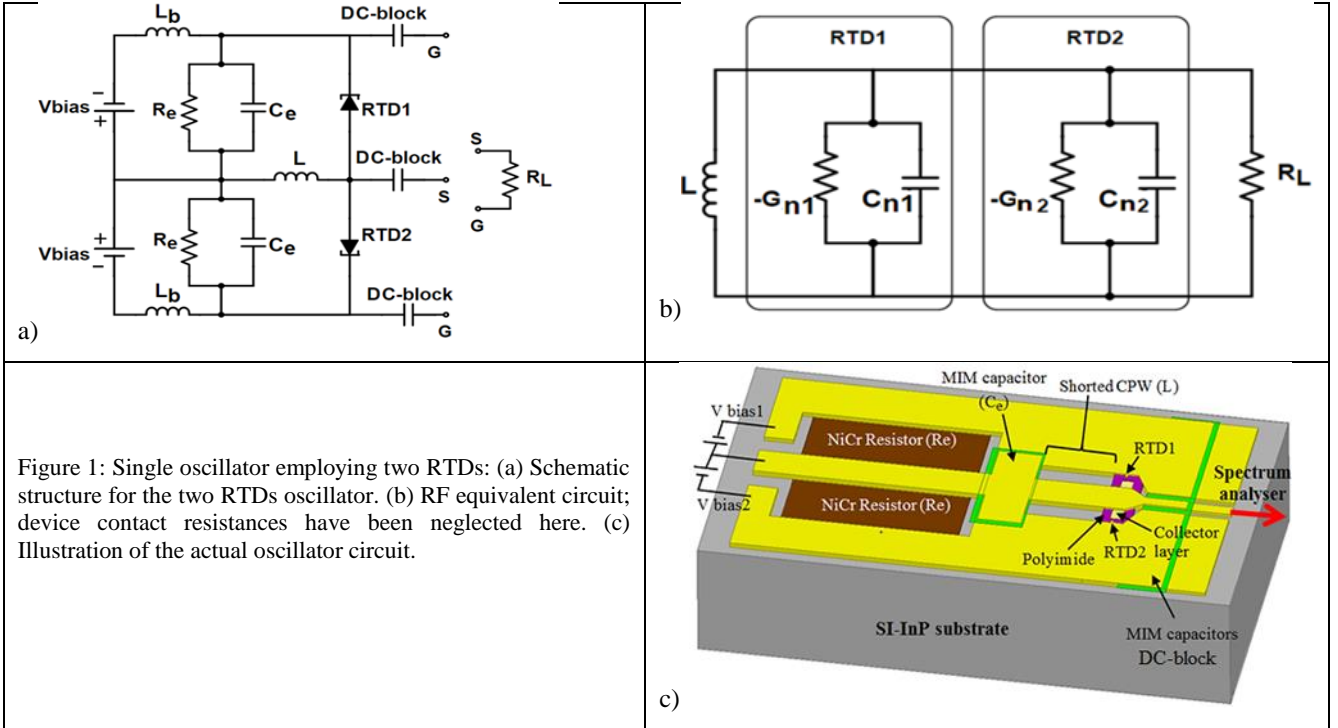


Figure 1: Single oscillator employing two RTDs: (a) Schematic structure for the two RTDs oscillator. (b) RF equivalent circuit; device contact resistances have been neglected here. (c) Illustration of the actual oscillator circuit.

The RF equivalent circuit of the two RTD oscillator is shown Fig. 1b where each RTD is modeled by its lumped small signal equivalent circuit comprising the negative differential conductance ($-G_{n1}$ and $-G_{n2}$) in parallel with the self-capacitance (C_{n1} and C_{n2}) of each device. The device contact resistance is neglected in this description for simplicity. The

oscillation frequency for this parallel resonant circuit is given by
$$f_{osc} = \frac{1}{2\pi\sqrt{(C_{n1} + C_{n2})L}}.$$

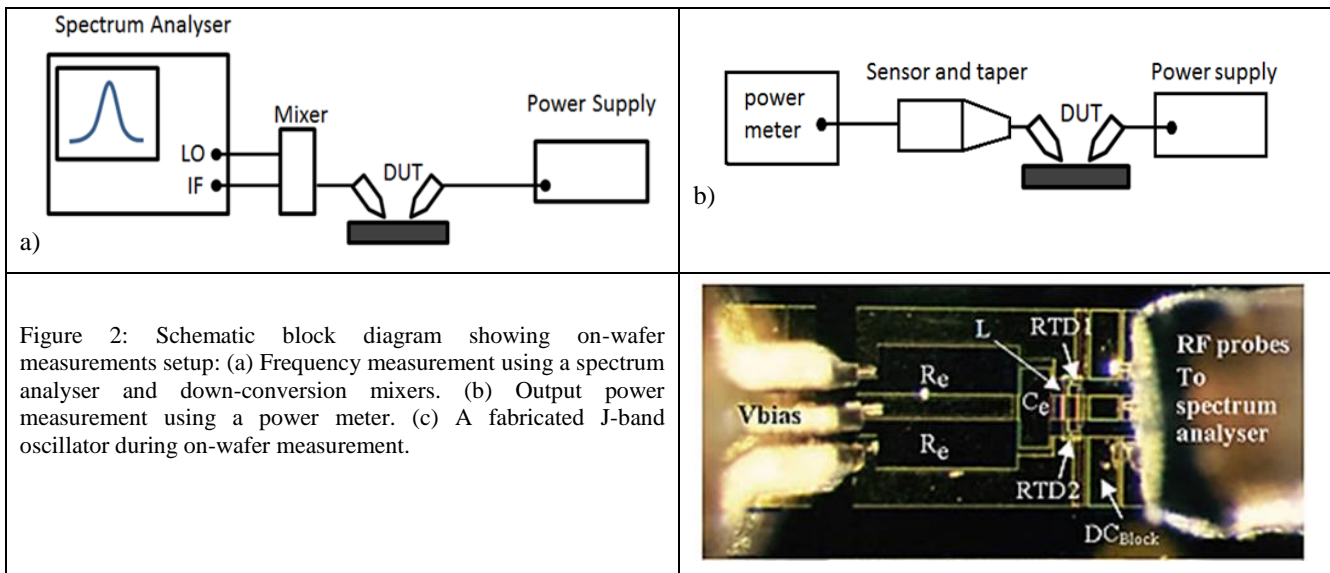
As the 9 or 16 μm^2 RTDs are the smallest components in the oscillator circuits, photolithography is used to define all features during circuit fabrication. The layout was designed by L-Edit software. The stabilizing resistor R_e was realised from a 33 nm thin film NiCr (60:40) which has a sheet resistance of $50 \Omega/\text{square}$. The designed resistor value was 22Ω , realised with dimensions of $300 \mu\text{m} \times 130 \mu\text{m}$ and the measured value from process control structures (PCM) was within $\pm 10\%$. Metal-insulator-metal (MIM) capacitors were designed and fabricated to realise C_e and the DC-blocks in the circuits. The dielectric layer used was Si_3N_4 with 75 nm thickness, which corresponds to $0.8 \text{ fF}/\mu\text{m}^2$. It was deposited using inductively coupled plasma (ICP) chemical vapor deposition (CVD). The designed value of C_e was 10 pF realised

with MIM size of $210 \mu\text{m} \times 60 \mu\text{m}$) and that of DC-Block capacitor was 800 fF realised with MIM size of $20 \mu\text{m} \times 80 \mu\text{m}$. Measurements of PCM structures confirmed that the realised structures had expected values³⁴. The inductance L was realised by appropriate lengths of CPW transmission lines with Z_0 of 25Ω , 32Ω , or 50Ω shorted by the bypass capacitor C_e . For the 25Ω , 32Ω , and 50Ω CPW lines, the signal lines widths are $126 \mu\text{m}$, $110 \mu\text{m}$, and $60 \mu\text{m}$ and the gap distances between the signal lines to the ground planes are $7 \mu\text{m}$, $15 \mu\text{m}$, and $40 \mu\text{m}$, respectively. Modelling of the passive parts, such as the resonating inductance and MIM capacitors, was done through both High Frequency Structural Simulator (HFSS) and validation of the models done via S-parameter measurements³⁴. An illustration of the two RTD device oscillator is shown in Fig. 1c.

4. OSCILLATOR MEASUREMENT

4.1 Measurement Setups

The fabricated oscillators were measured on-wafer by using Keysight E4448A spectrum analyzer, with biasing provided through a ground-signal-ground (GSG) Cascade probe. The schematic diagram of the measurement setup is shown in Fig. 2(a). It consists of a J-band ($220 - 325 \text{ GHz}$) $50 \mu\text{m}$ pitch GSG probe, a J-band harmonic mixer (Farran Technology WHM-03) to mix down the signal. A diplexer is used to separate the local oscillator (LO) and intermediate frequencies (IF). With the built-in signal identification function of the spectrum analyzer, the oscillation frequency was accurately identified. The mixer loss is specified by the manufacturer to be approximately 50 dB for the J-band. As calibration of this is however non-trivial (reference sources at the desired frequencies are not easily available), we considered the mixer to be non-calibrated, and so the actual output power was measured by a calibrated power meter (Erikson PM4). Since the input of power sensor head is WR-10 (W-band) waveguide, a WR-3 to WR-10 tapered waveguide was used as shown in Fig. 2b, and therefore losses due to the probe (3 dB) and waveguide (0.5 dB) were taken into account. Fig. 2c shows one of the fabricated oscillators during on-wafer characterisation.



As the characterization of THz oscillators using a 50 GHz spectrum analyser with down conversion mixers is time consuming and non-trivial, a recently developed quicker technique which uses a vector network analyser (VNA) was used to identify the oscillation frequencies of the circuits³⁵. These were therefore known prior to using the spectrum analyser for detailed characterization. Nonetheless, it is useful to point out that as part of the standard characterization methodology for the oscillators, initially the output is connected directly to a 50 GHz spectrum analyser to ascertain if any oscillations up to this frequency are being generated; and then with appropriate down conversion mixers, the higher bands (V-, W- and G-bands) are also checked and so the observed frequencies are deemed to be the true fundamental oscillation frequencies.

4.2 Oscillation Frequency Results

Different oscillators, each employing either two $4\ \mu\text{m} \times 4\ \mu\text{m}$ or $3\ \mu\text{m} \times 3\ \mu\text{m}$ RTD devices, with different Z_0 CPW shorted stubs were fabricated and characterised. Two different samples from two different runs labelled sample-1 and sample-2 were fabricated to investigate the repeatability and reproducibility of the process by monitoring the oscillators' performance. Example/typical frequency measurements from sample1 are summarized here. Figure 3a shows the detected signal for a 308 GHz oscillator using the VNA³⁵ and spectrum analyser techniques when $V_{\text{bias}} = 1.42\ \text{V}$ and $I_{\text{bias}} = 176\ \text{mA}$. It employs two $4\ \mu\text{m} \times 4\ \mu\text{m}$ RTDs and a $10\ \mu\text{m}$ long $25\ \Omega$ CPW shorted stub. The sharp reversal in the return loss in Fig. 3a, from under $-10\ \text{dB}$ to about $8\ \text{dB}$ in this case, is known to indicate the existence of an external oscillation frequency being sensed by the VNA. Figure 3b shows the spectrum of the same oscillator measured using the spectrum analyser.

A variety of oscillation frequencies were observed for different CPW lengths and impedances. For other oscillator employing two $4\ \mu\text{m} \times 4\ \mu\text{m}$ RTDs, a $3\ \mu\text{m}$ long $50\ \Omega$ CPW shorted stub circuits showed an oscillation frequency of 304 GHz, while for an oscillator with $7\ \mu\text{m}$ long $50\ \Omega$ CPW shorted stub, the oscillation frequency was 245 GHz. A higher oscillation frequency of 309 GHz was observed from oscillator with same ($7\ \mu\text{m}$) CPW length but with $Z_0 = 25\ \Omega$, as expected. And, an oscillation frequency of 254 GHz was measured for $8\ \mu\text{m}$ long $32\ \Omega$ CPW shorted stub. These results demonstrate that large RTD devices and longer lower Z_0 transmission lines can be employed and fabricated accurately using photolithography to realise THz RTD oscillators.

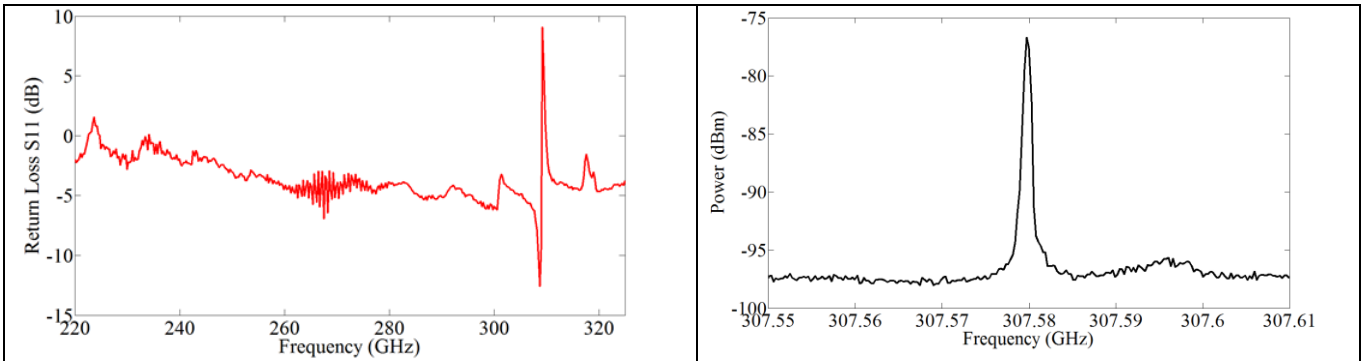


Figure 3: Measured spectrum of the 308 GHz oscillator. (a) Frequency detection technique using VNA showing gain at 308 GHz. (b) Measured spectrum using spectrum analyser.

4.3 Oscillators Output Power Results

The oscillator output power measurements were made using the setup in Figure 2(b), using the J-band (220 – 325 GHz) rectangular waveguide measurement system. Figure 4 shows the measured output power versus bias for the 308 GHz oscillator which employs two $4\ \mu\text{m} \times 4\ \mu\text{m}$ RTDs. There is relatively little variation with bias for the oscillators on sample-2. Note that the output power data shown in Figure 4 has been compensated the probe and taper insertion losses which is 3.5 dB as per manufacturer specifications, while the spectrum shown in Fig. 3b is the actual measured trace. Using the power measured by the power meter, the losses of the frequency measurement setup which uses the spectrum analyser was estimated to be approximately 72 dB which accounts for the probe losses, mixer loss, waveguide losses and IF cabling losses. The DC to RF conversion efficiency for all oscillators was under 0.2%. Future work will consider advanced bias stabilisation criteria/circuit so as to avoid the large DC power dissipated in R_e ³⁶. Note also that the relatively high measured output powers indicate that the cut-off frequencies for the devices may be much higher than predicted by (1).

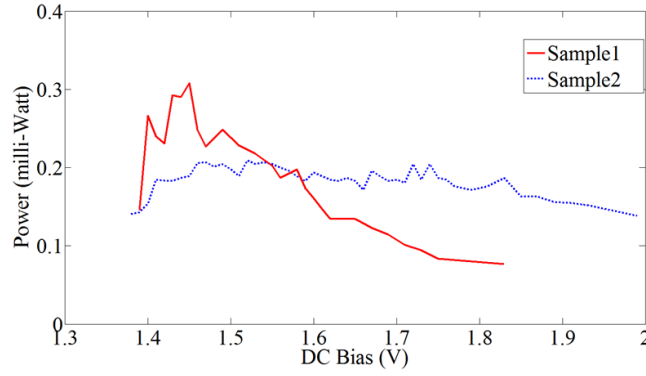


Figure 4: Measured output power of the 308 GHz oscillators versus bias voltage.

Table 1 compares the measured highest oscillator output power in this work with SiGe HBT, Gunn diode, InP DHBT and CMOS technologies for which on-wafer probing measurement setup was also used. The RTD oscillator compares well in terms of output power even though presently the DC power consumption is high. The key advantages for RTDs are the reduced lithography requirements and the simple circuitry for THz sources.

Table 1: Comparison of RTD oscillators with other on-wafer probed THz oscillators.

Frequency (GHz)	Power (dBm)	DC Power (mW)	Technology	Ref.
284	-3.4	250	3 $\mu\text{m} \times 3 \mu\text{m}$ RTD	This work
367	-8	64	130 nm SiGe	[37]
307	-15.5	196	In _{0.53} Ga _{0.47} As planar Gunn diode	[38]
310	-6.2	76	256 nm InP DHBT	[39]
290	-1.2	325	65 LP bulk CMOS	[40]

Figure 5 shows the estimated oscillation frequency versus different Z_0 CPW shorted stubs and different RTD device sizes that could be considered for implementation in new oscillator circuits. They show that 10 μm 25 Ω CPW long lines can be used to realise oscillators up to about 600 GHz; and oscillators with extremely short 2 μm lines can reach up to 1.4 THz. To achieve these high frequencies, the device cut-off frequency needs to be extended and this can be achieved through lower resistance Ohmic contacts, for instance, by using a high indium content contact layer to realise very low specific contact resistance (ρ) as already demonstrated by others⁴¹. For example, 2.9 THz and 3.25 THz are estimated using equation (1) for the 3 $\mu\text{m} \times 3 \mu\text{m}$ and the 4 $\mu\text{m} \times 4 \mu\text{m}$ devices, respectively, if $\rho = 0.73 \Omega\text{-}\mu\text{m}^2$ (reported in Ref. [42]) was successfully realised. As already noted, equation (1) shows that the cut-off frequency (f_{max}) can be increased by reducing R_s and R_n , both of which can be realised by using larger mesa area RTD devices. Careful epitaxial layer design may also help offset the increased device capacitance from the larger devices.

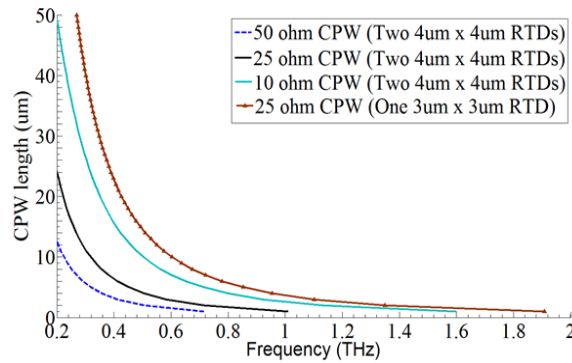


Figure 5: Calculated oscillation frequency versus CPW length.

5. RTD-BASED OPTOELECTRONIC CIRCUITS

The operation and functionality of a considerable number of new optoelectronic devices and circuits based on III-V semiconductor compound materials can benefit from the integration with DBQW-RTDs taking advantage of the negative differential conductance (NDC) produced by the DBQW structures. Such optoelectronic devices include laser diodes, semiconductor optical amplifiers, photo-detectors, and semiconductor electro-refraction and electro-absorption modulators. Particularly, the integration of DBQWs with unipolar photodetectors by embedding the DBQW within or close to the light sensitive region gives rise to a new device known from now as resonant tunneling diode photodetector (RTD-PD)⁴³⁻⁴⁵.

RTD-PDs for operation at 1300 nm and 1550 nm were implemented with various optical windows sizes and shapes. The RTD-PD epi-layer was grown on SI-InP, comprising 1.7 nm AlAs barriers and 5.7 nm quantum well, two InGaAlAs light absorbing layers 500 nm thick each side of the DBQW, and two InAlAs light layers. Figure 6(a) illustrates the energy bands schematic diagram of an RTD-PD obtained by the integration of an InGaAs/AlAs DBQW with a unipolar InGaAlAs/InAlAs photoconductive structure lattice matched to InP. Figure 6(b) shows typical current-voltage characteristics under dark and illuminated conditions, where a global shift of the I-V to lower voltage induced by the light absorption is clearly visible. Figure 6(c) represents the responsivity of the devices as function of the applied voltage. An intensity modulated light can induced peak to valley switching giving rise to a current change equivalent to the peak-to-valley current difference which can lead to absolute responsivities of tens of A/W.

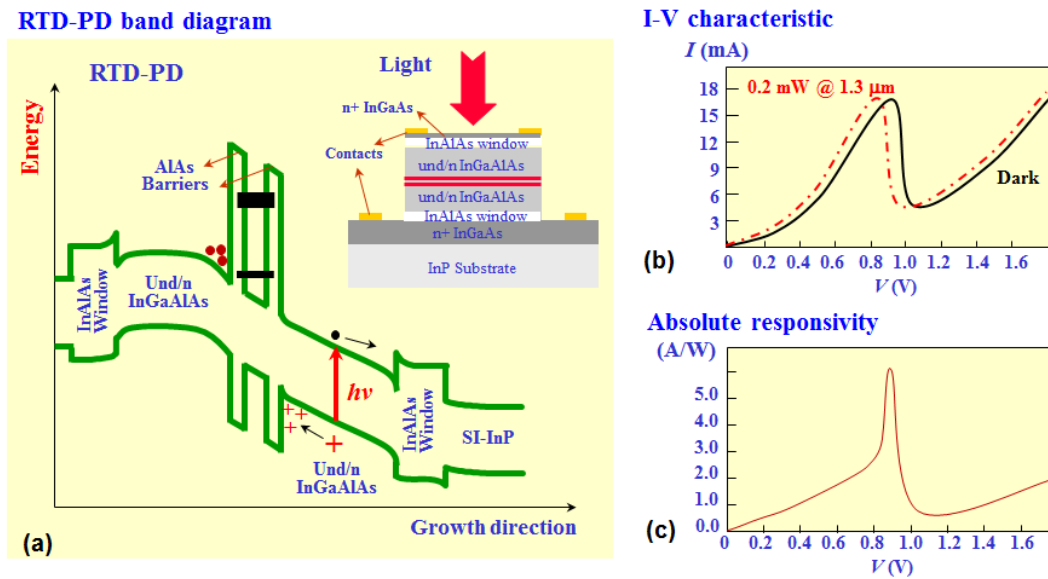


Figure 6: (a) Schematic of the band energy diagram of a surface illuminated InGaAlAs/AlAs RTD-PD. Inset is shown a representation of the RTD-PD mesa cross-section. (b) Typical dark and illuminated I-V characteristics for moderate illumination. (c) Absolute responsivity as function of the applied bias.

The light-generated holes in the collector region of the device leads to an enhancement of electric field across the DBQW and the adjacent collector region, which leads to a global shift of the I-V to lower voltage with an additional current increase, which is dependent on the optical power levels being absorbed. Moreover, due to the internal electrical field dependence the device responsivity (the ratio between the photocurrent and the optical power absorbed) becomes a function of the bias voltage and the power level. Figure 7 shows the typical optical response of RTD-PDs for different optical power levels at the wavelength 1310 nm.

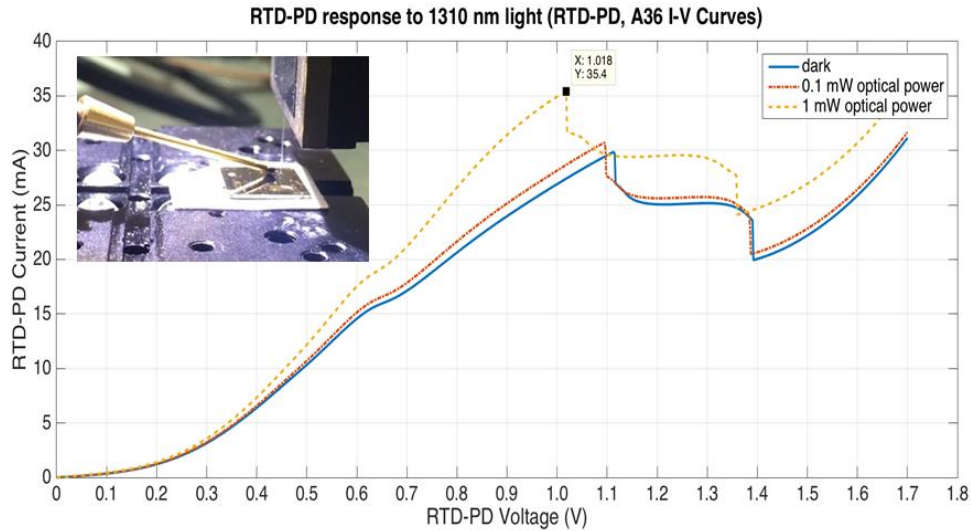


Figure 7: Typical response of RTD-PDs for different optical power levels at the wavelength 1310 nm. The steady state responsivity in the first positive differential conductance region can reach tens of A/W.

RTD-PDs can be operated either in the steady state mode (when polarized in one of the positive differential conductance (PDC) regions of the I-V curve), or in the oscillatory mode (when polarized in the NDC region). These modes of operation and the RTD-PDs functionalities make them potential candidates for implementation of low cost highly efficient optical-to-wireless interfaces capable of transferring electrical signals embedded in the optical carriers used in fibre optic communications to mm-wave/THz carriers. Moreover, when directly illuminated by low power intensity modulated optical signals the RTD-PD oscillators can lock to the optical signals subcarriers, with the RTD-PD electrical output power being merely determined by the NDC region characteristics. Indeed, microwave optoelectronic RTD-PD oscillators with record RF output powers over 10 mW around the X-band have recently been demonstrated on iBROW and are being characterized for these functionalities, details of which will be reported elsewhere.

Besides, then polarized in the PDCs but close to the NDC, the RTD circuits can behave as excitable systems: when undisturbed RTDs remain in the quiescent (equilibrium) states, producing a small response to a “small” perturbation (stimulus) but fire a large transient response when the small perturbation (stimulus) exceeds a certain threshold then settling back to the quiescent point in what is called the refractory period, after which the RTDs can be excited again. Moreover, for small perturbations away from the equilibrium, the return is monotonic; however, for perturbations beyond a threshold value, the return is not monotonic, but undergoes a large excursion before settling down. The excitability properties make intensity modulated optical signals capable to induce RTD-PD transition from the peak to the valley, producing electrical signals that mimic the optical signals subcarriers.

Optimized RTD-PDs for operation at 1550 nm are currently under development. With such structures we expect to achieve: i) the reduction of the optical power levels required to ensuring that the system can be driven by an optical subcarrier RF signal (obtained larger light signal detection dynamic range); ii) the maximization of the optical-electrical power gain; iii) significant improvement of linearity and spectral response; v) an enhancement of differential responsivity (sensitivity) and responsivity-gain product.

6. CONCLUSION

An RTD oscillator design methodology to provide high power and high oscillation frequency was described in this paper. All the circuit components including the RTD devices are large and can be easily fabricated using photolithography. The approach was demonstrated up to ~ 0.3 THz with practically relevant output powers of up to 0.5 mW. Theoretical estimation shows that ~ 1.4 THz could be achieved with using the same approach through lower resistance Ohmic contacts, while optimised epitaxial designs and oscillator designs/layouts are expected to yield at least 1 mW output power at 0.3 THz. Indeed, 1 mW RTD sources in the J-band has already been demonstrated by the group using a synchronisation power combining circuit³⁴ and results will be published elsewhere soon. Optoelectronic

properties of RTDs have also been described. Future work will aim at improved designs and at integrating the oscillators with suitable antennas, and in employing them in prototype application scenarios.

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