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2017

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Development of Solution Processed, Flexible, CuInSe₂ Nanocrystal Solar Cells

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**Development of Solution Processed, Flexible, CuInSe₂ Nanocrystal Solar
Cells**

by

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Dedication

For my loving parents, Prabhakar Reddy Voggu and Mallika Voggu. For all those who are working towards the development of clean energy technologies.

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Development of Solution Processed, Flexible, CuInSe₂ Nanocrystal Solar Cells

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Supervisor: Brian A. Korgel

Clean sources of energy, especially photovoltaics (PVs), are urgently needed to cope with global energy shortage and environmental pollution. For PVs to play a significant role in energy production, the current prices must be brought down. Thin film PVs made using layered Mo or Au/CuInGaSe₂(CIGS)/CdS/ZnO/ITO have already shown high efficiencies. Traditionally, most layers in CIGS solar cells are deposited using high-cost techniques requiring high temperatures and ultra-low pressures. By replacing the traditionally processed CIGS with a nanocrystal layer that can be deposited at mild processing conditions, the fabrication cost can be reduced. In this study, a high yielding synthesis method for CuInSe₂ nanocrystals has been developed which gives the best efficiency (3.1%), so far, for low-temperature processed CuInSe₂ nanocrystal PVs.

An important challenge that nanocrystal solar cells currently face is low device efficiency, resulting in higher operating cost. CuInSe₂ nanocrystals can remain suspended in solution because of the long chain organic ligands attached to the surface. However, these ligands hinder charge transfer between nanocrystals causing low device efficiency. These ligands have been successfully replaced with smaller sulfide ions thereby improving

the best efficiency of low-temperature processed CuInSe₂ nanocrystal solar cells from 3.1 % to 3.5%.

Another approach to reducing the cost of CuInSe₂ PVs is by replacing the glass support medium with cheaper alternatives like paper. Flexible CuInSe₂ nanocrystal solar cells are successfully fabricated on paper with efficiencies reaching up to 2.25%. This is the first time a nanocrystal solar cell has been fabricated on paper. There is no significant loss in PV device performance after more than 100 flexes to 5 mm radius, and the devices continue to perform when folded into a crease.

Apart from the absorber layer, the replacement of other high-temperature and vacuum processed device layers with ambient solution-processed layers lowers the manufacturing cost. This has been achieved by spin coating suitable nanomaterials as device layers.

Lastly, for commercialization of CuInSe₂ nanocrystal solar cells, multiple devices need to be connected to achieve the desired current and voltage. A fabrication process has been developed for building multiple nanocrystal PVs on a single substrate using 3D printed masks.

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Chapter 1: Introduction

1.1 INTRODUCTION TO PHOTOVOLTAICS

Solar cells, also referred to as photovoltaics (PVs), are devices that convert light into electricity. Since solar energy is available in abundant and is a renewable resource, there are economic and environmental benefits associated with the use of photovoltaics. Even though the cost of grid-parity solar is very competitive to fossil fuels, the main challenge is the high cost of module and installation leading to long payback periods.¹ Additionally, rooftop solar electricity costs higher than electricity generated from conventional energy sources, and it has difficulty in competing with electricity generated from fossil fuels.² Thus, reducing the cost of PV modules will lead to a greater penetration of solar energy into the energy sector.

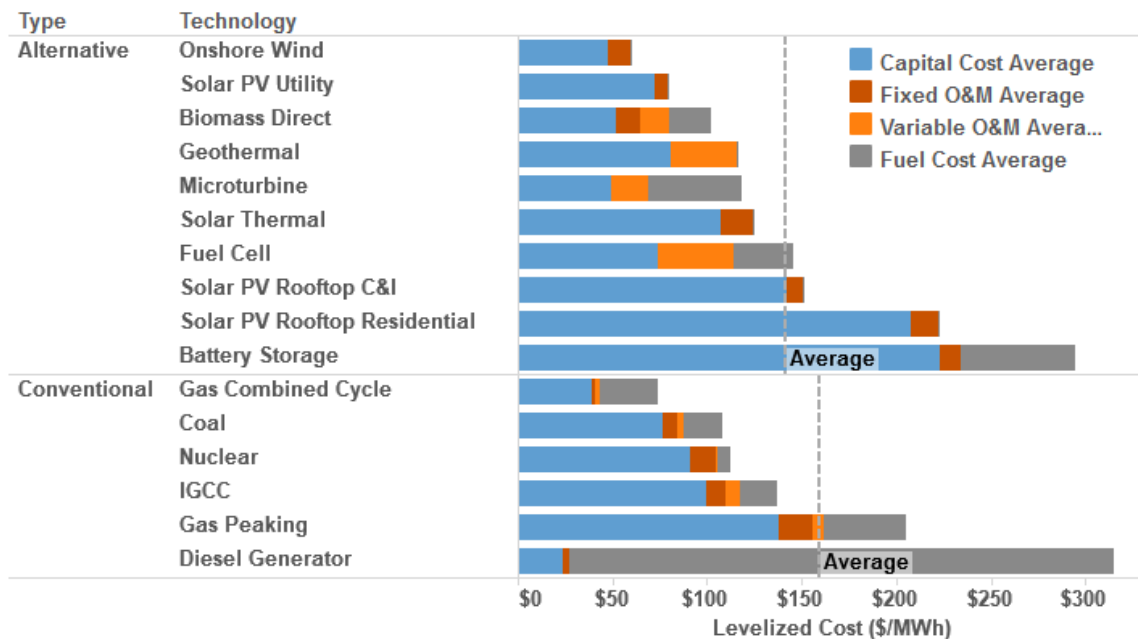


Figure 1.1: Levelized cost of electricity for various types of energy sources. ²

1.2 SOLAR CELL TECHNOLOGIES

Various types of solar cells have been invented and they can broadly be categorized as silicon solar cells, thin film solar cells and solution processable solar cells.

1.2.1 Silicon Solar Cells

Silicon solar cells are the first kind of solar cells discovered. They were discovered in 1954 at Bell labs. These solar cells are the most efficient and widely used, serving 90% of the solar energy market.³ Even though silicon is very abundant and inexpensive, the processing costs are higher because of the high temperature and ultralow vacuum needed during the deposition of silicon. Also, with silicon being an indirect band gap semiconductor, thick ($\approx 100 \mu\text{m}$) silicon layer is needed to absorb the light incident on the solar cell. Thick layers of high purity crystalline silicon significantly add to the cost of the solar device. Another disadvantage with thick absorber layers is that these solar cells cannot be flexed. Hence, they need to be fabricated on heavy rigid substrates like glass and are not a solution for portable power supply needs.

1.2.2 Thin Film Solar Cells

These solar cells typically employ amorphous-silicon, chalcogenide compounds (CdTe, Cu(In,Ga)(S,Se)). These materials are direct bandgap semiconductors and are capable of absorbing most of the incident light with thicknesses as low as 3-4 microns.⁴⁻⁶ Such low thickness reduces the processing cost and makes the devices light-weight. Light-weight devices reduce the transportation and installation costs. GaAs is another material

used in thin film solar cells. Efficiencies as high as 27.5% have been obtained for these solar cells, but the cost of making the GaAs solar cells is higher than other thin film materials due to high processing costs.

1.2.3 Solution Processable Solar Cells

Even though thin film materials reduce the processing cost because of thinner absorber layers. Ultra-low vacuum and high temperature is still required for depositing those layers and leads to increased module costs. Solution processable solar cells eliminate this need for vacuum and in some cases don't even require temperatures above 200 °C.^{7,8} There are various kinds of solution processable solar cells including organic photovoltaics, dye sensitized solar cells, perovskites and nanocrystal solar cells.

In organic solar cells, the absorber material has a delocalized π electron cloud system that can absorb the sunlight and generate charge carriers. The advantage of organic solar cells is that they are flexible and light-weight but the materials used in organic solar cells have stability concerns and degrade in the air due to water and oxygen.^{9,10} Also, the materials used in organic photovoltaics suffer from photochemical stability.¹¹

Dye sensitized solar cells (DSSC) have similar advantages and disadvantages as an organic solar cell. At the heart of the device is a mesoporous oxide layer composed of a network of TiO₂ nanoparticles that are sintered together. A layer of charge-transfer dye is deposited over the surface of the mesoporous oxide layer. Photoexcitation of this layer results in injection of the electron into the conduction band of the oxide layer and this causes the oxidation of the dye. The dye is restored to the ground state by transfer of

electrons from electrolyte, usually an organic solvent containing the iodide/triiodide redox system.¹²

Perovskite solar cells have lately gained lot of popularity because of the high efficiencies achieved (up to 21%).¹³ Compared to traditional silicon solar cells, these devices have an upper hand because of the ease of fabrication, strong light absorption and low non-radiative recombination losses.¹⁴ Perovskites are the materials described by the formula ABX_3 , where X is an anion, and A and B are cations of different sizes. The photogenerated electrons and holes coexist in the perovskite material which are separated and travel to their respective electrodes.¹⁵ Just like OPVs and DSSCs, even perovskite suffer from stability concerns.¹⁶

Nanocrystal solar cells are devices where the absorber material is made up of nanocrystals. These devices have the benefits of solution processability, multi-excitation generation, band-gap tuning, flexibility, ambient temperature and pressure processing, and high absorption coefficient.^{17,18} Quite a few nanocrystals have been explored for use in solar cells including CdTe,¹⁹ PbS,^{20,21} PbSe,^{22,23} CuInS₂, CuInSe₂,^{24,25} Cu₂ZnSnS₄,^{26,27} and perovskites.^{28,29} In comparison with other solution processable solar cells, these devices have the added benefit of the materials being inherently stable.¹⁸ The challenge for these devices is the hindrance to charge transfer caused by the long chain organic ligand surrounding the inorganic nanocrystal core, but a great deal of research has been done in replacing these long chain insulating ligands with small chain ligands which lead to an increase in device efficiency.³⁰⁻³² Complete removal of ligand has also been exploited in techniques such as selenization, which is used in Cu(InGa)(S,Se) solar cells where the

nanocrystals are sintered at high temperature (≈ 550 °C) under selenium vapor. In this process, the ligands are removed and nanocrystals are sintered forming a polycrystalline film. Even though there is an improvement in efficiency, the high processing temperature defeats one of the processing advantages that nanocrystal solar cells could otherwise possess.³³

1.3 DEVICE PHYSICS

Solar cells typically involve two kinds of semiconductors – p type and n type. Holes are the majority carriers in a p-type semiconductor, which are mainly responsible for the charge flow and hence can be termed as free holes. Whereas, in an n-type semiconductor, electrons are the majority charge carriers and are responsible for charge flow. Hence, these electrons are termed as free electrons. These two kinds of semiconductors are placed next to each other in a solar cell leading to the creation of a depletion region at the junction. When brought together, the free electrons and holes at the junction drift towards each other and recombine leaving no free electrons and holes at the junction. This region is called depletion region. This leads to the creation of an electric field from n-type to the p-type which prevent further recombination of free electrons and holes. If either both or one of the semiconductors have the right bandgap to absorb the photons and create electron hole pairs in the depletion region, the in-built electric field drives the electrons and holes causing current to flow in the circuit. The current flowing in the circuit when no opposing voltage is applied is termed as short circuit current of the solar cell (I_{sc}). The opposing voltage that is required to nullify the photocurrent generated in the circuit is termed as open circuit

voltage (V_{oc}). These two points are shown in the figure 1.2c on the IV curve. IV characteristics of a solar cell is a plot between the device current for a voltage sweep. A solar cell is operated in the fourth quadrant to get a power output. The maximum power point shown in the IV curve is where the product of current and voltage is maximum. It is desirable to operate the solar cell at the maximum power point (P_{max}). At either side of the junction there are electrodes for collecting these electrons or holes. One of the electrodes must be a transparent conducting oxide (TCO) so light can reach the junction.

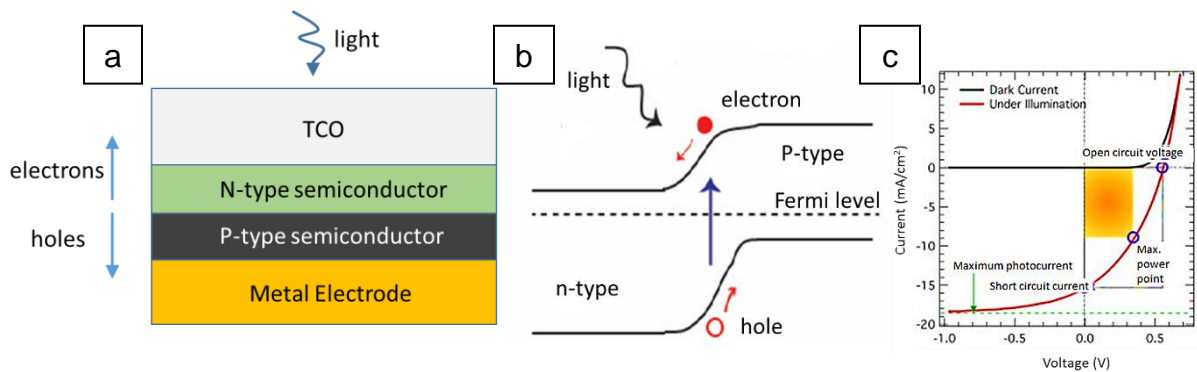


Figure 1.2: (a) Schematic shows different layers in a typical solar cell. (b) Band diagram of a p-n junction indicating the photo-generated carriers. (c) IV characteristics of a solar cell.

The power conversion efficiency (PCE) of a solar cell is defined as P_{max}/P_{in} . P_{in} is the intensity of light falling on the solar cell. Hence, PCE depends on the intensity of light being used. For terrestrial applications, PCE is usually measured under simulated Air Mass 1.5 Global (AM1.5) full-sun illumination with 1 sun intensity ($P_{in} = 100 \text{ mW/cm}^2$).³⁴ Hence with A.M. 1.5, the PCE is $(J_{sc} * V_{oc} * FF / 100 \text{ mW/cm}^2)$ where J_{sc} is the short circuit current density, V_{oc} is the open circuit voltage and FF is the fill factor. The other important efficiency measurement that can be done on solar cells is quantum efficiency. There are two kinds of

quantum efficiency measurements, external quantum efficiency (EQE) and internal quantum efficiency (IQE). EQE gives a measure of number of charge carriers extracted relative to the number of photons incident on the solar cell and IQE gives a measure of number of charge carriers extracted relative to the photons absorbed by the solar cell, at specific wavelengths. For a single junction solar cell that cannot extract hot electrons and holes, EQE and IQE values can be equal to 100% at certain wavelengths; whereas PCE is fundamentally limited to 34%.³⁵

1.4 FLEXIBLE SOLAR CELLS

Flexible solar cells help the transition from fossil fuels to clean energy. Light-weight, flexible solar will require lower maintenance and can be easily integrated into already existing buildings, textiles, and any kind of surface. Compact solar power generators are needed for portable electronics and emergency situations. Conformal PV coverage of vehicles is needed in the transportation industry. Printed electronics and the packaging industry has the need of an on-board power generation that matches the form factor of consumer product labels and reduces reliance on batteries with finite lifetimes. Flexible, light-weight, low-cost solar cells would meet these needs and open entirely new applications for solar cells, leading to truly a universal use of photovoltaics. Typically, the light absorbing material in flexible solar cells is printed on a roll to roll conducting substrate which makes the fabrication process very effective. There are a few different types of flexible solar cells developed till date, and amorphous silicon solar cells are the most successful flexible PVs developed so far.^{36,37} In comparison with crystalline silicon they have advantages of reduced processing temperature and material consumption.^{38,39} Even though, the deposition of a-Si is done at low temperatures, vacuum is still needed for deposition which adds to the cost of manufacturing. Cu(In,Ga)Se thin film solar cells are the other kind of flexible solar cells that could be produced by roll-to-roll manufacturing,

but these solar cells are fabricated at higher temperatures and ultra-low vacuums resulting in high production costs.⁴⁰

There has been a lot of hope in developing cheaper flexible solar cells after the emergence of organic and dye sensitized solar cells.^{12,41} The main hurdle with these types of flexible solar cells is the encapsulation needed due to the instability of the solar cells. These solar cells are stable only when encapsulated in tightly sealed glass and would therefore make them inflexible.⁴² CuInSe_2 nanocrystal solar cells are stable in the air and hence do not need tightly sealed glass packaging. The production costs are very low because of the ambient temperature and pressure fabrication process making CuInSe_2 nanocrystal solar cells the ideal choice for flexible PV applications. Shown in figure 1.3 are CuInSe_2 nanocrystal solar cells on paper. The portability and low weight make it possible for them to be used as a wearable power supply source.

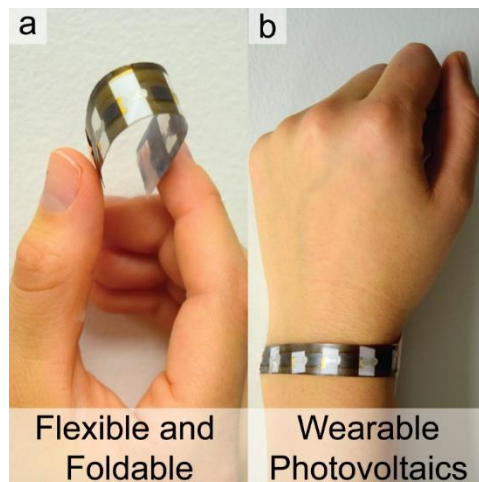


Figure 1.3: (a) Flexed CuInSe_2 nanocrystal solar cells on paper being bent. (b) CuInSe_2 nanocrystal solar cell on paper being used as a wearable.

1.5 CUINSE₂ NANOCRYSTAL SOLAR CELLS

The advantages of nanocrystal solar cells are discussed in section in 1.2.3. My research focuses on the development of CuInSe₂ nanocrystal solar cells. Compared to other materials, CuInSe₂ has benefits of longer stability and less toxicity. Bulk Cu(In,Ga)Se₂ devices have demonstrated efficiencies above 20%.⁴ Nanocrystal Cu(In,Ga)Se₂ solar cells processed at high temperature (550 °C) have demonstrated efficiencies above 7%.³³ The combination of lower toxicity and high efficiency exhibited by bulk and high temperature processed nanocrystal solar cells make Cu(In,Ga)Se₂ the ideal material to explore as nanocrystals. These nanocrystal solar cells could be fabricated on cheap substrates like paper and plastic thus enabling low cost fabrication. Such low-cost fabrication can enable the use of solar into new market areas where conventional solar panels cannot reach.

Figure 1.4 shows the different layers in a CuInSe₂ nanocrystal solar cell. The CuInSe₂ nanocrystal layer is deposited by techniques such as spray coating or spin coating. Typically, 4 to 8 solar cells having an area of 0.1 cm² are fabricated on a single substrate in order to have statistical data for a single set of experiments. The photograph of one such lab scale device on flexible substrate is provided in figure 1.4b.

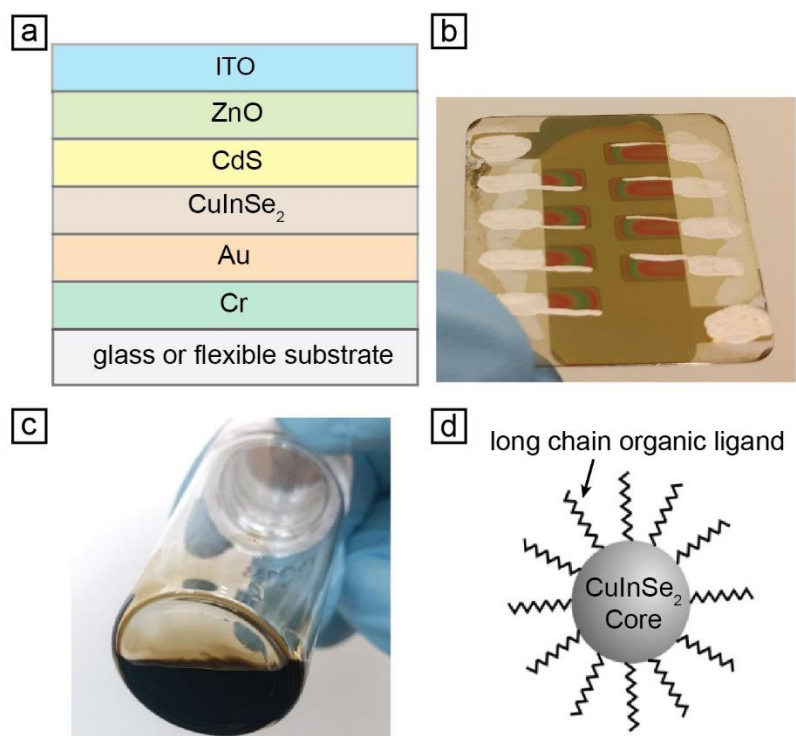


Figure 1.4: (a) Device layers in a CuInSe₂ solar cell. (b) Lab-scale CuInSe₂ nanocrystal solar cell showing 8 solar cells on Corning glass substrate. (c) Photograph of CuInSe₂ nanocrystal ink. (d) Illustration showing a CuInSe₂ nanocrystal core capped with long chain organic ligand.

Despite low-cost processing, CuInSe₂ nanocrystal solar cells are still at the development stage in the lab. This is primarily due to the low efficiency (3%) of CuInSe₂ nanocrystal solar cells and low product yield for CuInSe₂ nanocrystal synthesis.⁴³ However, their low-cost fabrication makes CuInSe₂ nanocrystal devices fit for commercialization at PCEs lower than that of the required PCE for other commercially available solar cells. Thus, efforts to bridge the gap between the PCEs of lab-scale prototypes and commercially available panels will enable cost-effective commercialization.

1.6 CONCLUSIONS AND DISSERTATION OVERVIEW

Herein, this gap is bridged by not only improving the device PCE, but also by further reducing the processing and material costs. To decrease nanocrystal device cost, studies have focused on finding suitable novel nanomaterials, increasing nanocrystal synthesis yields,⁴⁴⁻⁴⁶ scaling up nanocrystal synthesis,⁴⁷ developing device fabrication on inexpensive, flexible substrates,^{48,49} and engineering ligand exchanges.^{8,30-32,50,51} In this work, a few of these objectives have been applied to CuInSe₂ nanocrystal devices by improving reaction yield, device efficiency, and processing methods.

Chapter 2 discusses about a new method developed for synthesizing CuInSe₂ nanocrystals that results in an improvement in the product yield without compromising on device efficiency. Chapter 3 focuses on improvement in the solar cell efficiency by ligand engineering. Ammonium sulfide has been used to replace the long chain organic ligands with sulfide ions, thus improving the charge transfer between nanocrystals. In chapter 4, processing methods for fabricating all-solution processed CuInSe₂ nanocrystal solar cells have been explained. Along with the already solution processed CuInSe₂ nanocrystal absorber layer, the thermally evaporated gold layer, sputtered zinc oxide and sputtered indium tin oxide layers have been replaced by solution processed layers of gold nanocrystals, zinc oxide nanocrystals and silver nanowires respectively. Chapter 5 focuses on CuInSe₂ nanocrystal solar cells fabricated on paper. Very flexible solar cells have been fabricated on bacterial cellulose paper. Chapter 6 discusses the development of large area CuInSe₂ nanocrystal solar cells. This includes development of prototypes that deliver the

required amount of voltage and current depending on the application. Finally, chapter 7 provides the overall conclusions and future directions for this research.

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Chapter 2: Improved CuInSe₂ Nanocrystal Synthesis

2.1 INTRODUCTION

CuInSe₂ is a promising material for nanocrystal solar cells because it is well-characterized with low toxicity and can be solution processed in ambient conditions. There have been multiple different batch synthesis routes for preparing CuInSe₂ nanocrystals.¹⁻⁵ The highest reported efficiencies achieved in solar cells fabricated at low temperature (≤ 200 °C) employing these nanocrystals have been 3.1-3.2%, but we observed that these reactions have low yields.^{5,6} Improving the quantity of nanocrystals produced from one batch of nanocrystal synthesis reduces material costs. We define reaction yield as

$$\frac{\text{experimental mass of product obtained}}{\text{theoretical mass of product obtained if complete conversion achieved}} \times 100\%$$

We will discuss reaction yields of three different synthesis routes previously published in literature that we slightly modified and explored in solar cells.^{1,6,7} We termed the three reactions “One pot synthesis,” “DPP:Se injection synthesis,” and “TBP:Se injection synthesis” for ease of reference. As the names indicate, the DPP:Se and TBP:Se injection methods have DPP and TBP in addition to all the other chemicals present in a one pot synthesis. We identify that tributylphosphine (TBP) introduced during CuInSe₂ nanocrystal synthesis binds to the nanocrystal surface and improves the CuInSe₂ nanocrystal device performance. This finding aided our development of an optimized synthesis method for CuInSe₂ nanocrystals involving both diphenylphosphine (DPP) to increase the product yield and tributylphosphine to increase the device performance. We

termed this reaction as the “Hybrid reaction”, which is a combination of the DPP:Se and TBP:Se injection synthesis.

2.2 EXPERIMENTAL METHODS

2.2.1 Materials

Oleylamine (OLA, 70%); copper (I) chloride (CuCl; 99.99+%), selenium powder (Se; 99.99%), diphenylphosphine (DPP, 98%), thiourea (< 99.0%), cadmium sulfate (CdSO₄; 99.999%), copper, indium and selenium standard ICP solutions with concentrations of 1mg/ml, 70% wt. nitric acid were purchased from Aldrich Chemical Co.; indium (III) chloride (InCl₃; 99.999%) was purchased from Strem Chemicals; ammonium hydroxide (18M NH₃; ACS certified), toluene (99.99%), ethanol (absolute) were purchased from Fischer Scientific. Prior to use, oleylamine was degassed overnight under vacuum at 110 °C using a standard Schlenk line set up in a 3-neck round bottom flask and stored in a nitrogen filled glovebox. All other chemicals were used as received without further purification.

2.2.2 CuInSe₂ Quantum Dot Synthesis

2.2.2.1 One pot Method

CuInSe₂ nanocrystals from the one pot synthesis were prepared based on a previously published method.¹ CuCl (0.495 g, 5 mmol), InCl₃ (1.11 g, 5 mmol), Se (0.79 g, 10 mmol) and 50 ml of OLA are added to a 150 mL, 3-neck round bottom flask inside a nitrogen-filled glovebox and sealed with a rubber septa before bringing the flask out of the glovebox. This flask is attached to a schlenk line, vacuum is applied, the flask is heated to 110 °C and maintained at this temperature overnight. Then, the flask is filled with nitrogen and the temperature is ramped at 12 °C/min to 240 °C. The reaction mixture is maintained

at this temperature for 45 minutes, during which CuInSe₂ nanocrystals grow. The heating mantle is then removed and the reaction mixture is allowed to cool to room temperature under nitrogen.

The reaction mixture is transferred into glass centrifuge tubes and 20 mL of ethanol is added. This mixture is centrifuged at 4500 rpm for 10 minutes to isolate the nanocrystals from the reaction mixture. The dark green supernatant is discarded and toluene is added in steps of 1 mL to the separated nanocrystals, using a glass pipette, until they are redispersed. 20 mL of ethanol is then added to the redispersed nanocrystals and centrifuged at 4500 rpm for 10 more minutes. The supernatant is discarded and nanocrystals are redispersed in a minimal amount of toluene. Toluene is then evaporated from this dispersion using a rotovap and the dried nanocrystals are moved into a nitrogen filled glovebox, where they are dispersed in anhydrous toluene and stored at a concentration of 100 mg/mL until further use.

2.2.2.2 DPP:Se Injection Method

This synthesis is a modification of a previously published method.⁷ CuCl (0.198 g, 2 mmol), InCl₃ (0.442 g, 2 mmol) and 10 mL OLA are mixed in a 50 mL, 3-neck round bottom flask inside a nitrogen-filled glovebox and sealed with a rubber septa. The flask is moved out and vacuum is applied using a schlenk line. The mixture is heated to 110 °C and maintained at that temperature overnight. The flask is then purged with nitrogen gas and maintained at 110 °C for 10 minutes. The reaction temperature is then ramped to 180 °C and a solution of Se (0.316 g, 4 mmol) and DPP (1.5 mL), which is prepared in the

glovebox, is injected rapidly into the reaction mixture using a glass syringe. The reaction mixture is then heated to 240 °C for 45 minutes and allowed to cool to room temperature in a nitrogen atmosphere.

To separate the nanocrystals, the reaction mixture is transferred into glass centrifuge tubes and 20 mL of ethanol is added. The mixture is centrifuged at 4500 rpm for 10 minutes and the resulting supernatant is discarded. The separated nanocrystals are redispersed in minimal amount of toluene, which is added drop wise using a glass pipette. 20 mL of ethanol is added and the dispersion is again centrifuged at 4500 rpm for 10 minutes. The supernatant is again discarded and the nanocrystals are redispersed in a minimal amount of toluene. The dispersion is then transferred into a 20-mL glass vial and dried out using a rotovap. The dried-out nanocrystal vial is transferred into a nitrogen filled glovebox and anhydrous toluene is added to redisperse the nanocrystals. The dispersion is stored at a concentration of 100 mg/mL in the nitrogen-filled glovebox.

2.2.2.3 TBP:Se Injection Method

CuInSe₂ nanocrystals were synthesized as previously described.⁶ Briefly, 5 mmol of CuCl (0.495 g), 5 mmol of InCl₃ (1.11 g), 50 mL of degassed OLA and a magnetic stir bar are sealed in a 125-mL three-neck flask with rubber septa in a N₂ filled glovebox and attached to a conventional Schlenk line setup. Vacuum is pulled in the flask at 110 °C for 30 min, followed by N₂ bubbling at 110 °C for 10 minutes while stirring. A 1M solution of Se in TBP is separately prepared in the glovebox by dissolving 10 mmol of Se (0.79 g) in 10 ml TBP in a 20 mL vial under magnetic stirring. The resulting Se reactant solution is

drawn into a syringe and taken outside the glovebox in preparation for injection into the reaction flask on the Schlenk line. At this point, the temperature of the reaction flask is raised to 240 °C. When the temperature in the flask reaches 180 °C, the TBP:Se stock solution is injected into the flask. The reaction mixture is then maintained at 240 °C for 10 minutes. The heating mantle is removed from the flask to let the contents in the flask to cool to room temperature.

The contents of the reaction flask are transferred to a centrifuge tube and 20 ml of ethanol is added. The nanocrystal product is precipitated by centrifugation at 4500 rpm for 10 minutes. The supernatant is discarded. The nanocrystal product is redispersed in 5 mL of toluene and 6 ml of ethanol is added. This mixture is again centrifuged at 4500 rpm for 10 minutes to precipitate the nanocrystals. The supernatant is discarded, and the solid product is redispersed in toluene to a final concentration of 100 mg/ml in a nitrogen filled glovebox.

2.2.2.4 Hybrid Method

CuInSe₂ nanocrystals were synthesized using reported methods.⁸ In a N₂ filled glove box, 0.495 g CuCl, 1.11 g InCl₃, 1.5 ml DPP and 50 mL oleylamine are combined in a three neck flask, removed from the glovebox and attached to a standard Schlenk line. After degassing the reaction by pulling vacuum at 110 °C for 30 min, The flask is filled with N₂ and the temperature is maintained at 110°C for 10 min. The temperature is then raised to 180°C and 0.79 g of Se dissolved in 10 ml of TBP is rapidly injected into the reaction mixture. The temperature is then raised to 240 °C and held for 10 minutes.

Purification of the nanocrystals is conducted by centrifugation using toluene/ethanol as solvent and antisolvent. The nanocrystals are precipitated by centrifuging at 4500 rpm for 10 min after adding 20 ml of ethanol, then redispersed in 5 ml of toluene and 6 ml of ethanol is added. The mixture is then centrifuged again at 4500 rpm for 10 minutes. The final solution is prepared by redispersing the nanocrystals in anhydrous toluene in a nitrogen filled glove box to a concentration of 100 mg/ml.

2.2.3 Materials Characterization

Transmission electron microscopy (TEM) was performed using FEI Tecnai G2 Spirit BioTwin microscopy operated at 80 kV. TEM samples were prepared by drop casting nanocrystals dispersed in chloroform onto a 200 mesh copper grid with a carbon film (Electron Microscopy Sciences). Energy dispersive X-ray spectroscopy (EDS) was carried out using an Bruker Quantax 200 detector mounted on a Hitachi S-5500 STEM. X-ray diffraction (XRD) was performed using a Rigaku R-Axis Spider diffractometer using Cu K α ($\lambda = 1.54 \text{ \AA}$) radiation operated at 40 kV and 40 mA. Inductively Coupled Plasma-Optical Emission Spectrometers (ICP-OES) composition measurements were carried out on a Varian 710 ICP-OES instrument. Standard solutions for each of Cu, In and Se were prepared with their respective standards at concentrations of 0, 0.1, 0.5, 1, 5, and 10 mg/L. ICP-OES samples were prepared by digesting approximately 5 mg of CuInSe₂ nanocrystals into 1 ml of 70% nitric acid. The samples were then further diluted with 9 mL of DI water. 0.1 mL of each sample was taken and mixed with 9.9 mL of 3% nitric acid to obtain a final product concentration of ~5 mg/L. Each element's concentration in the solution is

estimated from the calibration curve that is created by fitting the emission intensity of the standard solutions to a linear trend line.

2.2.4 PV Device Fabrication and Testing

Polished glass slides of size 25 mm x 12.5 mm were obtained from Delta Technologies. These slides were cleaned by sonicating in a 1:1 ratio of acetone and isopropyl alcohol for 5 minutes, followed by sonication in DI water for 5 minutes. The glass slides are then air dried. A 5-nm chrome (99.999%, Lesker) layer followed by a 60-nm layer of gold (99.95%, Lesker) is deposited by thermal evaporation. The gold layer serves as the back-contact electrode for the solar cell. CuInSe₂ nanocrystal layer is deposited by spin coating as follows. 100 mg/mL of CuInSe₂ nanocrystal solution is filtered through a 0.45 μm pore size filter and then diluted to 50 mg/mL by adding toluene. This dispersion is drop casted onto the gold coated glass substrate, which is spun at 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds to deposit a 200 nm thick CuInSe₂ nanocrystal layer. CdS was deposited by chemical surface deposition method. 0.7 mL of a CdS precursor solution (1.25 mL of 15 mM CdSO₄, 2.2 mL of 1.5 M thiourea, and 2.8 mL of 18 M NH₄OH in water) was drop casted onto a pre-heated CuInSe₂ and gold coated glass substrate maintained at 90 °C using a hot plate and covered with an inverted crystallization dish. The CdS precursor solution is allowed to react for 2 minutes, after which the excess solution is rinsed off with DI water. The substrates are then dried by blowing compressed nitrogen. The next two layers i-ZnO and ITO were deposited by Radio Frequency (RF) sputtering using ZnO (99.9%) and ITO (99.99 % In₂O₃:SnO₂ 9:1) targets

in Argon atmosphere. ZnO and ITO are deposited selectively onto 4 rectangular regions of area ranging from 0.08 cm² to 0.13 cm² using shadow masking. Silver paint is applied to the top and bottom contacts for PV testing. For baking the devices, they are placed in a rapid thermal processing (RTP) furnace and ramped at 1.5 °C/minute to reach the required temperature under air/argon/vacuum.

The current-voltage measurements were performed under an A.M1.5 solar simulation, using a Keithley 2400 general purpose source meter. The light source is calibrated using a NIST- calibrated Si photodiode (Hamamatsu, S1787-08).

2.3 RESULTS AND DISCUSSION

Figure 2.1 shows the reaction yields and device characteristics for the CuInSe₂ nanocrystals synthesized from the four different synthesis routes mentioned above. All the synthesis routes produce a product of nanocrystals with ligands attached to their surface. We are interested in the yield of nanocrystals without their ligands. Hence, we subtracted the weight of the ligands from the weight of the final product when we calculated reaction yield. TGA was used to determine the weight of ligands that needed to be subtracted.

The DPP:Se injection reaction yields the maximum product, with a reaction yield of 87 %, while the TBP:Se injection method yields the least amount, with a reaction yield of only 4%. This role of DPP, a secondary phosphine, at improving reaction yield has been observed before, in II-VI and IV-VI nanocrystal syntheses.⁹ However, the nanocrystals synthesized from the DPP:Se injection method are not the ideal choice for solar cells because the average PCE of the devices is two-fold lower for nanocrystals made from

DPP:Se than from the TBP:Se injection method (figure 1). In order to obtain the advantages of high yield from the DPP:Se injection method and high PCE from TBP:Se injection method, we developed the Hybrid synthesis method, which involves both DPP and TBP in the reaction mixture.

We are interested in comparing the three reaction method device efficiencies. In literature, heating devices after fabrication at 200 °C in vacuum for 10 minutes led to an increase in PCE for devices made from particles synthesized by TBP:Se injection method.⁶ We observed that the devices fabricated utilizing nanocrystals from the one pot and DPP:Se injection method were not able to withstand the temperature of 200 °C, and hence, heating the devices resulted in shorting. Also, for fabricating CuInSe₂ nanocrystal devices on inexpensive substrates like plastics, processing temperatures must be lower than 100 °C. Hence, we were interested in comparing device efficiencies before baking. We studied ligand content, crystal structure, nanocrystal size, nanocrystal stoichiometry, and type of ligand in each method to account for their devices' differences in PCE.

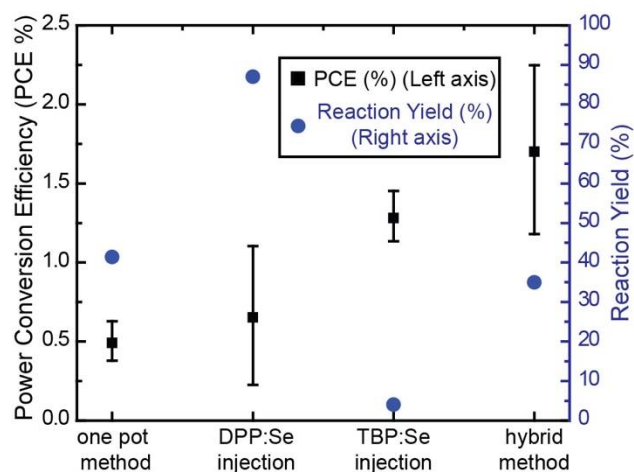


Figure 2.1: Plot showing PCEs (%) and reaction yields (%) for different nanocrystal synthesis routes. PCE values (■) are plotted with the scale on the left axis and the reaction yields (●) with the scale on the right axis.

Thermogravimetric analysis (TGA) was used to determine the amount of organic capping ligand in the synthesized nanocrystals. TGA data in figure 2.2 shows no direct correlation between the amount of organic ligand content and PCE because low organic content did not yield a high PCE. Hence, ligand content is not the main determining factor in device performance. Nanocrystals from the one pot method have the least amount of organic content, but not the highest PCE. Similarly, nanocrystal size did not show any direct correlation and nanocrystal structure was the same, chalcopyrite, for each synthesis route. The data supporting nanocrystal size and structure is available in the figure 2.3.

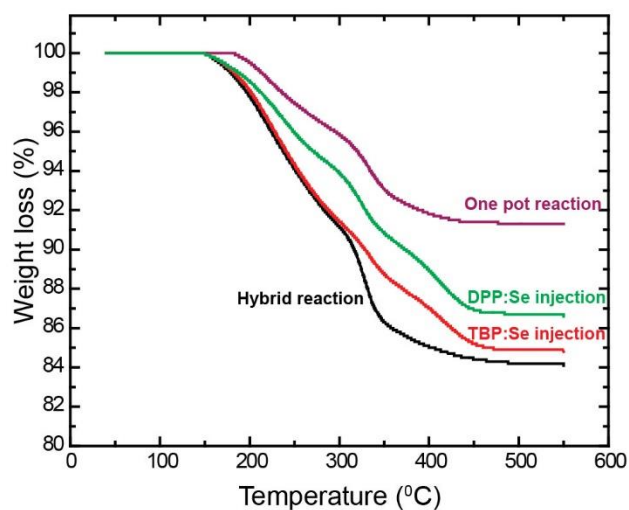


Figure 2.2: Thermogravimetric analysis performed on CuInSe_2 nanocrystals synthesized by various methods

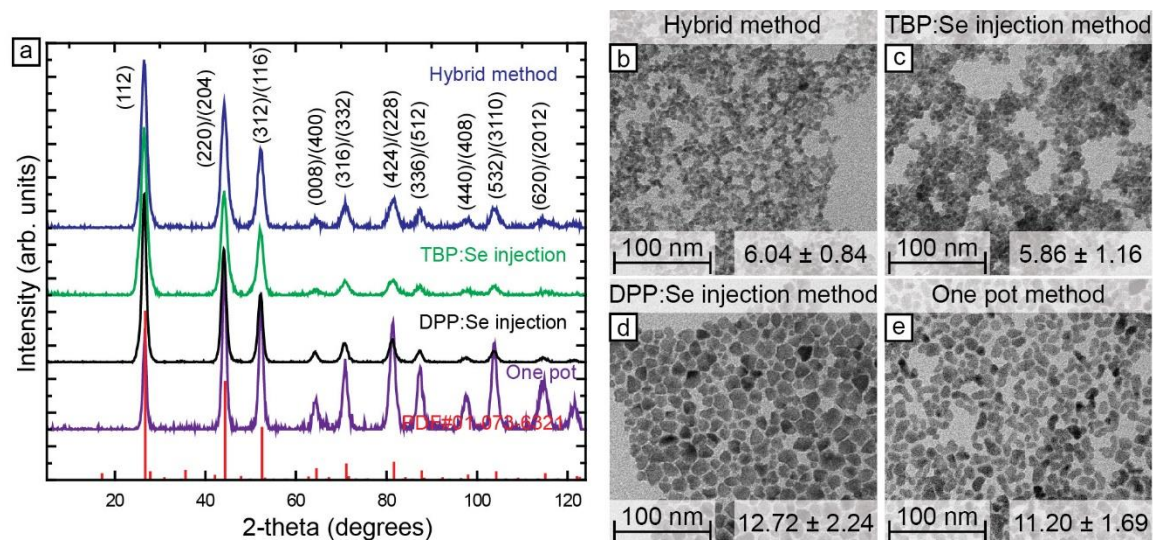


Figure 2.3: (a) XRD and (b-e) TEM of CuInSe_2 nanocrystals synthesized from four different synthesis. The size of nanocrystals with standard deviation is shown on each TEM image and we did not observe any direct correlation between size of nanocrystals and PCE (plotted in figure 2.1)

The two main factors that affect device performance are stoichiometry of the nanocrystals and the type of ligands attached to the nanocrystal surface. Table 2.1 shows

the elemental analysis obtained from inductively coupled plasma atomic emission spectroscopy (ICP-AES), normalized to a selenium content of 2. EDX data (table 2.2) showed similar trend in the stoichiometry. Lower copper content in the nanocrystals improved the device performance (ref. figure 2.1). A copper content lower than the stoichiometric amounts has been proven essential for the better performance of bulk copper indium gallium selenide (CIGS) devices.^{10,11} We observe a similar trend with the CuInSe₂ nanocrystal solar cells.

Synthesis	Cu	In	Se
One pot	0.96	1.04	2
DPP:Se injection	0.90	1.04	2
TBP:Se injection	0.81	1.03	2
Hybrid	0.76	1.04	2

Table 2.1: Elemental analysis of CuInSe₂ nanocrystals synthesized by different methods obtained from ICP-AES

element	Cu	In	Se
One pot	0.94 ± 0.05	1.09 ± 0.13	2.00 ± 0.13
DPP:Se injection	0.87 ± 0.05	1.08 ± 0.12	2.00 ± 0.12
TBP:Se injection	0.84 ± 0.04	1.06 ± 0.11	2.00 ± 0.12
Hybrid	0.80 ± 0.05	1.16 ± 0.14	2.00 ± 0.13

Table 2.2: Elemental analysis obtained from EDX analysis for CuInSe₂ nanocrystals prepared from various synthesis methods.

The other important factor that seems to affect device performance is the type of ligand attached to the nanocrystals. There are three ligands involved in the four reactions discussed: oleylamine, TBP, and DPP. In order to know if these molecules are acting as a capping ligand for the nanocrystals, we used proton nuclear magnetic resonance (¹H

NMR). NMR is a useful tool to distinguish a bound ligand from an unbound ligand.¹² The restricted rotation of the molecules attached to the nanocrystals causes the peaks for bound ligands to be much broader than the peaks of free molecules.¹³ Thus, broader peaks on the NMR spectra correspond to the hydrogens of bound ligands, and some of the peaks are so broad that they are unidentifiable from the rest of the spectrum. Of the hydrogens attached to the bound ligand, these broad peaks correspond to those closest to the nanocrystal surface. Another observation of the NMR spectra is a downfield shift of the broad peaks caused by an increase in the electropositivity of protons, which is induced by attachment of the ligand to the nanocrystal surface.

The effect of ligands on the electronic properties of nanocrystals is a well-known factor on PCE. Drastic improvements in device performance have been achieved by choosing the right kind of ligand.¹⁴⁻¹⁹ The NMR spectra of the nanocrystals synthesized by the four different synthesis methods is shown in figure 2.4a, and the signature matches with that of neat oleylamine. The non-overlapping peaks will be utilized to distinguish one ligand from the other in systems employing multiple ligands. Figure 2.4c, shows the magnified spectra from 8 to 7.5 ppm, where the characteristic DPP peaks can be observed for the DPP:Se injection method and the Hybrid reaction method. The peaks are broadened and shifted compared to 'free' DPP molecules (figure 2.4c), indicating its attachment to the nanocrystal surface. Most peaks from oleylamine and TBP overlap, the most distinguishable peak corresponding to TBP is circled in figure 2.4d.

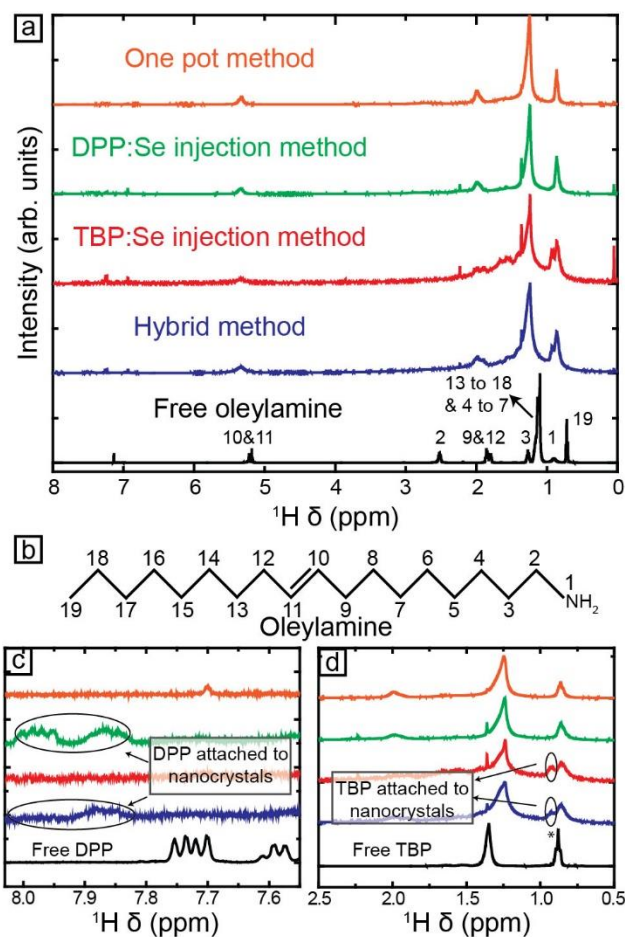


Figure 2.4: (a) ^1H NMR spectra of oleylamine and CuInSe_2 nanocrystals synthesized from various routes. (b) Oleylamine molecule with numbered atoms to indicate the corresponding proton peaks in (a). (c, d) Magnified spectra of (a) from 7.6 to 8 ppm with unbound DPP spectra shown in black and from 0.5 to 2.5 ppm with unbound TBP spectra in black, respectively. The peak marked with * in the TBP spectra is distinguishable from the oleylamine peaks.

Hence, ^1H NMR data proves that all the ligands used in each reaction are bounded to the nanocrystal surface. Based on the device PCE, we observe that the TBP ligand is crucial in increasing the device performance. Both the TBP:Se injection method particles and the hybrid method particles (containing TBP) show significantly higher PCEs in

devices than the other two reactions, in which TBP does not act as a ligand. The partial replacement of long chain oleylamine molecules with shorter chain TBP molecules allows for easier charge transfer between nanocrystals. Better charge transfer improves short circuit current, which improves PCE. Similarly, when DPP molecules partially replace the oleylamine molecules in the DPP:Se injection method, the PCE increase is less than the increase when TBP is acting as a ligand.

2.4 CONCLUSIONS

The Hybrid reaction combines the reaction yield benefits from the DPP:Se injection method and the PCE gains from the TBP:Se injection method. As shown in figure 1, the Hybrid reaction devices show a 160 % increase in average PCE from the DPP:Se injection particles, and a 32 % increase in PCE from the TBP:Se injection nanocrystals. Additionally, the reaction yield is increased by 9-fold compared to the TBP:Se injection reaction. Thus, we conclude that the hybrid reaction, developed herein, is the most promising method for CuInSe₂ nanocrystal solar cells. To further improve the PCE of devices, fabricated from CuInSe₂ nanocrystals that are synthesized from the Hybrid reaction, we baked the devices at 200 °C for 10 minutes in vacuum. The JV curve and EQE of the best performing device, fabricated from the Hybrid reaction nanocrystals, is shown in figure 4. We obtained an efficiency of 3.04 %, which is similar to record efficiencies obtained for low temperature processed (≤ 200 °C) nanocrystal CuInSe₂ devices previously published in literature, but with an added advantage of higher yields than other methods.⁶

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Chapter 3: Ligand Engineering of CuInSe₂ Nanocrystals for Improvement in Device Performance

3.1 INTRODUCTION

The common way to improve nanocrystal PV efficiency is ligand engineering: replacing longer chain ligands with smaller chain ligands or introducing ligands that better passivate nanocrystal surfaces.^{1,2} This technique led to huge improvements in device efficiencies in various nanocrystal systems. For example, first reports employing Pb(S,Se) nanocrystals as the absorber material achieved efficiencies as low as 3%.³ By choosing suitable ligands, the Pb(S,Se) nanocrystal solar cells were able to achieve efficiencies as high as 10.6%.⁴ The improvement in device efficiency with ligand exchange can be due to the increase in the short circuit current resulting from the replacement of insulating long chain organic ligands with less insulating shorter molecules or by an increase in open circuit voltage resulting from better passivation or by band engineering.^{2,5-10} Ligand engineering for CuInSe₂ nanocrystals is still in its infancy. To our knowledge, only two reports have been published for ligand exchanged CuInSe₂ nanocrystal solar cells.^{11,12} However, their ligand engineering did not increase their device efficiency above 3.1 (the reported maximum efficiency for CuInSe₂ nanocrystal solar cells fabricated under mild processing conditions).¹³ Here we develop a ligand exchange procedure for CuInSe₂ nanocrystals using ammonium sulfide to increase the efficiency beyond 3.1%. Ammonium sulfide was chosen as the chemical to facilitate the ligand exchange of long chain oleylamine ligands with shorter sulfide ions. Such use of ammonium sulfide to replace

insulating organic ligands with smaller ions in order to achieve performance gains has been exploited before in $\text{Cu}_2\text{ZnSnS}_4$ nanocrystals.¹⁴

3.2 EXPERIMENTAL DETAILS

3.2.1 Materials

Chemicals: Copper(I) chloride (CuCl , 99.99%), elemental Selenium (Se , 99.99%), ammonium sulfide (20% in water), cadmium sulfate (CdSO_4 , 99.99%), thiourea (99%), formamide (99.5%), oleylamine (OLA; >70%), tributylphosphine (TBP; 97%), diphenylphosphine (DPP; 98%), and anhydrous toluene (99.8%) were obtained from Aldrich; indium (III) chloride (InCl_3 , 99.99%) was obtained from Strem Chemical; toluene, ethanol, chloroform-d and ammonium hydroxide (18 M NH_4OH) were obtained from Fisher Scientific. The oleylamine stock solution is prepared by degassing it at 1100°C under vacuum overnight and stored in a nitrogen filled glovebox for future use. CuCl and InCl_3 are stored in the glovebox to prevent degradation. All other chemicals are used as received.

3.2.2 CuInSe_2 Nanocrystal Synthesis

CuInSe_2 nanocrystals have been prepared by methods previously described by Voggu et. al. In a typical synthesis 5 mmol of CuCl , 5 mmol of InCl_3 , 1.5 ml of DPP and 50 ml of OLA are heated in a 3-neck reaction flask at 110°C under vacuum, for 30 minutes, followed by heating at 110°C , for 10 minutes, under nitrogen atmosphere. The flask and its contents are then heated to 180°C where a 10 mmol of Se in 10 ml of TBP solution is injected rapidly into the reaction mixture. This mixture is then heated to 240°C for 10

minutes. The reaction mixture is then allowed to cool to room temperature while maintaining the nitrogen atmosphere. The nanocrystals were then separated by adding ethanol and centrifuging at 4500 rpm for 10 minutes. This washing process is repeated one more time. The nanocrystals are dispersed in anhydrous toluene and stored at a concentration of 100 mg/mL in a nitrogen filled glove box.

3.2.3 Solid-State Ligand Exchange Procedure with Ammonium Sulfide

A layer by layer solid state ligand exchange has been employed. A 20 mg/mL of CuInSe₂ nanocrystal dispersion is spin coated at 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds. This produces a 30-nm thick CuInSe₂ nanocrystal layer. This layer is then treated with a solution of ammonium sulfide (20 wt % in water) in methanol. 70 μ L of ammonium sulfide is added to 2 mL of methanol solution and mixed well. This solution is drop casted onto the CuInSe₂ nanocrystal film and is left to react for 15 seconds. The layer is then rinsed off with methanol. The nanocrystal layer is not soluble in toluene anymore, indicating the ligand exchange with ammonium sulfide. This makes it possible to spin coat another layer of CuInSe₂ nanocrystals on top of this layer. The process is repeated a few times to achieve the desired thickness. The number of layers of CuInSe₂ to be spin coated is optimized for maximum PCE, and we observe that four coats give the best performance. A final layer of CuInSe₂ nanocrystals is spin coated to fill in the pores that were caused by removal of the long chain oleylamine molecule.

3.2.4 Solution Phase Ligand Exchange Procedure with Ammonium Sulfide

A 5 mL solution of 10 mg/mL CuInSe₂ nanocrystals, that were prepared through the Hybrid synthesis, dispersed in toluene is mixed with a solution containing 0.5 mL of ammonium sulfide (20 wt % in H₂O) and 4.5 mL of formamide inside a nitrogen filled glovebox. This mixture is allowed to stir for 12 hours in the glovebox. The oleylamine capped CuInSe₂ nanocrystals dispersed in the non-polar solvent toluene are transferred into the polar formamide solvent. The two solvents are phase separated with the higher density formamide being the bottom phase. The nanocrystal-formamide dispersion is then separated and centrifuged at 6000 rpm for 3 minutes. After stirring overnight, fresh toluene is added and the mixture is vigorously shaken to remove the remaining oleylamine ligand. This process is repeated one more time and 10 mL of acetonitrile is added to the nanocrystal dispersion in formamide and centrifuged at 6000 rpm for 3 minutes. A yellow colored supernatant is separated, which indicates the presence of ammonium sulfide, and is discarded. The precipitate is redispersed in methanol and rotovaped. Dried nanocrystals are redispersed in methanol and stored at a concentration of 100 mg/mL. The polar and non-polar solvent system is chosen in such a way that they are immiscible with each other. Toluene and formamide are immiscible.

3.2.5 PV Device Fabrication and Testing

A 5-nm chrome (99.999%, Lesker) layer followed by a 60-nm layer of gold (99.95%, Lesker) are deposited by thermal evaporation onto a polished glass slide. In a non-ligand exchanged device, CuInSe₂ nanocrystal layer is deposited by spin coating as

follows. 100 mg/mL of CuInSe₂ nanocrystal solution is filtered through a 0.45 μm pore size filter and then diluted to 50 mg/mL by adding toluene. This dispersion is drop casted onto the gold coated glass substrate and spun at 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds to deposit a CuInSe₂ nanocrystal layer that is approximately 200 nm thick. For devices incorporating ligand exchanged CuInSe₂ nanocrystal layer the procedure is described in section 3.2.3. CdS was deposited by chemical surface deposition method.¹⁵ 0.7 mL of a CdS precursor solution which is a mixture of 1.25 mL of 15 mM CdSO₄, 2.2 mL of 1.5 M thiourea, and 2.8 mL of 18 M NH₄OH in water is taken in a 1 mL pipette and was drop casted onto a pre-heated CuInSe₂ and gold coated glass substrate maintained at 90 °C and covered with an inverted crystallization dish. The CdS precursor solution is left on the substrate for 2 minutes and a CdS layer is deposited. The excess CdS precursor solution is rinsed off with a jet of DI water. The substrates are then dried by blowing compressed nitrogen. The next two layers are i-ZnO and ITO. They were deposited by Radio Frequency (RF) sputtering using ZnO (99.9%) and ITO (99.99 % In₂O₃:SnO₂ 9:1) targets in Argon atmosphere. ZnO and ITO are deposited selectively onto 4 or 8 rectangular regions of area ranging from 0.08 cm² to 0.13 cm² using shadow masking. Silver paint is applied to the top ITO contact and the bottom gold contact for PV testing. Baking of the devices is done in a rapid thermal processing (RTP) furnace where the temperature is ramped at 1.5 °C/minute to reach the required temperature under air/argon/vacuum.

Keithley 2400 general purpose source meter was used to perform the current-voltage measurements under an A.M1.5 solar simulation. The light source is calibrated using a NIST- calibrated Si photodiode (Hamamatsu, S1787-08). External Quantum

Efficiency measurements were made using a monochromatic light generated from a commercial monochromator (Newport Cornerstone 260 1/4M), chopped at 213 Hz, and focused to a spot size of 1 mm in diameter on the active region of the device. The monochromatic light is calibrated using silicon (Hamamatsu) and germanium (Judson) photodiodes. For these measurements, the monochromatic light is swept across a wavelength from 320 nm to 1330 nm with a step size increment of 10 nm using a lock-in-amplifier (Stanford Research Systems, model SR830).

3.2.6 Characterization

Transmission electron microscopy (TEM) was performed on a FEI Tecnai G2 Spirit BioTwin microscopy operated at 80 kV. Scanning electron microscopy (SEM) was done on a Zeiss Supra 40 VP SEM operated at 5 keV accelerating voltage. X-ray diffraction (XRD) was performed on a Rigaku R-axis Spider diffractometer with an image plate detector and using graphite monochromatized Cu K α ($\lambda = 1.5418 \text{ \AA}$) radiation operated at 40 kV and 40 mA. For XRD measurements, the nanocrystals are dried onto a glass substrate and the nanocrystal powder is scraped off onto a 0.5 mm nylon loop, which is rotated at 1° per second for 10 minutes while X-rays are being shined. Energy Dispersive Spectroscopy (EDS) was done on a Hitachi S5500 SEM operated at 30 kV.

3.3 RESULTS AND DISCUSSION

Herein, we report a PCE of 3.5 % using a solid-state ligand exchange with ammonium sulfide on CuInSe₂ nanocrystals prepared by the Hybrid method that is described in chapter 2. Baking to remove the solvent remains from ligand exchange is very

essential in improvement of device performance as shown in figure 3.1. All these devices have been baked in an RTP furnace at 200 °C under vacuum.

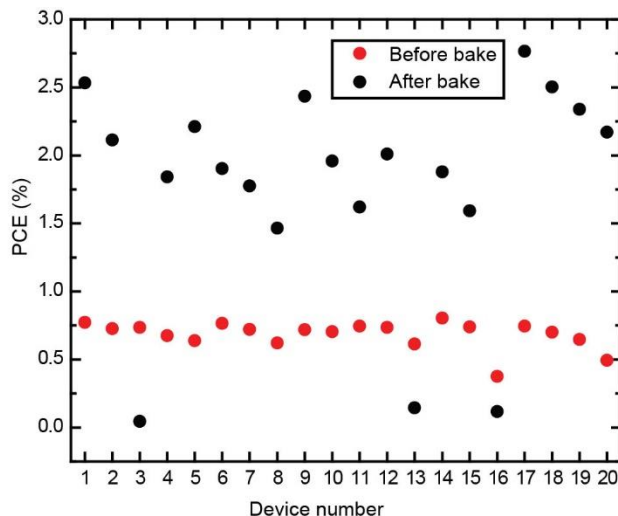


Figure 3.1: PCE for solar cells employing ligand exchanged CuInSe₂ nanocrystals before (●) and after (●) baking at 200 °C in vacuum.

3.3.1 Optimization of Solid State Ammonium Sulfide Ligand Exchange for Devices

The data supporting various optimization experiments is provided in figures 3.2 and 3.3. Figure 3.2 shows the device characteristics of ligand exchanged CuInSe₂ nanocrystal solar cells with top most CuInSe₂ nanocrystal coating to be untreated with ammonium sulfide versus it being treated with ammonium sulfide. There is an increase in average PCE from 0.6% to 2.44% by having an additional non-ligand exchanged CuInSe₂ nanocrystal coat. We attribute this increase in PCE, on using untreated CuInSe₂ nanocrystal top layer, is due to the filling up of void spaces caused by removal of long chain oleylamine molecules during ammonium sulfide treatment.

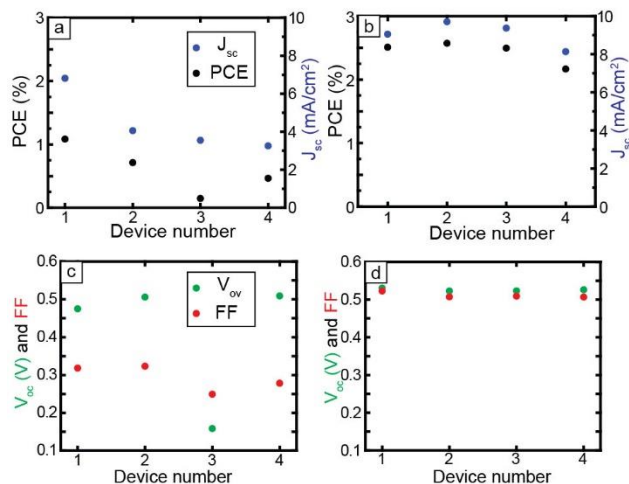


Figure 3.2: Device characteristics for four CuInSe₂ nanocrystal solar cells with the top-most CuInSe₂ nanocrystal layer (a and c) treated with ammonium sulfide and (b and d) untreated with ammonium sulfide.

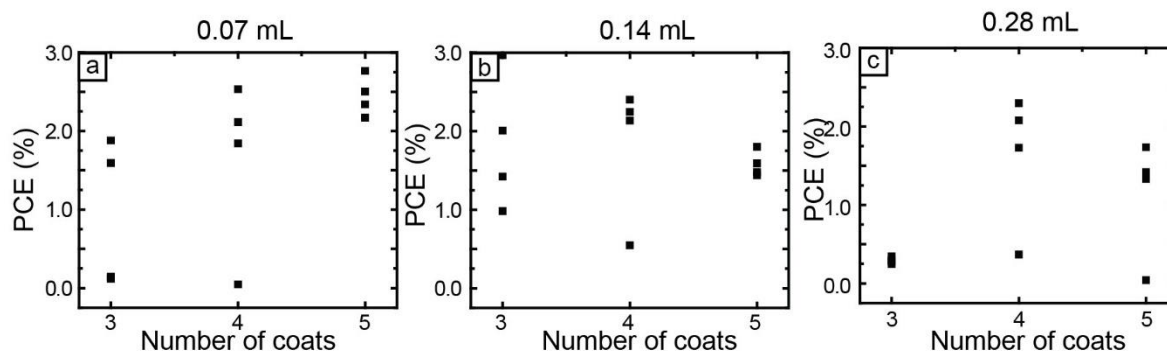


Figure 3.3: Power conversion efficiency as a function of CuInSe₂ nanocrystal coats at various concentrations of ammonium sulfide used for ligand exchange. Plots (a-c) show the PCE at 3, 4 and 5 coats of CuInSe₂ nanocrystal layer after treatment with (a) 70 μ L, (b) 140 μ L and (c) 280 μ L of ammonium sulfide in 2 mL of methanol solution. There is an additional CuInSe₂ coat on each of these devices that is not treated with ammonium sulfide.

The JV characteristics for 4 devices made with CuInSe₂ nanocrystals (Hybrid method) with and without ammonium sulfide treatment with these optimized fabrication

conditions are shown in figure 3.4. The average device performance for devices with $(\text{NH}_4)_2\text{S}$ treatment (2.68 %) is 11 % higher than the average PCE of devices fabricated without any $(\text{NH}_4)_2\text{S}$ treatment (2.41%). The JV characteristics of the best device and its external quantum efficiency (EQE) are shown in figure 3.5. The short circuit current calculated from the EQE equals 11.42 mA/cm^2 , which is within 20 % of the short circuit current that was measured from JV testing. We predict that the slight reduction in open circuit voltage (V_{oc}) from 0.52 V to 0.49 V is due to a decreased passivation of the nanocrystal surface after ammonium sulfide treatment.

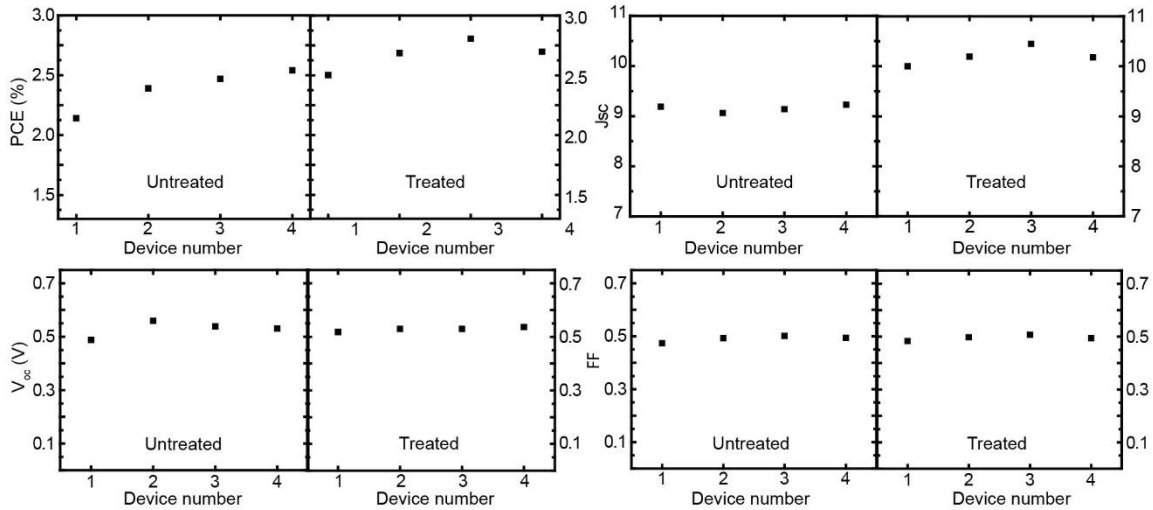


Figure 3.4: Device characteristics for CuInSe_2 nanocrystal solar cells with and without solid state ligand exchange with ammonium sulfide measured under AM1.5G illumination. The nanocrystals used are synthesized from the Hybrid method. An increase in J_{sc} , leading to an increase in PCE can be observed after the ammonium sulfide treatment, while V_{oc} and FF remain the same.

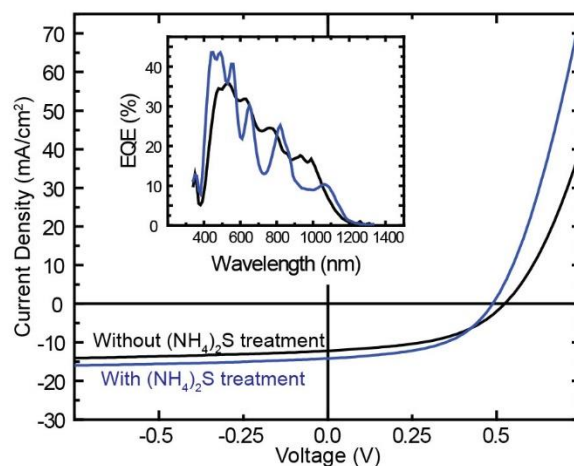


Figure 3.5: JV curves of best performing solar cells utilizing CuInSe₂ nanocrystals synthesized from Hybrid method with (blue) and without (black) ammonium sulfide treatment. The device with ammonium sulfide treatment has a PCE of 3.49%, short-circuit current density (J_{sc}) of 14.16 mA/cm², open-circuit voltage (V_{oc}) of 0.49 V and fill factor (FF) of 0.51. The device without ammonium sulfide treatment has a PCE of 3.04%, J_{sc} of 12.18 mA/cm², V_{oc} of 0.52 V and FF of 0.48 V. The JV characteristics are measured under A.M. 1.5G illumination. Inset shows EQE for these devices measured under a white light bias of 50 mW/cm². The short circuit current calculated from EQE for (NH₄)₂S treated device is 11.62 mA/cm² and for untreated device is 9.89 mA/cm².

3.3.2 NMR Spectroscopy Study on Addition of (NH₄)₂S to CuInSe₂ Nanocrystals

In this study, we observed that ammonium sulfide displaces the ligands oleylamine and TBP from the nanocrystal surface. This displacement reduces the barrier to charge transfer between nanocrystals and causes an increase in short circuit current, which directly increases the PCE as can be seen in figure 3.4. The removal of oleylamine and TBP ligands from nanocrystal surface can be observed in the NMR spectra shown in figure 3.6. When 6 μg of (NH₄)₂S aqueous solution is added in 100 mg of CuInSe₂ nanocrystals with d-chloroform as the solvent, we noticed strong TBP signal in the NMR proving that TBP has

been dislodged by $(\text{NH}_4)_2\text{S}$ as shown in figure 3.6b. When even more $(\text{NH}_4)_2\text{S}$ ($40 \mu\text{g}$) is added, OLA is also dislodged. As seen in the figure 3.6c, the characteristic peak corresponding to the hydrogen attached to the double bonded carbon, at 5.35 ppm , appears as a sharp peak. The peak corresponding to the hydrogen attached to the carbon next to the amine group, at 2.7 ppm , that is otherwise absent due to strong restricted motion, also appears as sharp peaks. When $40 \mu\text{g}$ of $(\text{NH}_4)_2\text{S}$ is added the nanocrystals precipitate out of solution and NMR is conducted on the supernatant (that contains dislodged ligands) which is collected by centrifugation.

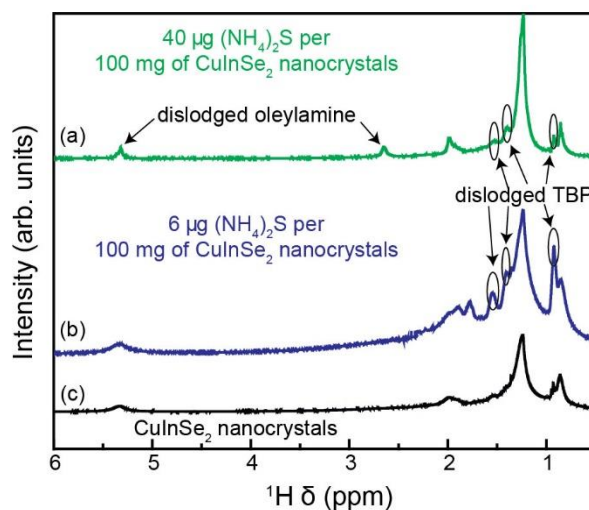


Figure 3.6: ^1H NMR of (a & b) CuInSe_2 nanocrystals dispersed in d -chloroform after addition of $40 \mu\text{g}$ and $6 \mu\text{g}$ of $(\text{NH}_4)_2\text{S}$ to 100 mg of nanocrystals, respectively and (c) ^1H NMR of CuInSe_2 nanocrystals. The nanocrystals are synthesized by Hybrid method.

3.3.3 FTIR and TGA on Ammonium Sulfide Treated CuInSe_2 Nanocrystals

In order to further confirm that ammonium sulfide removes the long chain oleylamine ligands, Fourier Transform Infrared Spectroscopy (FTIR) and TGA can be

performed on ammonium sulfide treated nanocrystals, but FTIR requires that samples be prepared on smooth, polished silicon substrates to maximize signal to noise ratio. However, the poor adherence of CuInSe₂ nanocrystals to the silicon wafer caused the CuInSe₂ nanocrystal layer to wash away during the methanol rinse step of the (NH₄)₂S treatment and hence we could not perform FTIR on solid state ligand exchanged nanocrystals. Hence, a solution-phase ligand exchange was employed in place of a solid-state ligand exchange. In order to readily extract the exchanged nanocrystals, we used two immiscible solvents, a polar solvent that dissolved the ammonium sulfide, and a non-polar solvent that dispersed the nanocrystals. The nanocrystals that underwent a ligand exchange transferred from the nonpolar to polar phase. Thus, we extracted the polar phase containing the ligand-exchanged CuInSe₂ nanocrystals. The nonpolar and polar solvent system that we studied was toluene and formamide, and the detailed procedure for this ligand exchange is discussed in section 3.2.3. TEM image of the nanocrystals before and after the ligand exchange is also provided in figure 3.7. It can be seen from these TEM images that the nanocrystals are more prone to agglomeration because of shorter ligands compared to oleylamine.

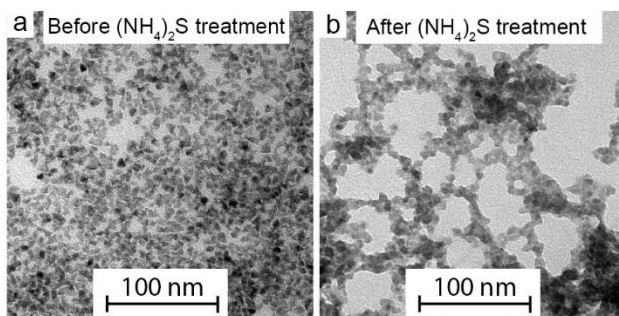


Figure 3.7: TEM images of CuInSe₂ nanocrystals (a) before and (b) after solution phase ligand exchange with ammonium sulfide.

Figure 3.8 shows the FTIR of CuInSe₂ nanocrystals before and after treatment with ammonium sulfide. The samples were prepared by drop casting 100 μ L of 100 mg/mL CuInSe₂ nanocrystal solution in a suitable solvent onto a double-sided, polished, un-doped silicon substrate. The bond stretches that correspond to oleylamine disappeared from the ammonium sulfide-treated sample, which indicated successful removal of oleylamine from the nanocrystals. The FTIR signature for the ligand-exchanged particles matches the FTIR signature of formamide, indicating the presence of residual formamide.

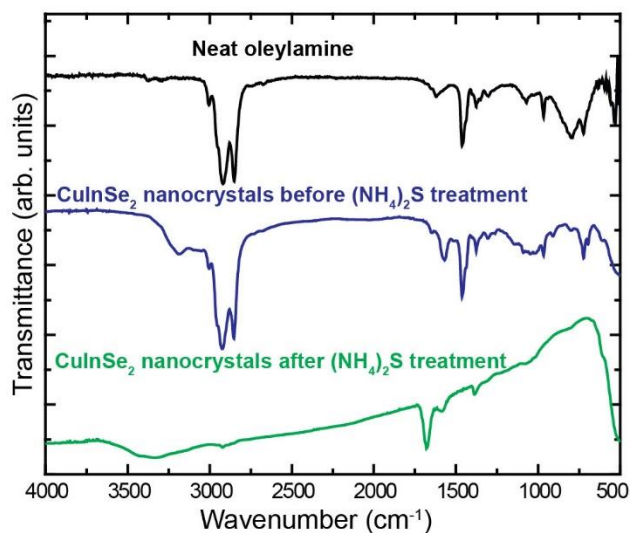


Figure 3.8: FTIR spectra of neat oleylamine, CuInSe₂ nanocrystals before and after treatment with ammonium sulfide.

The TGA results in figure 3.9 show that the sample had a low boiling point component, supporting the FTIR data that formamide remained in the nanocrystal dispersion. To confirm that the observed weight loss was formamide, TGA (figure 3.10) was conducted on pure formamide, and we observed a similar weight loss trend for both formamide and the ligand-exchanged CuInSe₂ nanocrystals. Thus, we conclude that formamide remained in the ammonium sulfide-exchanged particles.

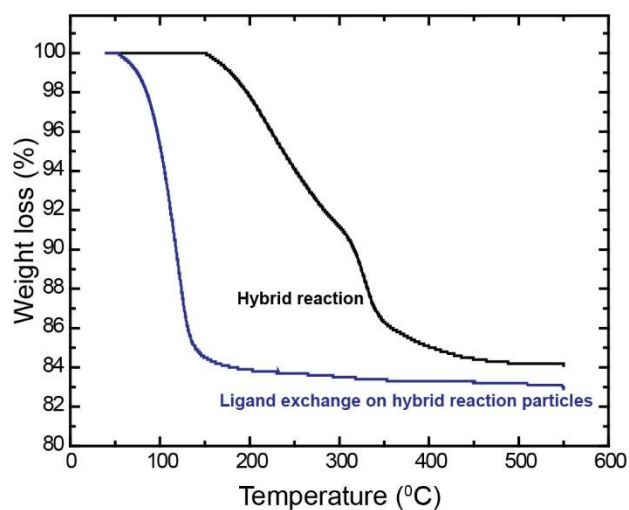


Figure 3.9: TGA of CuInSe₂ nanocrystals prepared from the Hybrid reaction before and after ligand exchange treatment with ammonium sulfide.

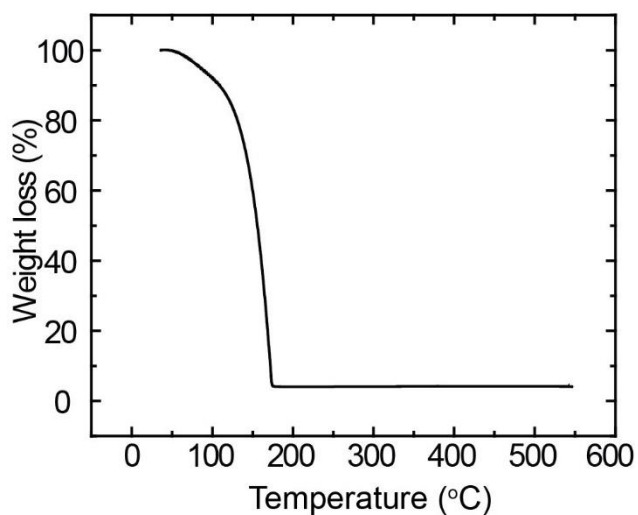


Figure 3.10: TGA of pure formamide

To conclude that the formamide remaining in the ligand-exchanged particles was residual and not acting as a capping ligand, a solution phase exchange was performed in the absence of ammonium sulfide. If formamide was the capping ligand, the nanocrystals

would have transferred to the polar formamide phase. However, no distinguishable phase transfer was observed. We were able to extract the nanocrystals by centrifugation instead, but they did not disperse in polar or nonpolar solvents, which indicated that neither formamide nor any other species was acting as a capping ligand. These results allow us to conclude that ammonium sulfide was indeed necessary for exchanging the native oleylamine ligands.

3.3.4 Elemental Analysis and X-ray Diffraction Studies on $(\text{NH}_4)_2\text{S}$ Treated CuInSe_2 Nanocrystals

For elemental analysis, EDX has been performed on ammonium sulfide treated nanocrystals. The data shown in table 3.1 is the average taken over three different spots on a sample. The nanocrystals were observed to be copper poor and indium rich. The EDX data has been normalized to indium to compare stoichiometric values for copper, selenium and sulfur. It can be observed that the copper and indium remain the same before and after treatment with ammonium sulfide but, a slight decrease in the amount of selenium is noted. We attribute this decrease to replacement of some of the selenium atoms with sulfur. XRD (figure 3.12) shows no peak shift due to incorporation of sulfur and hence we predict that sulfur is being adsorbed on the surface rather than being incorporated into the crystal lattice. These sulfide ions on the surface act as capping ligands and are responsible for nanocrystal dispersibility in polar solvents such as methanol and formamide. Such role of sulfide ions as the capping ligands by adsorption on the surface has been previously observed in CdSe nanocrystals.¹⁶

element	untreated nanocrystals	treated with (NH ₄) ₂ S in formamide & toluene
Cu	0.67 ± 0.04	0.65 ± 0.04
In	1.00 ± 0.11	1.00 ± 0.12
Se	1.69 ± 0.11	1.52 ± 0.10
S	0	0.24 ± 0.01

Table 3.1: Elemental analysis for CuInSe₂ nanocrystals before and after treatment with ammonium sulfide indicating the presence of sulfur after treatment.

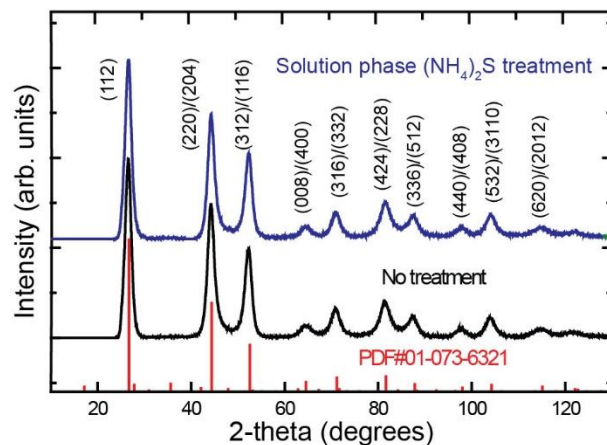


Figure 3.11: XRD pattern for CuInSe₂ nanocrystals synthesized by hybrid method before (black) and after treatment with (NH₄)₂S (blue). The red reference lines correspond to the chalcopyrite CuInSe₂ (PDF #01-073-3621).

3.4 CONCLUSIONS

We increased the PCE to 3.5%, the highest ever achieved for low temperature processed CuInSe₂ nanocrystal solar cells, by employing a ligand exchange with ammonium sulfide that replaces long chain oleylamine molecules with sulfide ions.

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Chapter 4: All-printed CuInSe₂ Nanocrystal Solar Cells

4.1 INTRODUCTION

The improvement that can be made to manufacturing solar cells, besides increasing yield and device efficiency, is reducing their processing costs. The processing costs are high due to high temperature and ultra-low vacuum that are traditionally required to deposit thin films. Instead, solution processing, or printing, of these thin films can be done at mild temperatures (<100 °C) and ambient pressure. Research on printed solar cells is concentrated in developing printed absorber layers while depositing all the other layers by traditional vacuum methods.¹⁻⁴ Considerable amount of research has also been done at identifying suitable solution processed alternatives to the traditional top transparent conductors deposited in ultra-low vacuum.⁵⁻⁸ Very few reports have been focused on fabrication of solar cells that have all solution processed layers.^{9,10} In all the devices discussed in other chapters, the CuInSe₂ nanocrystal absorber layer is the only printed layer. The gold back-contact, ZnO, and ITO layers are deposited in vacuum using thermal evaporation and sputtering, as per traditional methods of fabrication.

4.2 EXPERIMENTAL DETAILS

4.2.1 Materials

Copper(I) chloride (CuCl, 99.99%), elemental Selenium (Se, 99.99%), ammonium sulfide (20% in water), cadmium sulfate (CdSO₄, 99.99%), thiourea (99%), oleylamine (OLA; >70%), tributylphosphine (TBP; 97%), diphenylphosphine (DPP; 98%), zinc oxide nanocrystal dispersion in butyl acetate (40 wt.%) and anhydrous toluene (99.8%) were

obtained from Aldrich; indium (III) chloride (InCl_3 , 99.99%) was obtained from Strem Chemical; toluene, ethanol, ammonium hydroxide (18 M NH_4OH) were obtained from Fisher Scientific. Silver nanowires were obtained from Carestream Advanced Materials. The oleylamine stock solution is prepared by degassing it at 110°C under vacuum overnight and stored in a nitrogen filled glovebox for future use. CuCl and InCl_3 are stored in the glovebox to prevent degradation. All other chemicals are used as received.

4.2.2 CuInSe_2 Nanocrystal Synthesis

CuInSe_2 nanocrystals were synthesized by arrested precipitation following methods reported previously¹¹. Briefly, 5 mmol of CuCl , 5 mmol of InCl_3 and 1.5 ml of DPP are added to a three neck flask under an inert atmosphere. 10 mmol of Se powder is dissolved in 10 mL TBP. The flask is attached to a standard Schlenk line and degassed at 110°C for 30 minutes under vacuum. The flask is then purged with nitrogen and maintained at 110°C for 10 more minutes. The flask is then heated to 240°C . Once the flask reaches 180°C , the Se solution is injected and the flask is allowed to reach 240°C and held for 10 minutes. The heating mantle is then removed and the flask is allowed to cool to room temperature. The nanocrystals are precipitated by adding excess ethanol and centrifugation at 4500 rpm. The nanocrystals are washed by precipitation using toluene/ethanol solvent/antisolvent pair.

4.2.3 PV Device Fabrication and Testing

A 5 nm chrome layer followed by 60 nm of gold layer are deposited by thermal evaporation on polished glass substrates. CuInSe_2 nanocrystal layer has been spin coated from a 50

mg/mL solution. The spin coating is done at two speeds - 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds. A CdS layer (50 nm thick) was deposited on cured CuInSe₂ nanocrystal films by chemical surface deposition. 1.25 mL of 15 mM CdSO₄, 2.2 mL of 1.5 M thiourea, and 2.8 mL of 18 M NH₄OH in water are mixed and drop casted onto a heated (90°C) substrate and allowed to form a CdS layer for 2 minutes. The substrate is then rinsed off with DI water. ZnO (50 nm) and ITO (300 nm) was then deposited by rf-sputter coating at 2 mtorr. Physical shadow masks were used during window layer deposition, providing an active device area of 0.1 cm². For the devices described as I, II, III and IV one or two of Au, ZnO and ITO layers are deposited in a different way and are described in their respective sections.

Current-Voltage characteristics were measured using a Keithley 2400 general purpose source meter. The devices were illuminated using a Xenon lamp solar simulator (Newport) equipped with an AM1.5G optical filter and calibrated to 100 mW/cm² light intensity with a NIST-calibrated Si photodiode (Hamamatsu, S1787-08). Incident photon conversion efficiency (IPCE) was measured using a home-built device with lock-in amplifier (Stanford Research Systems, model SR830) and monochromator (Newport Cornerstone 260 1/4M), and calibrated with Si and Ge photodiodes (Hamamatsu).

4.2.4 Characterization

Scanning electron microscopy (SEM) was conducted using an In-lens detector and a 5 keV accelerating voltage on a Zeiss Supra VP SEM.

4.3 RESULTS AND DISCUSSION

Herein, we discuss a fabrication process for an all-printed CuInSe₂ solar cell that greatly reduces its processing costs by reducing processing time and utilizing mild processing conditions. The fabrication is as simple as printing a newspaper and eliminates the costs associated with ultra-low vacuum and high temperature. Traditionally, gold is thermally evaporated, ZnO and ITO are sputtered. Instead, we solution processed layers of Au nanocrystals, ZnO nanocrystals, and Ag nanowires. Each of these new solution-processed layers were studied independently by building four types of devices, shown in Table 4.1 below. Each device has one traditionally processed layer exchanged with a solution processed alternative, keeping the other layers traditionally processed. In summary, we explored the effect of each new solution-processed layer on device performance and analyzed how the solution-processing procedure affects processing time.

Device Number	Description
I	Au nanocrystal layer , sputtered ZnO, and sputtered ITO
II	thermally evaporated gold, ZnO nanocrystals , and sputtered ITO
III	thermally evaporated gold, sputtered ZnO, and Ag nanowires
IV	thermally evaporated gold, ZnO nanocrystals, and Ag nanowires

Table 4.1: Table showing the four device types investigated. All of the four device types included solution-processed CuInSe₂ nanocrystal and CdS buffer layers.

4.3.1 Study of Device I: Solution Processed Gold Back-contact

The first device type includes a gold nanocrystal layer instead of a thermally evaporated gold layer. Gold nanocrystals were prepared using the method described by Yu et al.¹² The gold nanocrystals, dispersed in toluene, were diluted to 100 mg/mL and spin coated onto a glass substrate at 600 rpm for 3 seconds, followed by 1000 rpm for 40 seconds. The glass substrate with the gold nanocrystal layer was heated to 200 °C using a hot plate, which sinters the gold nanocrystals. While these conditions yield the desired gold-thickness of 50 nm, complete coverage could not be obtained, and there were regions of exposed glass. In order to eliminate such regions, multiple layers of gold nanocrystals were deposited with heat treatment at 200° C in between each deposition.

The change in surface morphology and thickness after each spin coating and sintering step is shown in the SEM images in figure 4.1. The other layers in this device type were deposited as described in the photovoltaic device fabrication section 4.2.3. As shown in table 4.3, the highest device efficiency with the gold nanocrystal layer as the back contact is considerably lower than when gold is deposited by thermal evaporation. The JV characteristics of the device type with gold nanocrystals are displayed in figure 4.2.

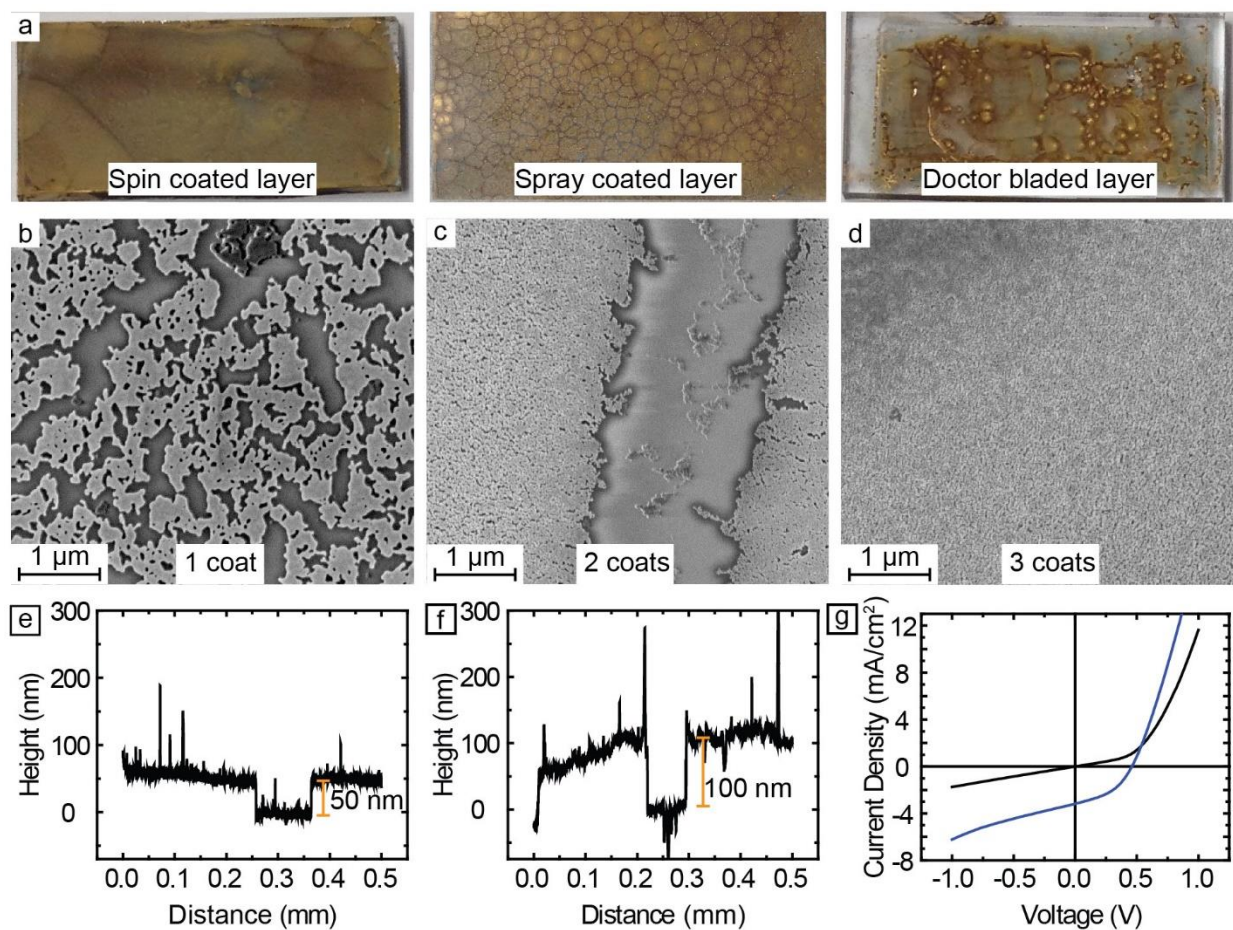


Figure 4.1: (a) Photographs of the sintered nanocrystal gold layer deposited by spin coating, spray coating and doctor blading. (b), (c) and (d) show SEM images of sintered gold nanocrystals after 1, 2 and 3 nanocrystal layer depositions by spin coating. Before each deposition, the gold nanocrystal layer is sintered. (e & f) Profilometry plots

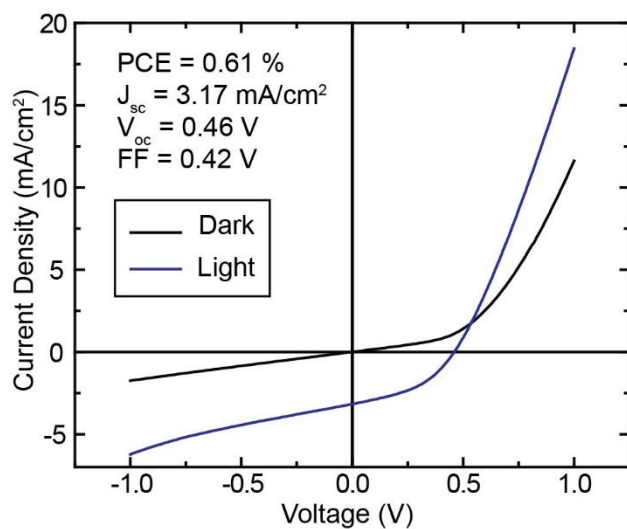


Figure 4.2: JV characteristics of the best performing device I.

4.3.2 Study of Device II: Solution Processed ZnO n-type Material

Similarly, devices were fabricated using a ZnO nanocrystal layer instead of sputtered ZnO as the n-type material. A ZnO nanocrystal dispersion in butyl acetate was purchased from Sigma Aldrich. 0.3 mL of this solution was diluted with 0.5 mL of ethyl alcohol and spin coated onto the CuInSe₂/CdS layer at 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds. A ZnO solution was spin coated again to form a more uniform film.

We observed that baking the devices in a rapid thermal processing (RTP) improves device performance. We tested two bake procedures, shown in Table 4.2 below.

bake procedure	description
A	bake after ZnO nanocrystal layer deposition
B	bake after ZnO nanocrystal layer deposition and bake after ITO sputtering

Table 4.2: Bake procedures for device II and device IV.

Bake A is crucial for the functionality of the device. Without any baking, the devices do not function (Figure 4.3a). Bake A evaporates the solvent from the ZnO nanocrystal layer, causing improved charge extraction and increased short circuit current. Performing bake A under inert atmosphere is more beneficial than baking in the air because baking in air causes oxidation of the device layers, which leads to changes in the layers' electronic properties. The JV characteristics of these devices are shown in figure 4.3a.

After ZnO baking and ITO sputtering, the device fabrication is complete. We found that light soaking the completed device improves the power conversion efficiency considerably. As shown in the figure 4.3c, there is an increase in J_{sc} and FF after light soaking the device for 10 minutes. A similar phenomenon was observed in bulk CIGS devices previously.^{13,14} The origin of this effect is presumably related to the defects in the CIGS layer and its interface, but further investigation is needed to fully understand the reason behind the improvement in device performance on light soaking

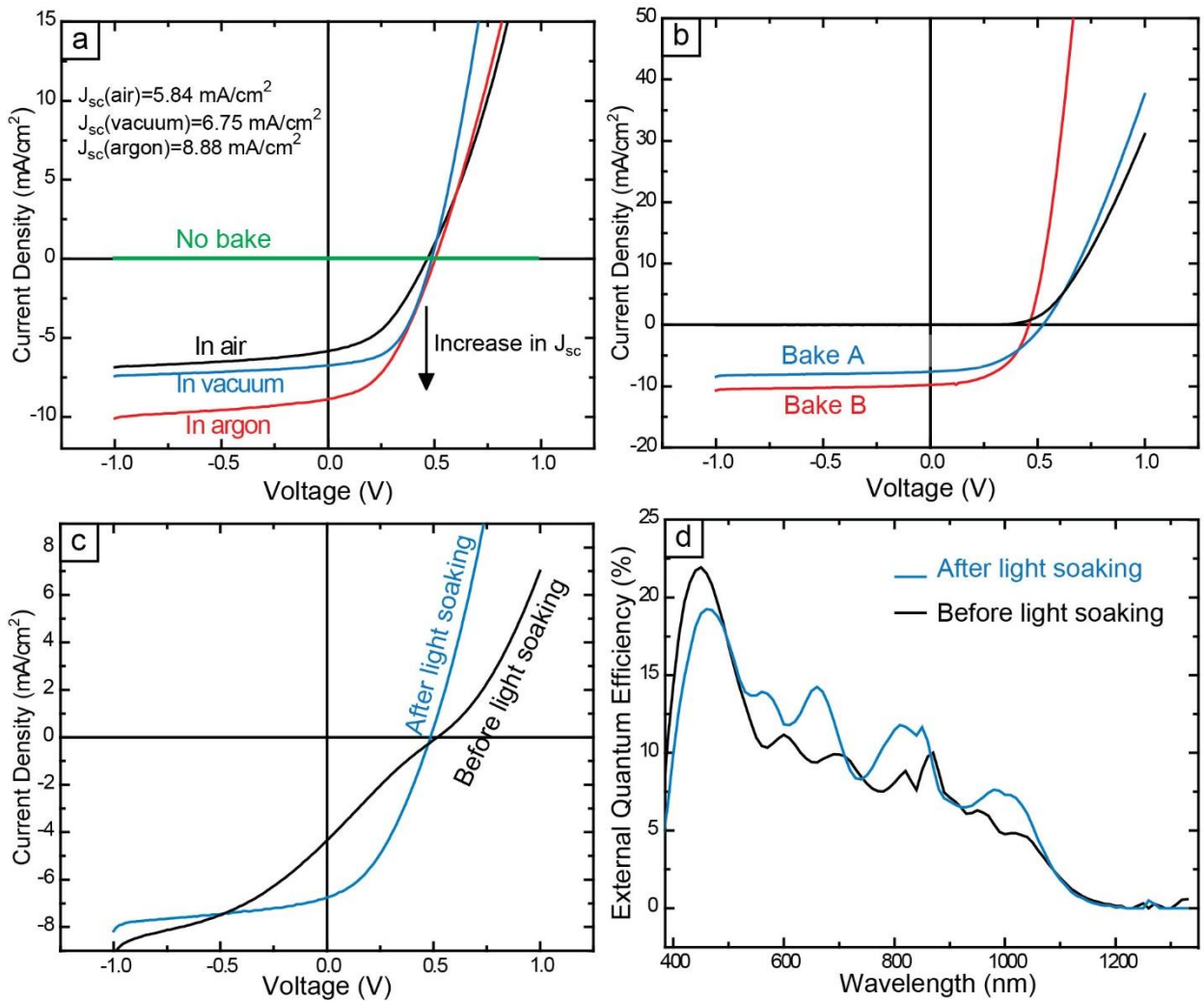


Figure 4.3: (a) JV light curves of device II with bake A at 200° C in air, vacuum, and argon. (b) JV curves of the best performing device II with bake A ($J_{sc}=7.63 \text{ mA/cm}^2$, $V_{oc}=0.52 \text{ V}$, $FF=0.47$, $PCE=1.89 \%$) and the best performing device with bake B ($J_{sc}=9.79 \text{ mA/cm}^2$, $V_{oc}=0.46 \text{ V}$, $FF=0.52 \text{ V}$, $PCE=2.34\%$) at 200° C in vacuum. (c) JV light curves of device II before ($J_{sc}=4.35 \text{ mA/cm}^2$, $V_{oc}=0.52 \text{ V}$, $FF=0.23$, $PCE=0.52 \%$) and after ($J_{sc}=6.76 \text{ mA/cm}^2$, $V_{oc}=0.49 \text{ V}$, $FF=0.38$, $PCE=1.26 \%$) light soaking for 10 minutes and (d) corresponding EQE measured for devices shown in (c) before and after light soaking.

4.3.3 Study of Device III: Solution Processed Silver Nanowire Top Contact

Silver nanowires were used as a solution processed, transparent top contact instead of a sputtered ITO layer. The silver nanowires used were purchased from Carestream Advanced Materials. The nanowire solution was drop-casted onto the sputtered ZnO layer and spin coated at 600 rpm for 3 seconds followed by 2000 rpm for 40 seconds. This spin coated process was repeated one more time to create an evenly coated silver nanowire layer. After the Ag nanowire deposition, the solar device was heated to 100° C for 10 minutes on a hotplate in air to complete the device III fabrication.

Next, each 1” x 0.5” device was separated into four separate devices by scribing the continuous silver nanowire layer with a needle in order to obtain statistical replicates. To ensure that the devices were separated, each device’s current was measured while only one device was placed under monochromatic light, focused to a spot size of 1 mm. If the devices were fully separated, no current was observed from the devices adjacent to the device under light.

4.3.4 Study of Device IV: Solution Processed ZnO Nanocrystal n-type Material and Silver Nanowire Top Contact

Lastly, we successfully incorporated both ZnO nanocrystals and Ag nanowires into a single device. The cross-sectional SEM of this device IV is shown in figure 4.4. Bake A was performed on the device, but they were never baked in vacuum; thus, vacuum was only employed for gold deposition. Additionally, the devices were separated and tested in a procedure similar to that described for device III.

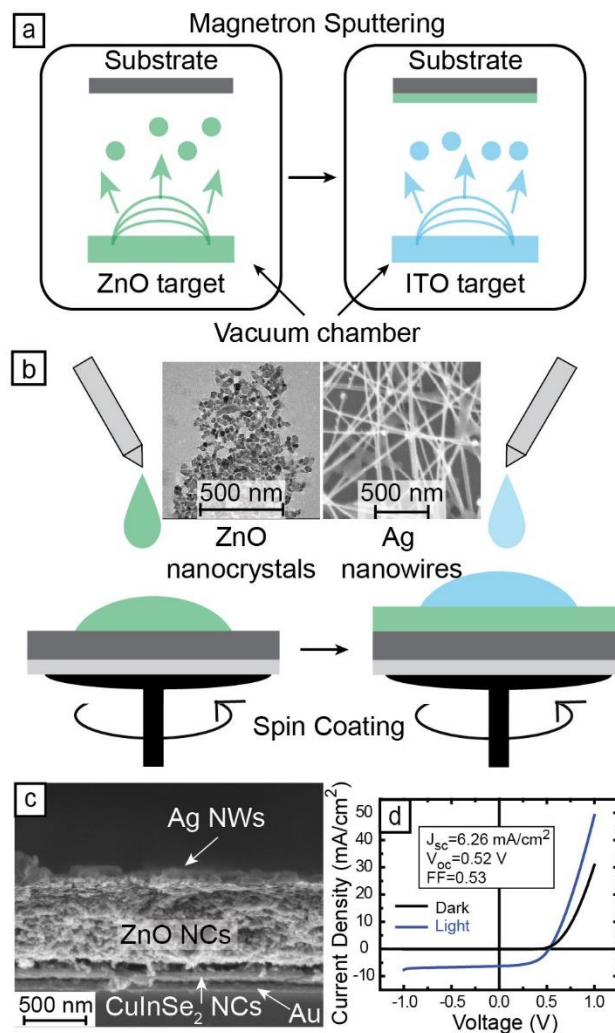


Figure 4.4: Illustrations showing the n-type material (ZnO) and transparent conductor (ITO/Ag NWs) deposition in CuInSe₂ nanocrystal solar cells using (a) vacuum based - magnetron sputtering method and (b) solution processed - spin coating method. (c) Cross-sectional SEM image of a CuInSe₂ nanocrystal solar cell with ZnO nanocrystals as n-type material and Ag nanowires as transparent electrode. (d) JV curve and device parameters of the solar cell shown in (c).

Device type	Best PCE (%)	Avg. PCE (%)	Avg. J_{sc} (mA/cm ²)	Avg. V_{oc} (V)	Avg. FF
I	0.61	0.60	3.08	0.46	0.43
II	1.89	1.45	8.25	0.49	0.42
III	1.94	1.62	6.03	0.50	0.54
IV	1.72	1.52	5.55	0.52	0.52
standard	2.04	1.93	6.95	0.48	0.58

Table 4.3: Device characteristics for the four types of CuInSe₂ nanocrystal solar cells without post fabrication vacuum bake. The values corresponding to ‘standard’ belong to a CuInSe₂ nanocrystal solar cell with thermally evaporated gold, spin coated CuInSe₂ layer, sputtered ZnO and sputtered ITO fabricated in the same batch, along with the 4 other devices, for comparison. The average is taken over 4 solar cells.

4.4 CONCLUSION

The highest PCE and the average device parameters of all four solar cell types discussed are displayed in Table 4.3. The device I performance is significantly lower than the others, and we conclude that the poor performance is due to cracks in the gold layer formed after sintering. We predict another cause of poor performance could be carbon residue remaining from the ligands of gold nanocrystals, which cause resistance to charge transfer. The gold-layer cracks are shown in figure 4.1a. In order to mitigate the gold cracking, we explored other deposition techniques such as spray coating and doctor blading, with their resulting films shown in figure 4.1a. Overall, spin coating produced the smoothest layers with the fewest cracks.

The other two solution processed layers, ZnO nanocrystals and Ag nanowires, did not have detrimental performance problems due to cracking. However, we found that the

bake A procedure is crucial to the functioning of device II. The highest PCE obtained for device II with bake A was 1.9%, and the efficiency was further improved after bake B. Thus, the highest efficiency for device II that is listed in Table 4.3 was measured after bake B. The JV curve for the best performing bake A device and the best performing bake B device are shown in figure 4.3b. Device III performance, also shown in Table 4.3, was remarkably similar to CuInSe₂ nanocrystal devices with vacuum-processed layers. Device IV includes both of the solution processed layers used in device II and III, and thus has comparable performance.

Finally, we compare the processing time required to fabricate a single device with one solution processed layer (or two, in the case of device IV) versus fabricating a single device with all vacuum processed layers. Each solution processed layer can be fabricated in under a minute, whereas the traditional, vacuum-based methods require 2-3 hours per layer. Thus, the overall processing time for devices I and III were reduced by 2-3 hours each because one layer was solution processed. However, device II's processing time was only reduced by 1-2 hours because the ZnO bake A requires an additional hour. Device IV required a ZnO bake as well, so its processing time was reduced by 3-5 hours instead of 4-6 hours due to its two solution processed layers.

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Chapter 5: Flexible CuInSe₂ Nanocrystal Solar Cells on Paper*

5.1 INTRODUCTION

As an innovation, paper has had profound global impact, enabling the written document a degree of adoptability beyond stone-based tablets.¹ The device described in this paper represents an analogous step forward in the field of photovoltaics. Whereas conventional solar panels have limits of use and adoptability because of their rigid and heavy design, flexible paper PVs have the potential to power a completely different class of devices that can be integrated into everyday life. As a commodity, paper and paper products are ubiquitous in the global economy, with nearly 400 million tons of paper is produced annually. That said, paper is not yet used as a substrate to build electronics on a commercial scale. Transistors,^{2–4} light emitting diodes,^{5,6} electrothermochromic displays,⁷ microfluidic devices,⁸ and touch pads⁹ have all been demonstrated on paper, but without a lightweight portable power supply. The use of paper as a substrate for these applications is countered by the need to use a stationary power supply or a heavy battery system. Paper solar cells could provide a useful power solution for these emerging devices.

Beyond portable electronics, paper solar cells offer advantages over PVs on glass or flexible substrates like metal foils and plastics, such as lower cost, consumer adoptability, eco-friendliness, biodegradability and ease of recycling.

* Large portions of this chapter were reproduced with permission from Voggu, V. R.; Sham, J.; Pfeffer, S.; Pate, J.; Phillip, L.; Harvey, T. B.; Brown, R. M.; Korgel, B. A. Flexible CuInSe₂ Nanocrystal Solar Cells on Paper. *ACS Energy Lett.* **2017**, *2*, 574–581. Copyright 2017 American Chemical Society. I am the first author of the paper. My contributions include fabricating solar cells; synthesizing nanocrystals; performing SEM, AFM, and device characterizations.

Extremely light paper PVs would allow for cheap and simple shipping and installation, unlike glass-mounted solar panels. Furthermore, paper-compatible roll-to-roll manufacturing processes could dramatically increase solar cell production rates, reducing the cost and time required for processing.¹⁰ From a manufacturing perspective, paper PVs—especially those made using nanocellulose produced using bacteria have the distinct advantage over conventional glass-mounted solar panels in that they can be cultivated and produced with little more than a simple wet laboratory set up, enabling for on-site fabrication.

To date, there have been only a few examples of paper solar cells. These have been made using organics (OPVs)^{10,11} and dye-sensitized solar cell materials (DSSCs).¹² The lack of air-stability for many OPV materials makes them less compatible with paper-based processing. And DSSCs that make use of a liquid electrolyte are particularly difficult to make compatible with paper substrates. Here, we show that CuInSe₂ nanocrystals spray coated on paper made from pure crystalline cellulose nanofibers (synthesized by the microorganism *Gluconacetobacter hansenii*) exhibit good thermal and air-stability, with power conversion efficiencies as high as 2.25% were made—the highest efficiency yet reported for solar cells fabricated directly on paper.^{13,14}

Furthermore, the paper PVs reported here show a high degree of mechanical flexibility without degradation. Devices retained their performance after more than 100 bending cycles to a radius as small as 5 mm. Past research on copper-indium-gallium-diselenide (CIGS) solar cells on flexible plastic and metal foil substrates has achieved

impressive efficiency results, similar to those obtained on glass; however, these devices have not been particularly flexible.¹⁵⁻¹⁸ The smallest bending radius reported for CIGS solar cells without considerable performance loss is only 2 cm.¹⁹ The nanocellulose paper PVs with a nanocrystal absorber layer of CuInSe₂ operated at much smaller bending radius (5 mm), even without replacing the typically brittle indium-tin-oxide (ITO) transparent conducting oxide (TCO) layer with alternative “flexible” transparent conducting electrode materials. The nanocrystal paper PVs also functioned when folded across the active region of the device.

5.2 EXPERIMENTAL DETAILS

5.2.1 Materials

Copper(I) chloride (CuCl, 99.99%), elemental selenium (Se, 99.99%), cadmium sulfate (CdSO₄, 99.99%), thiourea (99%), oleylamine (OLA; >70%), tributylphosphine (TBP; 97%), diphenylphosphine (DPP; 98%), carboxymethylcellulose and anhydrous toluene (99.8%) were obtained from Aldrich; indium (III) chloride (InCl₃, 99.99%) was obtained from Strem Chemical; toluene, ethanol, hexanes, and ammonium hydroxide (18 M NH₄OH) were obtained from Fisher Scientific. The cellulose membranes were produced in Schramm Hestrin (SH) medium (Schramm and Hestrin 1954) consisting of (per liter): 20.0 g of glucose (Fisher D16-10), 5.0 g of bacto peptone (BD 211820), 5.0 g bacto yeast extract (BD 212720), 2.7 g of sodium phosphate dibasic heptahydrate (Fisher 7782-85-6), and 1.5 g of citric acid (Mallinckrodt 0627-12). 200 mL of a stock solution of oleylamine was degassed under vacuum overnight at 110⁰C in a 250 mL glass 3 neck flask and stored in a nitrogen filled glovebox until further use. All other chemicals are used as received.

5.2.2 CuInSe₂ Quantum Dot Synthesis

CuInSe₂ nanocrystals were synthesized using a modification of published methods.^{20,21} Two reactant solutions were prepared in a nitrogen-filled glovebox: (1) an oleylamine reactant solution was prepared by combining CuCl (0.495 g, 5 mmol), InCl₃ (1.106 g, 5 mmol), and 1.5 mL DPP with 50 mL oleylamine in a 125 mL three-neck flask, and (2) a TBP:Se reactant solution prepared by dissolving elemental Se (0.79 g, 10 mmol) in 10 mL TBP. The oleylamine reactant flask was sealed with rubber septa, brought out of the glovebox and attached to a Schlenk line. The flask was heated to 110°C for 30 min under vacuum, then filled with nitrogen and the temperature is maintained at 110°C for 10 more minutes. Then the reaction flask is heated to 180°C while maintaining N₂ atmosphere. The Se precursor solution was loaded into a 12 mL plastic syringe, brought out of the glovebox and rapidly injected into the oleylamine-containing flask. The temperature was raised to 240°C and maintained for 10 min. The heating mantle was then removed and the reaction mixture was allowed to cool to room temperature.

The nanocrystals were isolated from the crude reaction mixture by precipitation with 20 mL of ethanol and centrifugation at 4500 rpm for 10 minutes. The yellow supernatant was discarded. Toluene is added to the nanocrystals using a pipette in steps of 1 mL, until all the nanocrystals redisperse (typically a total of 7 mL). 6 mL of ethanol is added to this dispersion to reprecipitate the nanocrystals. The dispersion was centrifuged at 4500 rpm for 10 min and the supernatant discarded. The precipitate was redispersed with a minimal amount of toluene (\approx 7 mL) for transfer to a 20 mL glass vial. The nanocrystals were dried, transferred into a N₂ filled glovebox and dispersed in anhydrous toluene at a concentration of 100 mg/mL for further use. The nanocrystals were diluted to a concentration of 10 mg/mL in toluene for spray deposition during PV device fabrication.

5.2.3 Synthesis and Preparation of Cellulose Substrates

Pure crystalline cellulose was produced using *Gluconacetobacter hansenii* ATCC 53582 strain NQ5.²² Liquid cultures of *G. hansenii* NQ5 were grown to log phase until reaching an optical density of 2 at 600 nm. 10 mL of the resulting inoculum was added to a culture tray containing 500 mL SH media²³ supplemented with 0%, 2%, or 4% carboxymethylcellulose (CMC). The trays were allowed to incubate under static conditions for 7 to 14 days at 28°C. The pellicles were harvested and the cells were removed using a 2% solution of Alconox (Sigma-Aldrich 242985) and stored in an aqueous solution of 20% ethanol until further use.

G. hansenii ATCC 53582 strain NQ5 is a rod shaped, aerobic, gram negative bacterium that synthesizes cellulose through linear pores along the long axis of the cell resulting in a cellulose membrane at the air liquid interface.²²⁻²⁴ Examination of the pellicle suggests two slightly different morphologies with respect to the top (surface at the air liquid interface) and bottom of the membrane (inside the liquid). The top appeared to be stronger and denser than the bottom and provided a smoother surface for solar cell application while the bottom would flake and peel after drying. It has not been extensively studied, but the discrepancy may be due to the synthesis of cellulose by the newly inoculated cells before increased cell division occurs. That initial cellulose appears to be less organized and weaker than cellulose produced after doubling time (1.5 to 8 hr.) due to the fewer number of cells in the medium.²⁵ During the division process, the cells continue synthesizing cellulose creating a branching point upon separation. Repetition of this produces a cascade of branchings that form the stronger matrix at the top of the membrane with respect to the bottom.^{22,25} To prepare the membrane as a PV substrate, the pellicle was placed with the nanocellulose surface at the air liquid interface faced down on a smooth Teflon sheet (16

cm x 16 cm). The pellicle was then pressed to eliminate bubbles. A cotton fiber fabric was then placed on top of the cellulose and further pressed to release air trapped between the materials. The Teflon sheet was then placed inside a 15-ton hydraulic press. The press was slowly lowered to remove as much water as possible, allowing for easier material handling. The plate and fabric were then removed from the press leaving behind the compressed cellulose, with the air liquid interface side face down on the Teflon sheet. A rectilinear stretching armature (made from surfaced pine wood) was placed on the microbial cellulose. Each side of the cellulose was gently lifted and a staple placed into the wood armature starting from the center, working outwardly toward the corners. After each staple placement, more pressure was applied before placing the next, allowing for even surface tension on the cellulose, and uniform pressure on the armature. The corners were evenly folded, in a manner similar to the conventional method of mounting linen onto canvas stretchers in preparation for an oil painting. After stretching, the cellulose was dried with a hair dryer on low heat for one hour to dry the surface. The substrate was left overnight at room temperature to dry completely.

5.2.4 PV Device Fabrication and Testing

Glass microscope slides cut into 1" x 1" dimensions were used as a support media for fabricating the solar cells on microbial cellulose substrates. The cellulose substrates were mounted on glass slides using either Kapton tape or PDMS as adhesives. PDMS was prepared by heating a siloxane monomer and a cross linking agent (Sylgard Elastomer 184, Dow Corning Corporation) in a 1:30 weight ratio on the glass slide at 150°C for 15 min. A 10 nm layer of chrome and 80 nm gold layer were thermally evaporated onto the cellulose layers. CuInSe₂ nanocrystals were spray deposited onto substrates heated to 100°C with a

Sonotek Exactacoat ultrasonic automated spray system equipped with a 120 kHz ultrasonic nozzle. The spray nozzle was rastered across a rectangular area with 3 mm raster spacing, a speed of 10 mm/sec, an ink injection rate of 0.1 mL/min, an air pressure of 1.6 psi and a nozzle-to-substrate separation of 11.5 cm. CdS was deposited by a chemical bath deposition procedure onto the CuInSe₂ nanocrystal layer.²⁶ 0.7 mL of CdS precursor solution (1.25 mL of 15 mM CdSO₄, 2.2 mL of 1.5 M thiourea, and 2.8 mL of 18 M NH₄OH in water) was dropped onto each 1"x1" CuInSe₂ deposited cellulose-glass substrate heated to 90°C on a hot plate and covered with an inverted crystallization dish for 2 min. The substrates were then rinsed with DI water and dried with compressed nitrogen. Top contact layers of i-ZnO and ITO were deposited by Radio Frequency (RF) sputtering using ZnO (99.9%) and ITO targets from Lesker (99.99% In₂O₃:SnO₂ 9:1) in Ar atmosphere. ZnO and ITO were deposited selectively onto 8 rectangular regions using shadow masking, with active device areas varying from 0.08 cm² to 0.15 cm². Silver paint was applied to the contacts for PV performance testing.

PV current-voltage characteristics were measured using a Keithley 2400 general purpose source meter and a xenon lamp source meter equipped with A.M. 1.5 filter. EQE measurements were made using a home-built spectrophotometer with lock-in amplifier (Stanford Research Systems, model SR830) and monochromator (Newport Cornerstone 260 1/4M), and calibrated with Si and Ge photodiodes (Hamamatsu). The monochromatic light used in the instrument is generated using a commercial monochromator (Newport Cornerstone 260 1/4M) chopped at 213 Hz and focused to a spot size of 1 mm diameter on the active region.²

5.2.5 Characterization

Scanning electron microscopy was done on a Zeiss Supra 40 VP SEM operated at 5 keV accelerating voltage. Atomic force microscopy was performed on Asylum MFP-3D AFM in tapping mode.

5.3 RESULTS AND DISCUSSION

5.3.1 The Role of Type of Paper Substrate on PV device Fabrication and Performance

Our first tests to make paper PVs used standard office paper as a substrate with the standard device stack of a bottom contact layer of gold, an ink spray-deposited layer of CuInSe₂ nanocrystals, a CdS buffer layer and a top contact of ZnO/ITO. None of these devices worked. It became immediately apparent that the type of paper used as the substrate is critical and that we would need to modify the paper to make a functioning solar cell.

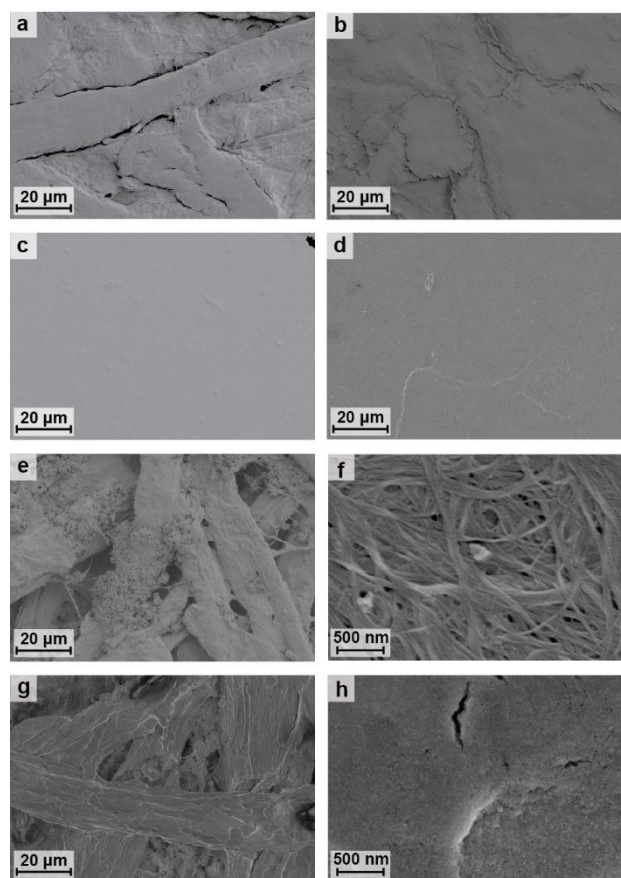


Figure 5.1: SEM images of different kinds of paper tested as PV substrates: (a) wax paper; (b) parafilm; (c) photo printing paper; (d) bacterial cellulose; (e) office paper; (f) bacterial cellulose. (g) and (h) show SEM images of (g) office paper and (h) bacterial cellulose paper coated with CuInSe₂ nanocrystals.

Figure 5.1 shows scanning electron microscope (SEM) images of the various types of paper that were tested. The cellulose fibers in standard office paper (Figure 5.1e) and wax paper (Figure 5.1a) are quite thick, generally more than 10 μm in diameter, resulting in very large pores in the substrate. The cellulose fibers in parafilm are not visible in the SEM images because of plastic fillers, but the surface of the paper is still very rough as a

result of the large cellulose fibers in the material (Figure 5.1b). None of the PVs fabricated on office paper, wax paper or parafilm worked. Photoprinting paper has a very smooth surface with fewer pores than office paper (Figure 5.1c). However, photoprinting paper also did not work because the device layers had very poor adhesion to the substrate. The deposited nanocrystal layer/Au/Cr back contact stack would rinse off the substrates during the CdS deposition step (see Figure 5.2). One other problem with all four of these types of paper was water uptake during the CdS deposition step. This led to wrinkling and made it impossible to fabricate PVs with reasonable device integrity (Figure 5.2 shows SEM images and photographs of these four types of paper after the CdS deposition step.) One way that others have avoided some of these problems has been to use only dry deposition processes, as in the case of organic solar cells.¹¹

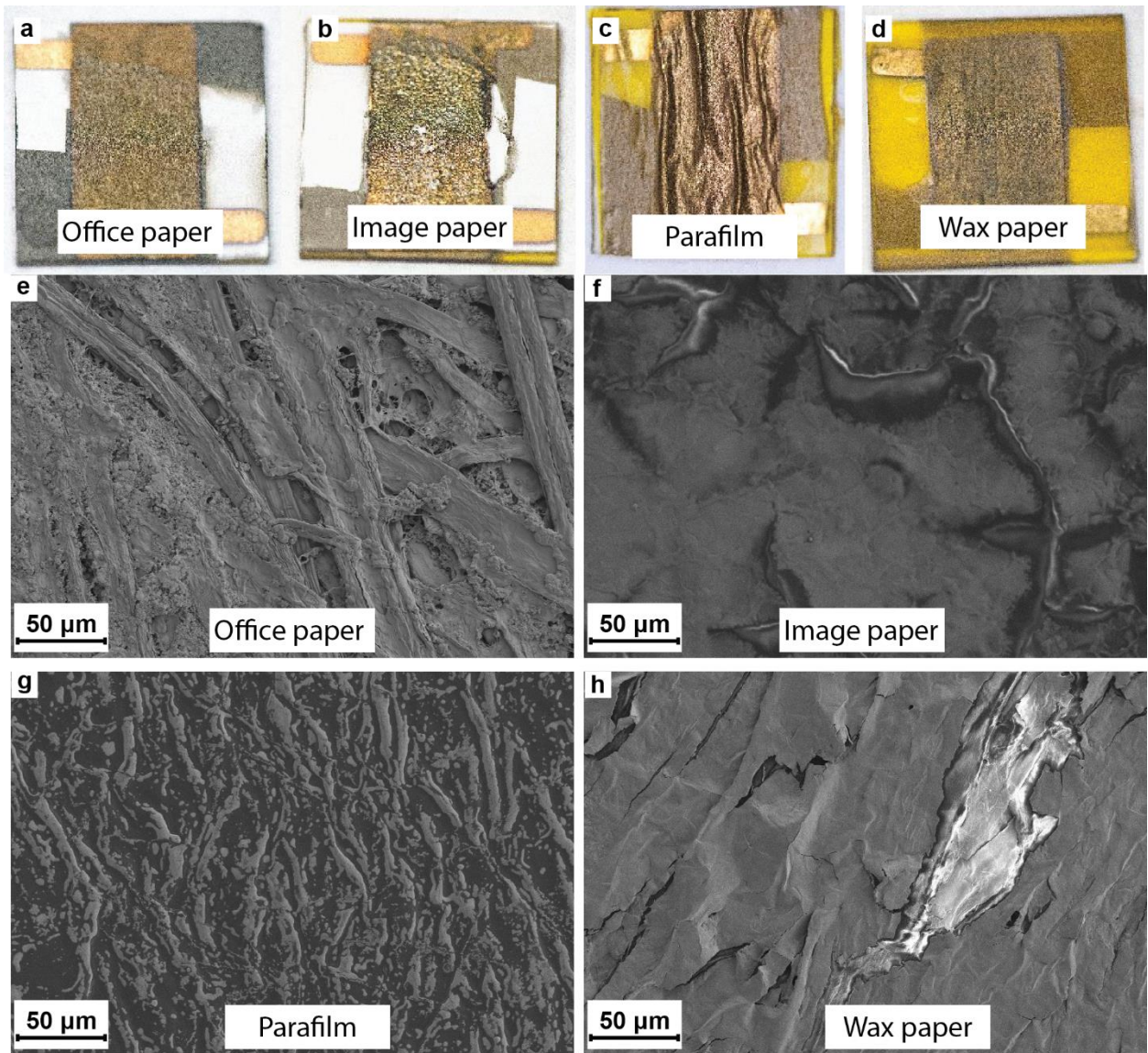


Figure 5.2: (a-d) Photographs of nanocrystal PVs after CdS deposition on different kinds of paper. (e-h) SEM images of different kinds of paper after CdS deposition.

Paper made of bacterial nanocellulose did not have the same process incompatibilities as the other paper substrates. Bacterial nanocellulose fibers are orders of magnitude smaller in diameter (~ 100 nm) than the cellulose fibers found naturally in plant material (>10 μm in diameter), and provide a much smoother paper surface with smaller pores.²⁷ Figure 5.1d shows an SEM image of a bacterial cellulose substrate. Figures 5.1g and 5.1h show SEM images of standard office paper and nanocellulose paper with 80 nm of gold and a layer of CuInSe_2 nanocrystals. The pores of the underlying office paper are still visible (Figure 5.1g), but there are no observable pores when bacterial cellulose paper is the substrate (Figure 5.1h). Pores lead to electrical shorts in the device and having a uniform, continuous coating of nanocrystals is critical for making paper PVs.

Nanocellulose paper PVs were made using the standard device stack of a bottom contact layer of chrome followed by gold, an ink spray-deposited layer of CuInSe_2 nanocrystals, a CdS buffer layer and a top contact of ZnO/ITO that have been optimized for devices on glass.²⁰ The performance of devices made with a range of nanocrystal layer thicknesses is shown in Figure 5.3a. AFM images of the nanocrystal-coated paper substrate and nanocrystal coated glass substrate are shown in Figures 5.3c and 5.3d. Figure 5.4 shows an SEM image of a cross-sectioned CuInSe_2 nanocrystal PV on a bacterial nanocellulose paper substrate. The device efficiency was the highest after three coats of nanocrystals had been applied (~ 150 nm layer thickness). A leveling off of device performance with increasing nanocrystal layer thickness is similar to what is observed on glass.²⁰

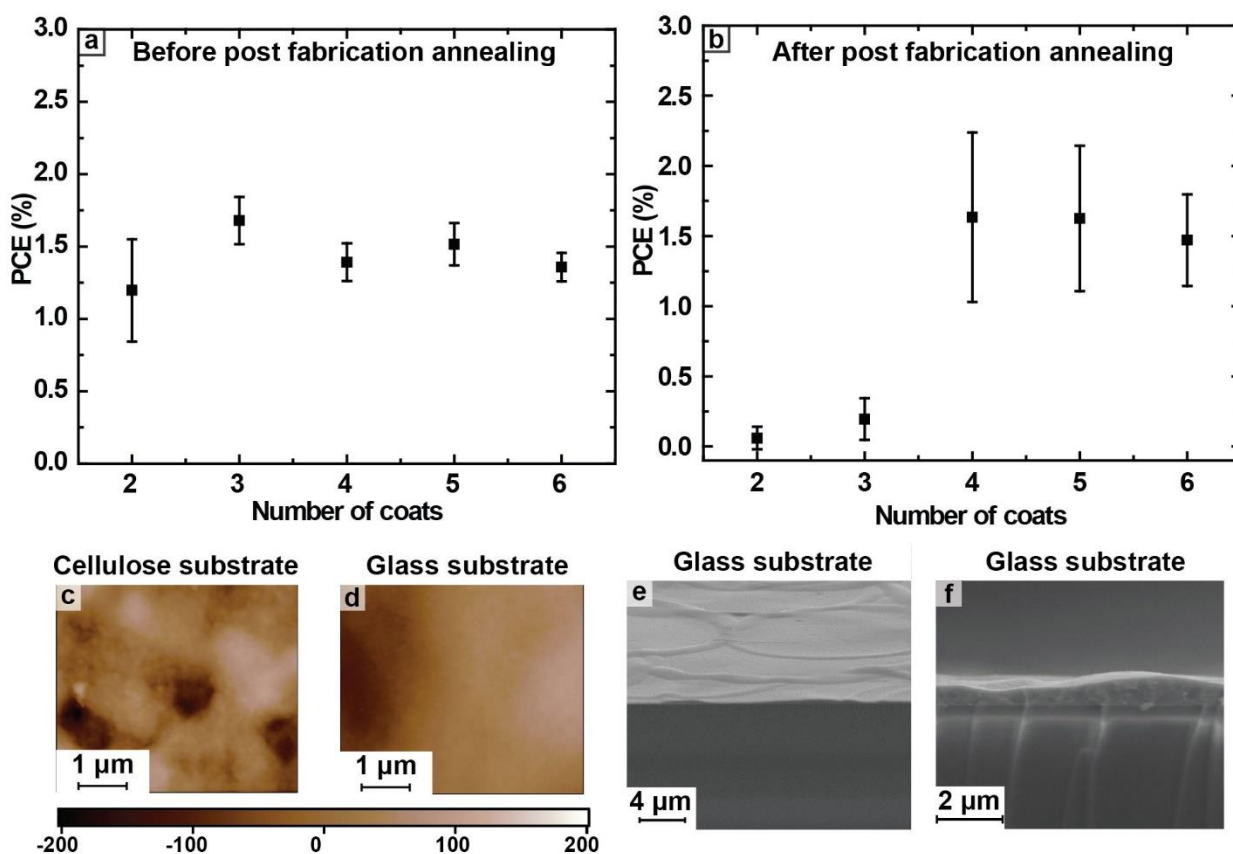


Figure 5.3: Power conversion efficiency (PCE) of CuInSe₂ nanocrystal PVs on bacterial cellulose paper measured under AM 1.5 illumination (100 mW/cm²) (a) before and (b) after rapid thermal processing (RTP) of completed devices at 200^oC for 5 min under vacuum. Error bars represent standard deviation. AFM images of CuInSe₂ layer on (c) bacterial cellulose and (d) glass substrate. (e,f) SEM images of a sprayed CuInSe₂ nanocrystal layer on gold-coated glass. Devices made with only one coat of nanocrystals are not plotted because these devices were generally shorted.

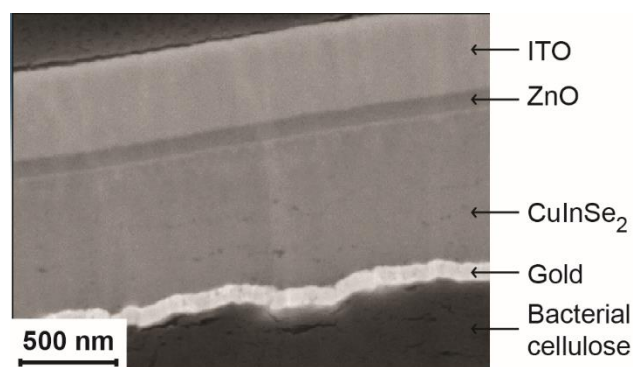


Figure 5.4: SEM image of a cross-sectioned CuInSe₂ nanocrystal PV on bacterial nanocellulose. (Published with permission from JEOL USA Inc.) An Argon ion beam was used to create pristine cross-sections.

CuInSe₂ nanocrystal PVs on glass are generally put through a post-fabrication heating step to improve device efficiency by about a factor of two.²⁰ We believe that the increase in PCE after annealing is due to defect passivation of surface selenium deficiencies similar to what is observed by Rau et al.²⁸ In very thin CuInSe₂ films (less than 4 coats) the number of surface defects is smaller than thicker films which results in excess incorporation of oxygen leading to poor device performance. We carried out an analogous post-fabrication thermal treatment on the paper PVs by rapid thermal processing (RTP) at 200°C under vacuum for 5 min (See figure 5.5 for more RTP device data). The average PCE and standard deviation in PCE as a function of thickness of CuInSe₂ layer after annealing is shown in Figure 5.3b. the device data in both the plots 5.3a and 5.3b is over 6 to 8 devices depending on the number of shorted devices excluded that are excluded from the statistics. The performance of devices with thicker nanocrystal layers (>3 coats) improved in half of the devices but also fell in the remaining half (figure 5.3b and 5.6) making the average PCE to be around the same value while pushing the best efficiency to

2.25%. In devices with thinner nanocrystal layer, the devices performed very poorly due to the above-mentioned reasons of excess oxygen incorporation.

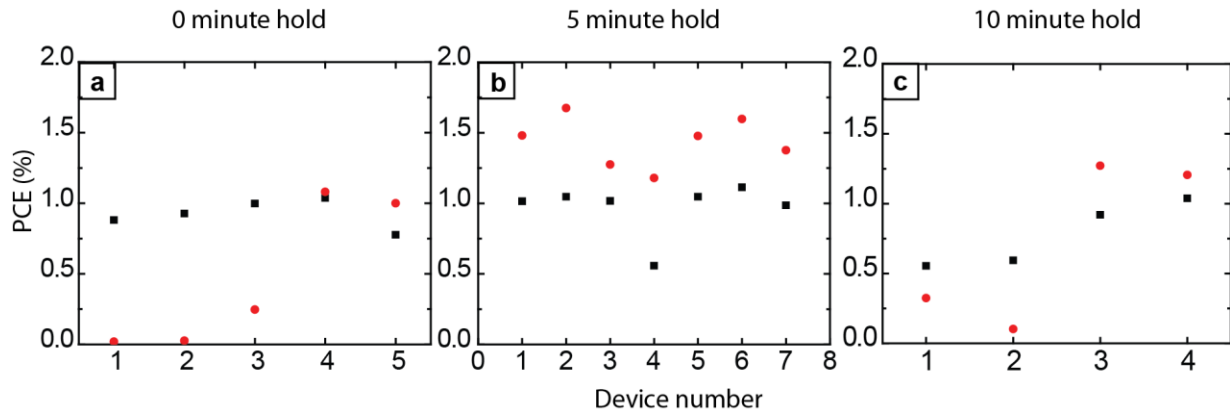


Figure 5.5: Power conversion efficiency (PCE) of CuInSe₂ nanocrystal PVs tested under AM 1.5 illumination (100 mW/cm²) on bacterial cellulose (■) before and (●) after heating at 200°C under vacuum for (a) 0 min, (b) 5 min, and (c) 10 min.

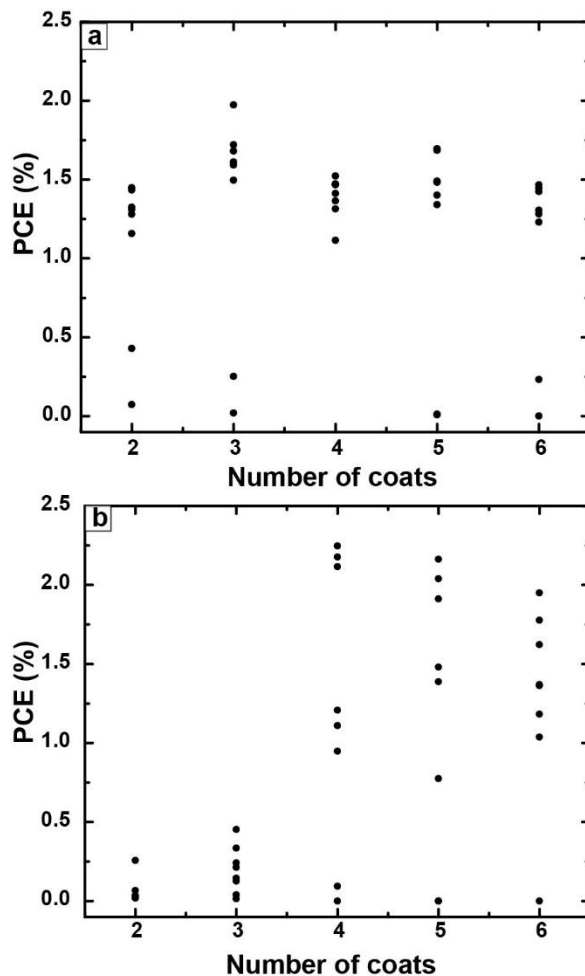


Figure 5.6: Power conversion efficiency (PCE) of CuInSe₂ nanocrystal PVs on bacterial cellulose paper measured under AM 1.5 illumination (100 mW/cm²) (a) before and (b) after rapid thermal processing (RTP) of completed devices at 200^oC for 5 min under vacuum.

Figure 5.7 shows the current-voltage response of a typical device after this heating step. The PCE of 2.25% is the highest achieved to date for a solar cell fabricated directly on paper. The previously reported best efficiency was 1.3% for an OPV device on paper,¹⁰ and there is one report of 4% PCE on paper, but this device was fabricated on a zinc-coated

polypropylene foil (functioning as back electrode) glued onto a piece of paper.²⁹ The power to weight ratio of these devices is around 0.35 to 0.4 W/g, which is similar to high efficiency Si and triple junction solar cells with power to weight ratios of 0.82 and 0.39 W/g, respectively.³⁰ The paper PVs are much lighter, although less efficient.³⁰

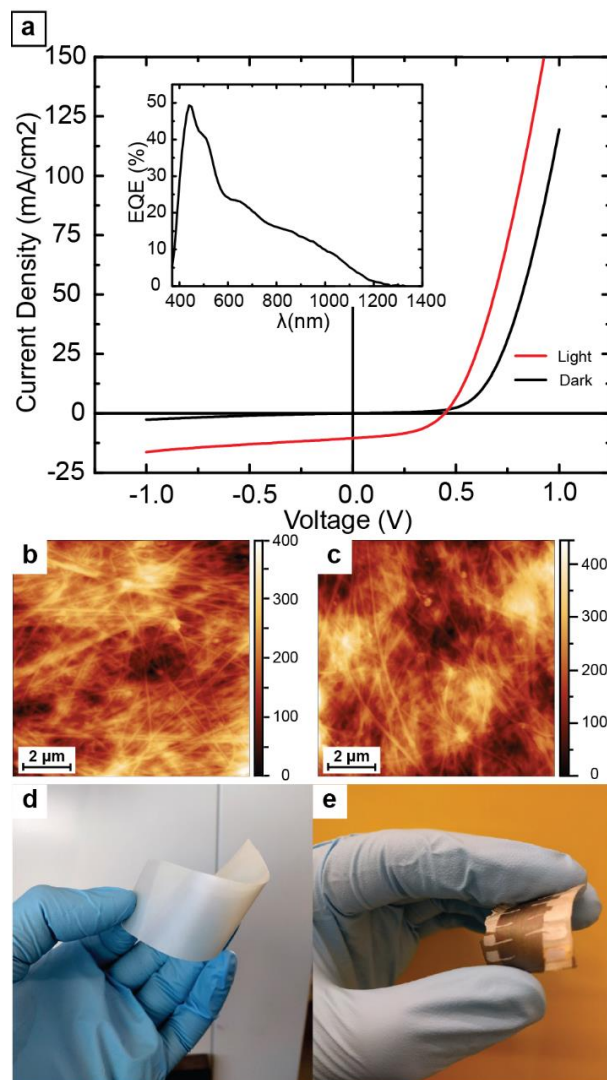


Figure 5.7: (a) J - V curves of a CuInSe_2 nanocrystal PV on bacterial cellulose with a PCE of 2.25%, short circuit current density $J_{sc}=10.47 \text{ Ma/cm}^2$, open circuit voltage $V_{oc}=0.44 \text{ V}$ and fill factor $FF=0.48$. Inset: External quantum efficiency (EQE) of the device measured under AM 1.5 with 50 mW/cm^2 white light bias. The short circuit current calculated from the EQE curve measured under white light bias is 9.2 mA/cm^2 , which is 12% less than the J_{sc} measured for the device and within reasonable limits. (b, c) AFM images of bacterial cellulose substrates before and after depositing 80 nm of Au. (d) Photograph of bacterial cellulose paper. (e) A bacterial cellulose substrate with eight CuInSe_2 nanocrystal PVs, each with an active area of 0.1 cm^2 .

One aspect of the paper PVs that requires future improvement is the need to reduce the variation in device performance. The variation in device performance on paper is considerable compared to similar CuInSe₂ nanocrystal PVs made on glass. On glass, the devices before the post fabrication baking step with 5 coats of CuInSe₂ nanocrystals, have a standard deviation in PCE of about 5.6% (Data in Figure 5.8). The standard deviation in device PCE before post baking on paper was 32% (devices with five spray coats). Post-fabrication heating helped improve overall device performance (Figure 5.3b), but did not help alleviate large variations in performance. The performance variation on paper results from uneven surface morphology, as shown in the AFM images in Figures 5.3c and 5.7b. The surface roughness of the bacterial cellulose substrates is about 100 nm and gold deposition does not reduce the surface roughness (Figure 5.7c). This surface roughness also limits the device efficiency to some extent, as devices fabricated on glass have achieved higher efficiencies of up to 3.1%.²⁰ Spray-deposited CuInSe₂ nanocrystal layers on glass also exhibit some thickness non-uniformity due to solvent drying rings (Figures 5.3d-5.3f), but this is relatively insignificant compared to the non-uniformities related to the paper substrate. Increasing the number of nanocrystal layer deposition cycles did not improve the layer uniformity either (Figure 5.9), although the variation in device performance was slightly better with the thicker nanocrystal films. The standard deviations in PCE for devices before post baking made with 4, 5 and 6 spray coats were 37%, 32% and 22%, respectively. In all cases, the device yield is less than desirable, as at least one or two devices in eight were shorted and did not function.

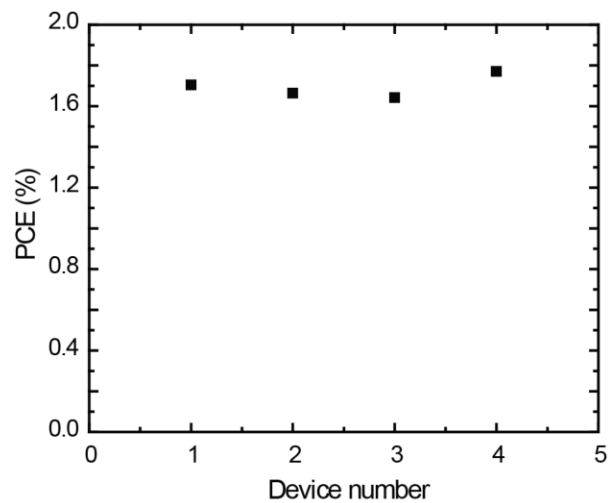


Figure 5.8: PCE of CuInSe₂ nanocrystal PVs on glass before heating with 5 deposition cycles of CuInSe₂ nanocrystals.

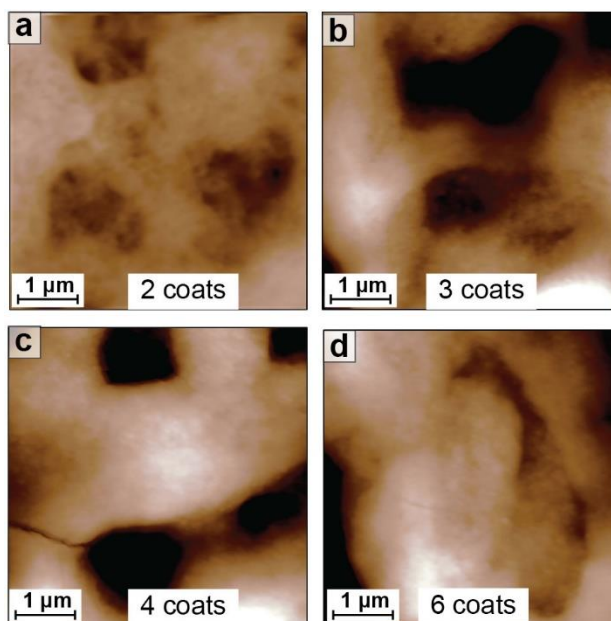


Figure 5.9: AFM images of CuInSe₂ nanocrystals sprayed onto gold coated bacterial cellulose substrates at various thicknesses.

5.3.2 Flexibility of CuInSe₂ Nanocrystal Solar Cells on Paper

Most research making solution-processed flexible solar cells has focused on organic materials on transparent conducting ITO layers; however, ITO is brittle and tends to limit the mechanical flexibility of the device. As a result, a number of ITO alternatives have been studied for P3HT:PCBM OPVs including carbon nanotubes, graphene, PEDOT:PSS, metal based transparent electrodes (Ag, Cu nanowires),³¹⁻³⁴ but the ITO-based devices have had significantly higher device efficiencies.³⁵ We find that ITO layers on nanocellulose paper substrates tolerate much more mechanical flexing than on plastic. As shown in Figures 5.10a-5.10c, the ITO and nanocrystal layers crack when deposited on polyethylene terephthalate (PET) substrates after bending to a radius of 10 to 15 mm. The nanocrystal devices on nanocellulose substrates are much more structurally resilient. Figure 5.10d plots the device characteristics of the PVs on nanocellulose substrates measured after a series of flex cycles to a 5 mm bending radius. There was no significant change in performance after 120 mechanical flexing cycles. Movie 2 in supporting information shows the open circuit voltage that is being measured after bending the device. As shown in the SEM image in Figure 5.10f, these solar cells did not show any cracks in the ITO layer even after bending to a radius of 5 mm for more than 100 times. The curved line features in the SEM image are due to differences in ITO thickness caused by the rough bacterial cellulose substrate and the uneven CuInSe₂ layer, and are not cracks. The bacterial cellulose substrates enable the use of ITO as the top electrode without sacrificing mechanical flexibility.

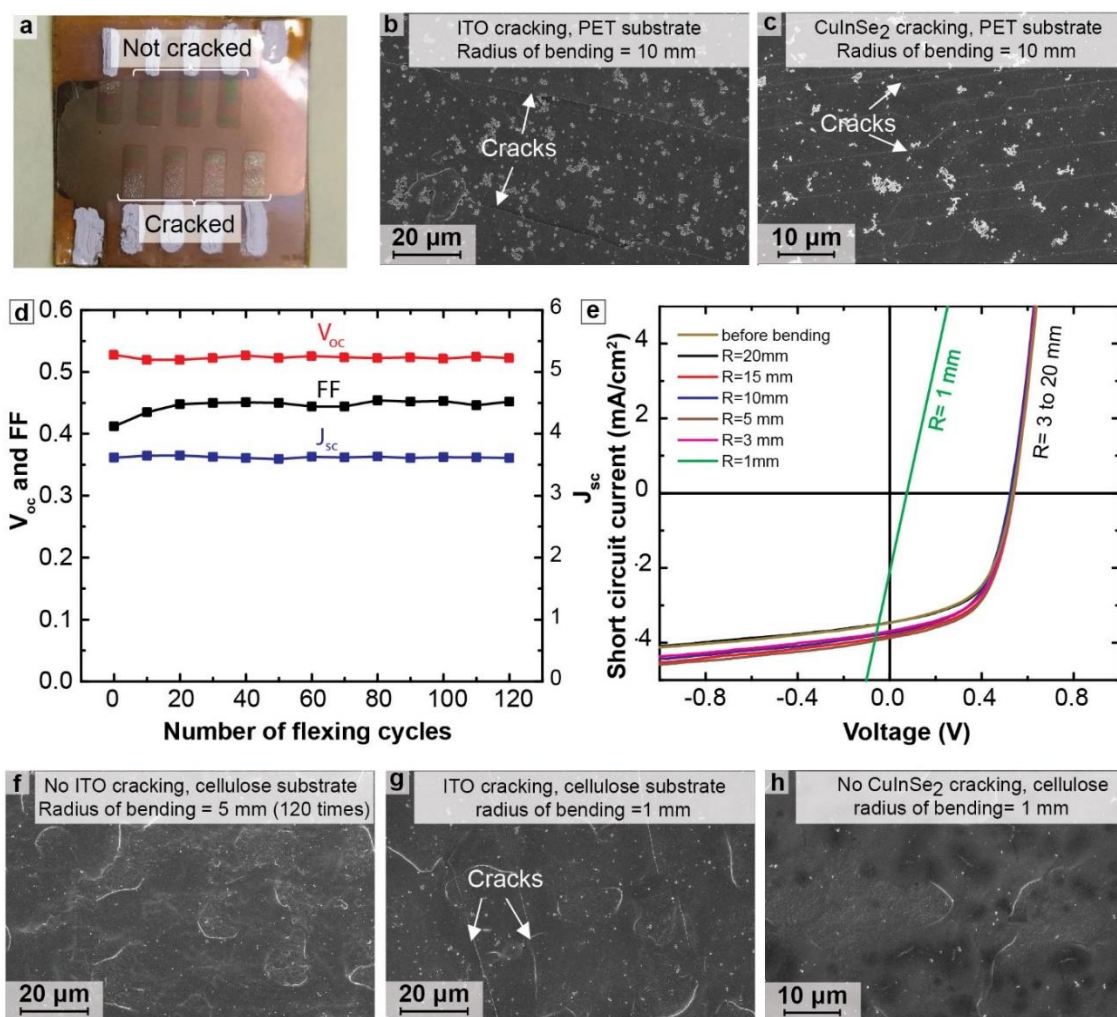


Figure 5.10:(a) CuInSe₂ nanocrystal PVs on PET: the top devices have not been bent and the bottom side devices have been bent to 10 mm radii. (b, c) SEM images of ITO and CuInSe₂ nanocrystal layers in PET substrate devices after bending to 10 mm radius. (d) Average device characteristics for four CuInSe₂ nanocrystal PVs on bacterial cellulose substrates measured as a function of number of flex cycles ($R=5$ mm). (e) J - V curves of CuInSe₂ nanocrystal PV device on bacterial cellulose in flat orientation after bending to various radii. (f) SEM image of the ITO layer in a PV device fabricated on bacterial cellulose after flexing 120 times ($R=5$ mm). (g, h) SEM images of the ITO and CuInSe₂ nanocrystal layers in bacterial cellulose devices after bending to 1 mm radius.

Figure 5.10e shows the device performance of the bacterial cellulose paper PVs after flexing to different radii of curvature. The devices work after bending to a radius as small as 3 mm. Bending further to a radius of 1 mm destroys the device. Bending to a radius of curvature of 1 mm led to cracks in the ITO layer as shown in the SEM image in Figure 5.10g. Even though these devices failed, the cracks were much smaller than those observed in the ITO layers on PET after flexing. It appears that the underlying roughness of the bacterial cellulose substrate helps to limit the propagation of cracks in the CuInSe₂ layer as shown in the SEM image in Figure 5.10h where no cracks were observed even after bending the layer to a radius of 1 mm. This limitation of the crack propagation in the CuInSe₂ layer and strong adhesion between the nanocrystal layer and the underlying nanocellulose substrate help stabilize the ITO layer.

PDMS was found to be an effective releasable adhesive for the nanocellulose device fabrication on a solid glass support. It was difficult to unmount the paper devices from the glass support without a sacrificial layer. When Kapton tape was used as an adherent, it was not possible to unmount the devices after fabrication without destroying the device. PDMS was stable at the processing temperatures and thermally conductive, and could be released for unmounting. PDMS has all these required properties and is the ideal glue for unmounting completed devices from glass.³⁶

Paper PVs were also tested while being bent, as shown in Figure 5.11a. The observed decrease in short circuit current with decreased bending radius results from the loss of incident light intensity upon bending, as illustrated in Figure 5.11a.³⁷ The *J-V*

characteristics return after returning the device to the flat configuration (inset in Figure 5.11b).

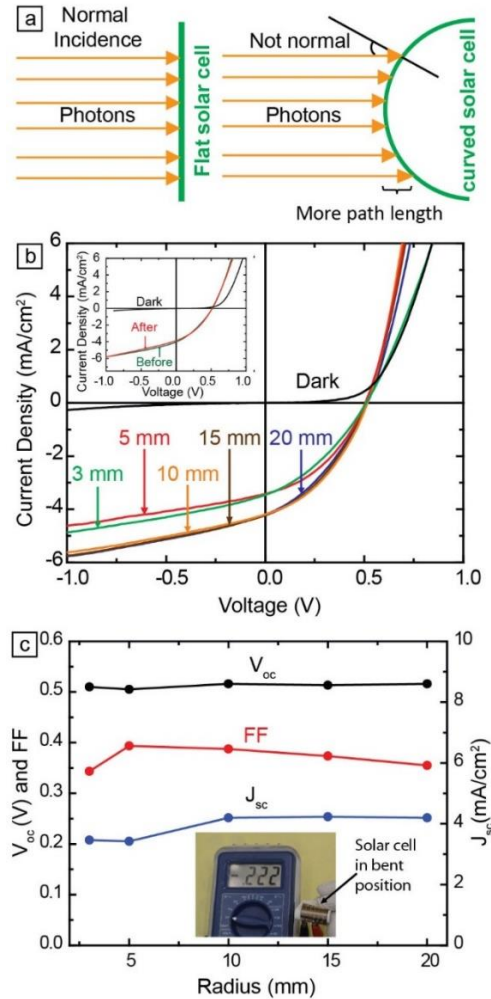


Figure 5.11: (a) Illustration of how the illumination changes when the device is curved. (b) J - V response of CuInSe₂ nanocrystal PVs on bacterial cellulose at various bending radii. Shown in the inset is the JV curve before and after all the bending tests were performed. (c) Device characteristics of CuInSe₂ nanocrystal PVs on bacterial cellulose measured while being bent plotted as a function of bending radii. Inset shows the picture of a nanocellulose device whose open circuit voltage is being measured with a voltmeter in ambient lighting.

5.3.3 Foldability of CuInSe₂ Nanocrystal Solar Cells on Paper

Figure 5.12a shows the device response of a folded CuInSe₂ nanocrystal PV on bacterial cellulose. The device retains more than 80% of its efficiency after folding and unfolding. There have been examples of “foldable” PVs reported in the literature, but these have only functioned when folded in places away from the active area of the devices.^{38,39} In the case of the bacterial nanocellulose PVs, the solar cell is actually folded across the active layer in the device and the device response was stable after 5 cycles of folding and unfolding. Eventually, the device failed after folding due to the formation of large cracks in the ITO and nanocrystal layers, as shown in the SEM images in Figures 5.12b and 5.12c. Cracking is observed in the nanocrystal layer only in regions where the layer was relatively thick (Figure 5.12c). When the device is folded, the open circuit voltage decreases considerably. “Movie 1” in Supporting Information shows the voltage of the device measured when folded under room lighting. When the device is folded, cracks form in the ITO layer, which reduce the device performance. When the device is unfolded, these cracks are largely healed. The other device layers remain intact upon folding except the ITO layer. Perhaps replacing the ITO layer with alternative flexible transparent conducting electrodes like PEDOT:PSS⁴⁰, graphene^{41,42}, nanowires^{43,44} could make the device function even better when folded. It seems like it could be possible to further develop these paper PVs so that they could be folded for transport and unfolded for use.

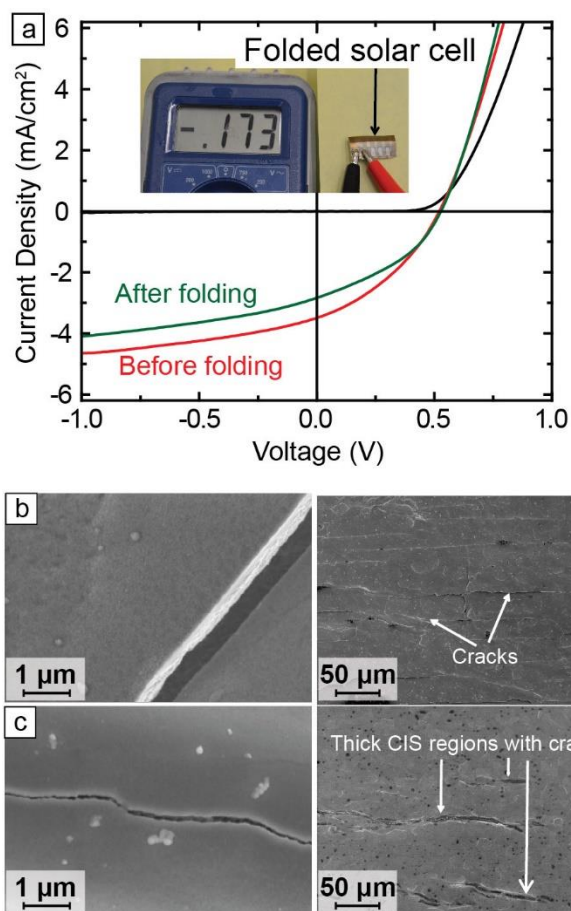


Figure 5.12: (a) J - V response of the CuInSe_2 nanocrystal device on bacterial cellulose shown in the inset before and after folding. (b) SEM images of the ITO layer of a folded CuInSe_2 nanocrystal device on bacterial cellulose showing cracks. (c) SEM images of the CuInSe_2 nanocrystal layer in a PV device on bacterial cellulose showing cracks in the thick CuInSe_2 regions.

5.3.4 CuInSe_2 Nanocrystal Solar Cell Prototype on Paper to Power an LCD

Display

To power electronics, multiple CuInSe_2 nanocrystal PV devices can be fabricated and interconnected on bacterial cellulose paper substrates to provide sufficient power.

Figure 5.13a shows the fabrication of ten devices with areas of 0.1 cm^2 on a 3"x1" bacterial cellulose substrate. These devices can be electrically connected either in series or parallel depending on the application needs. To demonstrate the manipulation of these devices, they have been cut in the middle as shown in Figure 5.13b into two strips of 5 cells. The ten devices are then connected in series. Figures 5.13c-5.13e show the devices mounted to flat and curved surfaces to power electronic devices. (See the associated Movies 3 and 4 in Supporting Information). Each row of devices delivers 1.5 V under indoor fluorescent lighting, which is close to the expected summation of voltages from the individual devices. The active area of each device is relatively small to provide predictable buildup of voltage (by connecting cells in series) since device shorting is reduced. Both the mechanical flexibility and the ability to power electronics have been demonstrated by sticking the flexible CuInSe_2 cellulose devices onto a variety of surfaces including the complex curvature of a water bottle and around a person's wrist. In multiple surface mountings, a calculator was powered and tested to give accurate summations.

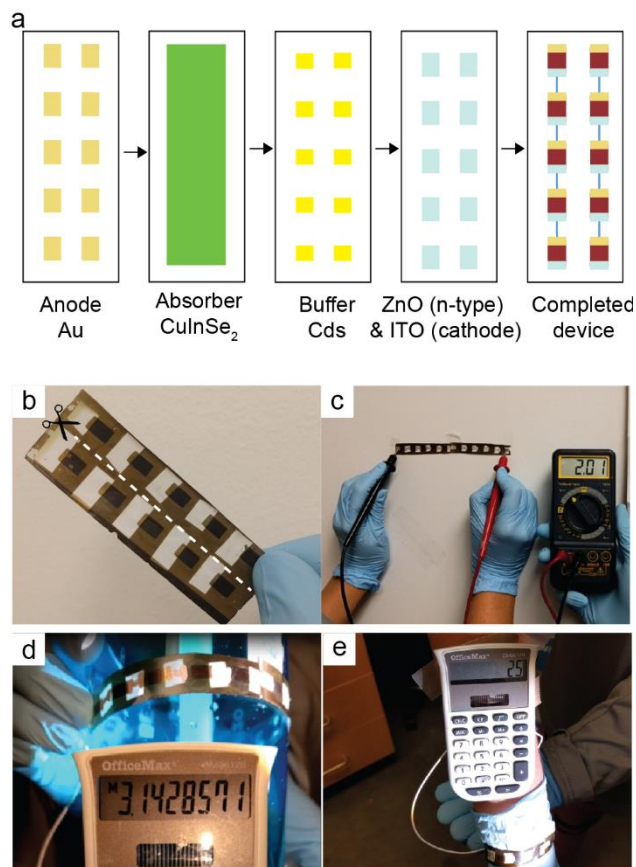


Figure 5.13: (a) Schematic showing different layers of the solar cell fabricated starting from the initial gold layer to the completed device. Photographs of (b) 10 CuInSe₂ nanocrystal PVs fabricated on bacterial cellulose paper; (c) a strip device with solar cells connected in series and voltage being measured with a voltmeter; (d,e) a CuInSe₂ nanocrystal strip device curved around a support powering an LCD screen.

5.4 CONCLUSIONS

Several different types of paper were investigated as substrates for nanocrystal-based PVs. Paper made of bacterial nanocellulose was found to be an excellent substrate for nanocrystal solar cells. These devices exhibited the highest efficiencies yet reported for

PVs made directly on paper^{10–12} and exhibited significantly greater bending flexibility compared to previously made CuInSe_2 and $\text{CuIn}_x\text{Ga}_{(1-x)}\text{Se}$ solar cells.¹⁹ Since bacterial cellulose is already used commercially in a variety of other applications, such as a temporary skin in medical care,^{45,46} in acoustic diaphragms,^{47,48} to make conductive carbon films,⁴⁹ and as a separation medium,^{50–53} it represents a realistic substrate for making paper PVs. Bacterial cellulose has also been used to make electronic paper displays⁵⁴ and as a substrate for light emitting diodes (LEDs).^{55,56} Its use as a wound dresser demonstrates its compatibility with the human body, indicating that solar cells on microbial cellulose could be adhered directly to the skin for human body portability and seamless integration into everyday life. The attributes of bacterial cellulose make it suitable for *extreme* integration of electronic devices—not only with the human body—but also in infrastructure, devices and sensors related to the “Internet of Things” and data-mining devices for dynamic field research practices. Furthermore, the fabrication of microbial cellulose is a process that can be semi-portable and enable on-site fabrication of PV cells where the resources of a wet laboratory are available. The process through which these solar cells are fabricated lends itself to development in extremely off-grid sites where the cost of shipment and consequently the weight of the solar cells becomes a limiting factor.

Few materials like paper exist with an ability to maintain a crease when folded and hold its structure—paper can improvise multiple structural configurations, as demonstrated by the practice of origami. Origami has been incorporated into photovoltaic design recently to maximize sunlight absorption using rigid non-foldable silicon devices connected by

serpentine-shaped interconnects.⁵⁷ The use of paper PVs could enable greater complexity in such designs since the paper PVs can withstand wrinkles and creases. Paper PVs could be mounted on nearly any type of surface, be it convex, concave or complex. In architectural applications, these PVs could be directly adhered onto walls, enhancing aesthetic building solutions and increasing design options in urban planning. Given that the CuInSe₂ nanocrystal layer can be printed,^{20,21} the flexibility of these devices allows for paper photovoltaics to function as custom designed PV wallpaper for both interior and exterior applications.

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Chapter 6: Large Area CuInSe₂ Nanocrystal Solar Cells

6.1 INTRODUCTION

Internet of Things (IoT) has been growing tremendously in recent years. IoT doesn't just encompass personal computers and mobile phones connected through the internet, but also encompasses billions of other devices that are interconnected, constantly communicating, and transmitting useful information through the internet or other wireless technologies. These devices are typically sensors, which are responsive to changes in pressure, temperature, humidity, gas, position, flowrate, etc.

The rapid Internet of Things (IoT) growth is projected to reach 20.4 billion devices in use by 2020.¹ The challenge is to find an inexpensive, portable power source that can deliver multiple years of unattended operation to these billions of sensors. Their power cannot be supplied through power cords because the location of these sensors is usually in remote places. Thus, a portable power source, such as a battery will be required, but batteries are not a viable option due to their cost, maintenance, and, in some cases, their incompatible size with the IoT device. An attractive solution to this portable power problem in a typically small sized IoT device is a lightweight, flexible solar cell.

With the current developments on IoT sensors, they now require very low power and most are voltage driven.^{2,3} Solar cells manufactured by traditional vacuum-based, high temperature processes can have significantly high efficiencies, but are rigid and might be too expensive for these applications. Many IoT devices do not require such high efficiency. Thus, a more fitting solution to this problem is to use solar cells fabricated at mild

processing temperatures and ambient pressures by solution processable, roll-to-roll techniques that are less expensive.^{4,5}

Amongst the various solution processable solar cell technologies, nanocrystal solar cells have higher stability, which makes them the ideal choice for the IoT device power supply. Their stability enables them to operate for over a decade without maintenance. Even though nanocrystal solar cells cannot generate currents as high as traditional Si and thin film solar cells, the voltages they produce are sufficient to power the IoT devices.

Herein, we report a processing paradigm to fabricate a CuInSe₂ nanocrystal micro grid solar cell system that provides the IoT-required amount of voltage and current. Typically, the voltage needed for IoT devices, like beacons, is 1.8 V to 3.6 V, and the current required ranges from μA to a few mA.⁶

6.2 EXPERIMENTAL DETAILS

6.2.1 Chemicals

Copper (I) chloride (CuCl, 99.99%), elemental selenium (Se, 99.99%), cadmium sulfate (CdSO₄, 99.99%), thiourea (99%), diphenylphosphine (DPP), tributylphosphine (TBP), oleylamine (70%), anhydrous toluene (99.8%) and anhydrous ethanol (99.5%), were obtained from Aldrich; indium (III) chloride (InCl₃, 99.99%) was obtained from Strem Chemical; toluene, ethanol, hexanes, and ammonium hydroxide (18M NH₄OH) were obtained from Fisher Scientific. Oleylamine was degassed by pulling vacuum overnight at ~ 200 mTorr at 110 °C and stored in an N₂ filled glovebox before use.

6.2.2 CuInSe₂ Nanocrystal Synthesis

CuInSe₂ nanocrystals were synthesized as previously described.⁷ In a typical reaction, 5 mmol of CuCl, 5 mmol of InCl₃, 1.5 ml of DPP, and 50 ml of degassed oleylamine are added to a 100 ml three neck flask inside an N₂ filled glovebox. The flask is attached to a standard Schlenk line and degassed at 110 °C under vacuum for 30 minutes. The flask is then filled with nitrogen held at 100 °C for 10 minutes. The flask and its contents are then heated to 240 °C. When the temperature reaches 180 °C, a solution of 10 mmol Se in 10 ml of TBP is rapidly injected into the reaction mixture and the temperature is allowed to rise to 240 °C. After 10 min, the heating mantle is removed and the reaction is allowed to cool to room temperature. The nanocrystals are washed via centrifugation using toluene and ethanol as the solvent and anti-solvent, respectively. The final nanocrystals dispersion is then transferred to a nitrogen filled glovebox.

6.2.3 CuInSe₂ Nanocrystal Device Fabrication

CuInSe₂ nanocrystal PVs were fabricated with a Au/CIS/CdS/i-ZnO/indium tin oxide (ITO) device structure. A 5-nm layer of chromium followed by 60 nm of gold were thermally evaporated onto polyethylene terephthalate (PET) substrate that is adhered to glass substrate for support. Films of CuInSe₂ nanocrystals were then deposited on to the gold layer. Oleylamine-capped nanocrystals diluted to 10 mg/ml in toluene were spray deposited at room temperature onto substrates heated to 100 °C using a Sonotek Exactacoat ultrasonic automated spray system equipped with a 120 kHz ultrasonic nozzle. The spray nozzle was rastered across a rectangular area with 3 mm raster spacing, a speed of 10 mm/sec, an ink injection rate of 0.1 mL/min, an air pressure of 1.6 psi, and a nozzle-

to-substrate separation of 11.5 cm. A CdS buffer layer was deposited by drop casting 0.7 mL of a CdS precursor solution (1.25 ml of 15 mM CdSO₄, 2.2 ml of 1.5 M thiourea, and 2.8 ml of 18 M NH₄OH in water) onto the CuInSe₂ nanocrystal film heated to 90 °C on a hot plate and covered with an inverted petri dish for 2 min.⁸ The substrate was removed from the hot plate, rinsed with DI water, and dried with a stream of compressed air. Top layers of i-ZnO and ITO were deposited by RF sputtering from a ZnO target (Lesker, 99.9%) in a 0.5% O₂ in Ar atmosphere (Praxair, 99.95%) and a ITO target (Lesker, 99.99% In₂O₃:SnO₂ 90:10) in Ar atmosphere (Praxair, research grade). ZnO and ITO are deposited selectively certain regions using 3D printed masks. Silver paint was applied for electrical contact to the devices. The patterning of the device layers is adopted according to the need of the application. Figure 6.1 shows one of the large area devices fabricated. The patterns of different layers shown in figure 6.1 is achieved by shadow masking using 3-D printed masks. 3-D printing the masks allows for rapid prototyping and development of microgrid PVs of various power outputs. We define microgrid system as a network of interconnected small PVs (area of around 0.1 cm²) that are referred to as pixel devices. The prototype shown in figure 6.1 has 60 pixel devices and the layers are deposited in such a way that the 10 pixel solar cells in each column have a connected gold back contact and a separate ITO top contact. The pixel devices in each column can be connected in parallel to have a current addition. Then the six columns of parallelly connected solar cells can in turn be connected in series to achieve a voltage addition. We have used silver paint to connect the devices as

necessary but, once again, a 3-D printed shadow mask can be created and a conductive metal-like silver or copper can be evaporated in the desired locations.

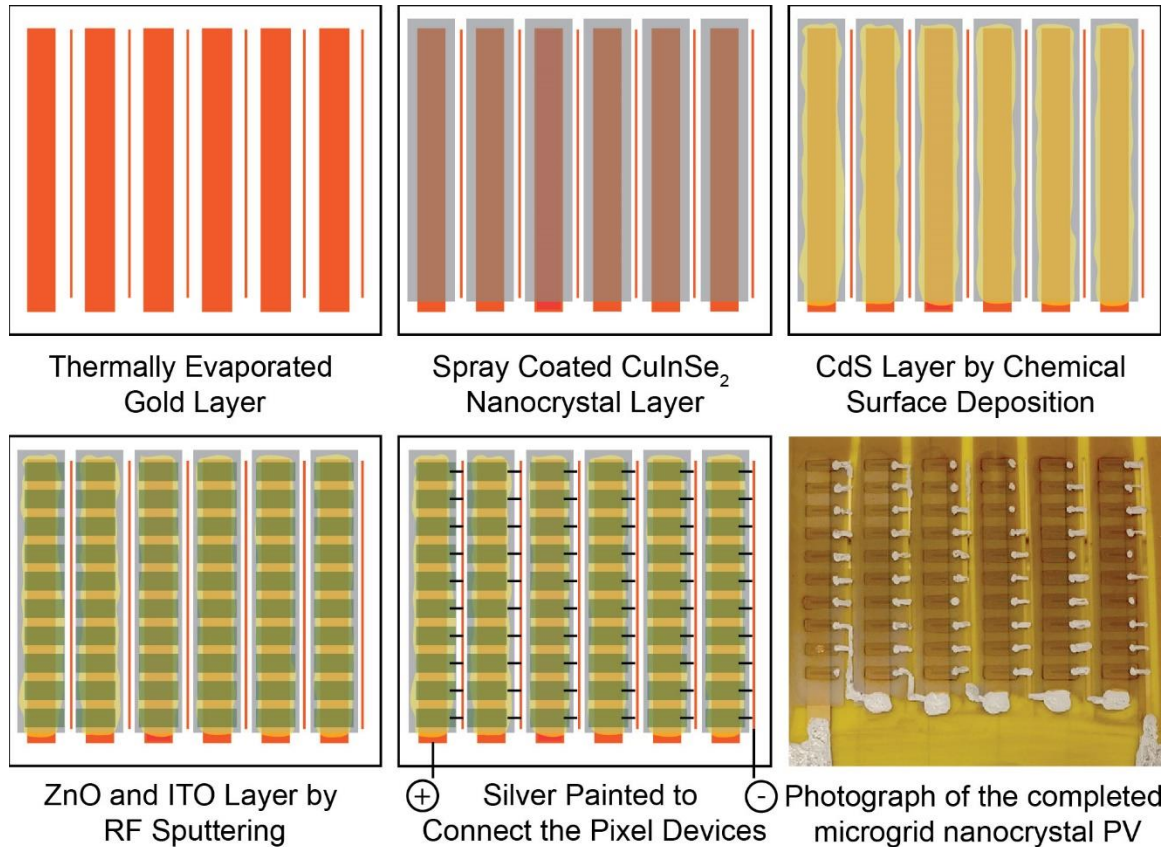


Figure 6.1: Illustration showing the patterning of various device layers

PV device response was measured using a Keithley 2400 General Purpose Sourcemeter under solar simulation using a Newport Xenon Lamp Solar Simulator with an AM 1.5 filter. Incident photon conversion efficiency (IPCE) was measured using a home-built device with lock-in amplifier (Stanford Research Systems, model SR830) and monochromator (Newport Cornerstone 260 1/4M), and calibrated with Si and Ge photodiodes (Hamamatsu).

6.3 RESULTS AND DISCUSSIONS

6.3.1 Device performance

A color coded plot of the maximum power points of each pixel device is shown in figure 6.2. Color coded plots with numerical values overlapped, for the maximum power point, open circuit voltage, fill factor, and power conversion efficiency are shown in figure 6.3. There is a greater chance of device failure when the device area is large, so the area of each pixel device was chosen to be around 0.1 cm^2 . The pixel devices are connected using silver paint in parallel to have current addition and in series to have voltage addition. As seen in figure 6.2, some devices have very low power output. This is due to the failure of the pixel device. Such devices are omitted from being connected to the other pixel devices to prevent them from affecting the whole microgrid. This ability to disconnect failing devices is one of the most important benefits of a micro grid system. When a single nanocrystal solar cell is fabricated on the whole area instead of building numerous pixel devices, the efficiency will be limited to the lowest efficiency pixel device.

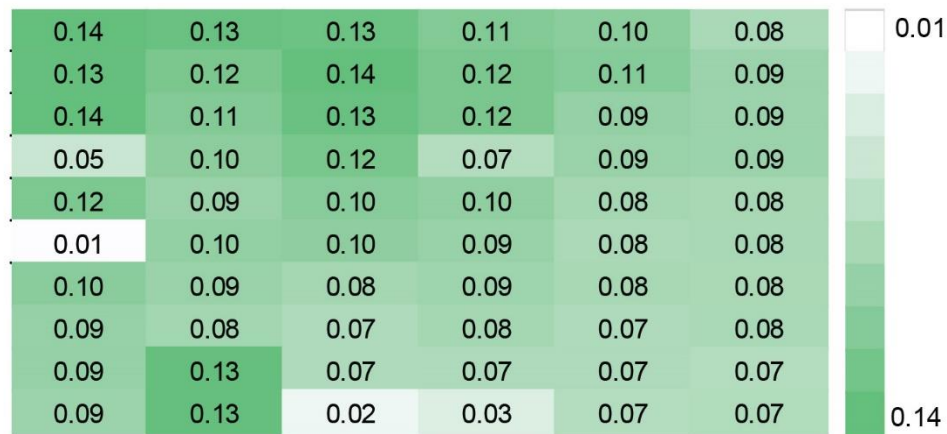


Figure 6.2: Color coded plot showing the maximum power output in milliwatts for the prototype device shown in figure 6.1 under AM1.5G illumination ($100\text{mW}/\text{cm}^2$)



Figure 6.3: Color coded plots showing (a) power conversion efficiency, (b) short circuit current density, (c) open circuit voltage, and (d) fill factor of each pixel device of the CuInSe₂ nanocrystal prototype solar cell shown in figure 6.1 under AM1.5G illumination (100mW/cm²)

6.3.2 Performance on connecting pixel devices

To reduce the number of steps in device fabrication and automate the process of connecting the pixel solar cells, the patterning is designed in such a way that the ITO and Au layers between adjacent devices can be connected without silver paint. Figure 6.4 shows such a prototype device. The IV characteristics of a single row of devices connected in series are shown in figure 6.4c and the IV characteristics after the three rows are connected in parallel are shown in figure 6.4d. It can be seen from the figure that no loss in power conversion efficiency is noted after the rows are connected. Also, the current obtained after

the three rows are connected is roughly thrice the amount of current obtained from a single row of devices, proving that there is no significant loss of current due to connecting them.

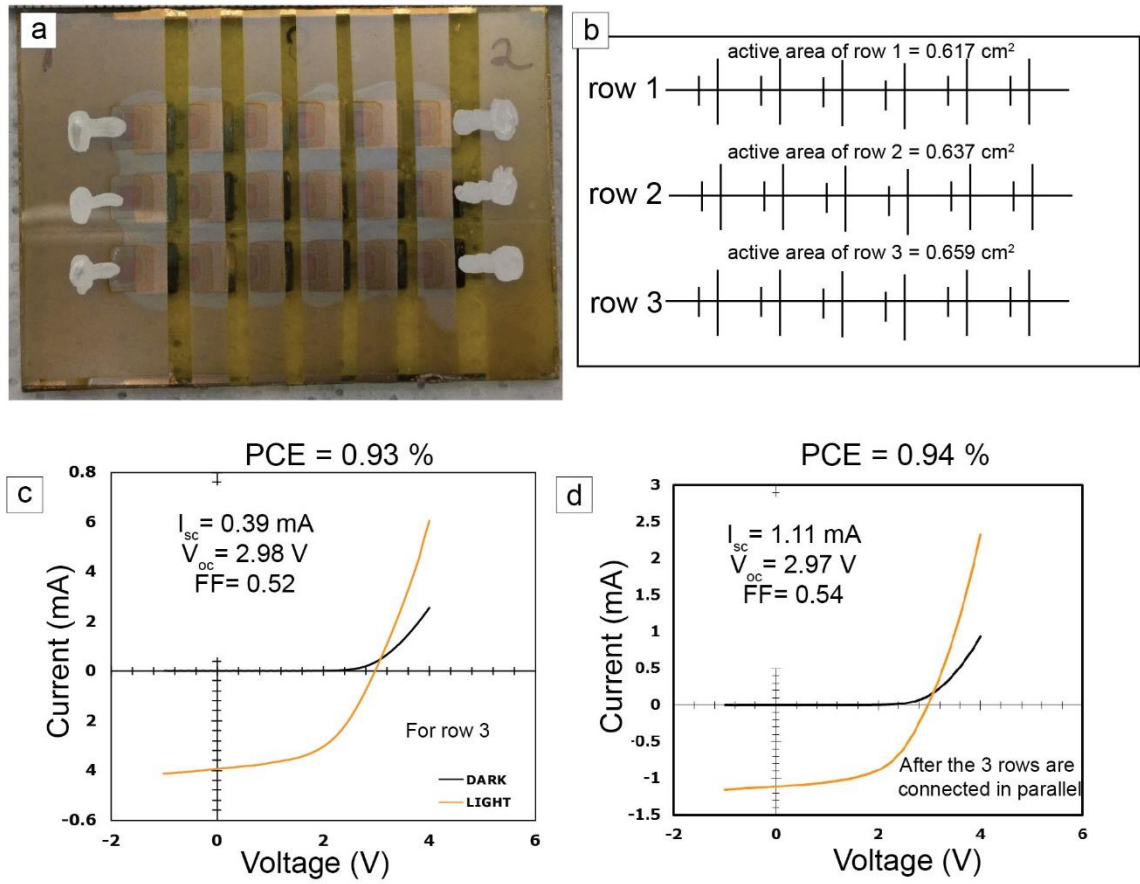


Figure 6.4: (a) Photograph of a microgrid CuInSe₂ nanocrystal solar cell (b) illustration showing the connections of prototype device shown in (a). (c) IV curve of row 3 of prototype shown in (a). (d) IV curve of the prototype shown in (a) after the three device rows are connected in parallel. IV curves are measured under AM1.5G illumination (100mW/cm²)

6.4 CONCLUSIONS

Fabricating a microgrid solar cell system for CuInSe₂ nanocrystal solar cells has been discussed. After the pixel devices are connected, no loss in performance has been

observed. Work is required to further improve the design from that shown in figure 6.1 that has an active area of only 20%. These further design improvements will increase the active area and thus increase the power output from the device.

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Chapter 7: Conclusions and Future Direction

7.1 CONCLUSIONS

The use of semiconductor nanocrystals as an absorbing material in solar cells is a promising route to making cheap photovoltaics. Low-temperature (<200 °C) spray-coated CuInSe₂ nanocrystals have been fabricated with a device efficiency of 3.1 %, in 2010.¹ Since then, no improvement in device efficiency has been achieved for CuInSe₂ nanocrystal solar cells. In contrast, a similar nanocrystal solar cell technology, employing PbS nanocrystals instead of CuInSe₂ nanocrystals, experienced improvements in device efficiency. With ligand engineering, the efficiency of PbS nanocrystal solar cells has improved to as high as 10.9%.² Though only a few similar strategies have been applied to CuInSe₂ nanocrystal solar cells, none of the tested ligands resulted in an increase in power conversion efficiency (PCE) greater than 3.1%.³ In the work reported, a device efficiency improvement through ligand engineering using ammonium sulfide is achieved. Hopefully, future ligand exchange research on CuInSe₂ nanocrystals continue and lead to further improvement in device efficiency.

Apart from efficiency improvements, developments were made on other aspects of CuInSe₂ nanocrystal solar cells, including development of cheaper fabrication methods, use of cheaper substrates, improvement in CuInSe₂ nanocrystal reaction yield, and design of large area nanocrystal solar cells.

7.1.1 Ligand Engineering of CuInSe₂ Nanocrystals

A solid-state ligand exchange was performed on the CuInSe₂ nanocrystals leading to the replacement of long chain organic molecules with shorter sulfide ions. This resulted in an improvement in charge transfer between nanocrystals and an increase in short circuit

current. Overall, this ligand engineering caused an increase in power conversion efficiency to 3.5% from a previous best of 3.1%.

7.1.2 Reduction in Manufacturing and Processing Costs

CuInSe₂ nanocrystals synthesized by several methods were used in photovoltaics to optimize performance. A crucial observation from these experiments was that diphenylphosphine improves reaction yield and tributylphosphine improves device performance. By using both reagents in a single reaction, a high yield synthesis method with device efficiencies over 3% is achieved. Such improvements in reaction yield results in reduction of material costs as less precursor materials will be needed to produce the desired quantity of CuInSe₂ nanocrystals.

To further reduce manufacturing costs, nanocrystals devices were fabricated on paper. This not only reduces the manufacturing costs, as paper is cheap, but also installation and transportation costs, due to the ease of handling and reduced weight. The CuInSe₂ nanocrystal paper solar cells demonstrated extreme flexibility, potentially allowing them to adhere on any kind of surface.⁴ This will lead to deeper market penetration of solar energy into places where heavy, rigid solar panels cannot be used.

Processing costs have further been reduced by replacing high temperature, vacuum processed layers, with solution processed layers. Solution processed layers can be deposited by techniques such as spray coating, spin coating, screen printing etc. that do not need high temperature or vacuum.⁵ This will result in reduced production costs.

7.1.3 Scale up of CuInSe₂ Nanocrystal Solar Cells

For semiconductor nanocrystal solar cells to enter the market, the devices need to be scaled up in area to provide the required power output. A rapid fabrication process has been developed to build multiple solar cells on a single substrate. This was achieved by using 3D printed masks. 3D printing the custom designed masks enables fabrication of solar cells at a rapid pace. Such easy fabrication of custom designed PVs will promote the use of solar energy in new application areas.

7.2 FUTURE DIRECTIONS

Better control of shape and size is needed for CuInSe₂ nanocrystals. Control of shape and size is very important because the properties of nanocrystals are highly dependent on their dimensions.⁶⁻⁹ Better control of the band alignment can be achieved through improved nanocrystal uniformity, achieved by size-selective precipitation¹⁰ or finding the optimal reaction conditions, which may result in better device performance.

Initial improvement of the device efficiency of CuInSe₂ nanocrystal solar cells with ligand exchange has been achieved. This was possible with the use of ammonium sulfide. There are numerous other ligands that have improved device efficiency in other nanocrystal systems.¹¹⁻¹⁵ Exploring these other ligands might lead to improvement in CuInSe₂ device efficiency.

Less expensive processes like photonic curing can be used to improve the nanocrystal device performance by sintering.¹⁶ Photonic curing uses a rapid pulse of broad band light. A great deal of work has been done on sintering the CuInSe₂ nanocrystal layer in solar cells using photonic curing, but an increase in PCE was not achieved. Melt ball formations caused exposed back contacts and led to low device efficiency. To prevent this,

low pulse energies are used to strip the ligands without melting and sintering the nanocrystals. This led to an increase in short circuit current, but the device efficiency was still low due to the loss in open circuit voltage.¹⁷ If the nanocrystal layer could be passivated with shorter ligands after stripping oleylamine, the loss in open circuit voltage could be curtailed and lead to improvements in efficiency.

The commercialization of CuInSe₂ nanocrystal solar cells is also challenged using expensive gold as a back contact. Gold has a very high work function which is best suited for these devices. Other high cheap work function metals like nickel can be explored to replace gold. The use of a thin layer of high work function molybdenum oxide on nickel has been explored in CdTe solar cells which showed promising results.¹⁸ Similar strategies should be applied to replace the gold layer in CuInSe₂ nanocrystal solar cells.

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