STRAIN BALANCED QUANTUM POSTS FOR INTERMEDIATE BAND SOLAR CELLS

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ABSTRACT

In this work we present strain balanced InAs quantum post of exceptional length in the context of photovoltaics. We discuss the general properties of these nanostructures and their impact in the practical implementation of an intermediate band solar cell. We have studied the photocurrent generated by strain balanced quantum posts embedded in a GaAs single crystal, and compared our results with quantum dot based devices. The incorporation of phosphorous in the matrix to partially compensate the accumulated stress enables a significant increase of the quantum post maximum length. The relative importance of tunneling and thermal escape processes is found to depend strongly on the geometry of the nanostructures.

INTRODUCTION

Over the last decades semiconductor nanostructures have been suggested as a means to increase the efficiency of solar cells. Strain balanced multi-quantum-wells (SB-MQW) have been successfully implemented as the bottom GaAs cell of a current matched tandem configuration to tailor the absorption edge and increase the overall efficiency.[1] In the case of quantum dots (QDs), there are different approaches that may lead to a better conversion of the sunlight into electricity, being one of them the intermediate band solar cell concept (IBSC).[2, 3] Recent detailed balance calculations, based in more realistic assumptions than previous results and including an intermediate band with multiple levels thermally interconnected, seem to indicate that even InAs/GaAs QDs, with bandgaps different from the optimum ones, can exhibit higher efficiency than a single gap cell at 1000 suns.[4] Yet, despite the existing effort to model QDs correctly in the context of IBSC theory, the experimental achievements are still far from the predicted values.

One of the main disadvantages of QDs is their small density and, hence, their reduced contribution to the total photocurrent. Several tens of QDs layers are necessary to absorb an appreciably amount of sunlight, and, due to the lattice mismatch between QD and host materials, they often have a detrimental effect on the crystal quality and the device properties. [5, 6] Strain balanced growth has been used before to address this difficulty.[7-9] In this way, we have fabricated solar cells with up to 50 QD layers keeping a reasonable structural quality but still without surpassing the efficiency of a single gap conventional device.[7] Other approaches that may lead to a more favorable strain distribution and better material quality are based on high index and off axis-cut substrates.[10] The best QD-IBSC (efficiency 18.3%) has been obtained by Blokhin et al.[11]

WHAT IS A QUANTUM POST?

Quantum posts (QPs) form spontaneously when a very short period InAs/GaAs superlattice (SL) is grown on top of an InAs QDs seed layer (Fig. 1). In some sense, QPs are related with vertically coupled QD (VC-QDs) in the limit of small spacer layer.[12] Due to the strain field generated by the seed, the adatoms of the SL redistribute during growth producing In accumulation on top of QDs and Ga accumulation around them. As a result, In rich columns are created surrounded by a Ga rich matrix. The diameter of the columns can be controlled tailoring the size of the QD seeds and the height - the length of the QPs - by varying the number of periods of the SL.



Figure 1 Schema of the quantum post formation.

This technique is very versatile and gives another degree of freedom when growing nanostructures, their height. Normally, dots with small lateral dimension are also very shallow whereas tall QDs have a very large base. Following the QP growth protocol, the diameter of the dot can be varied independently of its height. The former is set by the seed and the latter by the number of periods. For example, it is possible to grow conical nanostructures, dots-in-a-rod or high aspect ratio QDs.[13]

QUANTUM POSTS IN PHOTOVOLTAICS

The main interest of using quantum posts instead of quantum dots to form the intermediate band is intimately related with their elongated shape, and hence their potential to tailor the absorption of the photons that cause transitions from the IB to the CB. Typical QDs grown in the Stranski-Krastanov mode have a flat shape with the vertical dimension shorter than the lateral ones. QDs with increased vertical dimension might benefit of a stronger absorption related to IB to CB transitions.[14] The quantum posts presented here are not limited to a precise height and can be a means of producing the desired aspect ratio.

In addition to the above benefit, there is a change in the distribution of the density of states (DOS) of quantum posts. Being elongated structures, they may exhibit 1D behavior for large heights, when the quantum confinement along the post becomes negligible. The limit is roughly imposed by the exciton Bohr radius (~35 nm), and InAs quantum wires grown on different substrates show clear 1D behavior at relatively short lengths.[15] The advantage of having non-delta-like DOS relies on the larger potential occupation of the ground state instead of only two electrons or holes, which is what can hold the fundamental level in a QD at most. The drawbacks need to be investigated as well since the extra density of states can jeopardize the voltage preservation predicted for the IBSC.

As stated originally by Luque et al [3], in order to keep the open circuit voltage (V_{oc}) of the IBSC only limited by the gap of the host cell (GaAs in this case), the IB must be electrically isolated from the CB and the VB. Only radiative transitions between the bands are allowed. In conventional InAs/GaAs QDs, the confinement energies depend strongly on the QD size. For large QDs, the number of energy levels increases and their relative energy spacing diminishes. This favors the carrier thermalization between QD states and continuum states (wetting layer and GaAs barrier) and, as a consequence, degrades the V_{oc} of the device.

For QDs this effect can be avoided by reducing their size and reaching strong quantum confinement regime in the three directions. Small QDs with only one or two confined energy levels and a true 0D DOS show experimentally the higher V_{oc} . [11, 16, 17]

For similar reasons, the existence of a true 1D DOS in QPs might represent an important drawback for the realization of an IB. In a true 1D DOS there is a continuum of possible electronic states between the bottom of the IB and the CB that allows the mutual exchange of carriers by non radiative transitions. In this case reducing the lateral size of the QPs will reduce the number of 1D energy bands but will not eliminate completely the continuum of states between those bands and the CB.

QPs are good for providing the optimal solution since their height can be tailored independently of their radius. The transition from a 0D to a 1D DOS is not an abrupt change and there must be intermediate situations – intermediate lengths for QPs – where the effective density of states increases but without reaching the $E^{-1/2}$ dependency of a complete 1D DOS.[18] In this way, an appropriate choice of the QPs length give most of the above mentioned benefits but preserving the V_{oc} of the host cell at the same time.

Quantum posts have also another property whose impact in the intermediate band performance needs to be explored. That is their reduced radiative recombination efficiency.

As reported by several authors, due to the strain distribution and the In composition modulation, the ground state electron wavefunction is spread along the whole length of the post whereas the hole wavefunction is confined at the base.[12, 19] If compared with a QD, this charge redistribution decreases the electron-hole overlap and, therefore, the interband matrix element, decreasing the radiative recombination probability in these nanostructures. Accumulation times of several hundreds of microseconds have been found experimentally under moderate positive biases at low temperature.[20]

APROXIMATION TO QPs SOLAR CELLS

In this section, we present our results on strain balanced quantum posts in the context of photovoltaics. The studied samples do not represent optimized structures, but rather initial attempts In which we focus our attention on the VB to IB transition (recombination, absorption and extraction of carriers) as compared to regular QDs.

Window 26 nm p-Ga In P	Window 30 nm p-Al Ga Ga As
p(Be) ~ 2x10 ¹⁸ cm ⁻²	p(Be) ~ 1x10 ¹⁸ cm ⁻²
<i>Emitter</i> 100 nm p-GaAs p(Be) ~ 4x10 ¹⁸ cm ⁻²	<i>Emitter</i> 50 nm p-GaAs p(Be) ~ 2x10 ¹⁸ cm ⁻²
<i>Intrinsic region</i> ~310 nm	<i>Intrinsic region</i> ~250 nm
Base 100 nm n-GaAs n(Si) ~ 1x10 ¹⁸ cm ⁻²	Base 20 nm n-GaAs n(Si) ~ 5x10 ¹⁸ cm ⁻²
BSF 26 nm n-Ga _{0,51} In _{0,49} P	BSF 20 nm n-Al _{0,75} Ga _{0,25} As
n(Si) ~ 2x10 ¹⁸ cm ⁻²	n(Si) ~ 3x10 ¹⁸ cm ⁻²
n-GaAs n(Si) ~ 1x10 ¹⁶ cm ⁻²	n-GaAs n(Si) ~ 1x10 ¹⁸ cm²

Figure 2 Structure of the devices. Samples A, B and D follow the structure on the left and sample C the one on the right.



Figure 3 Bright field TEM image of several QPs

Samples growth

The samples were grown by solid source molecular beam epitaxy (MBE) on Si doped GaAs (001) substrates. All samples were grown using strain balance techniques to avoid the detrimental effect of stacking multiple strained layers as explained above.[5,7,8] The structure, composition and doping level of the samples can be seen in figure 2. The nanostructures were grown in the intrinsic region of the solar cell according to the following sequences. The QDs sample (sample A) was grown starting with 4 nm of GaAs followed by 2 ML of InAs. After formation of the QDs, they were capped by 8 nm of GaAs and 3 nm of GaAs_{1-x}P_x (x ~ 18%) as strain compensating layer. This sequence leads to a spacer thickness of 15 nm between QD layers which we repeated 17 times. For the QPs (samples B and C), the growth sequence starts with 2 ML of InAs to grow the QDs seed layer. After formation of the seed, we deposited a very short period superlattice of 0,85 nm of GaAs_{1-x}P_x (x ~ 14%) and 0.22 nm of InAs completing 175 periods for sample B and 100 periods for sample C. For all samples, GaAs intrinsic regions were introduced to separate the nanostructures from the base and the emitter. This GaAs barrier was 25-nm-thick below and above in samples A and B, whereas sample C has a barrier of 110 nm below and 20 nm above the nanostructures. We grew also a reference GaAs cell with the same geometry that samples A and B (sample D) but only with GaAs in the intrinsic region. The substrate temperature was kept at 510° C during the growth of the nanostructures and the spacers and at 600° C for the rest of the structure. The As₄ beam equivalent pressure was 1.5x10⁻⁶ mbar in all cases.

As it can be seen in Figure 2, the p-i-n diodes are very thin (<500 nm) so changes in the intrinsic region absorption or emission properties should have a large impact on the device characteristics. The use of phosphorus in the spacer increases the overall crystal quality and allows the growth of longer quantum posts. The degree of strain compensation in the three samples is ~60% as revealed by in-situ accumulated stress measurements (not shown).



Figure 4 Comparison of the low temperature photoluminescence

Structural and optical properties

Figure 3 shows a transmission electron microscope (TEM) image of sample C. Bright columns are clearly visible across the intrinsic region indicating the successful formation of extremely long QPs (around 120 nm). The region between the posts forms a wide InGaAsP quantum well consequence of the mixing of the remaining In and the GaAsP of the barriers in the SL. The strain field dominates the image contrast around each QP introducing uncertainty in the nanostructure diameter which we estimate ~20 nm.

Very few dislocations are visible in the upper part of the device ($\sim 10^7$ cm⁻²) indicating good crystal quality for QPs of this length. Nevertheless, keeping constant the degree of strain compensation, we observe that the defect density increases with the number of periods, as expected.

The photoluminescence (PL) spectra excited with a 532 nm CW laser at 20 K are shown in Figure 4 for the different samples. PL of sample A suggests the existence of QDs families of different size, probably corresponding to different QD layers. This evolution of the QD size with the number of layers is often found in closely spaced stacks of QDs and is a consequence of In segregation and the strain field of the buried dots.[19] Both QPs samples, on the contrary, show a narrower emission band centered at 1130 nm (with a full width at half maximum of 33 nm for sample C) which is consistent with strong electronic coupling between the layers. The peak corresponding to sample B is slightly broader than that of sample C, but, the integrated PL is the same in both QPs samples and approximately half of the integrated emission of the QDs families in sample A.



Figure 5 Comparison of the room temperature photocurrent.



Figure 6 Photocurrent at room temperature as a function of the applied bias

Photocurrent experiments

Photocurrent (PC) measurements give information not only of the absorption spectrum of the samples but also of the extraction mechanisms that govern carrier transport from the nanostructures to the device contacts. To this aim, 700 micron wide diodes (400 micron for sample C) were fabricated by standard UV lithography, wet etching and electron beam metal deposition. The optical window was 600 micron in diameter (300 micron). There was no antireflecting coating or metal grid to reduce the series resistance of the devices.

For an ideal IBSC, there should not be photocurrent at all below the bandgap except in the case of two photon absorption. In practice, however, all IBSCs show some single photon thermally and/or tunneling assisted photocurrent below the gap which, in turn, serves as a diagnosis of the performance of the nanostructures as light absorbers.[21] Figure 5 shows the room temperature photocurrent density spectrum in logarithmic scale of the three samples considered. The GaAs reference sample is included for comparison. Due to the short length of the emitter in our samples, the above gap efficiency depends crucially on the composition of the intrinsic region. Above the GaAs edge, the photocurrent of samples A and B show a factor three reduction when compared with the reference GaAs cell (sample D). This might be attributed to carrier trapping and point defects introduced during the growth of the nanostructures. [7] The reduction is less drastic in sample C, although it can not be compared directly with the other samples due to its different design (see Figure 2). Nevertheless, the most relevant information is hindered below the gap. All the samples show intense absorption down to 1200 nm largely exceeding the GaAs reference solar cell. In this range samples A to C are comparable since the intrinsic region has similar. There are important differences among them, though. Both QPs samples show more photocurrent than the QD sample in the whole range being up to one order of magnitude in the case of sample C.

The processes that govern the extraction of carriers can be further investigated measuring the PC as a function of the applied voltage in samples A and B. In Figure 6, the PC of the QPs sample is greatly enhanced applying negative bias and can reach an important fraction of the current at the GaAs band edge. Due to the large electrical polarizability of QPs along the growth direction, absorbed electron hole pairs and carriers trapped by the QP from the surrounding regions are readily separated before recombination occurs. This charge separation effect, absent in shallow QDs, might be interesting to facilitate minority carrier extraction, minimizing the recombination probability in the nanostructure or the trapping by defects in the matrix. Tunneling, however, must be avoided to prevent the degradation of the Voc. The usage of a field damping layer at one end of the intrinsic region or a higher bandgap material in the barrier may serve this purpose.[22]

The evolution of QDs PC with the applied bias is much less pronounced, as shown in Figure 6. The weak dependence with voltage might indicate that the extraction is mainly due to thermally assisted escape.

To assess the role of thermally assisted processes in both kind of nanostructures, we show the temperature evolution of the photocurrent of sample A and B in Figures 7(a) and (b), respectively. For the QDs the signal increases in the whole wavelength range as the temperature rises indicating that the extraction of carriers from the nanostructures is a combination of thermal and tunnel assisted photocurrent.[21] On the contrary, the extraction of carriers from the QPs is almost constant at all temperatures, increasing slightly only for the longer wavelengths. In this case the predominant extraction mechanism is tunnel across the GaAs barriers, having the thermal extraction a minor importance.

We tentatively conclude from these experiments that unwanted photocurrent from the IB in QPs solar cells is largely dominated by the electric field. To prevent the extraction of carriers below the bandgap and the resulting drop in V_{oc} in a QPs solar cell, it might be enough to increase the barrier height or to introduce a field damping layer above the QPs layer. This implies a modification of the solar cell design and not of the nanostructures. In QDs, where the dominant mechanism at room temperature is thermal extraction, preventing the drop in Voc requires almost exclusively a change in the nanostructures themselves with the associated modification of their spectral response.[16, 17, 21] Additional time-resolved photoluminescence characterization of our samples should confirm the conclusions derived here.



Figure 7 Temperature dependence of the photocurrent for (a) sample A and (b) sample B.

CONCLUSIONS

In this work we have presented our initial studies on solar cells containing strain balanced InAs quantum posts. After introducing the physical concepts that makes them of interest for the realization of the intermediate band solar cell, we present practical examples of QPs devices that exhibit high photocurrent below the bandgap. The main drawback of the IBSC approach followed here is the drop in the V_{oc} consequence of the tunneling of carriers out of the nanostructures before the second photon absorption occurs. We propose the use of field damping layers or high energy bandgap barrier materials to overcome these difficulties

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