# Dielectric properties of $Pb_{0.5}Ca_{0.5}TiO_3$ thin films

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A dielectric study is carried out on chemical solution-deposited calcium lead titanate (Pb, Ca)TiO<sub>3</sub> thin films, with calcium contents of 50 at. %, by measuring capacitance versus voltage as a function of temperature. A moderate ferroelectric activity is observed that remains in a broad temperature range, and that depends on the applied voltage and thickness of the films. This behavior is associated with the built-in fields generated as result of the space charge inside the films. From the diffusivity of dielectric constant versus temperature (K' - T) curves, the relaxorlike nature of the films is also demonstrated. The feasibility of these films for fabrication of voltage tuneable capacitors and charge store devices (dynamic random access memories) is confirmed. © 2005 American Institute of Physics. [DOI: 10.1063/1.1984072]

#### I. INTRODUCTION

During the last years perovskite thin films such as  $Ba_xSr_{1-x}TiO_3$  have been intensively investigated for its use in many applications requiring high charge storage densities in thin-film capacitors<sup>1</sup> and high dielectric nonlinearity for microwave (MW) devices.<sup>2</sup> These high permittivity materials are used as an alternative to the current SiO<sub>2</sub> or silicon oxide/ nitride layers ("ONO"), whose thickness cannot be reduced beyond certain quantum limit, which is larger than the otherwise necessary to fabricate ultrahigh-density dynamic random access memories (DRAMs).<sup>3</sup>

Concerning the study of the classical PbTiO<sub>3</sub> perovskite, since the 1980s up to today, different modified PbTiO<sub>3</sub> compositions have been tested, starting by doping with diverse cations, by replacing Pb in the A site of the ABO<sub>3</sub> structure, to improve mainly piezoelectric and pyroelectric properties of bulk ceramic materials, as is described in Refs. 4-6. Pb has been substituted up to 50% for isovalent cations or rare earths, obtaining the highest performances for piezoelectric transducers in the case of doping with a 24% Ca instead of Pb. Rarely, doping has exceeded this amount. The main structure features reported were the reduced ferroelectricparaelectric phase-transition temperature and the diminishing of the tetragonality up to x=50% in  $Pb_{1-x}Ca_xTiO_3$ . Close to x=50% and beyond, the composition-temperature phase diagram results to be more complicated, by appearing a superlattice with orthorhombic symmetry with properties that remind a ferroelectric relaxor.<sup>7,8</sup>

Another way to approach the  $Pb_xCa_{1-x}TiO_3$  composition is from the other side of the solid solution, the CaTiO<sub>3</sub>. Lemanov et al.9 reported on CaTiO<sub>3</sub> as an incipient ferroelectric with a polar soft mode that may change by a small doping of Pb. The maximum of dielectric constant versus temperature (K' - T) appears for  $x \ge 0.28$ , that means the material shows a ferroelectric transition, from a polar phase to the centrosymmetric phase, Pbnm. This is the former structure of CaTiO<sub>3</sub> up to 1473 K,<sup>10</sup> which is responsible of superlattice reflections in these compositions, up to  $x \sim 0.65$ ,<sup>5</sup> according to the octahedral tilting model reported by Glazer.<sup>11</sup> These transitions have been reported as relaxorlike in Refs. 9, 12, and 13 since a shift of the temperature at the maximum dielectric constant,  $T_m$ , with frequency is measured that fits the Vogël-Fulcher law and, a reduced dielectric frequency dispersion above  $T_m$  is also shown. From the experimental values reported for Pb<sub>x</sub>Ca<sub>1-x</sub>TiO<sub>3</sub> bulk ceramics and thin films (see references therein and Refs. 12 and 14), the superlattice phase, Pbnm does not change abruptly into the tetragonal phase as the Pb amount increases at the expense of Ca. As Lemanov et al.9 suggested, a morphotropic phase boundary (MPB) as a mixture of the orthorhombic paraelectric Pbnm phase and the ferroelectric P4mm phase should coexist for 0.45 < x < 0.55. This is further experimentally confirmed by Chandra and Pandey in Ref. 15 by x-raydiffraction (XRD) analysis (the two phases coexist in the range for  $0.4 \le x \le 0.6$ ). Outside the MPB, the temperature phase transition, from ferroelectric, P4mm, to the paraelectric cubic phase, Pm3m, increases linearly with the amount of Pb substituted by Ca, as reported in Refs. 16 and 17.

Among the scarce publications on  $Pb_{0.5}Ca_{0.5}TiO_3$  thin films, the authors of this work have previously reported preliminary results on high dielectric properties of these thinfilm materials.<sup>18,19</sup> The aim of this paper is to progress into the knowledge of the  $Pb_{0.5}Ca_{0.5}TiO_3$  solid solution, as a thin

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film form onto Pt/TiO<sub>2</sub>/SiO<sub>2</sub>/(100)Si substrates, on the basis of their dielectric properties. Models for dielectric properties of polycrystalline ferroelectric thin films are lacking. Majority of the works on films that make use of the Landau-Ginzburg-Devonshire (LGD) phenomenological thermodynamic model are only for epitaxial films,<sup>20–22</sup> to justify dielectric properties,<sup>23–25</sup> including residual stresses produced by the processing. This model should not be applied to polycrystalline films.

Meanwhile, because the potential applications of these films deal with applied external voltages we propose in this work the use of the well-known experimental technique of measuring capacitance versus voltage (C-V) at several temperatures, to get information on the dielectric nature of the films, such as electric bias effects on the transition temperature, thickness dependence of nonlinearity, and dielectric diffusivity of transition. Finally, feasibility of the films for fabrication of voltage tunable capacitors and charge storage devices (DRAMs) is also evaluated.

### **II. EXPERIMENTAL PROCEDURES**

Calcium-modified lead titanate precursor solutions with nominal composition of Pb<sub>0.5</sub>Ca<sub>0.5</sub>TiO<sub>3</sub> synthesized by a solgel method,<sup>26</sup> were spin coated onto Pt/TiO<sub>2</sub>/SiO<sub>2</sub>/(100)Si substrates. Crystallization of the films was carried out by rapid thermal processing (RTP). Crystal structure and texture were controlled by x-ray diffraction. Three films,  $\sim 100$ ,  $\sim$ 295, and  $\sim$ 520 nm thick, were prepared. Dot Pt/Au electrodes of  $2 \times 10^{-3}$ -cm<sup>2</sup> area and 50 nm thick were deposited by sputtering on the films surfaces using a shadow mask to get an array of capacitors that permits to perform dielectric measurements. Capacitance versus bias voltage, C-V, has been measured for a wide temperature range with intervals of 5 K at 10 kHz with an impedance analyzer, HP 4194A. C -V measurements were performed in the films by biasing the capacitor with changing bias in a staircase way, while measuring the high-frequency capacitance in a stationary state with a small signal amplitude (0.01 V). Measurements were carried out by sweeping with the bias in both directions searching for hysteresis as a function of temperature. Leakage current densities were measured for the thickest film in the electric-field range employed by using a pulse generator HP 8116A and an electrometer Keithley 6514. In the thinnest film, testing of voltage drop of the capacitor after a pulse was performed by means of a homemade data acquisition set up. The setup sampled the voltage level of the capacitor by a data acquisition hardware, DAQ, after applying a pulse of 3 V of amplitude for 4 ms. Its input impedance was 100 G $\Omega$  and sampling rates up to  $2 \times 10^5$  samples/s with 16-bit accuracy. The measurement tasks were programmed by the commercial LABVIEW software. A single-phase bulk ceramic of the same composition than the films and with an average grain size around 1  $\mu$  was fabricated by the conventional ceramic method. Dielectric constant with temperature at 10 kHz at room temperature was measured on this ceramic sample to compare with the dielectric results of thin films.



FIG. 1. (a) XRD patterns,  $\theta$ -2 $\theta$  scans of the 100-, 295-, and 520-nm-thick films. Peaks with label 100, 110, and 200 correspond to the single PTCa50 perovskite phase. The strong 111 peaks of the high-oriented platinum bottom substrate are observed for K $\alpha$  and  $\beta$  wavelengths. (b) Mean microstrains and crystallite sizes are deduced from the straight lines obtained by fitting with Eq. (1).

#### **III. RESULTS AND DISCUSSION**

Single pseudocubic phase is observed by x-ray diffraction, with a high  $\langle 100 \rangle$ -preferred orientation (see Fig. 1). No change in the lattice parameters is measured with the film thickness, neither through the cross section. Applying the integral breadths method to the broadening Bragg lines 100 and 200 of the XRD patterns [see Fig. 1(a)], microstrains and effective crystallite size are calculated. According to Ref. 27,

$$(\beta_T/tg\,\theta)^2 = (K\lambda/\langle D\rangle)(\beta_T/tg\,\theta\sin\,\theta) + 16\langle\varepsilon\rangle^2,\tag{1}$$

where  $\beta_T$  is the total integral breath,  $\lambda$  the Cu K $\alpha$  wavelength,  $\langle D \rangle$  the mean effective crystallite size, and  $\langle \varepsilon \rangle$  the mean microstrains. Microstrains and grain size are calculated from the slope and the ordinate intercept of the fit straight lines [Fig. 1(b)]. Since the estimated error of  $\beta_T$  is ±5%, the same values of microstrains  $\langle \varepsilon \rangle \approx 3.5 \times 10^{-3} \pm 5\%$  and grain sizes  $\langle D \rangle \approx 60 \pm 10\%$  nm are obtained for the three films. Grain sizes calculated agree well with that deduced from previous atomic force microscopy (AFM) results of the authors<sup>26</sup> (50 nm). Figure 2 shows the C-V measurements of the three films at three discrete and very different temperatures, selected for clarity. Due to the measuring technique, these loops do not inform on charge contribution of domain switching, but for other polar elements, according to Brenann.<sup>28</sup> The asymmetry of the loops around zero voltage bias with different maximum values shows the existence of charge carriers (oxygen vacancies) at the film electrode interfaces that are of different nature due to the film and capacitor fabrication process. Besides this effect, we observe



FIG. 2. C-V measurements at three separate temperatures. The two higher temperatures capacitance values are displaced for clarity. (a) 100 nm thick. Data displacement: 313 K+0.5×10<sup>-9</sup> F and 373 K+1×10<sup>-9</sup> F, (b) 295 nm thick. Data displacement: 323 K+0.2×10<sup>-9</sup> F and 423 K+0.6×10<sup>-9</sup> F. (c) 520 nm thick. Data displacement: 313 K+0.15×10<sup>-9</sup> F and 373 K+0.3×10<sup>-9</sup> F. Hysteresis remains for the thinnest film even at 373 K.

noteworthy differences between the three C-V sets. For the two thickest films, the two branches are practically undistinguishable above certain temperature, well above that corresponding to the phase transition of a counterpart bulk ceramic; that is to say, ferroelectric behavior is weak but remains up to at least this temperature. However, the two branches continue being separated even at 373 K for the thinnest film, indicating a residual hysteresis behavior, related with polarization stabilization. Figure 3, compare the hysteresis loops calculated for the three films by integrating the C-V curves obtained at the lowest applied bias voltage, for two extreme temperatures: at 253 K (ferroelectric phase) and at 373 K (well above the maximum of K'). In these calculations, P is taken as the induced polarization since it does not include switching contributions, but informs us about domain-wall dynamic. This confirms the reduced but still polar nature of these films well above  $T_m$ . The loops are more slanted for the thinnest than for the thickest film; that means polarizability increases drastically with film thickness. Furthermore, nonlinearity of P versus electric field, E, clearly increases with the film thickness. Since the two branches of the loops are very similar, even at low temperatures, we have only taken one of the branches for our calculations, and the typical S shape of a P-E behavior in the



FIG. 3. Comparison of P-E loops obtained by integration of C-V curves of the three films (a) at 253 K and (b) at 373 K. Nonlinearity is significantly larger for the thickest film; the merged continuous line (only one branch) shows the good fitting with Eq. (2).

paraelectric state is obtained. These curves fit to the expression,

$$P = aE - bE^3,\tag{2}$$

where a and b are coefficients, P is the induced polarization, and E the applied electric field. This equation is fulfilled in a system with a centrosymmetrical paraelectric state at temperatures over the Curie temperature  $(T > T_c)$ . Here, values of the linear coefficient a are  $2.1 \times 10^{-9}$ ,  $2.5 \times 10^{-9}$ , and  $3.4 \times 10^{-9}$  C m<sup>-1</sup> V<sup>-1</sup>, for the 100-, 295-, and 520-nm-thick films, respectively. This parameter defines the slant degree of the hysteresis loop and corresponds to the permittivity value. The calculated b coefficient of the thickest film is an order of magnitude larger than that of the thinnest one. These values are  $1.19 \times 10^{-25}$ ,  $6.35 \times 10^{-25}$ , and  $1.06 \times 10^{-24}$  C m V<sup>-3</sup> for the 100-, 295-, and 520-nm-thick films, respectively. This means nonlinearity increases with the film thickness, and therefore the thickest film should show a higher tunability, as it will be discussed below. From the C-V data obtained at several temperatures, we can also draw the evolution of dielectric constant with temperature, K' - T, at different applied bias fields (Fig. 4). A family of decreasing K' - T curves is observed as the applied external bias increases. For the 520and 295-nm-thick films, the temperature of maximum,  $T_m$ , shifts to higher values with the increase of applied bias field, meanwhile for the other film it remains approximately at the same  $T_m$  value. Figure 5 shows a  $T_m$  dependence on the external bias for the 520-nm-thick film that results in a linear relation with a slope of  $\Delta T_m / \Delta E_b$  of ~0.6 K/kV cm<sup>-1</sup>. This is the expected behavior for a ferroelectric material, according to the expression



FIG. 4. Variation of K' - T as a function of the external bias field for the (a) 100-nm, (b) 295-nm, and c) 520-nm-thick films. For comparison see the K' - T curve (d) of a bulk ceramic with the same composition but with a larger grain size. The shift of the maximum,  $T_m$ , to higher temperatures as the external bias increases can be observed, but that remain fixed for the 100-nm-thick film.

$$\Delta T_c / \Delta E = 2/\beta \Delta D, \tag{3}$$

being  $\beta$  the inverse of the Curie-Weiss constant and D the electric displacement, according to Ref. 29. Figure 4 also shows that for the thickest film, beyond a high E value, the curves K' - T become very broad, being the maximum undistinguishable. This means polar activity is practically absent. This is more clearly shown in the graphic of the inverse 1/K'vs T of Fig. 6. Despite the experimental rounded maximum K'-T, certain linearity can be observed above  $T_m$  that remind a Curie-Weiss-like behavior. That is exhibited for values of bias from 0 to  $\sim$ 100 kV/cm in the 520-nm-thick film, but with decreasing slopes to K' values practically temperature independent. The behavior of the thinnest film is different [Fig. 6(a)] since the moderate small slope remains stable up to the highest applied electric bias without a noticeable  $T_m$ shift. This film offers a strong resistance to the shifting of  $T_m$ by an external bias. The strains generated in the films during processing and their microstructure (grain size) are sometimes invocated as causes of K' reduction in thin films,<sup>30,31</sup> but in this work, as described before, grain size and strains are similar in the three films. Therefore, other causes must be explored. The described resistance to maintain  $T_m$  in the thinnest film can be associated with its small thickness, probably through the built-in field generated as a result of the space



FIG. 5. Shift of  $T_m$  as a function of external bias field for the 520-nm-thick film. This follows a straight line, as expected for a ferroelectric material (Ref. 29). Above 70 kV/cm,  $T_m$  is difficult to distinguish due to the high broadening.



FIG. 6. Temperature variation of 1/K' as a function of external bias field of a) 100-, b) 295-, and c) 520-nm-thick films. Note the Curie-Weiss-like behavior differences with film thickness. For comparison, see the 1/K'-T curve (d) of a bulk ceramic with the same composition but larger grain size, showing the approximate Curie-Weiss-like behavior.

charge inside the film. Assuming charged defects into the films (mainly, oxygen vacancies), these defects have to be majority placed close to the bottom electrode interface, more than in the proximity of the top electrode, due to the processing conditions. Charge concentration will increase with decreasing film thickness<sup>32</sup> and therefore strong asymmetric built-in fields will be generated in very thin films, raising the coercive field of both signs. From C-V curves, internal bias fields of 22.5, 13.5, and 3.8 kV/cm are calculated for the 100-, 295-, and 520-nm-thick films, respectively. Nevertheless, we suspect that the lower the thickness of the film, the higher the built-in fields that would impede switching polarization and therefore, should be the cause of the maintenance of the  $T_m$  values against the external bias and the smaller K' values in the thinnest film.<sup>32</sup>

C-V measurements with temperature also provide the way to prove that the broadening of maximum K'-T (diffusivity) could be a consequence of the relaxorlike nature of the films. To study the diffuse phase transition in thin films, we have used the following approach of Uchino and Nomura:<sup>33</sup>

$$1/K'(T) = 1/K'_{m} [1 + (T - T_{m})^{\gamma}/2\Delta^{2}].$$
(4)

Equation (4) is fulfilled in the vicinity of a diffuse phase transition for any frequency, being  $\gamma=2$  for a relaxorlike be-



FIG. 7. Diffuse phase-transition behavior. Linear fitting of  $K'_m/K'$  vs  $(T-T_m)^2$  with Eq. (3) shows the relaxorlike nature of the films. Diffusivity increases as the film thickness is reduced.

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FIG. 8. (a) Variation of K' - E and  $tg \ \delta - E$  of the 520-nm-thick film for two separate temperatures, 253 and 333 K. (b) Tun<sub>rel</sub> for  $V_{\text{max}}$  as a function of temperature and variation of commutation quality factor, CQF and Tun<sub>rel</sub> with bias field, at 253 and 333 K.

havior and  $\Delta$  the broadening parameter. By using the plotting of  $K'_m/K'$  vs  $(T-T_m)^2$ , the linear relation is reasonably fulfilled for the three films. Figure 7 shows straight-line fittings for the three films, thus confirming the relaxorlike character previously reported by the authors.<sup>18</sup> The slope for the 520nm-thick film is larger than those of the other films, which gives us information about the less diffusivity degree in the former. The formal contradiction between the relaxor nature and Curie-Weiss behavior deduced from the experimental results could be due to the fact that the present composition corresponds to the above-suggested morphotropic phase boundary where two phases coexist. Further measurements at higher resolution are needed to solve the issue.

From the application point of view, the analysis of C-V measurements allows us to evaluate the feasibility of these films for high-frequency devices through calculations of their tunability as a function of temperature (the degree of variation of the dielectric constant or capacitance as a function of the applied electric field). Relative tunability,  $Tun_{rel}$ , is defined by,

$$Tun_{rel} = (C_{max} - C_{min})/C_{max},$$
(5)

where  $C_{\text{max}}$  and  $C_{\text{min}}$  are the values of maximum and minimum of capacitance corresponding to the minimum and maximum of applied voltages. But for applications, the parameter that combines tunability with loss tangent results more realistic. That is the commutation quality factor (CQF), given by

$$CQF = (T_r - 1)^2 / T_r tg \,\delta_1 \cdot tg \,\delta_2, \tag{6}$$

being  $tg \delta_1$  and  $tg \delta_2$  the tangent of losses for the minimum and maximum of the applied voltage and  $T_r$  the ratio between  $C_{\text{max}}$  and  $C_{\text{min}}$ . Figure 8(a) gives a general behavior of the dielectric constant, K', and the loss tangent,  $tg\delta$ , as a function of the applied electric field for the thickest film at two temperatures, 253 and 333 K. This film has been selected because of its large nonlinearity compared with the other two films that results from the previous analysis of Pvs E. A change larger than 1.6 times of K' at  $V_{\min}$  in the measured range means a reasonable good tunability. This change is shown in Fig. 8(b), where relative tunability versus electric field grows up to 45%. The values of  $Tun_{rel}$  for  $V_{max}$ as a function of temperature also show a moderate decrease in a range of temperature of 140 K. Despite the moderate values of  $tg \delta$ , those have not been corrected by the contribution of the electrode resistance (expected small for lowfrequency range), the variation of the CQF parameter as a function of the applied electric field for temperatures of 253 and 333 K seems to be adequate for tunable applications. CQF values are quite similar and close to 200 up to an applied electric field of 175 kV/cm. Although microwave applications require additional measurements at higher frequencies with interdigital electrodes (IDEs), we estimate in a first approach, that the low-frequency measurements here reported with a parallel-plate electrode configuration can provide a suitable method to characterize these films for integrated voltage-tunable devices (varactors). This is true for a normal ferroelectric (see a recent review of Tagantsev<sup>34</sup>), but not for a relaxor ferroelectric. However, despite the relaxor nature of the films of this work, the reported frequency dispersion occurs for temperatures up to the maximum of K', but not beyond that because the shift of  $T_m$  with frequency is small. Therefore, the results here obtained are promising to considerate these thin films as good candidates in MW applications.

Since the capacitor must be charged from zero to V, the C-V curves also inform us about the capability of these thin films for the DRAMs. The demand from the material used in these devices should be a high dielectric constant to get a high capacitance density and low leakage density current to retain its voltage once the capacitor is charged up. For the thinnest film of this work, more appropriate for high-density memories, a charge of about  $9 \times 10^{-9}$  C is involved in the broad temperature range of 253-333 K, where variations less than 5% are measured and a capacitance density close to 15 fF/ $\mu^2$  is obtained. This means an equivalent of an ~4-nm-thick ONO layer. In accordance with the current recommendations,<sup>35</sup> the ability of voltage retain of a small capacitor must be larger than 90%. This is fulfilled for the thinnest film of this work; see values shown in Fig. 9 for the 100-nm-thick film. The voltage drop after 200 ms (the usual refreshment time in a DRAM) is less than 5%. These results contribute to the reduced leakage current densities measured in the film, which are lower than  $10^{-7}$  A/cm<sup>2</sup> at 3 V.

# **IV. CONCLUSIONS**

The C-V measuring technique has proved to be suitable to study the dielectric behaviour of  $Pb_{0.5}Ca_{0.5}TiO_3$  thin films with different thickness.

Dielectric constant versus temperature (K' - T) curves of these films shows a large diffusivity from which the relaxor-

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FIG. 9. Time voltage drop after writing pulse in the 100-nm-thick film for the two signs of voltage charge.

like nature is confirmed for any film thickness. By integrating C-V loops, a weak polar activity is observed in the films up to temperatures well above the temperature of the maximum dielectric constant,  $T_m$ . Films with thickness ~100 nm offer a strong resistance to the shift up of  $T_m$  by an external bias. An increase of space charge density into the films by decreasing thickness is suggested as the cause of the reduction of K' and also as responsible of the fixed  $T_m$  with the applied voltage in the thinnest film, due to the built-in fields generated into the films.

The high tunability measured at 10 kHz (40%–50%) in a broad temperature range of a 520-nm-thick film suggests the feasibility of these films for MW devices application. Films with thickness below ~100 nm are shown as a chance for DRAM fabrication. They have a high capacitance density (~15 fF/ $\mu^2$ ), a low leakage current density (<10<sup>-7</sup> A/cm<sup>2</sup> at 3 V) and an ability to retain above the 95% of the written voltage after 200 ms.

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