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# Experimental Investigation and Verification of Traps affecting the performance of 3C-SiC-on-Si Schottky Barrier Diodes

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**Abstract**—Monolithic integration of wide bandgap (WBG) devices for power integrated circuits is currently of increased interest. The ability of the cubic phase (3C-) of Silicon Carbide (SiC) to grow heteroepitaxially on Silicon (Si) substrates (3C-SiC-on-Si) is an enabling feature for a cost-effective integration, including the possible integration with Si devices. The isotropic properties of 3C-SiC and the high thermal conductivity also emphasize its importance. In this paper, the authors evaluated the actual 3C-SiC-on-Si material and established a feasible fabrication methodology. In achieving this, non-freestanding lateral Schottky Barrier Diodes (LSBD) have been fabricated and tested. To gain a deep physical insight of the complex phenomena that take place in this material, an advanced Technology Computer Aided Design (TCAD) model was developed which allowed accurate match of measurements with simulations. The model incorporated the device geometry, an accurate representation of the bulk material properties and complex trapping/de-trapping and tunnelling phenomena which appear to affect the device performance. The observed non-uniformities of the Schottky Barrier Height (SBH) were also successfully modelled through the effect of the interfacial traps. The combination of TCAD with fabrication and measurements enabled the identification of the trap profiles and their impact on the electrical performance of this new material, a necessary step towards device designs that take advantage of its properties.

**Keywords**—Schottky contacts, inhomogeneity, traps, SiC, 3C-SiC-on-Si, TCAD

## I. INTRODUCTION

The demands of modern applications in power electronics make the Silicon- (Si) based power devices lag, mainly due to the moderate critical electric field value of the material [1], [2]. On the other hand, wide bandgap (WBG) semiconductors, like Silicon Carbide (SiC), promise advanced properties [3]–[5]. The 3C-SiC, also referred to as  $\beta$ -SiC, is the only form of SiC with a cubic crystal structure, similar to Si [6]. In consequence, its ability to heteroepitaxially grow on large Si substrates (3C-SiC-on-Si) meets the low-cost requirement and, concurrently, encourages possible integration with Si devices [7], [8]. The induced isotropic properties of 3C-SiC due to its cubic structure, along with its refractory nature, the superior electron mobility at high temperatures and the high thermal conductivity also emphasize its importance [9]–[11].

The larger energy band gap of 3C-SiC allows higher doping and thinner layers for power devices with given blocking capabilities. Thus, resulting in much lower specific on-resistance, particularly for unipolar devices, like Schottky Barrier Diodes (SBDs). Nonetheless, these diodes do not

reach their full potential due to an observed excessive leakage current [12]. This deteriorates their performance in both forward and reverse bias conditions and hence, currently obstructs them from commercialization. Notably, when the 3C-SiC grows on Si, various types of bulk traps are formed in the epitaxial layers [13]–[17], due to lattice and thermal mismatches at the heterointerface [18]. The electrical activity of such extended defects in 3C-SiC-on-Si, like stacking faults (SFs) and micro-twins [19], is a major concern for functional power devices. As a matter of fact, the current flows preferentially through these defects, leading to high leakages and low breakdown voltages. In addition, the deposition of a metal on any semiconductor leads to the formation of supplementary electronic states at the Schottky interface [20]. The presence of such a variety of traps affects the metal / 3C-SiC interfaces and the formation of Schottky barriers in particular [21].

Furthermore, reported inhomogeneous features of the Schottky contact can essentially add to the resulted leakage current before the onset of forward conductivity or while in blocking mode [22]. In the literature there have been various attempts to model such features. In [23]–[25] a simplified approach assumed the contact splitted in two parts, each one characterized by an independent SBH value. This introduced the concept of parallel conduction in which two or more discrete barrier heights are operating in parallel at the Metal / SiC interfaces [26]–[30]. In a similar manner, in [31]–[33] the SBH values were modelled with a Gaussian distribution resulting in a non-uniform behaviour of the Schottky contact under voltage stress. A very well established model was developed by Tung to describe the inhomogeneity observed in fabricated Schottky contacts and has emerged as the most complete method to incorporate parallel conduction methodology [34], [35]. According to Tung's theory, the inhomogeneous behaviour of the SBH is attributed to potential fluctuations at the Schottky interface, but no assumptions are made on the origins of this observed inhomogeneity [24]. These fluctuations are considered as a localized phenomenon and spatially limited on small portions of the contact, called patches [36]. The Tung's model is applied by determining the values of fitting parameters from a modified Thermionic Emission (TE) equation based on the suggestion that neighbouring patches interact. However, questions have been raised about the physical interpretation of the fitting parameters of this model for diodes that widely deviate from the ideal TE behaviour ( $\eta > 1.21$ ), irrespective of the material system [24], [37]. Such obtained highly unphysical parameters indicate that the source of the significant degree of

non-ideality in these SBDs is possibly extrinsic in nature, i.e. traps and/or process induced impurities.

It is of interest that both the degradation of the electrical performance and the observed inhomogeneous of a Schottky contact have been linked with the presence of traps in fabricated SBDs [38]–[43]. However, the characterization of these traps’ and their overall influence on the barrier height value of 3C-SiC-on-Si SBDs are still mostly unknown and unclear [25].

In this paper, 3C-SiC-on-Si lateral SBDs (LSBDs) were fabricated to perform an experimental investigation of the traps affecting the carrier transport mechanisms. Based on validated 3C-SiC material models [44], [45], an advanced Technology Computer Aided (TCAD) model is developed allowing for the identification of the trap profiles in the diode. These traps profiles include both bulk traps and Schottky interface states. The effect induced to the Schottky Barrier Height (SBH) by these traps was revealed with simulations and fully justifies the high leakage current observed in the fabricated diodes. Furthermore, the proposed model also attributes non-uniformities of the SBH to additional trap profiles at the Schottky interface. The multiple interfacial trap profiles are then differentiated by distinct spatial distributions on the active area of the contact. Although the simplicity of a LSBD, this structure allows for gaining the insights of the traps’ impact on the leakage current. The existence of the traps is correlated with complex trapping/de-trapping and tunnelling phenomena that enhance the recombination/generation mechanism of carriers, depending on the bias polarity. Thus, the proposed model is further applicable for diodes that exhibit high ideality factor values. To the credit of this work, the modelled influence of the traps is not limited to 3C-SiC-on-Si SBDs but can be also applied for any SiC SBD.

## II. DEVICE FABRICATION

Circular and rectangular/striped contacts have been fabricated on the 3C-SiC-on-Si wafer. The grown 3C-SiC layer is  $4.28\mu\text{m}$  thick and of  $5 \times 10^{16}\text{cm}^{-3}$  Nitrogen (N) doped. The Si substrate utilized is Boron (B) doped and features p-type conductivity characteristics with a resistivity value of 5–10 Ohm-cm. Nitrogen with doping around  $5 \times 10^{20}\text{cm}^{-3}$  was implanted in selective regions and activated at  $1350^\circ\text{C}$  for 2 hours. Ohmic contacts were obtained on these implanted regions by depositing a Ti30nm/Ni100nm bilayer and annealed at  $1000^\circ\text{C}$  for 1 min. The Schottky contacts were formed by evaporating 200 nm of Platinum (Pt) and no further treatment was applied.

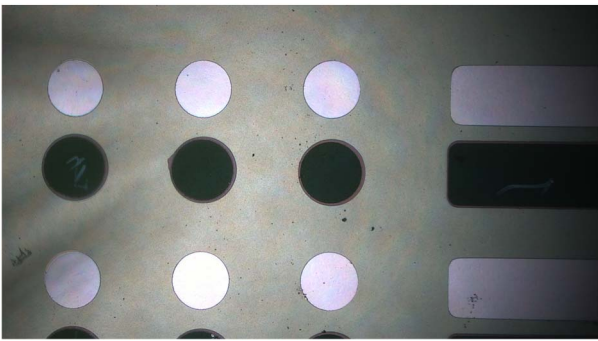


Fig. 1 Both circular and striped LSBDs were fabricated on 3C-SiC-on-Si samples.

A sample of the fabricated LSBDs on 3C-SiC(100)/Si(100) is shown in Fig. 1. The dark coloured

contacts are the ohmic contacts, whilst the brightest correspond to the Pt-Schottky ones. To obtain diode variants, two design variables were introduced in the design split. The diameter and/or the width accounted as the first variable for the circular and the rectangular contacts correspondingly. The circular shaped LSBDs featured  $\varnothing 100\mu\text{m}$  for the smaller contacts and  $\varnothing 150\mu\text{m}$  for the larger ones. Accordingly, small rectangular contacts were of  $100\mu\text{m}$  width, whereas the large ones were of  $150\mu\text{m}$  width. The lateral separation between the ohmic and Schottky contacts was the second design variable with values ranging 30, 35 and  $40\mu\text{m}$ .

## III. DEVICE MODELLING METHODOLOGY

The B1505A equipment [46] was utilized for the characterization process. Thereafter, the ideality factor ( $\eta$ ) was calculated from the I-V measurements for each LSBD. Most devices were found to feature large  $\eta$  values. In addition, the ideality factor of a diode was not constant rather it demonstrated a varying value with the applied forward bias. Based on their lowest  $\eta$  value exhibited, the small rectangular diodes were chosen for TCAD modelling and analysis through simulations.

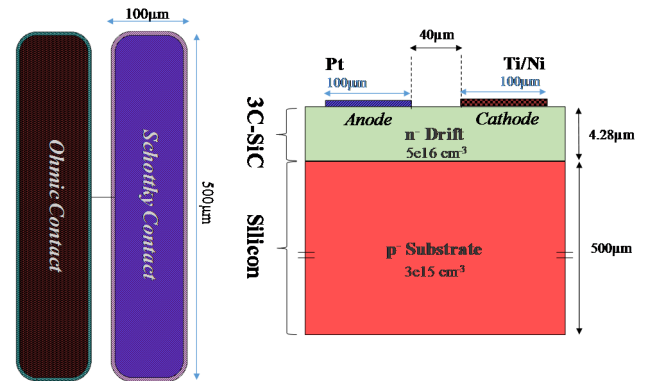


Fig. 2 The top view (left side) and cross-section (right side) of the 3C-SiC(100)/Si(100) LSBD characterised and modelled.

The cross-section view of the investigated as-deposited Pt/3C-SiC-on-Si LSBD is illustrated in Fig. 2. The TCAD simulated striped structure is based on state-of-the-art 3C-SiC material physical models [44]. The 3C-SiC material layer of  $4.28\mu\text{m}$  thickness and  $5 \times 10^{15}\text{cm}^{-3}$  Nitrogen (N) doped corresponds to the drift region. Hence, electrons act as the majority carriers. The Schottky metal is Platinum with a workfunction of  $q\Phi_M = 5.65\text{eV}$  [47]. The Schottky and ohmic contacts are laterally separated by  $40\mu\text{m}$ . The Si substrate is considered lowly doped and of opposite conductivity type compared to the drift, according to the wafer specifications. An area factor has been utilized to accurately represent the diode in the third dimension. Towards accurate simulations, additional to the Thermionic Emission (TE), the barrier lowering due to image force and the barrier tunnelling physical mechanisms are also considered.

## IV. VERIFICATION OF TRAPS AFFECTING THE PERFORMANCE OF THE FABRICATED 3C-SiC-ON-Si LSBD

Focusing on the on-state of the I-V characteristics of the fabricated diode, simulations were performed with the modelled structure in Fig. 2. Matching the simulations with the experiments, within this bias region, allowed for the identification of existing bulk traps in the drift of the investigated 3C-SiC-on-Si LSBD. The determined properties of these trap levels are presented in Table I. Their verified

behaviour as deep acceptors resembles the intrinsic Silicon Vacancies ( $V_{Si}$ ) defects [48]. Such defects are common in the 3C-SiC-on-Si originating from the out-diffusion of the Si atoms to form the SiC layer [7]. The activation energy of these bulk traps, as given in Table I, indicates a high probability of their being occupied with majority carriers even at zero bias, therefore, an effective Fermi level ( $E_F^{eff}$ ) is considered for 3C-SiC. This is illustrated in the band diagram in Fig. 3. Notably, in thermal equilibrium the relative position of the  $E_F^{eff}$  at the Schottky interface ( $E_F^{eff}|_{IT}$ ) also changes to a lower energy.

Table I. Identified trap profiles in the fabricated and investigated 3C-SiC-on-Si LSBSD.

Trap specification		Type / Concentration	Distributions	
			Energetic	Spatial
BULK	Deep levels due to 3C-SiC/Si hetero-interface to model the $V_{Si}$ defects	$4.85 \times 10^{16} \text{ cm}^{-3}$ of Acceptors	Single Level	Uniform covering all the drift 3C-SiC layer
			Activation Energy $E^A = 0.5 \text{ eV}$ (from $E_V^{3C}$ )	
INTERFACE TRAPS MODEL A	Schottky contact interfacial defects resulting in a specific effect on the barrier height	$8 \times 10^{18} \text{ cm}^{-3}$ of Donor-like states	Uniform Band $E_{Mid} = 0.6 \text{ eV}$ $E_{Sig} = 1.0 \text{ eV}$ (from $E_C^{3C}$ )	Uniform along all the contact in the lateral direction
		$2 \times 10^{18} \text{ cm}^{-3}$ of Acceptor-like states	Uniform Band $E_{Mid} = 0.2 \text{ eV}$ $E_{Sig} = 0.4 \text{ eV}$ (from $E_C^{3C}$ )	

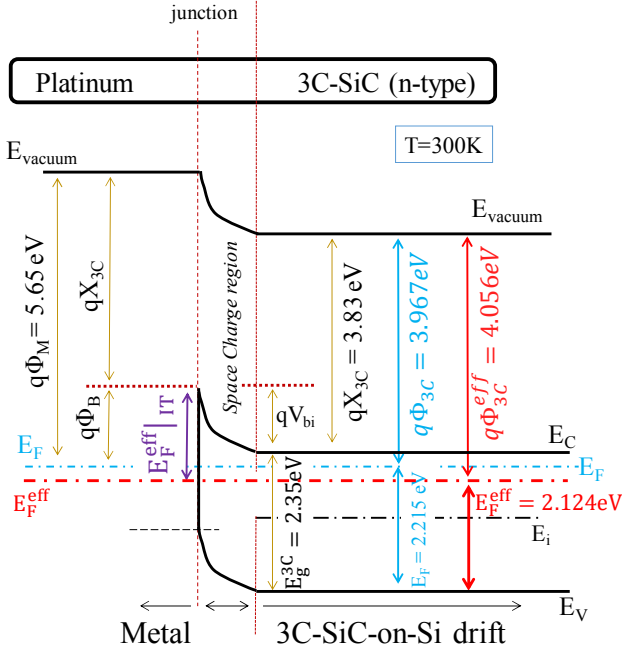


Fig. 3 In zero bias and  $T=300\text{K}$  the deep bulk acceptor levels, resembling  $V_{Si}$ , are highly likely to capture majority carriers, and thus, alter the energy of the Fermi level from an initial  $E_F$  into  $E_F^{eff}$  in the band diagram.

#### A. Initial consideration with a spatially uniform effect of the interfacial traps over all the Schottky contact

Although this specific fabricated LSBSD resulted in the lowest ideality factor values, these values of  $\eta \approx 3$  are still larger than unity, as shown in Fig. 4, indicating a poor

Schottky contact. Furthermore, the sub-threshold region of the forward log(I)-V is directly linked to the quality of the semiconductor material [49]. This can be interpreted with the existence of traps both at the Schottky interface and the drift. The observed variations of the value of  $\eta$  with the applied voltage signifies that the effect of these traps depends on the bias level and, thus, the Fermi level ( $E_F$ ) position [50].

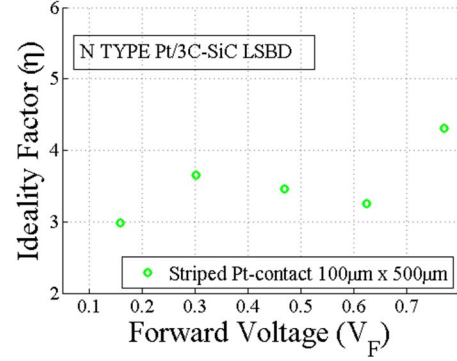


Fig. 4 The calculated  $\eta$  values with a step of 60 measurements of the applied forward voltage for the fabricated and characterized 3C-SiC-on-Si LSBSD (modelled in Fig. 2).

In order to replicate the measurements with simulations, an advanced TCAD model is proposed, which suggests the inclusion of further interfacial traps in the simulations and their sub-sequent effect on the carrier transport mechanisms. The properties of these interfacial (Model A) traps were also determined through FEM and are presented in Table I. Particularly, the interfacial Model A consists of both a distribution of donor-like states and a distribution of acceptor-like states. An illustration of how these states are energetically distributed at the interface between Pt and 3C-SiC-on-Si is shown in Fig. 5. Thereafter, the calculated relative position of the  $E_F^{eff}$  at the interface decides which of these states are ionized, and, thus, highly likely to become occupied by a carrier.

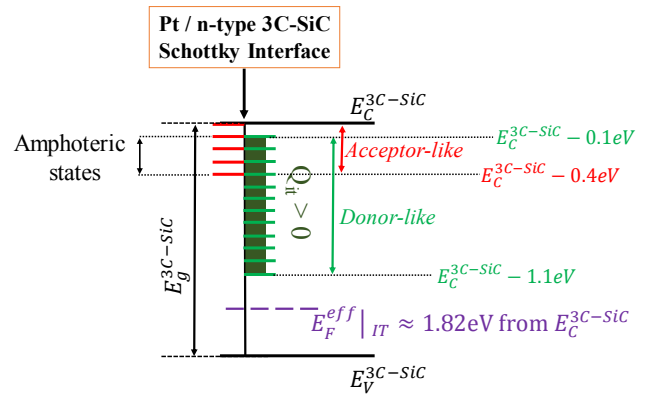


Fig. 5 The energetic distributions of the identified traps' model A at the Schottky interface. The position of the  $E_F^{eff}|_{IT}$  decides which interfacial states are highly likely to be occupied depending on their type.

It is clear, from Fig. 6, that the TE of majority carriers cannot accurately predict the shape of the log(I)-V when traps exist at the Schottky interface. This observation is in agreement with the large calculated ideality factor values of the investigated LSBSD. The elevated forward leakage current can be attributed to the presence of the interfacial traps in Table I. In traps' Model A, the donor-like states at the Schottky interface feature high concentration. These states, at the interface, energetically located above the  $E_F$  are able to



capture a hole emulating fixed positive charge. The electric field originating from the space charge region is now further enhanced. Hence, although in the early forward bias region, at the interface region the applied electric field increases stimulating the effect from phenomena mainly occurring in reverse bias. In particular, the barrier lowering allows for more majority carriers to cross the barrier through Thermionic Field Emission (TFE). Moreover, due to the high population of occupied donor-like states, the triangular-shaped barrier becomes thin enough to make the Field Emission (FE) a great contributor to the sub-threshold current.

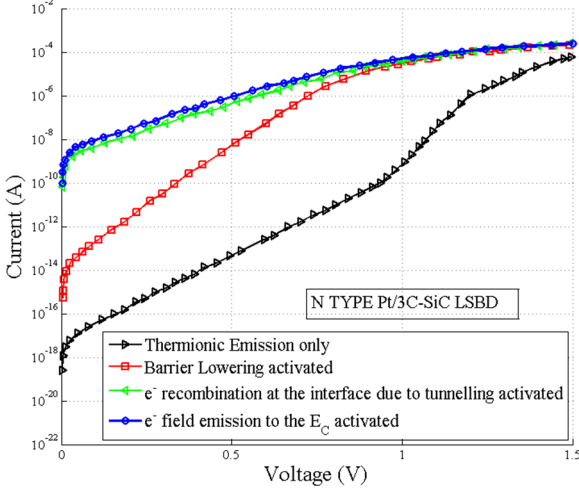


Fig. 6 The identified traps in Table I create the conditions for enhanced carrier transport mechanisms and, thus, a large sub-threshold current that the TE alone is not able to describe accurately. Such mechanisms include barrier lowering, recombination at the interface and direct tunnelling.

Part of the electrons that field emit through the barrier contribute to the current of the investigated 3C-SiC-on-Si SBD by completing a transition to the conduction band ( $E_C$ ) of the Metal. In addition, recombination of carriers also occurs at the interface. For recombination to happen both types of carriers need to be apparent. The metal system is assumed to be a pool of electrons and holes, thus contributing with holes. Thus, part of the majority electrons tunnel through the Schottky heterointerface, between Pt and 3C-SiC, recombine with holes at the valence band ( $E_V$ ) of the metal. Both the direct FE and recombination at the Schottky interface due to tunnelling have a strong impact on the ideality factor, deviating its value towards values greater than unity.

The amphoteric interfacial states highlight a set of energy levels that correspond to traps which can either behave as donors or acceptors. As long as the external bias level indicates the  $E_F$  at the Schottky interface is below these amphoteric states they are able to capture holes. With the increasing forward bias, the energy bands move upwards and these states will eventually drop below the  $E_F$ . In this case, they act as acceptors and are able to capture electrons. The latter, decreases the number of the majority carriers that can contribute to the sub-threshold current, or locally increases the resistance. Hence, the energetic distribution of the acceptor-like states in Model A, in Table I, has been specified so in order to balance the strong effect from the donor-like states of Model A. Interestingly, the acceptor-like states demonstrated a negligible contribution to the Trap-Assisted Tunnelling (TAT) recombination. This is attributed to the properties of the donor-like distribution of Model A, which energetically expands up to the  $E_C$ . The resultant strong band bending at the

Schottky interface, therefore, encourages the direct FE rather than the indirect TAT transition for the electrons.

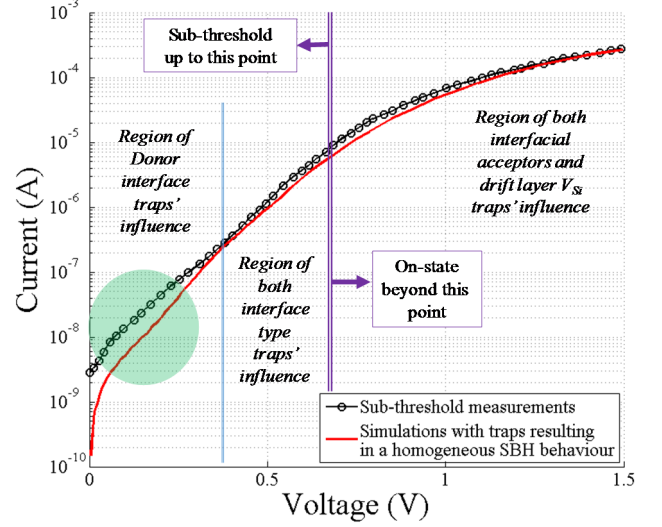


Fig. 7 The incorporation of both the bulk and interface traps model A, in Table I, in the simulations result in a very good prediction of the log(I)-V characteristics of the fabricated 3C-SiC-on-Si LSBD. The presented regions of influence are to illustrate where each one of the aforementioned trap distributions play a key role in the fitting process followed.

The simulated log(I)-V, in Fig. 7, splits upon regions of influence from the introduced traps, in Table I. These regions illustrate how the various identified trap types in the LSBD play a key role in the fitting process with the increasing forward bias. Interestingly, these traps feature uniform spatial properties along the Schottky contact. Hence, a homogeneous behaviour of the barrier height is modelled taking into account the total effect from both the interfacial and bulk traps. This homogeneous SBH approach, after the incorporation of the aforementioned traps in the TCAD model, enables for simulations that approximate with accuracy almost the entire measured data curve besides a minor part. This part of the log(I)-V, highlighted in a green cycle in Fig. 7, suggests some degree of inhomogeneity of the SBH value across the fabricated contact.

### B. Modelling the Inhomogeneous features of the SBH with additional distribution of states at the Schottky interface

The sub-threshold analysis in the previous sub-section indicated that for early forward bias conditions a lateral non-uniformity of the barrier height exists. Such inhomogeneous features are likely due to the large Schottky contacts on the 3C-SiC-on-Si surface [21].

The green cycled part, in Fig. 7, implies the contribution of additional states at the interface whose presence creates the conditions for more majority carriers to cross the barrier. These added states should feature a spatial distribution in a limited area of the Schottky contact active area. Thereafter, the joint effect from both the traps' model A, in Table I, and the added states will determine a weak region at the Schottky interface prone in conducting larger current for small forward bias values. Within this weak region, the SBH can be considered having a smaller value compared to the rest of the contact, essentially modelling an overall non-uniform behavior. The properties of these additional interfacial traps were determined with simulations and are presented in Table II as traps' model B.

Table II. Additional identified interface traps in the fabricated Pt/3C-SiC-on-Si resulting in a inhomogeneous Schottky contact.

Trap description		Type / Concentration	Distributions	
			Energetic	Spatial
INTERFACE TRAPS MODEL B	Superimposed Schottky interface states to model the barrier height non uniformity	2x10 <sup>18</sup> cm <sup>-3</sup> of Donor-like states	Uniform Band $E_{Mid} = 0.5eV$ $E_{Sig} = 0.2eV$ (from $E_C^{3C}$ )	Uniform along only 10% of the contact in the lateral direction

The traps' model B consists of a donor-like distribution of interfacial states. All the energy levels of these states are above the  $E_F^{eff}|_{IT}$  for zero-bias conditions. In consequence, they are considered ionized and, thus, highly likely to be occupied with holes. While occupied, they hold a positive charge. The limited spatial distribution of the traps' model B, as described in Table II, indicates a region on the Schottky contact which experiences an enhanced effect induced by the added concentrations of the charged donor-like states of both traps' model A and B. The accumulated positive charge at this region of the interface enhances the phenomena described in the previous sub-section; the lowering of the barrier becomes more intense allowing for more electrons to cross the barrier for the same bias and temperature conditions. Further, the induced band bending is stronger increasing the FE.

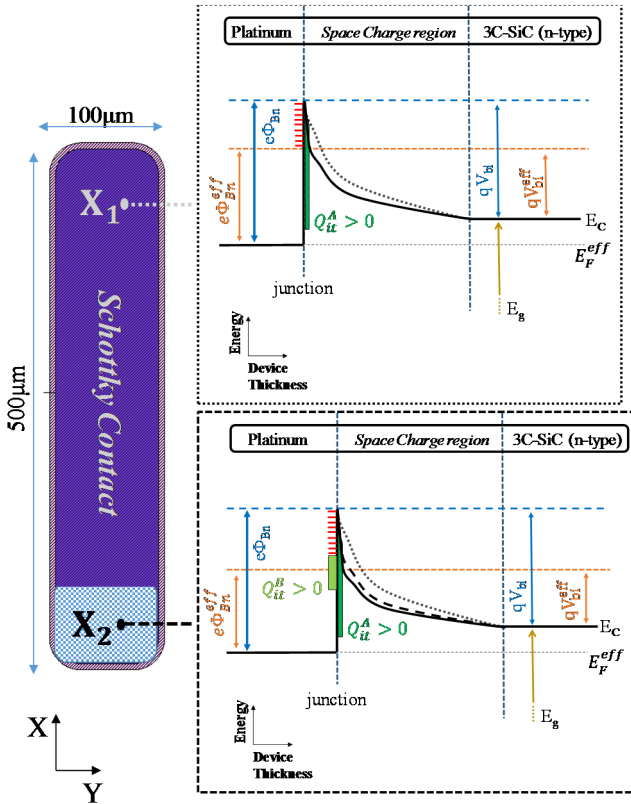


Fig. 8 The effect of the identified traps in modelling the inhomogeneous behaviour of the fabricated 3C-SiC-on-Si Schottky contacts. The X1 point is indicative of a location on the Schottky active area where the interfacial traps' model A is uniformly applied. The X2 point belongs to a portion of the Schottky active area in which the identified interfacial traps' model A and B spatially overlap. The additive effect of the traps within this portion of the contact forms a region of lower SBH value.

Fig. 8 depicts how the identified interfacial traps, in Table I and Table II, are able to model a non-uniform SBH along the Schottky contact by featuring different spatial distributions.

The X2 point belongs to the assumed weak region where the effect from both traps' model A and B applies. On the other hand, the X1 point is representative of any other point on the contact which experiences the effect from the traps' model A only. The dotted  $E_C$  line in Fig. 8 corresponds to the initial band bending due to the space charge region formation in thermal equilibrium. This is identical to the trap-free case illustrated at the band diagram in Fig. 3. The solid  $E_C$  line indicates the current band bending induced at each point due to the overall effect of the identified traps in Table I and Table II. Finally, the dashed  $E_C$  line in the inset band diagram at point X2 corresponds to the initial effect from the traps' model A, before the additional traps' model B results in the final solid line realizing the inhomogeneity of the SBH.

The energy levels of the donor-like states in traps' model B expand up to the lowest level of the acceptor-like states energetic distribution in model A. This also limits their contribution on a small range of forward bias values. With the increased forward bias, the  $E_F^{eff}|_{IT}$  will move upwards decreasing the number of the donor-like states that are likely to be occupied by a hole. When the forward bias reaches a sufficient value for the  $E_F^{eff}|_{IT}$  to ionize the acceptor-like states, specified in the traps' model A, then the effect of the traps' model B will become negligible. Thereafter, the simulated Schottky contact will demonstrate a uniform behaviour of the SBH, as the result of the traps' model A effect only.

The developed TCAD model, which incorporates all the trap distributions in Table I and Table II, is able to accurately describe the electrical performance of the fabricated LSBD. The final excellent match achieved between measurements and simulations, in Fig. 9, adds great confidence to the validity of the combined effect from the involved carrier transport mechanisms and the identified traps. The high prediction ability of the developed models underpins the competence to design improved power device structures utilizing 3C-SiC-on-Si.

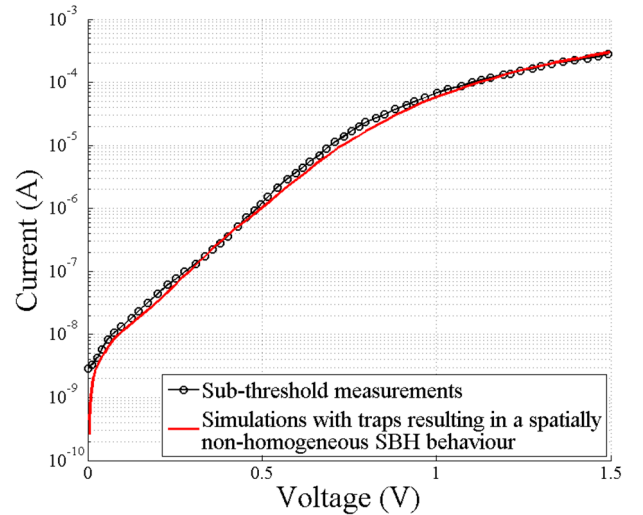


Fig. 9 The proposed TCAD model is able to accurately replicate the electrical performance of the fabricated Pt/3C-SiC-on-Si lateral Schottky contacts by physically linking the SBH behaviour, including any observed inhomogeneous features, to the effect induced by the identified traps.

## V. CONCLUSIONS

In this paper the fabrication of lateral 3C-SiC-on-Si SBDs is reported in an effort to investigate the origins of the leaky behaviour in such devices. In this context, the role of traps is highlighted as the source of the observed large leakage current, which otherwise cannot be explained by the native WBG properties of the SiC semiconductor.

An advanced TCAD model is proposed to link the significant degree of non-ideality with the existing traps by engaging complex trapping/de-trapping phenomena. Initially, bulk traps of acceptor type have been identified in the 3C-SiC-on-Si through simulations, with activation energy below the effective Fermi level. These deep acceptor levels resemble the intrinsic  $V_{Si}$  defects, introduced in 3C-SiC-on-Si due to the out-diffusion of Si atoms to form the SiC layer. Subsequently, it was established that the shape of the sub-threshold region of the  $\log(I)$ -V from the characterized LSBDD is mainly affected by Schottky interfacial states. The utilization of band diagrams, in respect to semiconductor physics, has been deployed to shed light on the effect of these states and, thereafter, their energetic distribution properties were determined through extensive simulations. In particular, an interfacial traps' profile was defined, of both donor and acceptor-like states, featuring a spatially uniform distribution over the Schottky active area.

The suggested method further enabled for the SBH inhomogeneity interpretation. It was found that a distinct distribution of donor-like states spatially limited in a small portion of the Schottky active area also exists. In turn, this portion of the Schottky contact experiences an additive effect from both the uniform interfacial traps' profile and the distinct donor-like distribution emulating a lower SBH region. Moreover, the simulations revealed that the additional donor-like distribution of states should be energetically expanding in a narrower band of energies at the Schottky interface. Consequently, this results in an inhomogeneous overall behaviour of the Schottky contact with forward bias.

After the inclusion of all the identified traps, both bulk and interfacial, the TCAD model is able to predict the characterized  $\log(I)$ -V of the fabricated 3C-SiC-on-Si LSBDD with excellent accuracy. The suggested model is not limited to 3C-SiC-on-Si, rather it can be applied for any Metal / Semiconductor junction to elucidate the effect from the inevitable presence of traps on the SBH and the electrical performance of the device. The contribution of this work could potentially assist in optimizing the fabrication strategies leading to a reduction of these traps. In turn, this can be of immense value towards monolithic integration and an effective co-existence of 3C-SiC-on-Si and Si devices

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