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# An Improved Direct Power Control for Doubly Fed Induction Generator

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**Abstract**—A novel Voltage Modulated Direct Power Control (VM-DPC) is firstly designed for a Back-to-Back (BTB) converter in the Doubly Fed Induction Generator (DFIG) system. The proposed VM-DPC is built in the stator stationary reference frame ( $\alpha\beta$ ). The proposed method uses a simple feed-forward and feedback structure without a phase-locked loop and the Park transformation. Therefore, it can be easily implemented in the BTB converter. Another essential advantage of the proposed VM-DPC is that it can transform the closed-loop DFIG system into a linear-time-invariant one, which can be analyzed and designed through multiple linear control techniques. The proposed method guarantees exponential stability in the stiff grid as well as in weak-grid integration, which is proved based on eigenvalue analysis. Simulation results demonstrate that the proposed VM-DPC has a faster transient response than conventional Vector Oriented Control (VOC). Also, it maintains a satisfactory steady-state performance at the same level as the VOC. The robustness of proposed VM-DPC against distorted voltage conditions and parameter mismatch is also tested. Finally, the proposed VM-DPC control strategy is validated in an experimental hardware prototype of a 7.5 kW DFIG system operating in real-time.

**Index Terms**—Doubly fed induction generator (DFIG), direct power control (DPC), transient response, steady-state.

## I. INTRODUCTION

**D**UE to the increasing consideration of the fossil energy crisis nowadays, a renewable energy source such as wind power has received worldwide attention. Among the existing wind turbines, the Doubly Fed Induction Generator (DFIG) is one of the mainstream topologies, accounting for around 50% of the total wind-energy market [1]. It has superiorities such as flexible power control capability, low cost of the Back-to-back (BTB) converter, and high reliability [2]–[4]. The increasing wind power penetration level also raises the demand for the control performance of the DFIG. According to the modern grid requirements, the DFIG should not only obtain fast and robust power regulation capability but also, operate under different grid conditions [5], [6].

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Generally, the control strategies of the DFIG are designed based on Vector Oriented Control (VOC) or Direct Power Control (DPC). The VOC requires a decoupling of three-phase components in a synchronous rotational reference frame by using a Phase-locked loop (PLL) [7]–[9]. The control structure is based on Linear Time-Invariant (LTI) characteristics, which can be analyzed by using the linear control method structure [10]. However, the performance and stability of the DFIG controlled by VOC are highly dependent on the performance of the PLL and the tuning of the control parameters.

Direct Power Control (DPC) technique can provide DFIG with direct power regulation capability. It avoids PLL and complex parameters tuning in the traditional VOC, which has been widely discussed in recent years [11]–[21]. The conventional DPC utilizes a Look-Up Table (LUT) structure to select switching signals [11]. The switching frequency of LUT based DPC is not fixed but changes over time, which usually results in power and electrical torque pulsations [22]. Model Predictive Control (MPC)-DPC is an improved DPC strategy that has recently been applied in DFIG [23], [24]. The control effect of MPC-DPC is guaranteed by a optimal selection of the voltage vector. However, the evaluation of the optimal control vector is time-consuming, which adds additional computational burden to the control system.

Several studies suggest combining the DPC methods with conventional modulation techniques to guarantee a constant switching frequency. The Sliding Mode Control (SMC)-DPC is a robust control method [25], which has been implemented in DFIG [15], [26]. It obtains a fast power convergence speed and a high robust property against DFIG parameters mismatch. However, it usually brings a power chattering problem [21], [27]. A Back-Stepping (BS) algorithm combined with direct power control is developed in [18], which has a simple structure and obtains satisfactory steady-state performance. However, a zero steady-state error of BS-DPC can hardly be obtained when there is disturbances or model mismatch since it only uses proportional control. A coordinated (C)-DPC without PLL is proposed in [17]. The method replaces the PLL by using a virtual phase signal for coordinate transformation. It features excellent steady-state performance, which was initially sensitive to a grid frequency deviation. The Voltage Modulated (VM)-DPC is a recently proposed simple yet effective control technique for voltage source converters [27]. It guarantees fast and reliable active and reactive power regulation. Moreover, it makes the controlled system Linear-Time-Invariant (LTI), which has been employed in various applications [28]–[34]. However, the implementation of VM-

DPC in DFIG has not been thoroughly discussed.

Motivated by [34], in this paper, the GVM-DPC is employed to the Grid Side Converter (GSC). Then, a novel VM-DPC is designed for the Rotor Side Converter (RSC). Consequently, the main contribution of the paper is that a robust yet straightforward method (VM-DPC) is designed for the BTB converter in the DFIG system, which has the following key features:

- 1) LTI system with DPC. The proposed VM-DPC transforms the DFIG system into an LTI one, which can be analysed and designed through various linear control techniques.
- 2) Simple and easy implementation. The proposed control algorithm uses a simple feed-forward and feed-back structure design. Moreover, since the PLL and the Park transformation for both GSC and RSC are unnecessary, it can be expected that the structure and computation can be simplified compared with the VOC.
- 3) Guaranteed exponential stability. It is proved that the proposed VM-DPC can stabilize the DFIG under the weak-grid integration scenario by using eigenvalue analysis, which is not easy to obtain by using other DPC methods.
- 4) Improved performance. The proposed VM-DPC has a faster transient response than conventional VOC. Also, it maintains a satisfactory steady-state performance at the same level as the VOC. In addition, the robustness of the proposed method against parameter mismatch and distorted grid condition is verified.

The rest of the paper is organized as follows: in Section II, the detailed mathematical models of the RSC and GSC in the  $\alpha\beta$  reference frame are presented. Section III describes the controller design. Section IV provides a stability analysis of DFIG with the proposed VM-DPC. Section V presents the simulation results, which show comparisons among the proposed VM-DPC, the conventional VOC, the C-DPC and the SMC-DPC strategies. Experimental test is presented in Section VI. Finally, the conclusions are given in Section VII.

## II. OVERVIEW OF DFIG MATHEMATICAL MODELLING

The mathematical models of RSC and GSC implemented in the  $\alpha\beta$  reference frames are analyzed in this section to understand how the proposed VM-DPC directly control the stator active and reactive power by modifying the rotor voltage space vector.

By referring the space vectors in the  $\alpha\beta$  reference frame, the stator side and rotor side voltage are represented as follows,

$$\begin{cases} \mathbf{v}_s^s = R_s \mathbf{i}_s^s + \frac{d\boldsymbol{\psi}_s^s}{dt} \\ \mathbf{v}_r^s = R_r \mathbf{i}_r^s + \frac{d\boldsymbol{\psi}_r^s}{dt} - j\omega_e \boldsymbol{\psi}_r^s \end{cases} \quad (1)$$

where  $\omega_e$  is the electrical angular frequency of rotor,  $R_s$  and  $R_r$  represent stator and rotor resistances, superscript 's' of vectors indicates the  $\alpha\beta$  reference frame, which is omitted in the rest of this paper for simplicity,  $\mathbf{v}_s$  and  $\mathbf{v}_r$  represent the stator and rotor voltage space vector,  $\mathbf{i}_s$  and  $\mathbf{i}_r$  represent stator and rotor current space vectors.  $\boldsymbol{\psi}_s$  and  $\boldsymbol{\psi}_r$  represent the stator and rotor flux space vectors, respectively. The flux linkage and

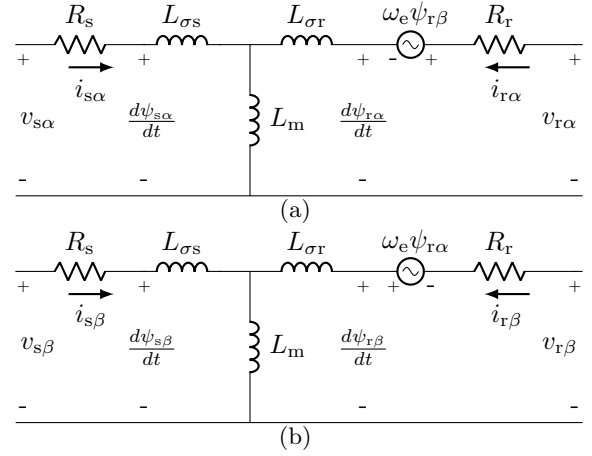


Fig. 1. Equivalent circuit diagram of DFIG. (a) Equivalent circuit in the  $\alpha$  reference frame. (b) Equivalent circuit in the  $\beta$  reference frame.

current relation in the stator and the rotor side are expressed as,

$$\begin{cases} \boldsymbol{\psi}_s = L_s \mathbf{i}_s + L_m \mathbf{i}_r \\ \boldsymbol{\psi}_r = L_r \mathbf{i}_r + L_m \mathbf{i}_s \end{cases} \quad (2)$$

where  $L_s$  and  $L_r$  are total self-inductance of the stator and rotor windings, respectively,  $L_m$  is the mutual inductance. The equivalent electrical diagram of DFIG in the  $\alpha\beta$  reference frame can be concluded, as shown in Fig. 1. By manipulating (2),  $\mathbf{i}_r$  is eliminated and the rotor flux linkage is expressed as follows,

$$\boldsymbol{\psi}_r = \sigma L_m \mathbf{i}_s + \frac{L_r}{L_m} \boldsymbol{\psi}_s \quad (3)$$

where  $\sigma = 1 - \frac{L_s L_r}{L_m^2}$  is defined as the leakage factor. By manipulating (1), (2) and (3), the relationship between stator/rotor currents, stator/rotor voltages and stator flux can be deduced as follows,

$$\begin{aligned} \mathbf{v}_r - \frac{L_r}{L_m} \mathbf{v}_s = R_r \mathbf{i}_r + \sigma L_m \frac{d\mathbf{i}_s}{dt} - j\omega_e (\sigma L_m \mathbf{i}_s \\ + \frac{L_r}{L_m} \boldsymbol{\psi}_s) - \frac{R_s L_r}{L_m} \mathbf{i}_s \end{aligned} \quad (4)$$

The space vector mentioned above are expressed by the components in the  $\alpha\beta$  reference frame as:  $\mathbf{v}_s = v_{s\alpha} + jv_{s\beta}$ ,  $\mathbf{v}_r = v_{r\alpha} + jv_{r\beta}$ ,  $\mathbf{i}_s = i_{s\alpha} + ji_{s\beta}$ ,  $\mathbf{i}_r = i_{r\alpha} + ji_{r\beta}$ ,  $\boldsymbol{\psi}_s = \psi_{s\alpha} + j\psi_{s\beta}$ , and  $\boldsymbol{\psi}_r = \psi_{r\alpha} + j\psi_{r\beta}$ . Based on (4), the instantaneous variation of stator current can be expressed in the  $\alpha\beta$  reference frame as follows,

$$\begin{cases} \frac{di_{s\alpha}}{dt} = \frac{1}{\sigma L_m} (v_{r\alpha} - \frac{L_r}{L_m} v_{s\alpha} - R_r i_{r\alpha} + \frac{R_s L_r}{L_m} i_{s\alpha} \\ \quad - \omega_e \frac{L_r}{L_m} \psi_{s\beta} - \sigma \omega_e L_m i_{s\beta}) \\ \frac{di_{s\beta}}{dt} = \frac{1}{\sigma L_m} (v_{r\beta} - \frac{L_r}{L_m} v_{s\beta} - R_r i_{r\beta} + \frac{R_s L_r}{L_m} i_{s\beta} \\ \quad + \omega_e \frac{L_r}{L_m} \psi_{s\alpha} + \sigma \omega_e L_m i_{s\alpha}) \end{cases} \quad (5)$$

The stator side active and reactive power are written as,

$$P_s + jQ_s = -\frac{3}{2}v_s \cdot i_s^{s*} \quad (6)$$

By differentiating (6), the instantaneous variations of the stator active and reactive power are derived as follows,

$$\begin{cases} \frac{dP_s}{dt} = -\frac{3}{2} \left( \frac{dv_{s\alpha}}{dt} i_{s\alpha} + \frac{di_{s\alpha}}{dt} v_{s\alpha} + \frac{dv_{s\beta}}{dt} i_{s\beta} + \frac{di_{s\beta}}{dt} v_{s\beta} \right) \\ \frac{dQ_s}{dt} = -\frac{3}{2} \left( \frac{dv_{s\beta}}{dt} i_{s\alpha} + \frac{di_{s\alpha}}{dt} v_{s\beta} - \frac{dv_{s\alpha}}{dt} i_{s\beta} - \frac{di_{s\beta}}{dt} v_{s\alpha} \right) \end{cases} \quad (7)$$

In this paper, an ideal network with angular frequency  $\omega_s$  is considered. Therefore, the stator voltage in the  $\alpha\beta$  reference frame can be expressed as  $v_{s\alpha} = |v_s| \cos(\omega_s t + \theta_0)$  and  $v_{s\beta} = |v_s| \sin(\omega_s t + \theta_0)$ . The instantaneous network voltage variations are obtained as,

$$\begin{cases} \frac{dv_{s\alpha}}{dt} = -\omega_s v_{s\beta} \\ \frac{dv_{s\beta}}{dt} = \omega_s v_{s\alpha} \end{cases} \quad (8)$$

The impact of stator resistance to the stator flux can be ignored and following can be obtained,

$$\psi_s = -j \frac{1}{\omega_s} v_s \quad (9)$$

By manipulating (5), (7), (8) and (9), the dynamics of instantaneous active and reactive power can be deduced,

$$\begin{cases} \frac{dP_s}{dt} = -\omega_r Q_s + \frac{L_r R_s}{\sigma L_m^2} P_s - \frac{3}{2L_m \sigma} [(v_{r\alpha} v_{s\alpha} + v_{r\beta} v_{s\beta}) \\ - R_r (i_{r\alpha} v_{s\alpha} + i_{r\beta} v_{s\beta}) - \frac{L_r \omega_r}{L_m \omega_s} |v_s|^2] \\ \frac{dQ_s}{dt} = \omega_r P_s + \frac{L_r R_s}{\sigma L_m^2} Q_s - \frac{3}{2L_m \sigma} [(v_{r\alpha} v_{s\beta} - v_{r\beta} v_{s\alpha}) \\ + R_r (i_{r\beta} v_{s\alpha} - i_{r\alpha} v_{s\beta})] \end{cases} \quad (10)$$

The improved VM-DPC for RSC is designed based on the expression of instantaneous active and reactive power of RSC, which will be explained in the next section.

To build the model of GSC, the stator voltage  $v_s$  is written in the  $\alpha\beta$  reference frame in terms of the GSC input current  $i_g$  and the controlled converter terminal voltage  $v_g$  as follows,

$$v_s = R_g i_g + L_g \frac{di_g}{dt} + v_g \quad (11)$$

where  $L_g$  and  $R_g$  are the inductance and resistance of GSC, respectively. The active and reactive power of the grid side converter can be written as  $P_g = \frac{3}{2} \text{Re}(v_g \cdot i_g)$  and  $Q_g = \frac{3}{2} \text{Im}(v_g \cdot i_g)$ , respectively. The instantaneous variations of the GSC active and reactive power can be derived as,

$$\begin{cases} \frac{dP_g}{dt} = \frac{3}{2} \left( \frac{dv_{g\alpha}}{dt} i_{g\alpha} + \frac{di_{g\alpha}}{dt} v_{g\alpha} + \frac{dv_{g\beta}}{dt} i_{g\beta} + \frac{di_{g\beta}}{dt} v_{g\beta} \right) \\ \frac{dQ_g}{dt} = \frac{3}{2} \left( \frac{dv_{g\beta}}{dt} i_{g\alpha} + \frac{di_{g\alpha}}{dt} v_{g\beta} - \frac{dv_{g\alpha}}{dt} i_{g\beta} - \frac{di_{g\beta}}{dt} v_{g\alpha} \right) \end{cases} \quad (12)$$

By assuming a balanced grid voltage, the dynamics of the instantaneous active and reactive power of grid side converter

in the  $\alpha\beta$  reference frame are obtained by using (11) and (12) as follows,

$$\begin{cases} \frac{dP_g}{dt} = -\frac{R_g}{L_g} P_g - \omega_s Q_g + \frac{3}{2L_g} (v_{s\alpha} v_{g\alpha} + v_{s\beta} v_{g\beta} - |v_s|^2) \\ \frac{dQ_g}{dt} = -\frac{R_g}{L_g} Q_g + \omega_s P_g + \frac{3}{2L_g} (v_{s\beta} v_{g\alpha} - v_{s\alpha} v_{g\beta}) \end{cases} \quad (13)$$

The expression of instantaneous power variations are used as the design basis of the GVM-DPC applied in the GSC.

### III. VM-DPC DESIGN FOR THE BTB CONVERTER

#### A. Rotor Side Controller Design

To achieve an LTI characteristic and introduce the structure of VM-DPC, Voltage Modulated Regulated (VMR) inputs are firstly defined as [27],

$$\begin{cases} U_{rP} = v_{r\alpha} v_{sf\alpha} + v_{r\beta} v_{sf\beta} - \frac{L_r \omega_r}{L_m \omega_s} |v_{sf}|^2 \\ U_{rQ} = v_{r\alpha} v_{sf\beta} - v_{r\beta} v_{sf\alpha} \end{cases} \quad (14)$$

where  $v_{sf}$  is defined as the input of VM-DPC. It can be assumed that  $v_{sf} = v_s$  under ideal voltage condition. However, the harmonics are largely existed in the stator voltage when DFIG is connected with a weak-grid, which will affect the control performance. Therefore, to guarantee the performance of the controller under weak-grid, a band-pass filter (BPF) is generally used to obtain the value of stator voltage. The inputs can be distinguished from the actual stator voltage and expressed by  $v_{sf} = G_f v_s$ . The voltage coupling terms in (10) can be replaced by the VMR inputs defined in (14) and the following is obtained,

$$\begin{cases} U_{rP} = -k_s \nu_{rp} - \underbrace{k_s \omega_r Q_{sf}}_{\text{feed-forward}} + \underbrace{R_r (v_{sf\alpha} i_{r\alpha} + v_{sf\beta} i_{r\beta})}_{\text{compensation term } C_P} \\ U_{rQ} = -k_s \nu_{rq} + \underbrace{k_s \omega_r P_{sf}}_{\text{feed-forward}} + \underbrace{R_r (v_{sf\beta} i_{r\alpha} - v_{sf\alpha} i_{r\beta})}_{\text{compensation term } C_Q} \end{cases} \quad (15)$$

where  $k_s = \frac{2\sigma L_m}{3}$  is the proportional control coefficient,  $P_{sf}$  and  $Q_{sf}$  are the calculated power components based on  $v_{sf}$  and  $i_s$ .  $C_P$  and  $C_Q$  are the feed-forward compensation terms representing the coupling between stator voltage and rotor current. The terms can be neglected in alternative since  $R_r$  is usually relatively small in the DFIG system with a large capacity, where it has little effect on the control performance. Therefore, the rotor side current measurement is not required and the control structure can be further simplified. In order to decouple the active and reactive power loop, the feed-forward compensation terms  $k_s \omega_r Q_{sf}$  and  $k_s \omega_r P_{sf}$  are designed for  $U_{rP}$  and  $U_{rQ}$ , respectively.  $\nu_{rp}$  and  $\nu_{rq}$  are defined as the active and reactive power terms with their differential expression, which can be written as follows,

$$\begin{cases} \nu_{rp} = \frac{dP_{sf}}{dt} - \frac{L_r R_s}{\sigma L_m^2} P_{sf} \\ \nu_{rq} = \frac{dQ_{sf}}{dt} - \frac{L_r R_s}{\sigma L_m^2} Q_{sf} \end{cases} \quad (16)$$

Since there is no coupling between the active and reactive power, various control techniques can be chosen to generate  $\nu_{rp}$  and  $\nu_{rq}$ . In this paper, a simple PI control is selected,

$$\begin{cases} \nu_{rP} = K_{rp}(P_s^* - P_{sf}) + K_{ri} \int (P_s^* - P_{sf}) dt \\ \nu_{rQ} = K_{rp}(Q_s^* - Q_{sf}) + K_{ri} \int (Q_s^* - Q_{sf}) dt \end{cases} \quad (17)$$

where  $P_s^*$  and  $Q_s^*$  are the references of stator active and reactive powers, respectively.  $K_{rp}$ ,  $K_{ri}$  are the control parameters of the rotor side VM-DPC. To prevent the RSC from over-current, the reference value of the stator powers are limited by following relationship,

$$\begin{cases} 0 \leq P_s^* \leq P_{s,\max} \\ Q_{s,\min} \leq Q_s^* \leq Q_{s,\max} \end{cases} \quad (18)$$

where  $P_{s,\max}$  is the maximum active power,  $Q_{s,\min}$  and  $Q_{s,\max}$  are the upper limit and lower limit of the reactive power, respectively. Assuming the  $d$ -axis is in the same direction as the stator flux and the resistance is small enough to be neglected,  $P_{sf}$  and  $Q_{sf}$  can be represented by the rotor current components in  $dq$  reference frame as [35],

$$\begin{cases} P_{sf} = \frac{3}{2} |v_{sf}| \frac{L_m}{L_s} i_{rq} \\ Q_{sf} = \frac{3}{2} (|v_{sf}| \frac{L_m}{L_s} i_{rd} - \frac{|v_{sf}|^2}{\omega_s L_s}) \end{cases} \quad (19)$$

Therefore,  $P_{s,\max}$  can be calculated according to the magnitude of the maximum rotor current  $i_{r,\max}$  and the magnitude of the measured stator voltage  $|v_{sf}|$  as,

$$P_{s,\max} = \frac{3}{2} |v_{sf}| \frac{L_m}{L_s} \underbrace{(0.9 i_{r,\max})}_{i_{rq,\max}} \quad (20)$$

The maximum rotor current component in  $d$  axis is determined by the maximum rotor current  $i_{r,\max}$  and the calculated real-time active power  $P_{sf}$  as follows,

$$i_{rd,\max} = \sqrt{i_{r,\max}^2 - \left(\frac{2L_s P_{sf}}{3L_m |v_{sf}|}\right)^2} \quad (21)$$

Therefore, the limitation of  $Q_s$  can be set as,

$$\begin{cases} Q_{s,\min} = \frac{3}{2} \left(-|v_{sf}| \frac{L_m}{L_s} (i_{rd,\max}) - \frac{|v_{sf}|^2}{\omega_s L_s}\right) \\ Q_{s,\max} = \frac{3}{2} \left(|v_{sf}| \frac{L_m}{L_s} i_{rd,\max} - \frac{|v_{sf}|^2}{\omega_s L_s}\right) \end{cases} \quad (22)$$

Finally, by using the inversion of (14), the controlled rotor side voltage signal in the  $\alpha\beta$  reference frame can be calculated as,

$$\begin{cases} v_{r\alpha} = \frac{v_{sf\alpha} U_{rP} + v_{sf\beta} U_{rQ}}{|v_{sf}|^2} + \frac{L_r \omega_r}{L_m \omega_s} v_{sf\alpha} \\ v_{r\beta} = \frac{v_{sf\beta} U_{rP} - v_{sf\alpha} U_{rQ}}{|v_{sf}|^2} + \frac{L_r \omega_r}{L_m \omega_s} v_{sf\beta} \end{cases} \quad (23)$$

The control inputs  $v_{r\alpha}$  and  $v_{r\beta}$  are transformed into the output switching signals by using the conventional SVPWM technique. The block control diagram of RSC is shown in Fig. 2.

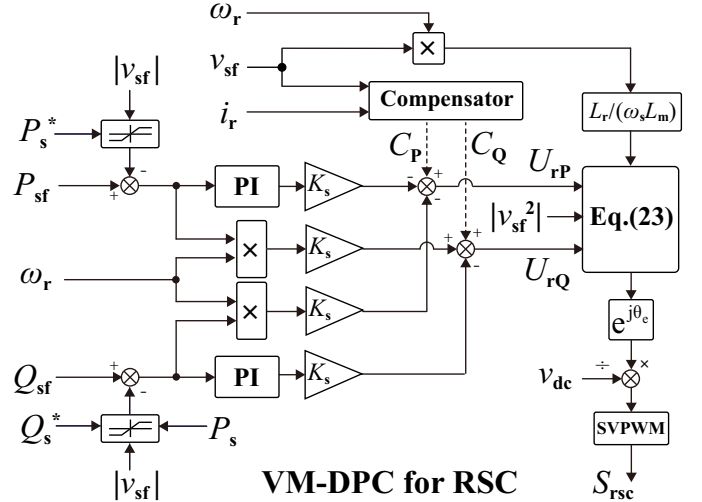


Fig. 2. Control system block diagram of RSC with proposed improved voltage-modulated direct power control (VM-DPC).

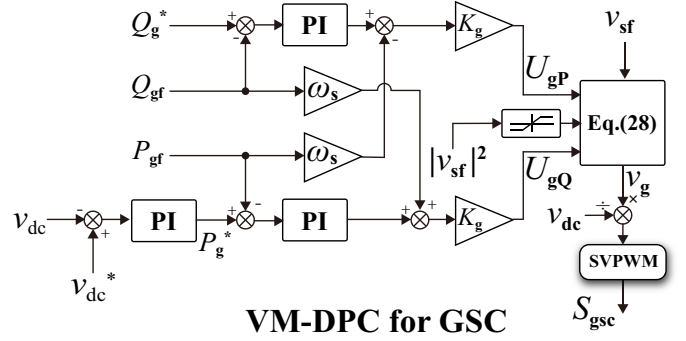


Fig. 3. Control system block diagram of GSC with voltage-modulated direct power control (VM-DPC).

### B. Grid Side Controller Design

The design of the grid side controller is also based on the concept of VM-DPC [24]. To transform the control of GSC into an LTI closed-loop system, the VMR inputs of GSC in (13) are defined as follows,

$$\begin{cases} U_{gP} = v_{sf\alpha} v_{g\alpha} + v_{sf\beta} v_{g\beta} - |v_s|^2 \\ U_{gQ} = v_{sf\beta} v_{g\alpha} - v_{sf\alpha} v_{g\beta} \end{cases} \quad (24)$$

By using the definition of VMR inputs, the coupling terms in (13) are replaced and the VMR inputs can be expressed in terms of the calculated power of GSC, i.e.,  $P_{gf}$  and  $Q_{gf}$  as,

$$\begin{cases} U_{gP} = k_g \omega_s Q_{gf} + k_g \left( \frac{dP_{gf}}{dt} + \frac{R_g}{L_g} P_{gf} \right) \\ U_{gQ} = -k_g \omega_s P_{gf} + k_g \left( \frac{dQ_{gf}}{dt} + \frac{R_g}{L_g} Q_{gf} \right) \end{cases} \quad (25)$$

where  $k_g = \frac{2L_g}{3}$  is the gain coefficient of the grid side controller. Feed-forward control is adopted to cancel the coupling between active and reactive power loops.  $\nu_{gp}$ ,  $\nu_{gq}$  are power

regulation terms of GSC. The PI controller of the inner loop of GSC side VM-DPC is chosen to have the same structure of RSC in (17) as follows,

$$\begin{cases} \nu_{gp} = K_{gp}(P_g^* - P_{gf}) + K_{gi} \int (P_g^* - P_{gf}) dt \\ \nu_{gq} = K_{gp}(Q_g^* - Q_{gf}) + K_{gi} \int (Q_g^* - Q_{gf}) dt \end{cases} \quad (26)$$

where  $K_{gp}$ ,  $K_{gi}$  are the control parameters. The power reference values are represented by  $P_g^*$  and  $Q_g^*$ , respectively. The GSC is considered to operate at the unity power factor operation mode. The active power loop is used to maintain a constant dc-link voltage. The reactive power reference is set to 0, and the outer loop control is designed as,

$$P_g^* = K_{gp,dc}(v_{dc}^* - v_{dc}) + K_{gi,dc} \int (v_{dc}^* - v_{dc}) dt \quad (27)$$

Finally, by using the inversion expression of (24), the voltage regulation signals of GSC can be deduced as,

$$\begin{cases} v_{g\alpha} = \frac{v_{sf\alpha} U_{gP} + v_{sf\beta} U_{gQ}}{|v_{sf}|^2} + v_{sf\alpha} \\ v_{g\beta} = \frac{v_{sf\beta} U_{gP} - v_{sf\alpha} U_{gQ}}{|v_{sf}|^2} + v_{sf\beta} \end{cases} \quad (28)$$

Consequently, the controller of the GSC can be designed and represented by a block control diagram as shown in Fig. 3.

### C. Linear Time Invariant Characteristic

By differentiating the simultaneous equations of (16) and (17) and using the Laplace transform, the dynamics of RSC control can be represented by a second-order transfer function as follows,

$$\begin{cases} \frac{P_s}{P_s^*} = \frac{K_{rp}s + K_{ri}}{s^2 + (-\frac{R_s L_r}{\sigma L_m^2} + K_{rp})s + K_{ri}} \\ \frac{Q_s}{Q_s^*} = \frac{K_{rp}s + K_{ri}}{s^2 + (-\frac{R_s L_r}{\sigma L_m^2} + K_{rp})s + K_{ri}} \end{cases} \quad (29)$$

The closed-loop dynamics of the active and reactive powers are described by the polynomial of transfer functions (29) as follows,

$$s^2 + \underbrace{\left(-\frac{R_s L_r}{\sigma L_m^2} + K_{rp}\right)}_{2\zeta\omega_n} s + \underbrace{K_{ri}}_{\omega_n^2} = 0 \quad (30)$$

in which  $\omega_n$  and  $\zeta$  denote the natural frequency and damping factor, respectively. From the definition of  $\sigma$ , it can easily be deduced that  $\sigma < 0$ , if the PI parameters  $K_{rp}$ ,  $K_{ri}$  are designed as positive values,  $\zeta\omega_n > 0$ , according to linear analysis method, the closed-loop system with the proposed method is globally exponentially stable [27], [34].

## IV. STABILITY ANALYSIS

It should be noted that the grid impedance strongly affects the stability of the grid-connected DFIG system. Consequently, in this section, the stability analysis of the DFIG with the proposed method considering the grid impedance is investigated.

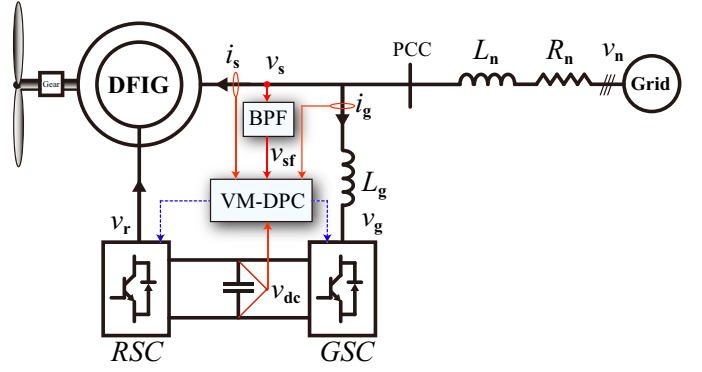


Fig. 4. Scheme of grid-connected DFIG using VM-DPC.

A general diagram of the grid-connected DFIG is shown in Fig. 4, where  $R_n$  and  $L_n$  are the equivalent grid resistance and inductance, respectively.

The short circuit ratio (SCR) is defined by the ratio of the short-circuit capacity of the system at the point of common coupling to the rated power of the equipment, such as  $SCR = S_{sc}/S_n$ , [36], [37], which is calculated as,

$$SCR = \frac{v_{s,nom}^2}{P_{s,nom}|Z_n|} \quad (31)$$

where  $v_{s,nom}$  is the nominal voltage at PCC,  $P_{s,nom} = P_s^*$  is the rated power of DFIG,  $Z_n = R_n + j\omega_s L_n$  denotes the total grid impedance. In this section, an eigenvalue analysis of the RSC with proposed VM-DPC is presented. The stability of the GSC could be referred from [27], where the stability for the GSC with the VM-DPC under a weak-grid condition is investigated. To guarantee the control performance of DFIG using VM-DPC under weak-grid condition, a BPF is used to remove the harmonics and obtain the fundamental component of the measured voltage. The transfer function of the BPF can be expressed as follows,

$$v_{sf} = \frac{2\zeta\omega_s s}{s^2 + \underbrace{2\zeta\omega_s s + \omega_s^2}_{G_f}} v_s \quad (32)$$

where  $v_{s,m}$  is the measured voltage at PCC,  $v_s$  is the output of BPF. In order to consider the dynamics of the BPF, the new state of the system  $\mathbf{x} \in \mathbb{R}^6$  is defined as,  $\mathbf{x} = [i_{s\alpha}, i_{s\beta}, x_\alpha, x_\beta, \dot{x}_\alpha, \dot{x}_\beta]^T$ , where the deviations of the state variable  $[x_\alpha, x_\beta, \dot{x}_\alpha, \dot{x}_\beta]$  can be expressed as,  $[\dot{x}_\alpha, \dot{x}_\beta, \ddot{x}_\alpha, \ddot{x}_\beta] = [v_{sf\alpha}, v_{sf\beta}, v_{sf\alpha}, v_{sf\beta}]$ . The voltage at PCC is the input of the controller,  $\mathbf{v}_s = [v_{s\alpha}, v_{s\beta}]^T \in \mathbb{R}^2$ .

By substituting (15), (23) and (32) into (5), the system

dynamics can be obtained as follows,

$$\begin{bmatrix} \dot{i}_{s\alpha} \\ \dot{i}_{s\beta} \\ \dot{x}_\alpha \\ \dot{x}_\beta \\ \dot{x}_\alpha \\ \dot{x}_\beta \end{bmatrix} = \underbrace{\begin{bmatrix} A_{11} & A_{12} & 0 & 0 & A_{15} & A_{16} \\ A_{21} & A_{22} & 0 & 0 & A_{25} & A_{26} \\ 0 & 0 & 0 & 0 & 1 & 0 \\ 0 & 0 & 0 & 0 & 0 & 1 \\ 0 & 0 & -\omega_s & 0 & -2\zeta\omega_s & 0 \\ 0 & 0 & 0 & -\omega_s & 0 & -2\zeta\omega_s \end{bmatrix}}_A \begin{bmatrix} i_{s\alpha} \\ i_{s\beta} \\ x_\alpha \\ x_\beta \\ \dot{x}_\alpha \\ \dot{x}_\beta \end{bmatrix} + \underbrace{\begin{bmatrix} B_{11} & 0 & 0 & 0 & 2\zeta\omega_s & 0 \\ 0 & B_{22} & 0 & 0 & 0 & 2\zeta\omega_s \end{bmatrix}}_B^T \begin{bmatrix} v_{s\alpha} \\ v_{s\beta} \end{bmatrix} \quad (33)$$

where the matrix parameters are,

$$\begin{aligned} A_{11} &= A_{22} = \frac{1}{\sigma L_m} \left( \frac{R_s L_r}{L_m} - \frac{3k_s K_{rp}}{2} \right) \\ A_{12} &= -A_{21} = \frac{1}{\sigma L_m} \left( -\sigma L_m \omega_e - \frac{3k_s \omega_r}{2} \right) \\ A_{15} &= A_{26} = \frac{1}{\sigma L_m} \left( -\frac{k_s K_{rp} P_s^*}{v_{s,ss}^2} + \frac{L_r \omega_r}{L_m \omega_s} \right) \\ A_{16} &= -A_{25} = \frac{1}{\sigma L_m} \left( -\frac{k_s K_{rp} Q_s^*}{v_{s,ss}^2} \right) \\ B_{11} &= B_{22} = -\frac{1}{\sigma L_m} \left( \frac{L_r \omega_r}{L_m \omega_s} \right) \end{aligned} \quad (34)$$

Note that the term  $v_{sf}^2$  is a dc value and the dynamics is comparatively slow [30]. Therefore, the dynamics of  $v_{sf}^2$  is not considered in (33), i.e.,  $|v_{sf}|^2 = v_{s,ss}^2$ , where  $v_{s,ss}^2$  denotes the squared value of the stator voltage magnitude under steady-state. For the sake of the simplicity, only proportional controller in (17) is considered for the stability analysis in this paper. The dynamic equation consisting of  $v_s$ ,  $i_s$  and the grid voltage  $v_n$  is expressed as follows,

$$\mathbf{v}_s = \mathbf{L}_n \dot{\mathbf{x}} + \mathbf{R}_n \mathbf{x} + \mathbf{I}_2 \mathbf{v}_n \quad (35)$$

where  $\mathbf{L}_n$ ,  $\mathbf{R}_n$ , and  $\mathbf{I}_2$  are the transfer matrices, which can be written as,  $\mathbf{L}_n = \begin{bmatrix} L_n & 0 & 0 & 0 & 0 & 0 \\ 0 & L_n & 0 & 0 & 0 & 0 \end{bmatrix}$ ,  $\mathbf{R}_n = \begin{bmatrix} R_n & 0 & 0 & 0 & 0 & 0 \\ 0 & R_n & 0 & 0 & 0 & 0 \end{bmatrix}$ , and  $\mathbf{I}_2 = \begin{bmatrix} 1 & 0 \\ 0 & 1 \end{bmatrix}$ , respectively.

The grid voltage input of the system is,  $\mathbf{v}_n = [v_{n\alpha}, v_{n\beta}]^T \in \mathbb{R}^2$ . By substituting (35) into (33), the state-space model of the closed-loop system is finally expressed as,

$$\dot{\mathbf{x}} = (\mathbf{I}_6 - \mathbf{B}\mathbf{L}_n)^{-1} (\mathbf{A} + \mathbf{B}\mathbf{R}_n) \mathbf{x} + (\mathbf{I}_6 - \mathbf{B}\mathbf{L}_n)^{-1} \mathbf{B} \mathbf{v}_n \quad (36)$$

where  $\mathbf{I}_6$  is a  $6 \times 6$  identity matrix. In this paper, the rated power of DFIG is considered as,  $P_{s,nom} = 1.5$  MW. The DFIG is set to operate in unity power factor mode. Moreover, the  $X_n/R_n$  ratio is set to 9. The detailed parameters of the system are listed in Table I.

Fig. 5 shows the eigenvalues of the closed-loop system with different BPF damping ratio ( $\zeta = 0.1, \zeta = 0.707$ ). It can be observed that the eigenvalues moves to the imaginary axis when SCR decreases, which indicates that the system tends to become unstable. Moreover, the decreasing of  $\zeta$  greatly changes the distribution of the eigenvalues near imaginary axis, the eigenvalues moves close to the imaginary axis when  $\zeta$

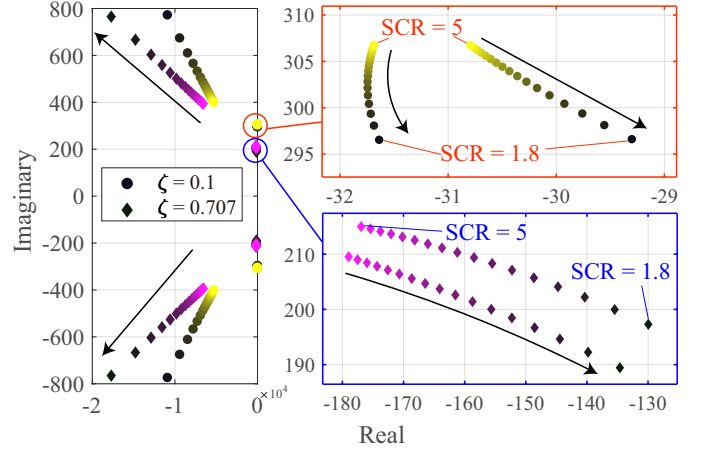


Fig. 5. Eigenvalues of the RSC closed-loop system with different BPF damping ratio when the SCR is changed from 5 to 1.8.

decreases. But in general, the eigenvalues are located in the left half-plane, which indicates that the system with the proposed method remains stable under different grid conditions. It is noted that the maximum active output power of the wind turbine under unity power factor is limited in a weak grid as described in [37],

$$P_{s,max} = \frac{1}{2} \cdot \frac{1}{1 - \frac{1}{1+(X_n/R_n)^2}} \cdot \frac{v_n^2}{|Z_n|} \quad (37)$$

According to (31) and (37), if the maximum active power is considered equal to the rated power, i.e.,  $P_{s,max} = P_{s,nom} = 1.5$  MW, then, the minimum SCR value can be calculated as  $SCR_{min} = 1.61$  when the DFIG is operating in unity power factor mode. If  $SCR < 1.61$ , to keep the system stable, the reactive power needs to be injected into the grid [30], [37]. Another stability analysis method is based on the impedance model [10]. The dynamics of the system can also be further studied based on the distribution of the eigenvalues. Moreover, the impedance stability analysis of the grid-connected DFIG with the proposed VM-DPC will be studied in future research work.

## V. SIMULATION RESULTS

A simulation model of a specific 1.5 MW DFIG system is studied in this paper. The RSC and GSC controllers are implemented by using Matlab/Simulink Simscape Power System. The simulation step is set to  $5 \mu s$ , and the sampling frequency is set to 4 kHz during the simulation process. The DFIG is set to operate at constant rotor speed (1200 rpm,  $\omega_e = 251.3$  rad/s). The GSC is mainly used to support the capacitor to build up a constant dc-link voltage at 1150 V, and ensure the bi-directional power flow through the GSC. The details of the parameters of the simulation system are shown in Table I, which is close to the real DFIG system [38].

### A. Steady-state Performance and Transient Response of Proposed VM-DPC

In this subsection, the steady-state and transient response of the DFIG system using the proposed VM-DPC is compared

TABLE I  
PARAMETERS OF THE STUDIED 2 MW DFIG SYSTEM

Parameter	Symbol	Value	Unit
Rated power	$P_s^*$	1.5	MW
Line-to-line voltage	$v_{s,rms}$	690	V
dc voltage	$v_{dc}^*$	1150	V
dc capacitor	$C_{dc}$	0.08	F
Sampling frequency	$f_a$	4	kHz
Switching frequency	$f_w$	4	kHz
Electrical angular speed	$\omega_e$	251	rad/s
System frequency	$f$	50	Hz
Stator resistance	$R_s$	2.6	m $\Omega$
Stator inductance	$L_s$	2.6	mH
Rotor resistance	$R_r$	2.9	m $\Omega$
Rotor inductance	$L_r$	2.6	mH
Mutual inductance	$L_m$	2.5	mH
GSC resistance	$R_g$	0.2	m $\Omega$
GSC inductance	$L_g$	0.4	mH
Grid resistance	$R_n$	8	m $\Omega$
Grid inductance	$L_n$	0.22	mH
Grid capacitance	$C_n$	50	$\mu$ F
Rotor current limitation	$i_{r,max}$	2.22	kA
Turns ratio	$u$	3	
PI control parameters			
Parameter	Value	Parameter	Value
$K_{rp}$	4000	$K_{ri}$	20000
$K_{gp}$	3750	$K_{gi}$	18750
$K_{gp,dc}$	-1000	$K_{gi,dc}$	-60000
$k_s$	-0.000118	$\zeta$	0.1

with a classical VOC which was originally designed in [39], also a SMC-DPC proposed in [22], and C-DPC [17]. The bandwidth of VM-DPC is designed to 628 Hz, which is calculated according to (29), and the bandwidth of the inner current loop of VOC is set to 644 Hz. It is noted that the BPF is not used in this comparative study. The stator active power reference and reactive power reference are set to 1.5 MW and 0 MVar respectively, at the beginning of the simulation. Two reference steps  $\Delta P_s^* = -0.75$  MW and  $\Delta Q_s^* = 0.75$  MVar are set at 3 s and 3.2 s separately to test the transient dynamics along with the coupling between the active and reactive power control loops. Fig. 6 shows the power and electromagnetic torque performance of four different control strategies at the power reference step. The results show the three DPC methods have faster transient response than the classical VOC with no overshoot at either the active and reactive power reference steps. The SMC-DPC induces power pulsations. The enlarged window of the reactive power reference step also shows the VM-DPC has the smallest interaction between the variations of active and reactive power among three DPC techniques. The differences between the steady-state electromagnetic torque performance of the four studied methods are not apparent.

Fig. 7 shows an enlarged window of stator current performance at the active power step between 2.98 s to 3.02 s. The results show that the steady-state performance of the VOC, C-DPC, and VM-DPC are satisfied. A comparison of the total harmonic distortion (THD) of the stator current with four different control techniques are shown in Fig. 8. The VOC, C-DPC, and the proposed improved VM-DPC achieve a stator current THD of about 1.4%, which indicates they have the same level of steady-state performance. The

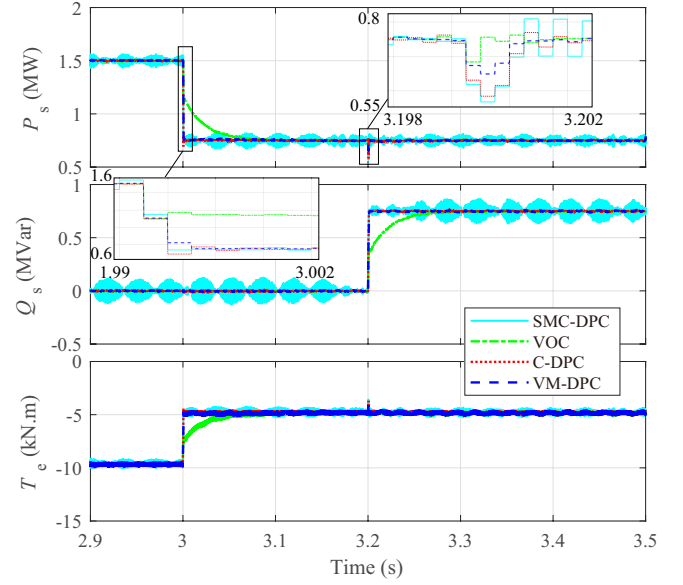


Fig. 6. Comparison of the transient performance of the active, reactive power and electromagnetic torque  $T_e$  at power reference step among the SMC-DPC, the classical VOC, the C-DPC and the proposed improved VM-DPC.

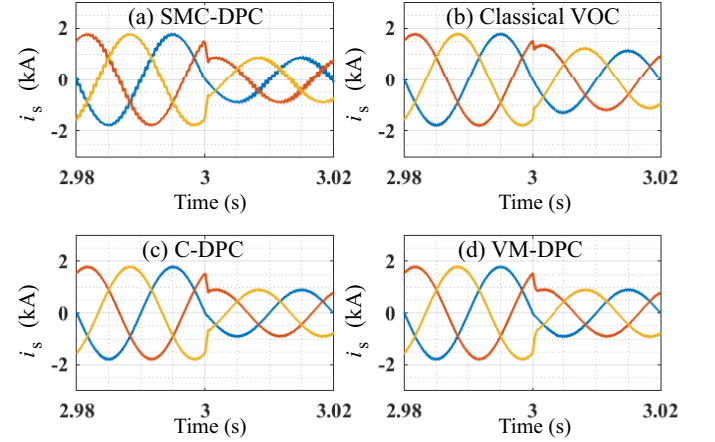


Fig. 7. Comparison of stator current response at transient power reference step among various control strategies.

SMC-DPC generates more stator current harmonics with a frequency around 2000 Hz. A comparison of the rotor current is presented in Fig. 9. It can be observed that the VM-DPC also manifests an excellent rotor current performance.

Detailed information about the comparison between four different methods is given in Table. II. The convergence time is considered to be the time spend of actual power value enter the 5% error band. The three DPC control techniques have approximately the same convergence time of about 1 ms, which is much lower than VOC. The THD of the VOC, C-DPC, and VM-DPC are around 1.4%, which indicate the three different methods have the same level of steady-state performance. The THD of SMC-DPC is 3.37%, which is the highest among the four control techniques. The decoupling effect between active and reactive power loops can be represented by the deviation rate of the active power when the reactive power reference value steps. The decoupling effect of four control strategies



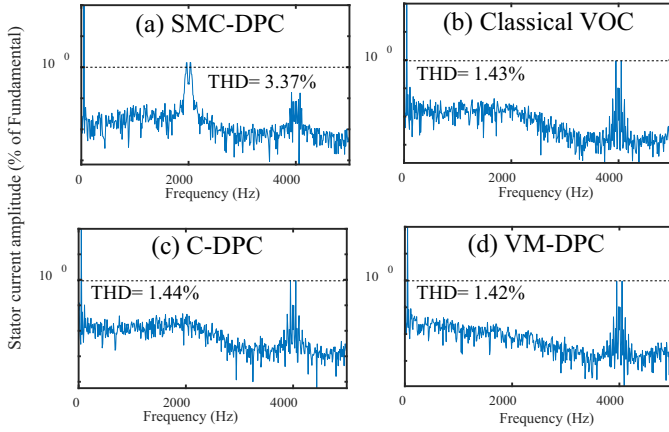


Fig. 8. Harmonic spectrum of stator output current in steady-state ( $P_s = 1.5$  MW,  $Q_s = 0$  Mvar).

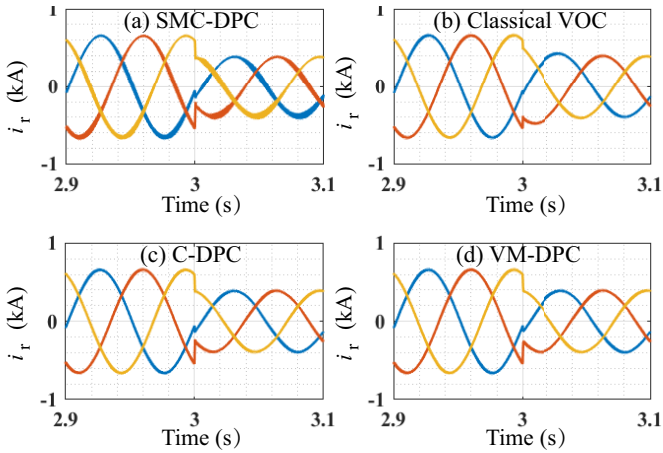


Fig. 9. Comparison of rotor current response at transient power reference step among various control strategies.

are compared as shown in the enlarged window of Fig. 6 and Table II. The results show that the VM-DPC has the second smallest deviation of about 6.7%, after the VOC method. Therefore, the effectiveness of the feed-forward compensation terms in the proposed VM-DPC is verified. Please note that the SMC-DPC and VM-DPC are directly built in the  $\alpha\beta$  reference frame. Therefore, the structures of SMC-DPC and VM-DPC are simpler than that of VOC and C-DPC, which requires a voltage phase signal for coordinate transformation. Consequently, the results show that the VM-DPC is applicable in the high power DFIG system.

TABLE II  
TRANSIENT AND STEADY-STATE PERFORMANCE WITH DIFFERENT CONTROL TECHNIQUES

Method	Performance			Structure PLL and Coordinates
	Convergence time	THD	Deviation rate	
SMC-DPC	$\approx 1$ ms	3.37%	12.0%	Not required ( $\alpha\beta$ )
VOC	$\approx 100$ ms	1.44%	4.2%	Required ( $dq$ )
C-DPC	$\approx 1$ ms	1.43%	10.5%	Virtual PLL ( $dq$ )
VM-DPC	$\approx 1$ ms	1.42%	6.7%	Not required ( $\alpha\beta$ )

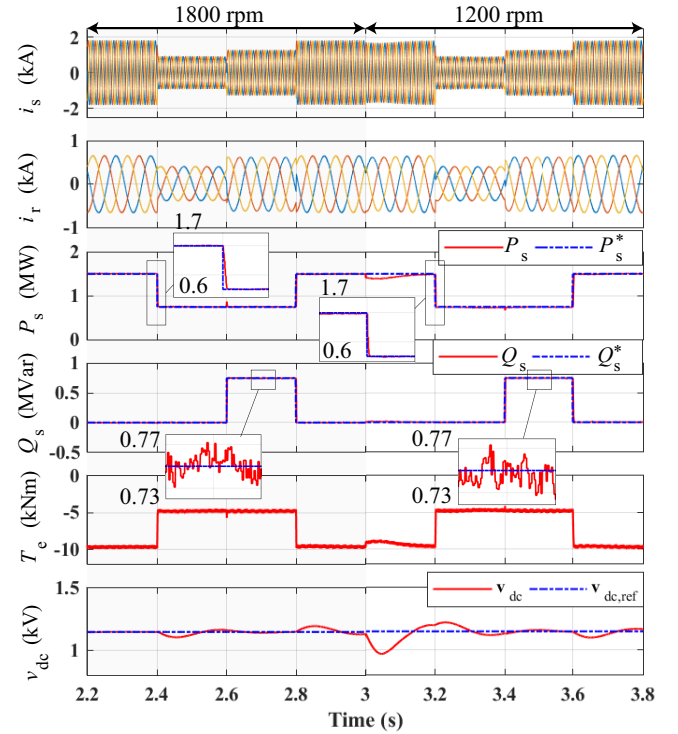


Fig. 10. Performance of DFIG using the proposed VM-DPC under different rotor speeds.

### B. Performance of VM-DPC Under Different Rotor Speed

In order to test the control performance of the proposed VM-DPC at different rotor speeds, a simulation is carried out as shown in Fig. 10. The rotor speed changes from 1800 rpm ( $\omega_e = 377$  rad/s) to 1200 rpm ( $\omega_e = 235.6$  rad/s) at 3 s. In order to compare the system performance at two different rotor speed. The steps of active power reference value  $\Delta P_s^* = -0.75$  MW are given at 2.4 s and 3.2 s, respectively. Also, the steps of reactive power reference value  $\Delta Q_s^* = 0.75$  MVar are set at 2.6 s and 3.4 s, respectively.

From the enlarged window of active and reactive power dynamics, it is observed that both transient response and steady-state performance of the proposed method are not affected by the variation of rotor speed. The transient dynamics of dc voltage at 3s is caused by the direction change of the power flow on the GSC side due to the variation of rotor speed.

### C. Robustness to Parameter Variation

Since there are many compensation terms in the proposed VM-DPC strategy and the values are closely related to the parameters of the generator, such as rotor resistance and mutual inductance, these parameters are difficult to be measured and may change over time because of the aging of devices and temperature variation. Therefore, to check the robustness against the parameters mismatch of the proposed VM-DPC, the mutual inductance  $L_m$  and rotor resistance  $R_r$  of the control parameters are increased by 30% at 3 s. The power reference steps are set as the same to Fig. 10. The simulation

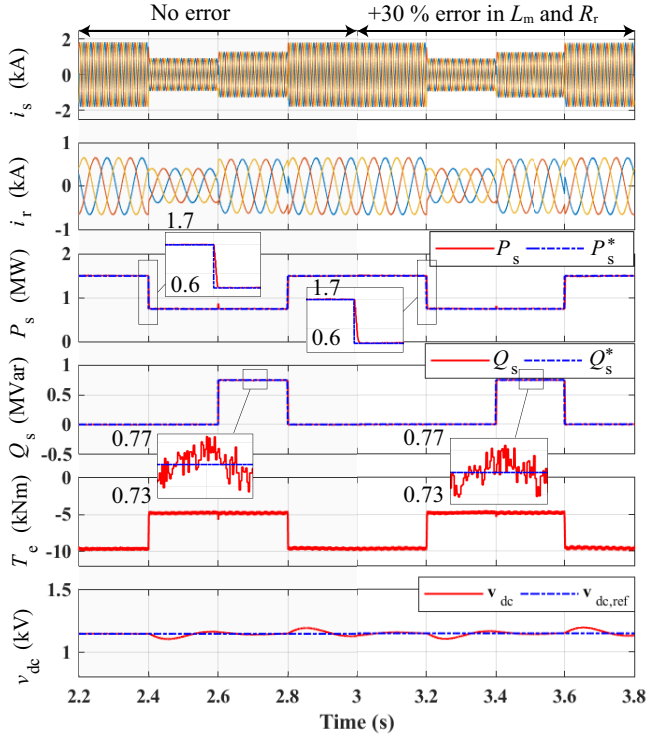


Fig. 11. Performance of DFIG using the proposed VM-DPC with  $+30\%L_m$  and  $+30\%R_r$  errors.

results presented in Fig. 11 confirm the good steady-state operation and fast transient response of the VM-DPC under  $+30\%$  parameter variation. Consequently, it is concluded that the proposed VM-DPC has a robust property to parameter variations.

#### D. Performance Under Harmonic Distorted and grid fault Conditions

In this subsection, the performance of the proposed method under distorted and unbalanced grid condition is tested. Since the accuracy of the control design depends on a non-distorted grid condition as presented in (8) and (9). The performance of DFIG using the VM-DPC under different voltage conditions is shown in Fig. 12, where the enlarged windows present the detailed waveforms of the stator voltage and currents. At first, DFIG is operated at a distorted grid, where the THD of the grid voltage is 3.3%. The results indicate the DFIG can maintain a steady-state performance with the current THD = 3.3%. At 3 s, a Phase-A voltage drop (0.9 p.u.) is set, and it excites the unbalanced components of stator currents and increase the current THD from 3.3% to 3.9%. Afterwards, at 3.2 s, the Phase-A voltage drops to 0.8 p.u and the phase-B, phase-C stator voltage drop to 0.9 p.u. It can be seen that the voltage dip introduces power ripples and increases the stator current harmonics from 3.9% to 4.2%. Moreover, from 3.2 s, the maximum active power ripples increase to 0.13 p.u, and the maximum electromagnetic torque ripples increase to 0.46 p.u. It can be seen that such unbalanced or distorted grid

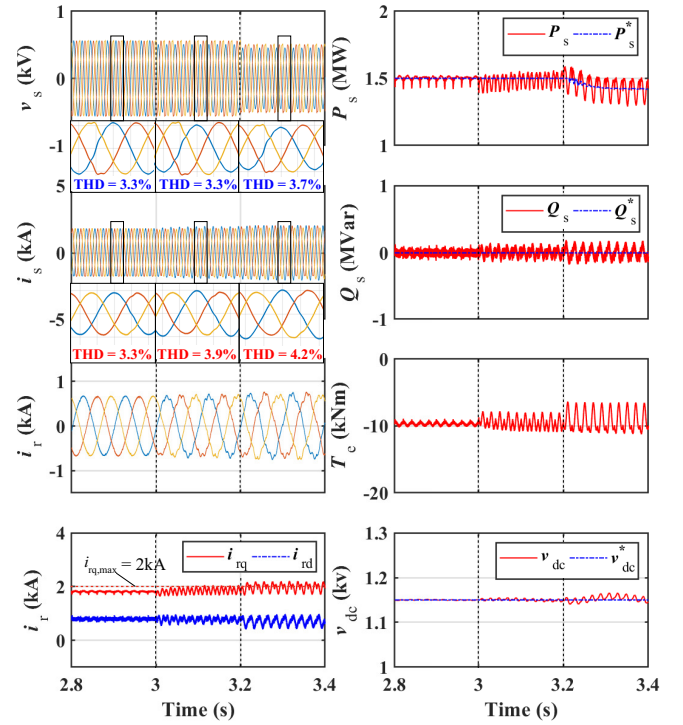


Fig. 12. Performance of DFIG using the proposed VM-DPC under harmonic distorted and unbalanced voltage dip conditions.

will slightly affect the performance, but the current THD is still below the range of the requirement [40]. In addition, it should be noted that the active power reference value drops to 1.3 MW after 3.2 s, which is caused by the limitation of the maximum rotor side currents. Furthermore, The performance of the proposed VM-DPC under severe voltage conditions is compared with conventional VOC as presented in Fig. 13, where a severe three-phase voltage drop (0.1 p.u.) is set at 2 s. It can be observed that the stator and rotor side currents are deformed, which are caused by the transient stator flux introduced by the grid fault. The proposed VM-DPC has a similar transient performance to the conventional VOC. The decaying process of the transient stator flux can be accelerated by the DPC based demagnetizing method proposed in [41]. The active power reference value  $P_s^*$  is decreased to 0.16 MW when the fault occurs, which verifies the proposed over-current protection algorithm.

Consequently, it is concluded that the DFIG controlled by the VM-DPC can operate under slight grid faults. The performance VM-DPC will be further improved to handle the severe unbalanced voltage drop conditions in future research works.

#### E. Performance Under Different Grid Parameters

The performance of DFIG with the proposed VM-DPC under the grid condition with different SCR values are tested as shown in Fig. 14. The damping ratio of the BPF is set to  $\zeta = 0.1$ . The DFIG is connected with a stiff grid from the beginning of the simulation. At 3 s, an inductance  $L_n = 0.22$  mH and a resistance  $R_n = 8$  m $\Omega$  are added in series into the

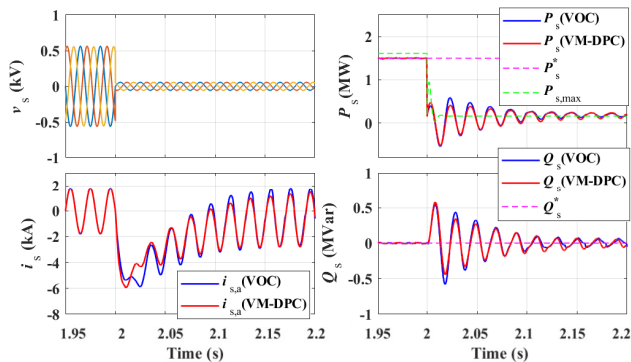


Fig. 13. Comparison of the performance under severe voltage drop conditions (0.1 p.u.) between conventional VOC and the proposed VM-DPC.

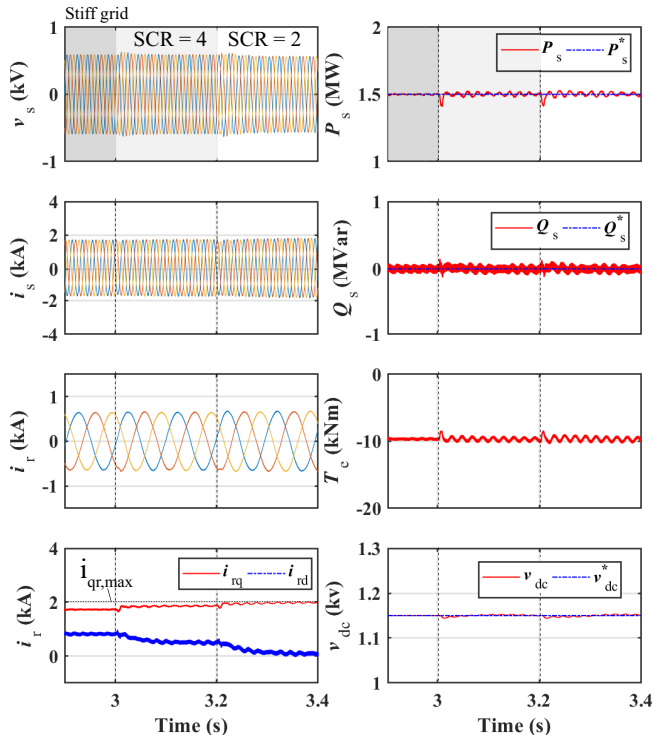


Fig. 14. Performance of DFIG using the proposed method in different grid impedance conditions.

grid, which make  $SCR = 4$ . Then, the  $SCR$  value is further decreased from 4 to 2 at 3.5 s. It is observed from Fig. 14 that the proposed VM-DPC controlled DFIG is stable and works well in different grid conditions. The results show consistency with the conclusions drawn in Section IV.

#### F. Discussion

In this section, the performance of the DFIG using the proposed VM-DPC is tested in the different conditions. A comparison of the stator current THD manifests the proposed VM-DPC has the same level of steady-state performance as the VOC under a similar control bandwidth. Besides, the VM-DPC also features a fast transient response under different rotor speeds. The simulation results show also that the DFIG using the proposed method works well in the weak-grid

TABLE III  
PARAMETERS OF EXPERIMENTAL DFIG PROTOTYPE

Parameter	Symbol	Value	Unit
Rated power	$P_s^*$	7.5	kW
Line-to-line voltage	$v_{s,rms}$	380	V
dc voltage	$v_{dc}^*$	650	V
dc capacitor	$C_{dc}$	600	$\mu\text{F}$
System frequency	$f$	50	Hz
Sampling frequency	$f_a$	10	kHz
Switching frequency	$f_w$	10	kHz
Stator resistance	$R_s$	0.44	$\Omega$
Stator inductance	$L_s$	82.7	mH
Rotor resistance	$R_r$	0.64	$\Omega$
Rotor inductance	$L_r$	84.6	mH
Mutual inductance	$L_m$	79.3	mH
GSC inductance	$L_g$	18	mH
Control Parameters			
Parameter	Value	Parameter	Value
$K_{rp}$	20000	$K_{ri}$	100000
$K_{gp}$	4500	$K_{gi}$	20000
$K_{gp,dc}$	-10	$K_{gi,dc}$	-1000
$k_s$	-0.0059		

conditions. Moreover, the proposed method features robustness against the generator parameter variations and the non-ideal voltage conditions. However, the performance of the DFIG using the proposed VM-DPC is susceptible to the unbalanced voltage dip conditions. Such grid faults will increase the stator current harmonics and cause ripples in the powers and the electromagnetic torque. It is noted that for the most of the DPC methods, the unbalanced voltage will introduce the negative-sequence components and third order harmonics in the stator currents, which affect the steady-state performance [12], [15], [23]. The problem can be solved by adding the compensation terms in the control loops as discussed in [15], [23]. However, these methods require further discussions about the relation of negative-sequence electromagnetic components of the DFIG under unbalanced voltage conditions, which are out of the scope of the paper. A voltage dip fault will result in natural flux in the stator side and cause a rotor side over-voltage [41]. In the extreme cases, such a phenomenon may even destroy the converter if not properly protected. The Low-Voltage-Ride-Through (LVRT) capability of the DFIG using DPC can be improved by using a stator natural flux cancellation technique [41]. The improvement of LVRT capability of the proposed method will be research in the future.

#### VI. EXPERIMENTAL VALIDATION

In order to verify the effectiveness of the proposed VM-DPC, a prototype experimental setup, as shown in Fig. 15 is used. The experimental setup is composed of a 7.5 kW DFIG, which is connected with a speed-controlled induction machine operating at 1200 rpm. The capacity of the GSC and RSC is 5 kW and 7.5 kW, respectively. The GSC is mainly used to support the capacitor to operate at a constant dc-link voltage at 650 V. The switching frequency of two converters is both 10 kHz as well as sampling frequency. The control schemes are implemented in the DS1006 dSPACE system. The switching signal of GSC and RSC are generated by using the DS5101 digital waveform output board. The

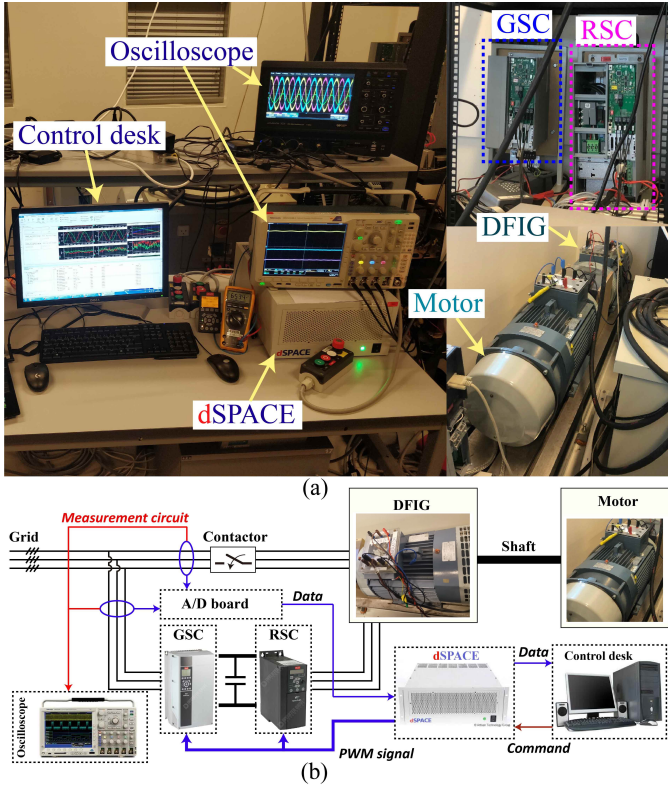


Fig. 15. Experimental setup to test DFIG with the proposed VM-DPC and VOC using dSPACE. (a) Photo. (b) Generic used system architecture.

voltage of the experimental platform is produced by the grid, which is not ideal. The THD of the grid voltage is 1.66%, which mainly contains 1% 5th, 0.8% 7th harmonics due to the background distortion. The harmonics of grid voltage affects the control performance and it influences the output stator current since no harmonic suppression is considered in the controllers. The parameters of the experimental prototype are shown in Table III. A prototype controlled by the traditional VOC method as discussed in [39] is tested for comparison. The active power control loop has an open-loop structure, which is directly controlled by the current on the  $d$ -axis. The reactive power control loop is a traditional PI feed-forward control, having an outer-power loop, and an inner-current loop structure.

#### A. Comparison of Steady-State and Transient Performance

Fig. 16 shows the transient response and steady-state performance of the DFIG system using the VOC. Two reference steps  $\Delta P_s^* = 0.23 \text{ kW}$  and  $\Delta Q_s^* = 2 \text{ kVar}$  are set separately to test the transient dynamics of the controllers. Since the active power control is an open-loop structure, the speed of active power response is faster than the reactive power response. The reactive power control loop of the tested VOC is a conventional method, where the time response of reactive power is about 60 ms. Fig. 17 describes the transient performance of the system using the proposed VM-DPC at power reference steps. It is observed that the convergence time is less than 5 ms, and there is nearly no overshoot of the active and reactive power response, which is in accordance with the simulation result.

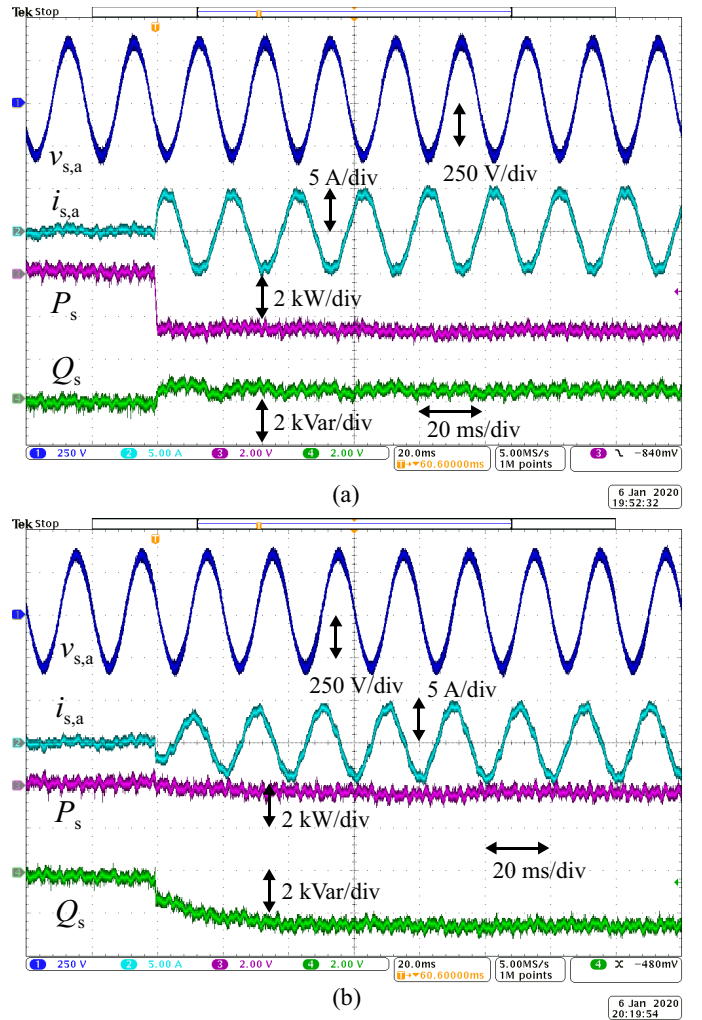


Fig. 16. Measured performance of DFIG system with VOC during active and reactive reference power steps. (a) Active power reference change from 0 kW to 2.3 kW. (b) Reactive power reference change from 0 kVar to 2.0 kVar. Blue:  $v_{s,a}$  [250 V/div]; sky blue:  $i_{s,a}$  [5 A/div], bubble pink  $P_s$  [2 kW/div], and green:  $Q_s$  [2 kVar/div].

Consequently, it can be concluded that the PI feed-forward structure successfully compensates the coupling between the two loops.

The stator current performance and THD analysis while using the VOC and the proposed VM-DPC are shown in Fig. 18 and Fig. 19, respectively. It is observed that the THD of the stator current when using the VOC is 6.03%. The current THD when using the proposed improved VM-DPC is 4.87%, which is less than VOC. Therefore, it is concluded that the proposed strategy works well in an actual grid condition.

#### B. Robustness to Parameter Mismatch

The robustness of DFIG using the proposed VM-DPC is also tested in the experimental prototype as shown in Fig. 20. It can be seen that the system maintains a fast power transient response and satisfied steady-state performance with  $+30\%L_m$  and  $+30\%R_r$  parameter error. The differences between Fig. 20 and Fig. 17 are barely noticeable.

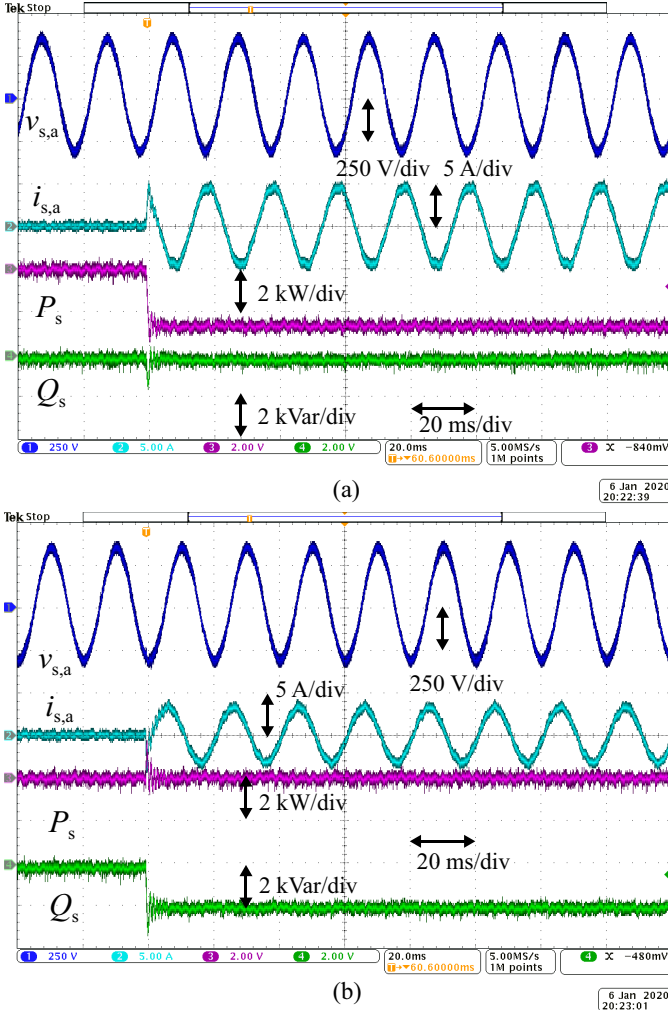


Fig. 17. Measured performance of DFIG system with proposed improved VM-DPC during active and reactive reference power steps. (a) Active power reference change from 0 kW to 2.3 kW. (b) Reactive power reference change from 0 kVar to 2.0 kVar. Blue:  $v_{s,a}$  [250 V/div]; sky blue:  $i_{s,a}$  [5 A/div], bubble pink  $P_s$  [2 kW/div], and green:  $Q_s$  [2 kVar/div].

## VII. CONCLUSION

In this paper, a robust yet straightforward improved VM-DPC control algorithm for the BTB converter of the DFIG system was presented. The proposed method is consisting of a simple feed-forward and feed-back structure without PLL and Park transformation, which reduces the computational burden and simplifies the structure. The proposed method guarantees global exponential stability. Moreover, the stability of the proposed VM-DPC under weak-grid condition is also verified by using eigenvalue analysis and simulations. The proposed VM-DPC is compared with three different control strategies. The results show the proposed VM-DPC can achieve satisfactory steady-state performance with a stator current THD = 1.42%, which is the same level as the conventional VOC. Furthermore, the proposed method also provides a fast transient response with a power convergence time about 1 ms. The robustness of the proposed VM-DPC against parameter variations and slight grid faults is also verified. Finally, the experimental results indicate that the proposed VM-DPC is capable of operating

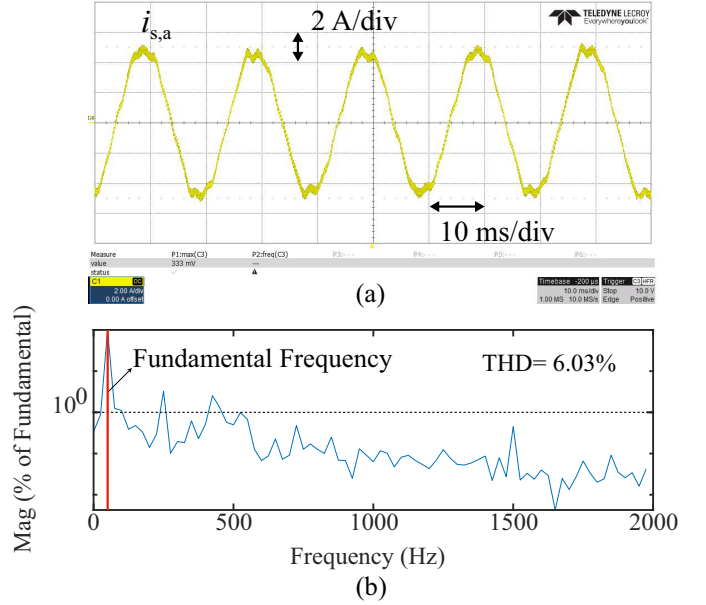


Fig. 18. Stator current spectrum of system using VOC at steady-state  $P_s = 2.3$  kW,  $Q_s = 0$  kVar. (a) Stator current waveform. (b) Stator current harmonic spectrum.

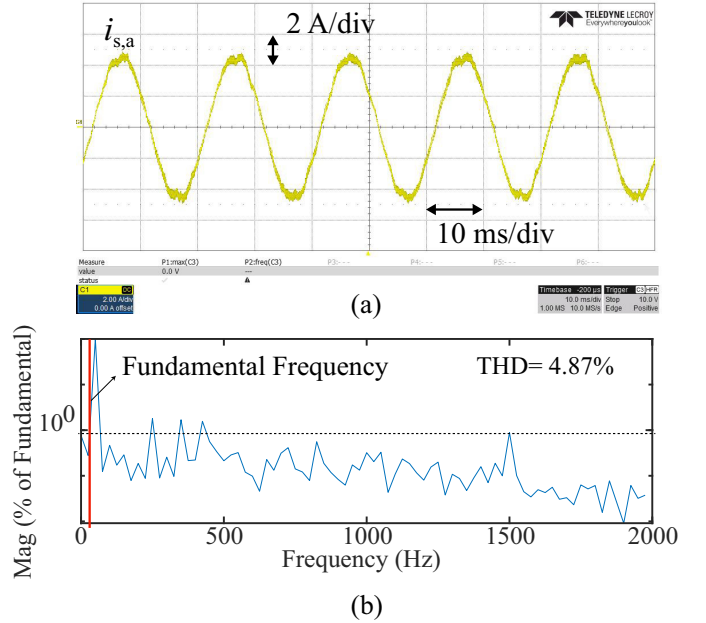


Fig. 19. Stator current spectrum of system using the proposed improved VM-DPC in steady-state  $P_s = 2.3$  kW,  $Q_s = 0$  kVar. (a) Stator current waveform. (b) Stator current harmonic spectrum.

in an actual grid condition, in which the THD of the stator current is 4.87%.

Our future perspective about the proposed control algorithm includes an investigation on its frequency impedance characteristics, advanced LVRT strategy, and a further improvement under unbalanced voltage conditions.

## APPENDIX

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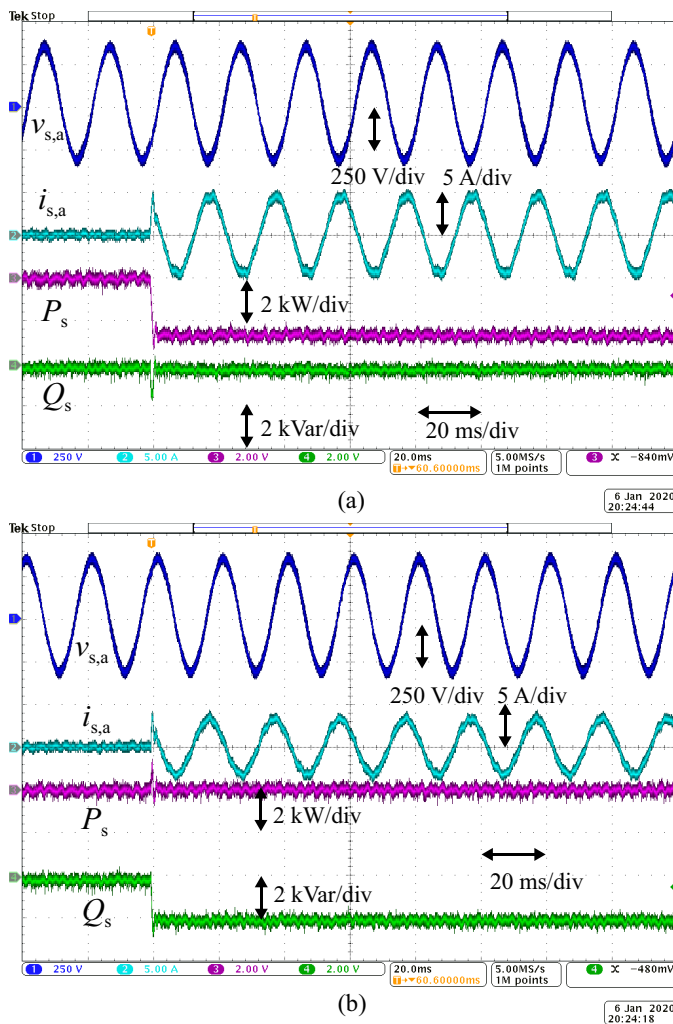


Fig. 20. Measured performance of DFIG system using proposed VM-DPC with  $+30\%L_m$  and  $+30\%R_r$  parameters mismatch during active and reactive reference power steps. (a) Active power reference change from 0 kW to 2.3 kW. (b) Reactive power reference change from 0 kVar to 2.0 kVar. Blue:  $v_{s,a}$  [250 V/div]; sky blue:  $i_{s,a}$  [5 A/div], bubble pink  $P_s$  [2 kW/div], and green:  $Q_s$  [2 kVar/div].

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