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Confocal sputtering of conformal α - β phase W films on etched Al features

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The authors report on thin-film processing improvements in the fabrication of superconducting quasiparticle-trap-assisted electrothermal-feedback transition-edge sensors used in the design of cryogenic dark matter search detectors. The work was performed as part of a detector upgrade project that included optimization of a new confocal sputtering system and development of etch recipes compatible with patterning 40 nm-thick, α - β mixed-phase W films deposited on 300–600 nm-thick, patterned Al. The authors found that their standard exothermic Al wet etch recipes provided inadequate W/Al interfaces and led to poor device performance. The authors developed a modified Al wet-etch recipe that effectively mitigates geometrical step-coverage limitations while maintaining their existing device design. Data presented here include scanning electron microscope and focused ion beam images of films and device interfaces obtained with the new Al etch method. The authors also introduce a method for quantitatively measuring the energy collection efficiency through these interfaces. © 2014 American Vacuum Society. [<http://dx.doi.org/10.1116/1.4904422>]

I. INTRODUCTION

The use of superconducting sensors for a wide range of physics applications has grown dramatically in the past two decades.^{1–6} For the cryogenic dark matter search (CDMS), thousands of interleaved ionization and phonon collection electrodes are photolithographically patterned on both sides of kilogram-scale, high-purity germanium substrates. Detector performance is well understood and dozens of full size detectors have been deployed in physics experiments operated at ~ 50 mK.^{7–10} During a recent fabrication facility upgrade at Stanford, the Balzers barrel-style sputtering system with 5×10 in. targets used for many years to deposit W, Al, and poly-Si for this work was replaced with a sputtering machine of confocal design using 4 in. diameter targets. Guided by previous rounds of process refinements on the original Balzers system at Stanford/SLAC (Ref. 11) and during the commissioning of the Segi system at Texas A&M University fabrication facility,¹² a series of test devices were fabricated to evaluate physics performance metrics of the films produced using the new machine. Measurements on as-deposited films of Al, W, and poly-Si included film-stress, room temperature electrical resistivity, thin-film superconducting T_c , and resistivity ratio, $R_{300K}/R_{4.2K}$. Deposited film thickness uniformity was better than 5% for W and better than 3% for Al and poly-Si over the 10 in. diameter, four-substrate carrier.

More advanced diagnostic measurements were made on fully patterned W/Al quasiparticle-trap-assisted electrothermal-feedback transition-edge sensor (QET) test devices. Energy transport within and between the Al and W sputtered films was measured at ~ 35 mK in a dilution refrigerator using a

collimated ^{55}Fe -NaCl x-ray fluorescence source mounted immediately above the QET.¹³ This work showed that previously established Al etch recipes used for CDMS detector (and QET) production were not optimal for the films deposited in the new machine. In particular, although film uniformity, resistivity, stress, and superconducting T_c met specification values, the chamber conditions required to deposit these films in our required device geometries (40 nm-thick W sputtered over 300 nm-thick, patterned Al) yielded inadequate W-on-Al sidewall coverage for devices beyond the central ~ 4 in. diameter region of the substrate carrier. Below, we report on the subsequent development of a new Al wet-etch recipe that consistently provides smooth, high-quality sidewalls that couple well to 40 nm-thick, superconducting W films sputtered over steep Al topography.

II. FABRICATION PROCESSES

A. QET test device fabrication

Fabrication of CDMS and corresponding QET test devices requires two thin-film deposition sequences (Si-Al-W trilayer; single W film) interspersed with standard micron-scale photolithography.¹¹ For the QET devices described in this paper, standard 100 mm-diameter, $\langle 100 \rangle$ silicon test wafers were diffusion-cleaned using traditional methods ($\text{H}_2\text{O}:\text{H}_2\text{O}_2:\text{NH}_4\text{OH}$, HF, and $\text{H}_2\text{O}:\text{H}_2\text{O}_2:\text{HCl}$) before being loaded into a four-wafer substrate carrier with 10 in. diameter. The wafer carrier was inserted via load-lock into a custom AJA International ATC-2200 confocal DC/RF magnetron sputtering system configured with four, 4 in. diameter targets in a sputter-up geometry. The chamber and substrate carrier were preconditioned *in situ* by sputtering $\sim 0.1 \mu\text{m}$ Al on all accessible surfaces prior to substrate loading.

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Depositions were done in 99.999% Ar gas after reaching a chamber base pressure of 8.0×10^{-8} Torr.

Clean wafers were lightly RF-etched *in situ* for 15 min at 100 W substrate power in 3 mT Ar. A 40 nm-thick layer of 99.999% pure ($>1 \Omega \text{ cm}$) Si was deposited at 5.5 mT Ar and 400 W target power (480 s with shutter open). The as-deposited poly-Si film stress was typically -100 MPa , as determined by comparing wafer curvature before and after depositions on test wafers using a Flexus 2320 stress gauge. Wafer curvature is determined by measuring the angle of deflection of a laser beam off the surface of the substrate. The Si deposition was followed immediately by a 300 nm-thick deposition of 99.9998% pure Al performed at 2 mT Ar using 700 W target power (1007 s). Finally, a 99.96% pure, 30 nm-thick W layer was deposited at 3 mT Ar and 400 W target power (226 s) with -150 V DC bias on the substrate carrier. The W layer inhibits oxidation of the Al film beneath it to ensure good Al/W film connectivity in the finished devices. The wafers were then removed from the sputtering chamber via load-lock.

Using S1813 photoresist and an EVG620 aligner for patterning, the devices were etched in 30% H_2O_2 at 25°C for 300 s to remove exposed portions of the W film, followed by an Al etch that differed in detail from run to run in a controlled way (this study). As discussed more below, the best Al etch recipe for kg scale devices was found to consist of a 300 s dip, with agitation, in KMG 80:3:15 NP at 25°C , followed by a series of three, 10 s dips in the same 80:3:15 NP bath interspersed with 20 s deionized (DI) water quenching dips (and no dry cycle). The three short follow-up etch/quench cycles were found to be critically important: they smoothed the Al film sidewalls exposed during the initial Al etch. The wafers were then subjected to an additional 850 s in 30% H_2O_2 (i.e., W etch) to compensate for undercutting that occurred during the Al etch. After resist stripping, the wafers were reloaded into the AJA sputtering system via load lock and the chamber was again evacuated to 8.0×10^{-8} Torr. The patterned wafers were RF-etched for 15 min in 3 mT Ar at 100 W before a final 40 nm-thick W layer was deposited at 3 mT Ar and 400 W target power (300 s). The second W layer was then patterned and etched for 675 s in 30% H_2O_2 . Dies were cut and the quality of the 40 nm-thick W film growth over the much thicker Al layer (300 or 600 nm depending on sample set) was inspected with a Hitachi S-4800 SEM. For completeness, some samples were also inspected using FIB imaging techniques. After visual confirmation that the W was continuous and uniform, the devices were tested for physics performance by Yen *et al.* with an ^{55}Fe -NaCl fluorescence source (Cl K_α at 2.62 keV) in a Kelvinox-15 dilution refrigerator.¹³

B. Al etchants

Three commercially available premixed Al etchants were tested during this study: Cyantek Al-11 (later decommissioned from our shared processing lab), KMG 16:1:1:2 NP, and KMG 80:3:15 NP. Their properties are summarized in Table I. The phosphoric acid contained in the etchants is responsible for dissolving the few nanometer of native oxide that grows on the Al film surface and the nitric acid oxidizes the bulk Al, which is then removed by the phosphoric acid.^{14,15} Acetic acid is used for wetting and buffering.¹⁵ The Al wet-etching process is highly exothermic and this is evident by the increase in bath temperature during etches. Our observations agree with Williams and Muller¹⁴ that agitation aids etch-rate uniformity across the wafer.

As expected, the Al etch rate for our devices was strongly dependent on temperature and was roughly six times faster at 40°C compared to 25°C for the etchants tested. Al wet etches are generally isotropic; so, undercutting of the photoresist mask is fairly common. It is in this etched “pocket” that localized heating occurs, accelerating the Al etchant, and causing further undercutting of the mask.¹⁵ Figure 1 shows this effect in one of our devices where we show the SEM of a device made with Cyantek Al-11 without compensation for the undercut. A room temperature Al etch recipe with repeated DI water quenches used by our CDMS collaborators at TAMU was shown to minimize the undercut of W by the Al, resulting in successful step coverage by the final W film.¹²

It is well known that H_2 produced during Al wet etching can reduce etch homogeneity.¹⁵ We believe the rough sidewall texture observed in our devices after continuous or long Al etches are caused primarily by H_2 bubbles. The surfactant added to some Al etchants helps mitigate this problem by improving the surface wetting of the Al and reducing surface tension.¹⁶ We have shown that by periodically interrupting the Al etch, and even interspersing the etch with a series of DI rinses effectively dissipates the H_2 bubbles and cools the wafer, resulting in vastly reduced sidewall texture. Results of three different processes using the same commercial etchant (KMG 16:1:1:2 NP) but different time profiles are shown in Fig. 2. It is clear that the etched Al sidewalls are significantly smoother when the etch is performed as a series of dips rather than as an extended single dip.

C. Growth of sputtered W on etched Al surfaces

We have found that the etched Al sidewalls of our devices must be relatively free of rough texture for 40 nm-thick sputtered W film to attach. Smoothest sidewalls are achieved with short (10–30 s depending on etchant) dips in etchant

TABLE I. Properties of premixed Al etchants.

Etchant	H_3PO_4 (%)	CH_3COOH (%)	HNO_3 (%)	Surfactant	Etch rate ($\text{\AA}/\text{s}$)	Etching interval (DI)
Cyantek Al-11	72	3	3	No	8 at 25°C	30 s (30 s)
KMG 16:1:1:2 NP	70–80	1–5	1–5	Yes	9 at 23°C	10 s (20 s)
KMG 80:3:15 NP	60–80	5–15	1–5	Yes	10 at 24°C	20 s (20 s)

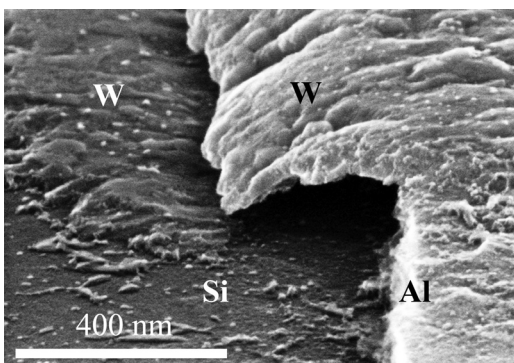


Fig. 1. W overhang of Al underetch in a QET after second W deposition. The horizontal W overhang produced during the first etch sequence gets bent toward the wafer's surface during the second W deposition.

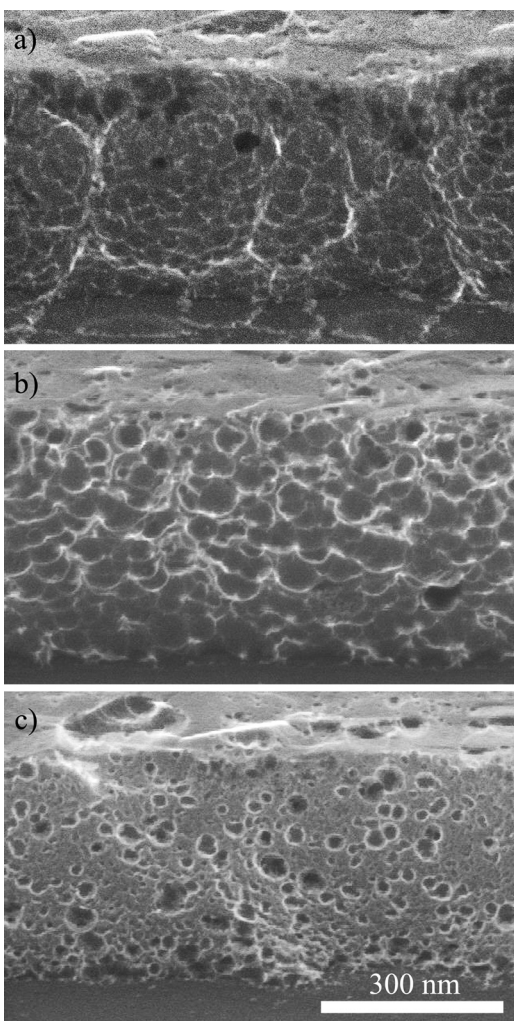


Fig. 2. SEM images of sputtered 300 nm-thick Al films etched with various time sequences using KMG 16:1:1:2 NP Al etchant at room temperature. The Al sidewall is smoother when the etch is performed as a series of dips interspersed with DI water quenches rather than extended dips without DI water quenches. (a) An uninterrupted 300 s Al etch removed the 300 nm-thick Al film, but left behind rough sidewalls with pronounced cavities \sim 200–300 nm in diameter. (b) Results of a series of 17 etch/DI quench dips (30 s/30 s). The large cavities seen in (a) are no longer present. Rough sidewall features remain. (c) Results from a series of 59 etch/DI quench dips (10 s/20 s). Here, sidewall roughness is significantly reduced. Cavities \sim 10 nm in diameter remain.

interspersed with DI water quenches. This routine is overly burdensome for kilogram scale CDMS detector processing so a “finishing etch” method was developed. Sufficiently smooth Al sidewalls are achievable by doing a continuous etch to get through the full Al thickness, followed by three short finishing etches (10 s etchant dips/20 s DI dips). A comparison of sputtered W film growth on Al features etched in four different ways is shown in Fig. 3. When using the long initial etch combined with a finishing etch, line width uniformity was compromised slightly by the formation of cavities 100–400 nm in diameter, but the effect was sufficiently minor to not require an all-short-dip etch process. Another successful fabrication recipe for W/Al QETs was developed by our TAMU collaborators who fabricate devices using a planar sputtering tool with substantially larger targets.¹²

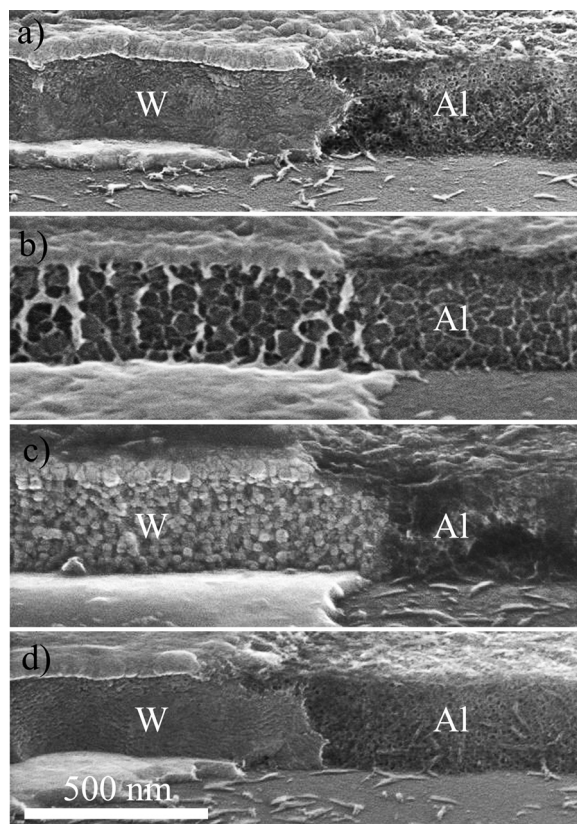


Fig. 3. Comparison showing how well 40 nm-thick sputtered W films conform to 300 nm-thick Al films, patterned using four different Al etch sequences. The W deposition parameters were held constant in all cases. (a) The W is observed to adhere uniformly to the Al film etched with (now unavailable) Cyantek AL-11 using 12 etch/DI quench dips (30 s/30 s). We believe this is due to the smooth Al sidewalls produced using this etch sequence. (b) W does not adhere uniformly to Al film etched in Cyantek AL-11 using one 300 s dip followed by a DI quench. The rough sidewall surface inhibits W film growth. (c) Al film first etched in KMG 80:3:15 NP for 300 s followed by three etch/DI quench dips (10 s/20 s). The three short etches were used to smooth out the Al sidewall surface to promote continuous W film growth in this region. The resulting W film was found to be continuous but highly granular. (d) Al film etched with KMG 80:3:15 NP using 25 etch/DI quench dips (20 s/20 s). This etch recipe provides smooth Al sidewalls which promote uniform W film growth.

III. FILM STEP COVERAGE MODEL

For CDMS compatibility, our AJA ATC-2200 sputtering system was designed to accommodate four 100 mm diameter \times 33.3 mm-thick \sim 1.4 kg (each) substrates at a time and provide *in vacuo* substrate flipping for depositions on both sides. The wafer carrier rotates about its center at \sim 15.5 rev/min during depositions and is suspended \sim 8 in. above the 4 in. diameter targets (racetrack \sim 3.5 in. in diameter), which sputter upwards. A schematic diagram of the sputtering geometry is shown in Fig. 4. Wafer flats are always oriented toward the outside of the carrier for diagnostic purposes.

To better understand the origin of irregularities in device performance, a model of the step coverage across the 10 in. substrate carrier was developed using thickness data obtained from an Al deposition on Si test wafers performed without platter rotation. The thickness data fit a 2D Gaussian fairly well, but a 2D King fit (flattened Gaussian) with flattening factor $1/\gamma = 0.1$ was more accurate for film locations farthest from the sputtering target. Mathematically rotating this distribution around the carrier rotation axis yielded a predicted thickness distribution across the full wafer carrier. The results were consistent with measured thin film data from depositions made with carrier rotation, although the model would benefit from introducing an ellipsoidal distribution to represent the gun tilt more accurately.

As is evident from the geometry shown in Fig. 4, die locations closer to the axis of rotation are more likely to achieve adequate step coverage than devices very near or very far from the gun, because the angle of incident flux is favorable and the deposition rate is still high. In order to model the deposition on a step, the angle between the carrier face and the gun central axis was calculated and the sine of this angle was multiplied by the 2D King nonrotating platter thickness distribution. Integrating around a circle of fixed radius from the carrier center then gave a predicted total film thickness on a step edge. By simulating various orientations of the step

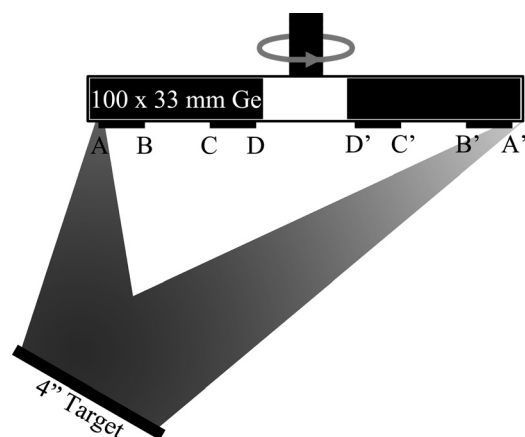


Fig. 4. Schematic of deposition geometry where previously patterned features across a 10 in. diameter substrate platter receive a subsequent deposition. Features at locations A, B, C, and D are rotated during deposition around a central axis (to locations A', B', C', and D'). In this figure, the sidewall of feature A receives predominantly vertical flux whereas A' is in shadow. This result in relatively poor step coverage at the edge of the platter compared to the center.

on the wafer, we found that the step 8 edges parallel-to and facing the flat should have the worst step coverage, and edges aligned perpendicular to the flat should have the best step coverage. Orientation dependent step coverage was confirmed in subsequent SEM imaging studies. Figure 5 shows our simulation for step coverage on a step parallel-to and facing the flat where simulations show that step coverage goes to zero. Figure 6 shows the step coverage for 600 nm Al devices near the flat compared to near the center of the platter. Limited step coverage in areas near the flat suggests that our model would improve by taking into account the scattering of target atoms by the Ar gas.

IV. DISCUSSION

A. W film structure

The zone structure model developed by Thornton^{17,18} is useful in understanding the growth of W films on the Al steps. This model describes the microstructure of sputtered

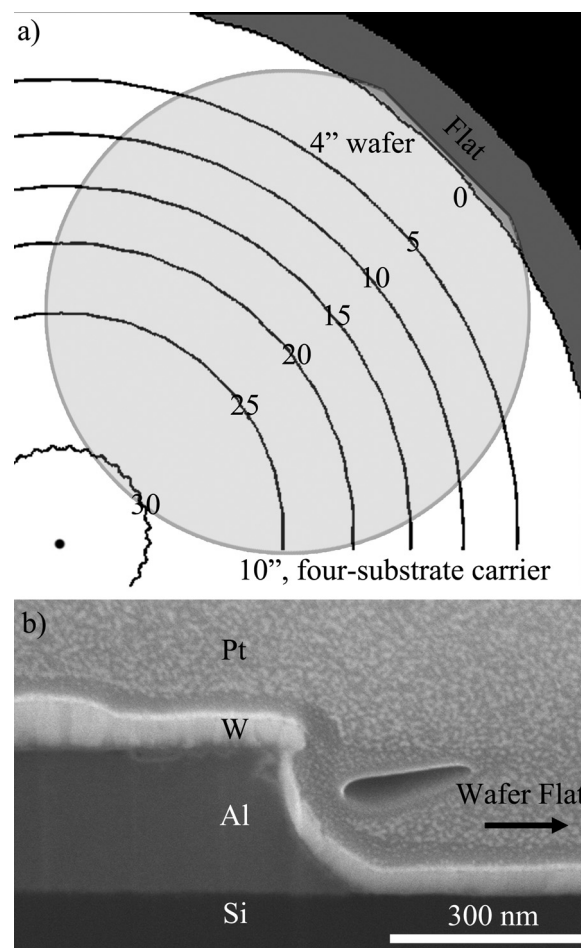


Fig. 5. Step coverage for features parallel-to and facing the wafer flat which was always oriented outwards. (a) Simulation showing the relative W coverage on the sidewall of etched Al features when deposited using a 10 in. diameter four-substrate carrier as a function of feature location. The small wiggles in the contours are a numerical artifact. (b) FIB image of a conformal coating of a 300-nm-thick Al film with a nominally 40-nm-thick W film at the center of the wafer shown in (a). The 40-nm-thick W film shows a minimum thickness of 22 nm at this location. (Pt was used to protect the features during FIB imaging.)

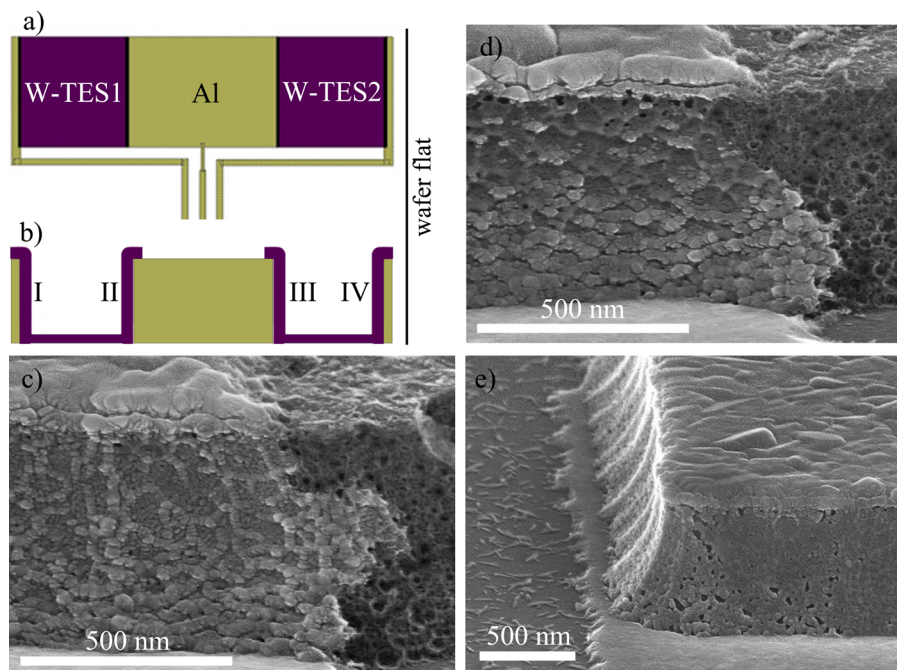


FIG. 6. (Color online) QET device where a 40 nm-thick W film achieves continuous step coverage over patterned 600 nm-thick Al features. In our confocal sputtering system, step coverage quality depends on the step's orientation and distance from the center of the wafer carrier. Al films were etched in 26 °C KMG 80:3:15 NP with 29 etch/DI quench dips (20 s/20 s). (a) Schematic of QET device where two W-TESs are attached to a central Al phonon collection film. The overlap regions are parallel to the flat of the wafer. (b) Cross-sectional schematic of QET device with film thickness greatly exaggerated. Each QET has four W/Al step coverage regions (I–IV). For a device located in the sputtering chamber 4.5 in. from the center of the substrate platter, an etched 600 nm-thick Al sidewall in orientation II or IV (c) experiences better step coverage than a sidewall in orientation I or III (d) for 40 nm-thick W, agreeing with simulations. (e) For a device 1.5 in. from the center of the platter, the step coverage for a sidewall in orientation I or III is improved, also agreeing with simulations.

films as a function of chamber pressure and T/T_m , where T is the local temperature at the deposition site and T_m is the melting point of the sputtered material. For our depositions, T/T_m is low (T_m for W is 3683 K) and the Ar sputter pressure is 3 mTorr so we can limit ourselves to zone 1 or zone T in the Thornton model. In zone 1, adatom diffusion is limited, and sputtered W atoms tend to adhere where they strike. This phenomenon tends to create open boundaries on steps because high points receive more flux than low ones, especially when an oblique component to the flux is present, like in our confocal geometry.¹⁷ Ion bombardment of biased substrates can be used to simulate an increase in T/T_m , and suppress zone 1 structure, promoting step coverage.¹⁸ However, when we varied the DC bias from -100 to -250 V during W depositions on rough Al surfaces, we saw no improvement in step coverage. The simulated increase in T/T_m was not enough to promote film growth. Instead, we found that the best strategy to achieve W-on-Al step coverage was to smooth out the surface on which the 40 nm-thick W would be deposited by modifying our predeposition Al wet etch.

Sputtered W thin films can consist of single crystal α -phase (bcc, $T_c = 15$ mK), single crystal β -phase (A15, $T_c = 1$ –4 K), or a mix of the two phases. For our experiment to function stably and to achieve high energy resolution, detectors need to be cooled to about half the T_c of the W films.¹⁹ The base temperature of our fully instrumented dilution refrigerator translates into a CDMS detector specification of about 65 mK for the superconducting transition temperature of the W films. Future experiments at a lower

base temperature would allow a lower W T_c and provide higher energy resolution. Work has been done to show that W T_c is tunable by varying target power and chamber pressure.²⁰ Several studies have been performed by our group and others that relate W film crystalline phase and T_c , film stress, resistivity, and other properties. Studies show a rapid change from tensile to compressive film stress for W films deposited at low pressure.^{21–25} The compressive stress is generally attributed to atomic peening caused by energetic sputter atoms.²¹ Our data for ~ 40 nm-thick W films are consistent with expectations: we observe compressive film stress of 0.7–2 GPa in W for Ar deposition pressures of 3–5 mTorr. Using x-ray diffraction, Haghiri-Gosnet *et al.* determined the structure of compressively stressed W films to consist entirely of α -phase W.²¹ Using TEM, the microstructure of compressive films were observed by Shen and Vink. Shen found that compressively stressed W films are either entirely α -phase, or a mixture of α - and β -phase W.²² Vink found that compressively stressed W films consist of mostly α -phase W with some β -phase also present.²³ Our cryogenic measurements of these films show superconducting transition temperatures ranging from 60 to 100 mK (with sharp individual film transitions of <1 mK), indicative of α and β mixed-phased W films. In addition, grazing incidence x-ray diffraction performed with a Bruker D8 Venture and a PANalytical X'Pert Pro revealed clear α -phase W and a hint of β -phase content in our 40 nm-thick compressively stressed W films. Unambiguous analytic detection of β -phase W in mixed-phase thin films is difficult to achieve with XRD

because the most intense scattering peaks for the two phases often overlap and minority phases are undetectable if their concentrations are less than $\sim 1\%$.²⁶ Measured stress in the final W layer of our devices was typically ~ 1 – 2 GPa (compressive) and W film resistivity was $\sim 15 \mu\Omega$ cm.

B. Quantitative step coverage diagnostics

Comprehensive cryogenic studies of the quality of our W/Al step coverage were performed at 35 mK in a dilution refrigerator. The results of these studies correlated well with SEM diagnostic data. An image of one test device is shown in the inset of Fig. 7, where two $250 \mu\text{m}$ -wide by $250 \mu\text{m}$ -long by 40 nm -thick W transition-edge sensors (W-TESs) overlap the edges of a $350 \mu\text{m}$ -long by $250 \mu\text{m}$ -wide by 300 nm -thick sputtered Al film. The entire structure is surrounded by a ring of smaller W/Al QETs used to veto substrate events in diagnostic experiments. In these experiments, devices are exposed to a collimated source of 2.62 keV K_α x-rays. The quality of the step coverage at the W/Al interface can be studied quantitatively with this set up.¹³

Data obtained with a well-processed (typical) device along with a device with poor W/Al step coverage with the same dimensions is shown in Fig. 7. Each data point corresponds to a 2.62 keV x-ray striking the central Al film. Event energy is collected using the attached W-TESs at the two ends of the Al film. An x-ray entering the detector closer to one W-TES than the other will result in increased energy collection in that TES. The band of events obtained from a device with good W/Al step coverage (black dots in Fig. 7) exhibits $\sim 2\times$ more total energy collection than a device with poor step coverage (magenta x's in Fig. 7) due to better coupling between the W and Al films. Even with good step

coverage, the full 2.62 keV is not recovered. Our results are consistent with known quasiparticle down-conversion processes where energy is lost to the substrate.¹³ This method provides valuable insight to the properties of the W/Al interface.

V. SUMMARY AND CONCLUSIONS

QETs are key to our search for weakly interacting massive particles. In order to optimize sensitivity, W-TESs must contain a minimal amount of material. In order to transport more phonon energy to the TES, the thickness of the attached Al fin was increased. Thornton's zone models suggest that smoother substrates require a lower T/T_m to promote continuous film growth. Step coverage by a 40 nm W deposition on 600 nm -thick Al features was achieved by etching the Al with KMG 80:3:15 NP (cycles of 20 s etch interspersed with 20 s quench). Further investigations will be made to quantify the improved detector performance and studies will be carried out to analyze device performance for dies across the platter.²⁷ If the thin step coverage near the wafer flat is not thick enough to support quasiparticle energy transport without driving the film normal, we will have to resort to doing single substrate depositions at the center of the platter where step coverage is thicker. The results of these studies will feed back into the fabrication recipe for the next generation of SuperCDMS detectors.

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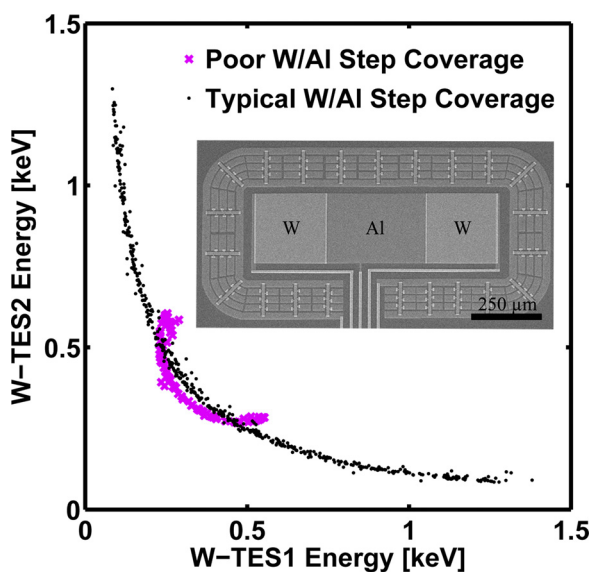


Fig. 7. (Color online) Results of diagnostic 2.62 keV x-ray fluorescence experiments (Ref. 13) performed at 35 mK to quantitatively evaluate the quality of W/Al interfaces. A SEM of a typical test device is shown (inset). The band of events from a device with poor filamentary W/Al step coverage (magenta crosses) is compressed and further removed from the axes than the band of events from a device with good W/Al interfaces (black dots). More energy is collected from a device with uniform W step coverage.

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