# Sublinear-Space Lexicographic Depth-First Search for Bounded Treewidth Graphs and Planar Graphs ${ }^{\dagger}$ 

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#### Abstract

The lexicographic depth-first search (Lex-DFS) is one of the first basic graph problems studied in the context of space-efficient algorithms. It is shown independently by Asano et al. [ISAAC 2014] and Elmasry et al. [STACS 2015] that Lex-DFS admits polynomial-time algorithms that run with $O(n)$-bit working memory, where $n$ is the number of vertices in the graph. Lex-DFS is known to be P-complete under logspace reduction, and giving or ruling out polynomial-time sublinear-space algorithms for Lex-DFS on general graphs is quite challenging. In this paper, we study Lex-DFS on graphs of bounded treewidth. We first show that given a tree decomposition of width $O\left(n^{1-\epsilon}\right)$ with $\epsilon>0$, Lex-DFS can be solved in sublinear space. We then complement this result by presenting a space-efficient algorithm that can compute, for $w \leq \sqrt{n}$, a tree decomposition of width $O(w \sqrt{n} \log n)$ or correctly decide that the graph has a treewidth more than $w$. This algorithm itself would be of independent interest as the first space-efficient algorithm for computing a tree decomposition of moderate (small but non-constant) width. By combining these results, we can show in particular that graphs of treewidth $O\left(n^{1 / 2-\epsilon}\right)$ for some $\epsilon>0$ admits a polynomial-time sublinear-space algorithm for Lex-DFS. We can also show that planar graphs admit a polynomial-time algorithm with $O\left(n^{1 / 2+\epsilon}\right)$-bit working memory for Lex-DFS.


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## 1 Introduction

### 1.1 Background and Motivation

Depth-First Search (DFS) is one of the most fundamental and elementary graph search algorithms with a huge number of applications. Lexicographic DFS (Lex-DFS) is a popular variant of DFS, which requires the search head always moves to the first undiscovered neighbor in the adjacency list of the current vertex (as long as it exists). Recently, the spaceefficient implementation of fundamental graph algorithms, including (Lex-)DFS, receives much attention $[3,6,15,16,23,26,30]$. These researches are roughly motivated by the

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two aspects as follows: First, the space matter is serious in the big-data (i.e., too large inputs) and/or IoT (i.e., too small computational devices) era. Second, the challenge of proving space-complexity lower bounds for problems within class P still lies at the core of computational complexity theory. One of the ultimate goals on this research direction is to prove or disprove the seminal $\mathrm{P} \neq \mathrm{L}$ conjecture ${ }^{2}$. We focus on the space complexity of LexDFS, particularly the algorithms using memory below the trivial $O(n \log n)$-bit bound. This can be motivated from both sides but much leans against the second one. The sublinear-space Lex-DFS problem is formulated as the one of outputting the Lex-DFS ordering of all vertices in streaming way, and its space complexity is measured by the required working-memory size, as the classical read-only model [25].

To argue the complexity of sublinear-space algorithms, the notion of P-completeness under logspace reduction plays an important role, which is analogous to NP-completeness in $\mathrm{P} \neq$ NP conjecture. Reif [40] shows that Lex-DFS is P-complete under logspace reduction. It implies that no Lex-DFS algorithm only using $O(\log n)$-bit working memory exists unless $\mathrm{P}=\mathrm{L}$ holds. A counterpart from the upper-bound side is recently obtained by a few literatures $[3,6,23,26]$. They focus on the implementation of (Lex-)DFS achieving both polynomial time and $o(n \log n)$-bit space complexity, where $n$ is the number of vertices in the input graph. Initiated by Asano et al. [3] and Elmasry et al. [23], a series of papers by several authors explore the time-space tradeoffs of (Lex-)DFS in the area of $o(n \log n)$-bit space-complexity. The state-of-the-art bounds are threefolds, $O\left(m \log ^{*} n\right)$ running time and $O(n)$-bit working memory, $O(m+n)$ running time and $O(n \log \log (4+m / n))$-bit working memory, and $O(m+n)$ running time and $O\left(n \log ^{(k)} n\right)$-bit working memory for any integer $k>1$, which are all proposed by Hagerup [26]. Looking at hidden coefficients, the smallestspace algorithm is the one by Asano et al. [3], which achieves a polynomial running time (with a large exponent) using the working memory of $n+o(n)$ bits. No algorithm so far attains $c n$-bit space complexity for $c<1$, and obtaining such an algorithm is commonly recognized as a very challenging problem. This open problem is also supported from yet another context of computational complexity theory. Lex-DFS on directed graphs is at least as hard as the directed $s$ - $t$ reachability problem, which is known to be NL-complete and thus its space-efficient (ideally, logspace) solution is closely related to the seminal $\mathrm{L}=\mathrm{NL}$ ? problem. In fact, any directed Lex-DFS algorithm achieving $O\left(n^{1-\epsilon}\right)$-bit space complexity for any small constant $\epsilon>0$ would be a breakthrough result on this research line.

### 1.2 Our Result

In the context of directed $s$ - $t$ reachability, there are many attempts of attaining $O\left(n^{1-\epsilon}\right)$-bit space complexity for a specific graph class such as planar or bounded treewidth graphs [1, $4,5,12,13,27,29]$, which naturally yields the interest to the feasibility of sublinear-space Lex-DFS for those classes. It should be noted that Lex-DFS is P-complete even for planar graphs [2], and thus its difficulty under log-space solvability is the same as the general case. One of the main results presented in this paper is a sublinear-space Lex-DFS algorithm for bounded treewidth graphs. The first theorem is stated as follows.

- Theorem 1. Let $0<\epsilon<1$ be an arbitrary positive constant, $G$ be any n-vertex directed graph of treewidth $w$, and $\left(\mathcal{T},\left\{B_{x}\right\}_{x \in V_{\mathcal{T}}}\right)$ be its tree decomposition of width $w^{\prime} \geq w$, where $\mathcal{T}=\left(V_{\mathcal{T}}, E_{\mathcal{T}}\right)$ is a tree and each node $x$ in $\mathcal{T}$ is associated with a subset $B_{x}$ of vertices in $G$. Assume a polynomial-time algorithm Alg enumerating the vertices in $B_{x}$ for all $x \in V_{\mathcal{T}}$ and the edges in $E_{\mathcal{T}}$. Then there exists a Lex-DFS algorithm of running time $O\left(n^{O(1 / \epsilon)}\right)$ using $O\left(\epsilon^{-1} w^{\prime} n^{\epsilon} \log n\right)$-bit memory (except for the space used by Alg).

[^1]It should be noted that Lex-DFS does not necessarily lie on the seminal framework known as Courcelle's theorem [19] and its logspace version [22] because the output depends on the order of vertices in the adjacency list of the input graph.

To figure out a "purely" sublinear-space Lex-DFS algorithm, it is necessary to implement a tree decomposition algorithm using only sublinear space. Elberfeld et al. [22] presents a logspace tree-decomposition algorithm for $w=O(1)$, but no sublinear-space algorithm covering the case of $w=\omega(1)$ has been known so far. Our second theorem provides a sublinear-space solution for tree decomposition:

- Theorem 2. There exists an algorithm that, given a graph $G$ of $n$ vertices and $w \leq$ $n^{1 / 2}$, either provides a tree decomposition of width $O\left(w n^{1 / 2} \log n\right)$ or correctly decides that the treewidth of $G$ is more than $w$. This algorithm runs in a polynomial time and uses $O\left(w n^{1 / 2} \log ^{2} n\right)$-bit space.

To the best of our knowledge, this is the first non-trivial tree-decomposition algorithm attaining both sublinear-space and polynomial time for $w=\omega(1)$. It is also worth noting that planar graphs admit a space-efficient algorithm of finding a balanced separator of size $O(\sqrt{n})$ using $\tilde{O}(\sqrt{n})$-bit space $[27]$, which can be translated into a small-space tree decomposition algorithm of width $O(\sqrt{n} \log n)$. Putting all the results above together, we obtain the following consequence:

- Corollary 3. Let $\epsilon>0$ be any positive constant. There exist the polynomial-time Lex-DFS algorithms respectively satisfying the following properties ${ }^{3}$.
- Using $O\left(n^{\epsilon}\right)$-bit working memory for directed graphs of treewidth $w=O(1)$.
- Using $O\left(w n^{1 / 2+\epsilon}\right)$-bit working memory for directed graphs of treewidth $w=O\left(n^{1 / 2}\right)$.
- Using $O\left(n^{1 / 2+\epsilon}\right)$-bit working memory for directed planar graphs.


### 1.3 Related Work

As stated above, the space complexity of Lex-DFS is one of the classical problems in the context of logspace computability. Following the P-completeness result by Reif [40], Anderson and Mayr [2] also shows a weaker variant of Lex-DFS (lexicographically first maximal path) is also P-complete even for planar graphs. The main interest of those earlier results is closely related to the $s$ - $t$ reachability problem. It is known that the space complexity of undirected $s-t$ connectivity drops into $O(\log n)$ bits for any input graphs, which is proved in Reingold's celebrating paper [41]. The best space upper bound of all polynomial-time directed $s$ - $t$ reachability algorithms is $O\left(n / 2^{\Omega(\sqrt{\log n})}\right)$ bits by Barnes et al. [9]. Its near optimality within a (naturally) restricted class of algorithms, called NNJAG [39], is also shown by Edmonds et al. [21].

More recently, the space-complexity matter of the directed $s$ - $t$ reachability problem for specific graph classes receives much attention, and a number of papers try to expand the graph class allowing sublinear-space directed reachability algorithms. Grid graphs [1, 5, 28], planar graphs [1, 4, 13, 27], bounded-genus graphs [12], and bounded-treewidth graphs [29] have been considered so far. Notice that the algorithms presented in [12] and [29] for bounded-genus graphs and bounded-treewidth graphs respectively require the surface embedding and the tree decomposition of the input graph (as Theorem 1), but it is not addressed how to compute them using sublinear space. Our tree-decomposition algorithm (by Theorem 2) yields the first

[^2]purely sublinear-space directed reachability algorithm for graphs of treewidth $w=O\left(n^{1 / 2-\epsilon}\right)$. Very recently, an $O(1)$-approximate tree decomposition algorithm using $O(w n)$-bit space is presented [31], which attains a non-trivial space complexity for $w=o(\log n)$.

Despite relatively rich literatures on directed $s$ - $t$ reachability, the space complexity of Lex-DFS receives less attention until recently. After the two concurrent results by Asano et al. [3] and Elmasry et al. [23], a few follow-up papers propose fundamental graph algorithms using only $o(n \log n)$-bit working memory, which cover Lex-BFS [6, 23], single-source shortest path [23], biconnected component decomposition [15, 30], s-t numbering [15], maximum cardinality search [16], and so on. It is also becoming active to consider sublinear-space algorithms for fundamental non-graph problems [7, 20, 34, 36, 42].

On the side of computational models, the read-only model is one of the classical models to consider the complexity of working memory. An earlier topic in this model is the time-space tradeoffs for sorting and/or selection [10, 17, 18, 25, 37, 38]. Recently, more unconventional models are also investigated; Stream model [33], restore model (algorithms can manipulate input memory but after the computation the initial input data must be recovered) [14, 32], and catalytic model (algorithms can use a large memory which are already used for other purpose, and after the computation the memory state must be recovered to the initial one) [11]. Some of the results in those models allow a Lex-DFS algorithm using only a small (exclusive) working memory, but they are incomparable to the ones in the standard read-only model. Barba et al. [8] provides a general scheme of realizing stack machines using only a small memory space. While the dominant part of the memory usage in Lex-DFS algorithms is the storage for a stack, the technique by Barba et al. only applies to the algorithms whose access pattern to stacks are non-adaptive. Thus that scheme does not work for saving the space complexity of Lex-DFS algorithms.

### 1.4 Organization of Paper

In Section 2, we introduce the model, notations, and several auxiliary matters for our LexDFS problem. Sections 3 and 4 respectively show the proofs of Theorem 1 and 2. Finally the paper is concluded in Section 5.

## 2 Preliminaries

### 2.1 Model and Notation

As stated in the introduction, this paper adopts the read-only model [25], where the space complexity of an algorithm is measured by the number of bits used for the working space, excluding the memory for inputs and outputs. The input memory is read-only, and the output memory is write-only. The memory-access model follows the standard RAM of $(\log n)$-bit words. Let $G$ be any directed input graph of $n$ vertices and $m$ edges, which is stored in the form of the adjacency list $A_{G}$. For any graph $G$, we denote the sets of the vertices and edges in $G$ by $V_{G}$ and $E_{G}$ respectively. We assume $V_{G}=[0, n-1]$, that is, each vertex in $V_{G}$ is uniquely identified by an integer in $[0, n-1]$. Letting $N_{G}(v) \subseteq V_{G}$ be the set of $v$ 's neighbors in $G$, we refer to the neighbor list of $v \in V_{G}$ as $A_{G, v}$ and denote the $i$-th vertex in $A_{G, v}$ by $A_{G, v}[i]$ (index $i$ starts from value zero). We use notation $u<_{v} u^{\prime}$ for $u, u^{\prime} \in N_{G}(v)$ if $u$ precedes $u^{\prime}$ in $A_{G, v}$. We define the inverse mapping of $A_{G, v}$ as $A_{G, v}^{-1}$, that is, for any neighbor $u$ of $v, A_{G, v}^{-1}[u]$ returns the position of $u$ in $A_{G, v}$. When we consider a subgraph $H \subseteq G$, the adjacency list of $H$ is inherited from that of $G$. More precisely, when we delete an edge $(u, v) \in E_{G}$, the adjacency list $A_{H, u}$ after deletion is defined as the one such that
$A_{H, u}[i]=A_{G, u}[i]$ for any $i<A_{G, u}^{-1}[v]$ and $A_{H, u}[i-1]=A_{G, u}[i]$ for any $i>A_{G, u}^{-1}[v]$. Since any subgraph is obtained by iterative deletion of edges (and removal of isolated vertices), the specification of the adjacency list after deletion of one edge also specifies the adjacency list $A_{H}$ for any subgraph $H$.

Letting $X$ be a set of vertices or edges, we denote by $G-X$ the graph obtained from $G$ by removing all the elements in $X$ (if $X$ is a vertex set, all the edges incident to a vertex in $X$ are also deleted). The subgraph of $G$ induced by $X$ is denoted by $G[X]$. If there is a polynomial-time algorithm $A l g$ enumerating all the elements in $X$, it naturally provides an access to the adjacency list $A_{H}$ for $H=G[X]$ or $H=G-X$ without explicitly constructing it in the working memory (i.e., $A l g$ works as a filter extracting the elements in $G[X]$ or $G-X$ from the adjacency list of $A_{G}$ ). Then we call Alg an emulator of $H$. The overhead of accessing to $A_{H}$ is a polynomial time (depending on the running time of Alg ) per one access. Thus we can run any polynomial-time algorithm taking $H$ as its input within a polynomial time. In the following argument, we often omit the subscript $G$ of the notations defined above if there is no ambiguity.

### 2.2 Lex-DFS

In what follows, we fix a starting vertex $s$ of Lex-DFS tasks. A Lex-DFS algorithm is presented in Algorithm 1. In the algorithm, we introduce the notion of time. At each time, the search head $v_{\text {cur }}$ moves to a neighbor decided by the algorithm. The search starts at time $t=1$ and finishes at time $t=2 n$. We define $h_{t}$ as the vertex pointed by the search head at the beginning of time $t$ in the Lex-DFS on $G$. For vertex $v \in V$, the discovery time $d(v)$ of $v$ is defined as the first time when the search head moves to $v$. Similarly, we define the leaving time $l(v)$ of $v$ as the last time when the search head moves from $v$. The discovery time of $s$ is defined as zero. Following the terminology in [3], a vertex $v$ is called a gray vertex at $t$ if $d(v) \leq t \leq l(v)$ holds. The (path) subgraph corresponding to the sequence of all gray vertices at time $t$ sorted by their discovery times is called the gray path at $t$, which is denoted by $S_{t}{ }^{4}$. For any vertex $u \in V_{S_{t}}$, we also denote by $p_{t}(u)$ and $s_{t}(u)$ the (immediate) predecessor and successor of $u$ in $S_{t}$, and by $S_{t}(u)$ the prefix of $S_{t}$ terminating at $u$. We define $p_{t}(s)=\perp$ and $s_{t}\left(h_{t}\right)=\perp$.

In Algorithm 1, we encapsulate the space-consuming parts of the algorithm by two abstract procedures called $\operatorname{Pivot}(t)$ and $\operatorname{Parent}(t)$. The procedure $\operatorname{Pivot}(t)$ tries to find the first undiscovered neighbor of $h_{t}$ with respect to the order $<_{h_{t}}$. If there is no undiscovered neighbor, it returns -1 . The procedure $\operatorname{Parent}(t)$ returns the predecessor $p_{t}\left(h_{t}\right)$ in the current gray path. It returns -1 if $h_{t}=s$ holds. It is obvious that Lex-DFS is implemented with the working memory of $f(n)+O(\log n)$ bits if both of $\operatorname{Pivot}(t)$ and $\operatorname{Parent}(t)$ are implemented with $f(n)$ bits.

### 2.3 Tree Decomposition and Balanced Separator

We first present the definition of tree decomposition.

- Definition 4. A tree decomposition of an undirected graph $G$ is a pair $\left(\mathcal{T},\left\{B_{x}\right\}_{x \in V_{\mathcal{T}}}\right)$, where $\mathcal{T}$ is a tree and each node $x \in V_{\mathcal{T}}$ is associated with a subset $B_{x}$ of vertices in $V_{G}$ (called the bag $x$ ) satisfying the following conditions:
- Any edge in $G$ is covered by at least one bag, i.e., $\forall(u, v) \in E_{G}: \exists x: u, v \in B_{x}$.
- Letting $\mathcal{T}(u)$ be the subgraph of $\mathcal{T}$ induced by the bags containing $u$, for any $u \in V_{G}, \mathcal{T}(u)$ is non-empty and connected.

[^3]Algorithm 1 Lex-DFS Algorithm for graph $G$ starting from $s$.

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\(v_{c u r} \leftarrow s ; t \leftarrow 1 \quad \triangleright v_{c u r}\) is the search head
    while true do
        \(v \leftarrow \operatorname{Pivot}(t) \quad \triangleright\) Find the first undiscovered neighbor of \(v_{c u r}\) in \(A_{G, s}\).
        if \(v=-1\) then
            \(v \leftarrow \operatorname{Parent}(t)\)
            if \(v=-1\) then halt
        else
            Output \(v \quad \triangleright\) Discovery of a new vertex
        \(v_{c u r} \leftarrow v\)
        \(t \leftarrow t+1\)
```

Note that tree decomposition is defined for undirected graphs. When we consider the tree decomposition of directed graphs $G$, we naturally adapt the same definition to the undirected graph obtained from $G$ by omitting the orientation of all edges ${ }^{5}$. Each bag is identified by an integer value in $\left[0,\left|V_{\mathcal{T}}\right|-1\right]$. Throughout this paper, we assume that any decomposition tree $\mathcal{T}$ is rooted, and that a tree decomposition is encoded as the sequence $\left(B_{1}, q(1)\right),\left(B_{2}, q(2)\right), \ldots,\left(B_{x}, q(x)\right), \ldots,\left(B_{\left|V_{\mathcal{T}}\right|-1}, q\left(\left|V_{\mathcal{T}}\right|-1\right)\right)$, where $q(x)$ is the ID of the parent of $x$ in $\mathcal{T}$. The parent of the root bag is defined as -1 . A sublinear-space tree decomposition algorithm must output this sequence in a streaming way. The width $w$ of a tree decomposition $\left(\mathcal{T},\left\{B_{x}\right\}_{x \in V_{\mathcal{T}}}\right)$ is defined as the maximum bag size minus one, i.e., $w=\left(\max _{x \in V_{\mathcal{T}}}\left|B_{x}\right|\right)-1$. The treewidth of a graph $G$ is the minimum width over all tree decompositions of $G$. It is a fundamental property that the removal of the vertices in any (non-leaf) bag $B_{x}$ from $G$ splits $G$ into several connected components, each of which corresponds to a subtree of $\mathcal{T}$ obtained by the removal of $x$ from $\mathcal{T}$.

Tree decomposition is closely related to the notion of balanced vertex separators. Let $G=(V, E)$ be any directed graph and $\mu: V_{G} \rightarrow \mathbb{N}$ be any vertex-weight function. We define $\mu(X)=\sum_{v \in X} \mu(v)$ for any $X \subseteq V_{G}$. A vertex subset $U \subseteq V$ is called a weighted $\alpha$-balanced separator of $G$ with respect to $\mu$ if any weakly-connected component $C$ in $G-U$ satisfies $\mu\left(V_{C}\right) / \mu\left(V_{G}\right) \leq \alpha$. If $\mu$ is a constant function, it is simply called an $\alpha$-balanced separator of $G$. Throughout this paper, we often consider a subgraph obtained by recursively removing separators. Let $c c(H, U)$ be the set of connected components in $H-U$ for any graph $H$ and its vertex subset $U \subseteq V_{H}$, and $v c c(H, U)=\left\{V_{C} \mid C \in c c(H, U)\right\}$. The following lemma holds.

- Lemma 5. Let $H_{0}, H_{1}, H_{2}, \ldots, H_{k-1}$ and $U_{0}, U_{1}, \ldots, U_{k-1}$ be respectively the sequences of subgraphs of $G$ and their vertex subsets such that $H_{i} \in c c\left(H_{i-1}, U_{i-1}\right)$ holds. Assuming $k$ algorithms respectively enumerating the vertices in $U_{i}$ for each $i \in[0, k-1]$, there exists a logspace algorithm of enumerating all the vertex subsets in $v c c\left(H_{k-1}, U_{k-1}\right)$ using them as black-box subroutines.

Proof. Since the straightforward recursive emulation of $H_{k-1}$ takes the overhead exponential of $k$, such an approach applies only to the case of $k=O(1)$. Instead of emulating $H_{i-1}-U_{i-1}$ recursively, we use the emulation of $G-U$ for $U=\bigcup_{0 \leq i \leq k-1} U_{i}$. Since we assume the algorithms of enumerating $U_{i}$ for all $i \in[0, k-1]$, this emulation works with a polynomialtime overhead independent of $k$. We have $c c\left(H_{k-1}, U_{k-1}\right) \subseteq c c(G, U)$ obviously. While

[^4]$c c(G, U)$ might contain a connected component not in $c c\left(H_{k-1}, U_{k-1}\right)$, one can identify $C \in c c\left(H_{k-1}, U_{k-1}\right)$ by checking if $C$ has an outgoing edge to a neighbor in $U_{k-1}$ because any component $C \notin c c\left(H_{k-1}, U_{k-1}\right)$ is separated from $H_{k-1}$ by $U_{0}, U_{1}, \ldots$ or $U_{k-2}$. Thus, letting $\partial U_{k-1}$ be the set of vertices in $G-\bigcup_{0 \leq i \leq k-1} U_{i}$ adjacent to a node in $U_{k-1}$, it suffices to obtain an algorithm enumerating all the components in $c c(G, U)$ intersecting $\partial U_{k-1}$. It is realized by the following procedure.

1. Let $c=1$, and $v_{0}, v_{1}, \ldots, v_{l-1}$ be the sequence of the vertices in $\partial U_{k-1}$ sorted by their IDs, which can be enumerated using logarithmic space and the algorithms for $U_{0}, U_{1}, \ldots, U_{k-1}$.
2. For each $v_{i}$, check if a vertex $v_{j}$ satisfying $j<i$ is reachable to $v_{i}$ in $G-U$. If such a vertex exists, repeat this step for $v_{i+1}$ (unless $i=l-1$ ). Otherwise, go to step 3.
3. Enumerate all the vertices reachable from $v_{i}$ as the vertices in the $c$-th component in $c c\left(H_{k-1}, U_{k-1}\right)$. After the enumeration, increment $c$ by one, and go back to step 2 for $v_{i+1}$.
The procedure above is implemented with $O(\log n)$-bit space by utilizing the logspace undirected $s$-t connectivity algorithm [41] (since $c c\left(H_{k-1}, U_{k-1}\right)$ is a set of weakly-connected components, undirected $s$ - $t$ connectivity suffices). Letting $v_{j}$ be the vertex with the minimum ID in $V_{C} \cap \partial U_{K-1}$ for a component $C \in c c\left(H_{k-1}, U_{k-1}\right), C$ is necessarily enumerated when the procedure above processes $v_{j}$. In addition, it is never enumerated twice because any other vertex in $V_{C} \cap \partial U_{k-1}$ has an ID larger than $v_{j}$ and is reachable to $v_{j}$ in $G-U$.

The lemma above implies that one can associate an unique integer ID with each connected component in $H_{k-1}-U_{k-1}$, and can emulate with a polynomial-time overhead the connected component in $H_{k-1}-U_{k-1}$ specified by a given ID. We also have the lemma below.

- Lemma 6. Let $G=(V, E)$ be any graph of treewidth $w$, and $0<\delta<1$ be an arbitrary positive constant. Assume an algorithm outputting a tree decomposition of width at most $w^{\prime}$ for $G$. Then, there exists an algorithm outputting an $O\left(n^{-\delta}\right)$-balanced vertex separator $U$ of size $O\left(w^{\prime} n^{\delta}\right)$ for $G$, which uses only $O\left(w^{\prime} n^{\delta} \log n\right)$-bit space (except for the space used by the tree-decomposition algorithm).

Proof. Let ( $\mathcal{T},\left\{B_{x}\right\}_{x \in V_{\mathcal{T}}}$ ) be the (rooted) tree decomposition constructed by the algorithm. For any subgraph $\mathcal{T}^{\prime} \subseteq \mathcal{T}$ and a subset $X \subseteq V_{G}$, we $\operatorname{define} \operatorname{vol}\left(\mathcal{T}^{\prime}, X\right)=\left|\bigcup_{y \in V_{\mathcal{T}^{\prime}}} B_{y} \backslash X\right|$. We also define $\mathcal{T}(x)$ as the subtree of $\mathcal{T}$ rooted by $x \in V_{\mathcal{T}}$.

The proof is constructive. The algorithm finds the $O\left(n^{\delta}\right)$ bags whose removal splits $\mathcal{T}$ into a small subtrees $\mathcal{T}_{0}, \mathcal{T}_{1}, \ldots, \mathcal{T}_{M-1}$ satisfying $\operatorname{vol}\left(\mathcal{T}_{i}, U\right) \leq n^{1-\delta}$ for any $i \in[1, M]$. The algorithm manages two sets $U^{\prime}$ and $U$. The set $U^{\prime}$ stores the set of bag IDs constituting the vertex subset $U$, i.e., $U=\bigcup_{x \in U^{\prime}} B_{x}$. The construction of $U$ is done by iteratively adding a vertex in $V_{\mathcal{T}}$ to $U^{\prime}$. Let $x_{i}$ be the vertex added to $U^{\prime}$ at the $i$-th iteration, and $U_{i}$ and $U_{i}^{\prime}$ be respectively the contents of $U$ and $U^{\prime}$ when $\left|U^{\prime}\right|=i$ holds. We further define $\mathcal{T}_{i}^{R}$ as the connected component of $\mathcal{T}-U_{i}^{\prime}$ containing the root. The algorithm chooses as $x_{i}$ the deepest vertex in $\mathcal{T}_{i-1}^{R}$ such that $\operatorname{vol}\left(\mathcal{T}_{i-1}^{R}\left(x_{i}\right), U_{i-1}\right) \geq n^{1-\delta}$ holds. The iteration terminates when $\operatorname{vol}\left(\mathcal{T}_{i}^{R}, U_{i}\right)$ becomes smaller than $n^{1-\delta}$. Since one iteration decreases $\operatorname{vol}\left(\mathcal{T}_{*}^{R}, U_{*}\right)$ by at least $n^{1-\delta}$, the algorithm terminates within $n^{\delta}$ iterations. In addition, for any children $y$ of $x_{i}$, we have $\operatorname{vol}\left(\mathcal{T}_{i-1}^{R}(y), U_{i-1}\right)<n^{1-\delta}$ because $x_{i}$ is the deepest vertex. It implies that any connected component in $G-U$ contains at most $n^{1-\delta}$ vertices.

The remaining issue is the time and space complexities for implementing the algorithm. Since the tree decomposition algorithm provides the whole topological information on $\mathcal{T}$, one can use it as the adjacency list of $\mathcal{T}$ incurring a polynomial-time overhead. Since $U^{\prime}$ is stored in the working memory, it is possible to emulate $\mathcal{T}_{i}^{R}$ for any $i$. The computation
of $\operatorname{vol}\left(\mathcal{T}_{i}^{R}(x), U_{i}\right)$ for any $x \in V_{\mathcal{T}_{i}^{R}}$ can be done in a polynomial time using the membership test of $v \in B_{y}$ for all pairs of $v \in V_{G} \backslash U_{i}$ and $y \in V_{\mathcal{T}_{i}^{R}(x)} \backslash\{x\}$. Consequently, the proposed algorithm can be implemented with the storage cost for $U$ and $U^{\prime}$. Since each bag contains at most $w^{\prime}$ vertices, the space complexity is $O\left(w^{\prime} n^{\delta} \log n\right)$ bits.

## 3 Small-Space Lex-DFS Algorithm for Graphs of Bounded Treewidth

### 3.1 Reduction to (Approximate) Gray-Path Membership

The algorithms shown in [3] reduces the procedures of $\operatorname{Pivot}(t)$ and $\operatorname{Parent}(t)$ to a single abstract task called $\operatorname{IsGray}(u, v)$, which tests if $v$ belongs to the prefix of the gray path by $u$ (i.e., tests if $v \in V_{S_{d(u)}}$ holds or not). The following lemma is proved in [3].

- Lemma 7 (Asano et al. [3]). Assume that there exists a polynomial-time algorithm $\operatorname{ISGRAY}(u, v)$ which is executable at any time $d(u) \leq t \leq l(u)$ and determines if $v \in V_{S_{d(u)}}$ holds or not. Letting $f(n)$ be the space complexity of that algorithm and $g(n)$ be the spacecomplexity of solving the directed s-t reachability problem, we have two polynomial-time algorithms which respectively implement $\operatorname{PiVot}(t)$ and $\operatorname{Parent}(t)$ using the memory of $f(n)+g(n)$ bits.

We slightly extend this lemma by introducing a new primitive called $\operatorname{AIsGray}(u, v)$ (approximate testing of gray vertex), which is a one-sided-error version of $\operatorname{IsGray}(u, v)$ satisfying the following two conditions:

1. If $v \in V_{S_{d(u)}}$ holds, $\operatorname{AIsGray}(u, v)$ always returns true.
2. If $d(v)>d(u)$ holds, $\operatorname{AIsGray}(u, v)$ always returns false.

The following lemma implies that we can replace $\operatorname{IsGray}(u, v)$ in Lemma 7 by $\operatorname{AIsGray}(u, v)$.

- Lemma 8. Assume a polynomial-time algorithm $\operatorname{AISGRAY}(u, v)$ executable at any time $d(u) \leq t \leq l(u)$ using $f(n)$-bit space, and a polynomial-time tree decomposition algorithm outputting the decomposition of width at most $w^{\prime}$ for any input graphs of treewidth $w$. Then we have the polynomial-time algorithm which implements $\operatorname{ISGRAY}(u, v)$ using the space of $f(n)+O\left(w^{\prime} \log n\right)$ bits (except for the space used by the tree decomposition algorithm).

Proof. To implement $\operatorname{IsGray}(u, v)$, it suffices to enumerate all the vertices in $S_{t}(u)$, which can be realized by repeatedly using an algorithm outputting $s_{t}(x)$ for given $x \in V_{S_{t}(u)} \backslash\left\{h_{t}\right\}$. Let $V^{\prime}(x)$ be the set of the vertices $v^{\prime}$ such that $\operatorname{AIsGray}\left(x, v^{\prime}\right)$ returns true. It has been shown in [3] that $s_{t}(x)$ is the first vertex $y \in N(x)$ with respect to the order of $A_{x}$ such that $y$ is reachable to $h_{t}$ in $G-V_{S_{t}(x)}$. We first show that this fact still holds even if we replace $V_{S_{t}(x)}$ by $V^{\prime}(x)$. Any $y$ preceding $s_{t}(x)$ in $A_{x}$ is unreachable to $h_{t}$ in $G-V^{\prime}(x)$ because it is unreachable in $G-V_{S_{t}(x)}$ and $V_{S_{t}(x)} \subseteq V^{\prime}(x)$ holds by the first condition of AIsGray. Letting $X$ be the graph corresponding to the suffix of $S_{t}$ from $s_{t}(x)$ to $h_{t}$, any vertex in $V_{X}$ has a discovery time larger than $d(x)$, and thus $V_{X} \cap V^{\prime}(x)=\emptyset$ holds by the second condition of AIsGray. It implies that $s_{t}(x)$ is reachable to $h_{t}$ in $G-V^{\prime}(x)$, and concludes that $s_{t}(x)$ is the first vertex $y \in N(x)$ such that $y$ is reachable to $h_{t}$ in $G-V^{\prime}(x)$. Since the procedure AIsGray $\left(x, v^{\prime}\right)$ works as the emulator of $G-V^{\prime}(x)$, we can obtain an algorithm of computing $s_{t}(x)$ using any directed $s$ - $t$ reachability algorithm. The algorithm by Jain et al. [29] matches our goal. It uses any tree decomposition of width $w^{\prime}$ as a side information, and runs in a polynomial time using $O\left(w^{\prime} \log n\right)$-bit space. The memory complexity is $f(n)$ bits for AIsGray, and $O\left(w^{\prime} \log n\right)$ bits for deciding $s-t$ reachability and for managing a constant number of pointers to vertices in $V_{G}$.


Figure 1 An example of decomposition by $U$.


Figure 2 Construction of $H_{i}$.

### 3.2 Implementation of $\operatorname{AlsGray}(u, v)$

As utilized in the proof of Lemma 8, directed s-t reachability is solvable using $O\left(w^{\prime} \log n\right)$-bit space with the side information of a tree decomposition of width $w^{\prime}$ [29]. Thus we have Lemma 7 with $g(n)=O\left(w^{\prime} \log n\right)$. The remaining part of our algorithm is to implement $\operatorname{AIsGray}(u, v)$ executable at any $d(u) \leq t \leq l(u)$. Let $U$ be the set shown in Lemma 6, and $\mathcal{X}=\left\{X_{0}, X_{1}, \ldots, X_{N-1}\right\}$ be the set of the connected components in $G-U$ (Figure 1). At each time $t$, our algorithm keeps track of the information of $\left(d(x), p_{t}(x), s_{t}(x)\right)$ for any $x \in V_{S_{t}} \cap U$. Specifically, we prepare the dictionary $Z$ which maps any vertex $x$ in $U$ to the corresponding triple $\left(d(x), p_{t}(x), s_{t}(x)\right)$ if $x \in S_{t}$ holds. We refer to the three elements in the triple for $x$ as $Z[x] . d, Z[x] . p$, and $Z[x] . s$ respectively. The contents of $Z$ is updated in the main routine of Lex-DFS when the search head moves. Let $\ell_{i}$ be the number of connected components in the subgraph $S_{d(u)}\left[V_{X_{i}}\right]$. For each connected component $C$ in $S_{d(u)}\left[V_{X_{i}}\right]$, we define its entrance and exit as the vertices with the minimum and maximum discovery times in $V_{C}$ respectively. Let $\mathcal{C}_{i}=C_{i, 0}, C_{i, 1}, \ldots, C_{i, \ell_{i}-1}$ be the sequence of the connected components in $S_{d(u)}\left[V_{X_{i}}\right]$ sorted by the discovery times of their entrances. We also define the exit of $C_{i,-1}$ as $s$, which works as a sentinel value. Let $Q_{i}=\left(q_{i, 0}, q_{i, 1}, \ldots, q_{i, \ell_{i}-1}\right)$ and $W_{i}=\left(w_{i,-1}, w_{i, 0}, w_{i, 1}, \ldots, w_{i, \ell_{i}-1}\right)$ be the sequences of the entrances and exits associated with each component in $\mathcal{C}_{i}$ respectively. Now we construct the graph $H_{i}$ from $G$ by the following procedure:

1. Remove all the vertices not in $V_{X_{i}} \cup\{s\}$ as well as their incident edges.
2. For all $0 \leq j \leq \ell_{i}-1$, contract the gray path from $w_{i, j-1}$ to $q_{i, j}$ into an edge. The positions of $q_{i, j}$ in $A_{w_{i, j-1}}$ and $w_{i, j-1}$ in $A_{q_{i, j}}$ are equal to those of $s_{t}\left(w_{i, j-1}\right)$ and $p_{t}\left(q_{i, j}\right)$ respectively.
Note that $s=w_{i,-1}=q_{i, 0}$ holds if $s \in V_{X_{i}}$. We illustrate an example of the construction in Figure 2. Consider the run of any Lex-DFS algorithm in $H_{i}$ until the discovery of $w_{i, \ell_{i}-1}$, which outputs a vertex set $L_{i} \subseteq V_{X_{i}} \cup\{s\}$. Let $L(u)=L_{0} \cup L_{1} \cup \cdots \cup L_{N-1} \cup\left(S_{d(u)} \cap U\right)$. An important fact is that $\operatorname{AIsGray}(v, u)$ can be implemented using the query if $v \in L(u)$ or not. The following lemma holds.

- Lemma 9. Let $L_{i}$ be the output sequence of the Lex-DFS running in $H_{i}$ until the discovery of $w_{i, \ell_{i}-1}$. Any vertex in $V_{S_{d(u)}\left[V_{X_{i}}\right]}$ is contained in $L_{i}$, and $d(x) \leq d\left(w_{i, \ell_{i}-1}\right) \leq d(u)$ holds for any $x \in L_{i}$.

Proof. For any $v \in V$, let $\mathcal{P}_{G, v}$ be the set of all simple paths from $s$ to $v$ in $G$, and $\mathcal{P}_{G}=\bigcup_{v \in V_{G}} \mathcal{P}_{G, v}$. For any path $P=s, u_{1}, \ldots, u_{j}, v$ in $\mathcal{P}_{G, v}$, we define its word $\gamma_{G}(P)$ as the sequence $A_{s}^{-1}\left[u_{1}\right], A_{u_{1}}^{-1}\left[u_{2}\right], \ldots, A_{u_{k}}^{-1}[v]$. Letting $\prec$ be the lexicographic order over all words, the minimum path $\pi_{G}(v) \in \mathcal{P}_{G, v}$ of a vertex $v \in V$ is defined as the one satisfying
$\gamma_{G}\left(\pi_{G}(v)\right) \prec \gamma_{G}(P)$ for any $P \in \mathcal{P}_{G, v}$. For any two vertices $v, v^{\prime} \in V_{H_{i}}$, the gray paths in $H_{i}$ to $v$ and $v^{\prime}$ are respectively obtained by contracting several common subpaths in the gray paths to $v$ and $v^{\prime}$ in $G$. Hence it is easy to check $\gamma_{G}\left(\pi_{G}(v)\right) \prec \gamma_{G}\left(\pi_{G}\left(v^{\prime}\right)\right)$ holds if and only if $\gamma_{H_{i}}\left(\pi_{H_{i}}(v)\right) \prec \gamma_{H_{i}}\left(\pi_{H_{i}}\left(v^{\prime}\right)\right)$ holds for any $v, v^{\prime} \in V_{H_{i}}$. Since it is well-known that the total ordering of $V_{G}$ with respect to $\prec$ over $\left\{\gamma_{G}\left(\pi_{G}(v)\right)\right\}_{v \in V_{G}}$ is equivalent to the Lex-DFS ordering of $V_{G}$, this fact implies that $L_{i}$ contains all the vertices in $V_{X_{i}}$ discovered earlier than $w_{i, \ell-1}$ in the Lex-DFS search in $G$, and contains no vertex in $V_{X_{i}}$ whose discovery time in the Lex-DFS search in $G$ is later than $d\left(w_{i, \ell-1}\right)$. The lemma is proved.

Since $H_{i}$ is a minor of $G$, its treewidth is also bounded by $w$. Thus we can perform our Lex-DFS algorithm recursively for graph $H_{i}$ of $O\left(n^{1-\epsilon}\right)$ vertices to output $L_{i}$. The graph $H_{i}$ can be emulated using the subset $U$ and the information stored in $Z$. Outputting $L_{i}$ for all $i \in[0, N-1]$ can answer the query if $v \in L(u)$ holds or not.

### 3.3 Algorithm Details for Lex-DFS

The pseudocode of our algorithm is given in Algorithm 2. It is defined as a recursive procedure LEx-DFS $(G, s, u)$, which outputs the Lex-DFS sequence of $G$ starting from $s$ until $u$ is discovered. If the procedure runs with $u \notin V_{G}$, it outputs the whole Lex-DFS sequence of $G$ starting from $s$. Note that the dictionary $Z$ is independently defined in each recursive call for the computed separator $U$. In addition, the size $O\left(n^{\epsilon}\right)$ of separator $U$ is fixed independently of recursion depth. That is, the variable $n$ in the size parameter $O\left(n^{\epsilon}\right)$ is always the number of vertices in the original input graph, not the number of vertices in the input graph taken as an argument of LEX-DFS. The main routine LEX-DFS almost follows Algorithm 1, except for using AIsGray to compute Pivot and Parent and managing $Z$. The core of the algorithm is the implementation of AIsGray, in particular, the emulation of $H_{i}$ for each $V_{i}(0 \leq i \leq N-1)$. That part corresponds to the lines 23-30. First, we identify the set $Q_{i}$ and $W_{i}$, which can be done by extracting the nodes $x \in U$ satisfying $Z[x] . s \in V_{i}$ as a member of $Q_{i}$ (or those satisfying $Z[x] . p \in V_{i}$ as $W_{i}$ ). Since each node in $S_{t}(u) \cap U$ stores its discovery time in $Z$, we can add the nodes into $Q_{i}$ or $W_{i}$ in the order of their discovery times. Following the order of $Q_{i}$ and $W_{i}$, we create the edge set $F$, which corresponds to the edges obtained by the contraction of gray subpaths in the step 2 of the construction.

### 3.4 Complexity

Since Pivot and Parent are called at most $2 n$ times in each recursive call, a polynomialtime invocations of AIsGray suffices to implement them. Let $n^{c}$ be the upper bound for the number of invocations of AIsGray in one execution of Pivot or Parent. One invocation of AIsGray calls LEx-DFS at most $n$ times. Putting all them together, $n^{c+2}$ recursive invocations of LEX-DFS occur per one call of LEX-DFS. Since the recursion depth is obviously bounded by $O(1 / \epsilon)$, at most $O\left(n^{(c+2) / \epsilon}\right)$ calls of LEX-DFS are invoked in total. By Lemma 5, the input graph to each recursive call can be emulated with a polynomial-time overhead, and thus one invocation of Lex-DFS excepting the run of recursive calls has a polynomially-bounded running time. Consequently, the total running time is $n^{O(1 / \epsilon)}$.

In each recursive call, the information on $U$ and $Z$ is stored in the working memory. The space for storing $U$ and $Z$ are bounded by $O\left(w^{\prime} n^{\epsilon} \log n\right)$ bits. Except for the space used by the tree decomposition algorithm, the space of $O\left(w^{\prime} \log n\right)$ bits is necessary for implementing Parent and Pivot from AIsGray. Since the recursion depth is $O(1 / \epsilon)$, the total space complexity is $O\left(w^{\prime} \epsilon^{-1} n^{\epsilon} \log n\right)$ bits.

Algorithm 2 Lex-DFS Algorithm for graph $G$ of treewidth $k$ (starting from $s$ ).
function $\operatorname{LEX}-\operatorname{DFS}(G, s, u)$
if $\left|V_{G}\right| \leq n^{\epsilon}$ then run the standard Lex-DFS algorithm and halt
Find a separator $U$ of size $O\left(n^{\epsilon}\right)$
Initialize $Z: U \rightarrow[0, n-1] \times V_{G} \times V_{G}$
$v_{\text {cur }} \leftarrow s ; t \leftarrow 1$
output $s ; Z[s] \leftarrow(1, \perp, \perp)$
while true do
$v \leftarrow \operatorname{Pivot}(t)$ using $\operatorname{AISGray}\left(v_{c u r}, \cdot\right)$
if $v=-1$ then $\quad \triangleright$ All neighbors have been already visited
$v \leftarrow \operatorname{Parent}(t)$ using $\operatorname{AIsGray}\left(v_{c u r}, \cdot\right)$
$Z\left[v_{\text {cur }}\right] \leftarrow$ null; $Z[v] \leftarrow(Z[v] . d, Z[v] . p, \perp)$
if $v=-1$ then halt $\quad \triangleright$ All vertices are visited else
$Z\left[v_{c u r}\right] \leftarrow\left(Z\left[v_{c u r}\right] \cdot d, Z\left[v_{c u r}\right] \cdot p, v\right) ; Z[v] \leftarrow\left(t, v_{c u r}, \perp\right)$
Output $v$
if $v=u$ then halt
$v_{c u r} \leftarrow v$
$t \leftarrow t+1$
function $\operatorname{AIsGray}(u, v)$
if $Z[v] \neq$ null then return true
Let $X_{0}, X_{1}, \ldots, X_{N-1}$ be the connected components in $G-U$
for $i=0,1, \ldots, N-1$ do
$Q \leftarrow() ; W \leftarrow(s)$
for $\forall x \in U: Z[x] \neq$ null in ascending order of $Z[x] . d$ do
if $Z[x] . s \in V_{X_{i}}$ then append $Z[x] . s$ to $Q$
if $Z[x] . p \in V_{X_{i}}$ then append $Z[x] . p$ to $W$
if $u \in V_{X_{i}}$ then append $u$ to $W$
$\ell_{i} \leftarrow|Q|$
for $j=0,1, \ldots, \ell_{i}-1$ do
$F \leftarrow F \cup\{(W[j], Q[j])\}$
$H_{i} \leftarrow G\left[V_{i}\right]+F \quad \triangleright$ Not explicitly constructed
if $v \in \operatorname{LEX}-\operatorname{DFS}\left(H_{i}, s, W\left[\ell_{i}-1\right]\right)$ then return true
return false

## 4 Tree Decomposition using Small Space

In this section, we present a tree-decomposition algorithm, which uses $O\left(w n^{1 / 2} \log n\right)$-bit space and outputs a decomposition of width $O\left(w n^{1 / 2} \log n\right)$ for any undirected graph $G=(V, E)$ of treewidth $w \leq \sqrt{n}$. We first introduce a space-saving variant of the known weighted balanced separator algorithm.

- Lemma 10 (Extended from Theorem 1.1 of Fomin et al. [24]). There exists a polynomial-time algorithm that, given a graph $G$ on $n$ vertices, any vertex-weight function $\mu$, and a positive integer $k$, either provides a weighted $O(1)$-balanced separator of $G$ with respect to $\mu$ consisting
of $O\left(k^{2}\right)$ vertices, or concludes that the treewidth of $G$ is more than $k$. The algorithm uses the memory space required for finding the minimum unweighted s-t vertex cut in $G$ plus $O\left(k^{2} \log n\right)$ bits.

Proof. We refer to the algorithm proposed in Theorem 1.1 of [24] as SEp. Except for the space complexity matter, the correctness of the lemma completely follows that of SEP presented in [24]. Thus it suffices to show how SEP is implemented using the memory space claimed in this lemma. The algorithm SEP roughly works as follows. Let $G$ be any input graph of treewidth at most $k$.

1. First the algorithm SEP constructs any rooted spanning tree $T$ of $G$, and decomposes it into a set $\mathcal{X}$ of $\Theta(k)$ connected subtrees such that at most $O(k)$ vertices can belong to two or more subtrees in $\mathcal{X}$ : Let $T(x)$ be the subtree of $T$ rooted by $x$. The algorithm SEP starts with $T^{\prime}=T$, and for $i=0,1, \ldots$, iteratively finds the deepest vertex $x_{i}$ such that $\left|V_{T^{\prime}\left(x_{i}\right)}\right| \geq \alpha n / k$ holds for an appropriate constant $\alpha$. Then the subtree $T^{\prime}\left(x_{i}\right)$ is split into several subtrees of size $\Theta(n / k)$ sharing $x_{i}$, each of which becomes a member of $\mathcal{X}$. After updating $T^{\prime}$ as $T^{\prime} \leftarrow T^{\prime}-T^{\prime}\left(x_{i}\right)$, the algorithm proceeds to the next iteration.
2. For any $X_{i}, X_{j} \in \mathcal{X}$ such that $V_{X_{i}} \cap V_{X_{j}}=\emptyset$, SEP emulates the graph $H_{i, j}$ obtained from $G$ by contracting $X_{i}$ and $X_{j}$ into two vertices $x_{i}$ and $x_{j}$. Then it computes the minimum $x_{i}-x_{j}$ vertex cut in $H_{i, j}$. If there exists a pair $(i, j)$ such that the output cut contains at most $k$ vertices, the algorithm adds it to the separator set $U$.
3. The steps 1 and 2 are iteratively applied to the largest connected component after the removal of the computed vertex cut, until $U$ becomes an $O(1)$-balanced separator of $G$. It is proved in [24] that this iteration terminates within $O(k)$ times if the treewidth of the input graph is at most $k$.
The small-space implementation of step 1 is very similar with the algorithm shown in the proof of Lemma 6 . With the support of the emulator of $T$, finding $x_{i}$ and the emulation of $T^{\prime}$ can be done in the same way as the proof of Lemma 6 . The spanning tree $T$ can be emulated using the logspace undirected connectivity: We introduce an arbitrary logspacecomputable edge-weight function $g: E_{G} \rightarrow \mathbb{N}$ which assigns all edges with different weights. Let $e_{0}, e_{1}, \ldots, e_{m-1}$ be the sequence of all edges sorted in the ascending order of their weights, and $E_{i}=\left\{e_{0}, e_{1}, \ldots, e_{i-1}\right\}$. Then an edge $e_{i}=(u, v)$ is contained in the minimum spanning tree of $G$ with respect to $g$ if and only if $u$ and $v$ is connected in $G\left[E_{i}\right]$, which directly deduces the emulator of the minimum spanning tree.

Assuming an algorithm computing the set $\mathcal{X}, H_{i, j}$ can be emulated with a polynomial-time overhead. Thus the step 2 can be implemented within a polynomial time using $O\left(k^{2} \log n\right)$ bits (except for the space used by the vertex-cut algorithm).

### 4.1 A Small-Space Balanced Separator Algorithm

Let $I(G)$ be the maximum independent set of $G$, (if two or more maximum independent sets exist, an arbitrary one is chosen), and $\bar{I}(G)=V \backslash I(G)$ for short. The first key ingredient of our algorithm is a space-saving algorithm for the minimum $s-t$ vertex cut problem.

- Lemma 11. Let $G$ be any n-vertex undirected graph. For any $s, t \in V_{G}$, the minimum (unweighted) s-t vertex cut of $G$ can be found in a polynomial time using $O(|\bar{I}(G)| \log n)$ bits.

Proof. The algorithm basically follows the vertex-cut version of Ford-Fulkerson algorithm, which manages a set of augmenting paths for recognizing the current residual graph (see Section 3.5 in [35] for example). In the case of unweighted vertex cuts, any set of augmenting paths is a set of vertex-disjoint $s$ - $t$ paths in $G$. Letting $L$ be the maximum total length of
managed $s$ - $t$ paths, the algorithm can be implemented using $O(L \log n)$-bit space. Thus it suffices to show that $L=O(|\bar{I}(G)|)$ holds for any instance $G$. Let $R_{1}, R_{2}, \ldots, R_{y}$ be any set of vertex-disjoint $s$ - $t$ paths in $G$. Since no two vertices in $I(G)$ consecutively appears in any path, we have $\left|V_{R_{i}} \cap I(G)\right| \leq\left|V_{R_{i}} \cap \bar{I}(G)\right|+1$ for any $i \in[1, y]$. Then $\left|V_{R_{i}}\right| \leq 3\left|V_{R_{i}} \cap \bar{I}(G)\right|$ holds. Since $V_{R_{i}}$ for all $i \in[1, y]$ are mutually disjoint, it follows $\sum_{1 \leq i \leq y}\left|V_{R_{i}}\right|=O(|\bar{I}(G)|)$. The lemma is proved.

Consider a partition of $V_{G}$ into a family $\mathcal{P}=\left\{P_{0}, P_{2}, \ldots, P_{N-1}\right\}$ of $N$ subsets such that $G\left[P_{i}\right]$ is a connected subgraph of $G$. We denote by $G / \mathcal{P}$ the graph obtained by contracting each subgraph $P_{i}$ into a single vertex $u_{i}$ with weight $\mu\left(u_{i}\right)=\left|V_{P_{i}}\right|$ (parallel edges are merged into the single one). The second key ingredient is to reduce the (approximate) tree decomposition of $G$ into that of another graph $G / \mathcal{P}$ for an appropriate partition $\mathcal{P}$ such that $|\bar{I}(G / \mathcal{P})|$ becomes small. Since treewidth never increases by edge contraction, $G / \mathcal{P}$ also has a treewidth at most $k$. Thus we can run the balanced-separator algorithm obtained from Lemmas 10 and 11 on $G / \mathcal{P}$ using only $O\left(\left(|\bar{I}(G / \mathcal{P})|+k^{2}\right) \log n\right)$-bit space. For the computed separator $B$, we replace each $u_{i} \in B$ by $V_{P_{i}}$. That is, we create a vertex subset $B^{\prime}=\bigcup_{u_{i} \in B} V_{P_{i}}$, which is obviously a balanced separator of $G$ consisting of $O\left(k^{2} \max _{i}\left\{\left|V_{P_{i}}\right|\right\}\right)$ vertices. To attain the space complexity of Theorem 2 following this approach, we have to construct a polynomial-time algorithm outputting the partition $\mathcal{P}$ satisfying $|\bar{I}(G / \mathcal{P})|=O\left(k n^{1 / 2}\right)$ and $\left|P_{i}\right|=O\left(n^{1 / 2} / k\right)$ for any $P_{i} \in \mathcal{P}$. In addition, we have to guarantee that the algorithm uses only $O\left(k n^{1 / 2} \log n\right)$ bits.

We argue the implementation of such an algorithm. Let us define an arbitrary total ordering of edges in $E_{G}$. This can be easily realized by any ordering function $f: E \rightarrow \mathbb{N}$ which can be computed in logarithmic space (e.g., the lexicographic ordering of endpoint ID pairs). Let $e_{1}, e_{2}, \ldots, e_{m}$ be the sequence of all edges sorted in this order, and $E_{i}=\left\{e_{1}, e_{2}, \ldots, e_{i}\right\}$. For any subgraph $H \subseteq G$, we also define $S(H, v, i)$ as the set of the vertices which are reachable from $v$ in $H\left[E_{i}\right]$. The partition is constructed by the following algorithm:

1. Let $\mathcal{P}=\emptyset, R \leftarrow \emptyset$, and $H \leftarrow G$.
2. Find the minimum $\ell$ such that the largest connected component $C$ in $H\left[E_{\ell}\right]$ contains at least $n^{1 / 2} / k$ vertices, and add $V_{C}$ to $\mathcal{P}$. Letting $v$ be the vertex with the smallest ID in $V_{C}$, we store the pair $(v, \ell)$ into $R$.
3. Update $H \leftarrow G-\bigcup_{(v, \ell) \in R} S(G, v, \ell)$.
4. Repeat steps 2 and 3 until the size of any connected component in $H$ becomes less than $n^{1 / 2} / k$.
5. Letting $Q=\bigcup_{P_{i} \in \mathcal{P}} P_{i}$, add $V_{C^{\prime}}$ to $\mathcal{P}$ for any connected component $C^{\prime}$ in $G-Q$.

The actual algorithm does not store $\mathcal{P}$ explicitly. Except for step 5 , the set $\mathcal{P}$ is write-only, and it is easy to verify that steps 1-4 can be implemented only with the space for storing $R$. The matter of the space complexity relies on how to restore the set $Q$ in step 5 only from the information of $R$. Let $C_{i}$ be the connected component found in the $i$-th iteration of step 2 , and $\left(u_{i}, j_{i}\right)$ be the entries added to $R$ then. We denote by $H_{i}$ the graph stored in $H$ immediately after the $i$-th iteration of step 2 . It is easy to enumerate the vertices in $S(G, u, j)$ by the logspace undirected $s$ - $t$ connectivity algorithm [41] (recall that we omit the orientation of edges in considering the tree decomposition for directed graphs), and thus we obtain an emulator of $H_{i}$ using the information in $R$ and extra $O(\log n)$-bit space. It also yields an algorithm for enumerating $C_{i}=S\left(H_{i-1}, u_{i}, j_{i}\right)$. Consequently, this algorithm works using only $O(|R| \log n)$-bit space. The following lemma guarantees the correctness of the output.

- Lemma 12. Let $\mathcal{P}=\left\{P_{0}, P_{1}, \ldots, P_{N-1}\right\}$ be the partition outputted by the algorithm above. Then for any $P_{i} \in \mathcal{P},\left|P_{i}\right| \leq 2 n^{1 / 2} / k$ holds. In addition, $|\bar{I}(G / \mathcal{P})| \leq k n^{1 / 2}$ holds.

Proof. We first show that $\mathcal{P}$ is actually a partition of $V_{G}$. By step 5 , it is obvious that any vertex in $V_{G}$ is contained in at least one subset $P_{i} \in \mathcal{P}$. The subset added in step 5 does not intersect other subsets. Consider any two subsets $C_{i}$ and $C_{k}$ added in step $2(i<k)$. By the construction of $C_{i}$ and $C_{k}$, we have $C_{i}=S\left(H_{i-1}, u_{i}, j_{i}\right)$ and $C_{k}=S\left(H_{k-1}, u_{k}, j_{k}\right)$. Since $i<k$ holds, $H_{k-1}$ does not contain any vertex in $S\left(G, u_{i}, j_{i}\right)$. It implies $H_{k-1}$ does not contain any vertex in $C_{i}=S\left(H_{i-1}, u_{j}, j_{i}\right)$ because of $H_{i-1} \subseteq G$. That is, $C_{i}$ and $C_{k}$ are mutually disjoint. Next, we bound the size of each $P_{i}$. Any subset added in step 5 has a cardinality less than $n^{1 / 2} / k$. Since the algorithm finds the smallest $\ell$ such that the cardinality of $C$ becomes at least $n^{1 / 2} / k$, any connected component in $H_{i-1}\left[E_{\ell-1}\right]$ has a size less than $n^{1 / 2} / k$. Thus the size of any connected component in $H_{i-1}\left[E_{\ell}\right]$ is at most $2 n^{1 / 2} / k$. Finally, we show $|\bar{I}(G / \mathcal{P})| \leq k n^{1 / 2}$. We call a subset $P_{i} \in \mathcal{P}$ a red subset if $P_{i}$ is added in step 5 , and also call the corresponding vertex $u_{i} \in V_{G / \mathcal{P}}$ a red vertex. It is obvious that any red subset $P_{i}$ has no outgoing edge to other red subsets in $G$, the set of all red vertices forms an independent set of $G / \mathcal{P}$. Since the cardinality of any non-red subset is at least $n^{1 / 2} / k$, at most $k n^{1 / 2}$ non-red vertices exist in $G / \mathcal{P}$. It implies $\bar{I}(G / \mathcal{P}) \leq k n^{1 / 2}$. The lemma is proved.

This lemma also implies that the size of $R$ is at most $k n^{1 / 2}$. Thus the space complexity of the algorithm is bounded by $O\left(k n^{1 / 2} \log n\right)$ bits. The combination of Lemmas 10, 11, and 12 yields the following lemma.

- Lemma 13. There exists a polynomial-time algorithm that, given a graph $G$ on $n$ vertices and a positive integer $k$, either provides an $O(1)$-balanced separator of $G$ consisting of $O\left(k n^{1 / 2}\right)$ vertices, or concludes that the treewidth of $G$ is more than $k$. The algorithm uses $O\left(k n^{1 / 2} \log n\right)$-bit space.

The remaining part is to transform the separator algorithm into a tree-decomposition algorithm. The following lemma obviously deduces Theorem 2 .

- Lemma 14. Assume an algorithm Alg which outputs an $O(1)$-balanced separator of size $k(w, n)$ for any graph $G$ of treewidth $w$ using $g(n)$-bit space. Then there exists a polynomialtime tree decomposition algorithm which outputs a tree decomposition of width $O(k(w, n) \log n)$ using $O\left(g(n)+k(w, n) \log ^{2} n\right)$-bit working memory.

Proof. The proof is constructive. We refer to the constructed algorithm as Alg. The algorithm $A l g$ first computes an $O(1)$-balanced separator $U$ of $G$, and then recursively constructs the tree decomposition of each connected component in $G-U$ whose size is larger than $k(w, n)$. A component having at most $k(w, n)$ vertices is treated as the subgraph with the tree decomposition consisting of a single bag of the whole component. Let $H_{0}, H_{1}, \ldots, H_{\ell-1}$ be the connected components in $G-U$ sorted in the order specified by the enumeration algorithm of Lemma 5, and $T_{i}$ be the output sequence of the recursive call for $H_{i}$. Defining the binary operator $\circ$ for concatenating two sequences, let $T=T_{0} \circ T_{1} \circ \cdots \circ T_{\ell-1}$. We further define $m_{i}=\sum_{0 \leq j \leq i}\left|T_{i}\right|$. The algorithm $A l g$ modifies the sequence $T$ in the following way: Any pair $(B, q) \in \bar{T}_{i}$ is replaced by $\left(B \cup U, q+m_{i}\right)$ if $q \neq-1$ or $\left(B \cup U, m_{\ell-1}\right)$ otherwise. Finally, we append the pair $(U,-1)$ at the tail of $T$. Intuitively, this modification is for relabeling bag identifiers to guarantee their uniqueness, and for merging all the subgraph decompositions into a single one rooted by the last bag $(U,-1)$. It is easy to verify that the modified sequence is a tree decomposition of $G$. By Lemma 5 , the input graph at any recursion level is emulated with a polynomial-time overhead. Thus the running time at any recursion level is a polynomial time. One recursive call remove one bag from the input graph, the number of recursive calls is bounded by $n$. Totally the algorithm $A l g$ finishes within a polynomial time.

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Let $w_{i}$ be the maximum width of all the output tree decompositions at the $i$-th recursion level. Then we have the inequality $w_{i+1} \leq k(w, n)+w_{i}$. Since the separator is $O(1)$-balanced, the recursion finishes at the depth of $O(\log n)$. It implies that the maximum bag size is $O(k(w, n) \log n)$. The modification of the sequence $T$ can be done in a streaming way. Thus the space complexity of $\operatorname{Alg}$ is $O(|U| \log n)=O(k(w, n) \log n)$ bits per one recursion. The largest space consumption is at the bottom-level recursion, where $\operatorname{Alg}$ uses $O\left(k(w, n) \log ^{2} n\right)$ bits in total.

The lemma above also deduces the consequence for planar graphs in Corollary 3. Since planar graphs admit a $\tilde{O}(\sqrt{n})$-bit space $O(1)$-balanced separator algorithm [27], we can use it instead of Lemma 13.

## 5 Conclusion

In this paper, we presented a Lex-DFS algorithm for directed graphs of bounded treewidth $w$. It is not only the first algorithm solving Lex-DFS using sublinear space for $w=\omega(1)$, but also the first algorithm solving directed $s$ - $t$ reachability in the same situation. One of the key tools is a new sublinear-space tree decomposition algorithm covering the case of moderate (small but non-constant) treewidth. The authors believe that this is a strong tool for designing small-space algorithms for other fundamental graph problems on bounded-treewidth graphs.

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[^1]:    ${ }^{2} \mathrm{~L}$ is the class of problems decidable with $O(\log n)$-bit working space (and thus in $\operatorname{poly}(n)$ time).

[^2]:    ${ }^{3}$ Since one can choose an arbitrary $\epsilon>0$, $\operatorname{poly} \log (n)$ factors are absorbed in the part of $n^{\epsilon}$ in the statements of this corollary.

[^3]:    ${ }^{4}$ Intuitively, the gray path $S_{t}$ is the path from $s$ to $h_{t}$ in the Lex-DFS tree of $G$.

[^4]:    ${ }^{5}$ Precisely, if two directed edges $(u, v)$ and $(v, u)$ exist, omitting their orientation causes two multiedges between $u$ and $v$. Then those edges are merged into a single undirected edge.

