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Title	Isotropic conduction and negative photoconduction in ultrathin PtSe ₂ films
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Publication date	2020-11-09
Original citation	Urban, F., Gity, F., Hurley, P. K., McEvoy, N. and Bartolomeo, A. D. (2020) 'Isotropic conduction and negative photoconduction in ultrathin PtSe ₂ films', Applied Physics Letters, 117(19), 193102 (7 pp). doi: 10.1063/5.0021009
Type of publication	Article (peer-reviewed)
Link to publisher's version	https://aip.scitation.org/doi/abs/10.1063/5.0021009 http://dx.doi.org/10.1063/5.0021009 Access to the full text of the published version may require a subscription.
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Item downloaded from	http://hdl.handle.net/10468/10745

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Isotropic conduction and negative photoconduction in ultrathin PtSe₂ films

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Received: date; Accepted: date; Published: date

Abstract: PtSe₂ ultrathin films are used as the channel of back-gated field-effect transistors (FETs) that are investigated at different temperatures and under super-continuous white laser irradiation. The temperature-dependent behavior confirms the semiconducting nature of multilayer PtSe₂, with p-type conduction, a hole field-effect mobility up to $40 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ and significant gate modulation. Electrical conduction measured along different directions shows isotropic transport. A reduction of PtSe₂ channel conductance is observed under exposure to light. Such a negative photoconductivity is explained by a photogating effect caused by photo-charge accumulation in SiO₂ and at the Si/SiO₂ interface.

Keywords: PtSe₂; field effect transistor; laser irradiation; electrical conduction; temperature; negative photoconductivity;

The family of transition-metal dichalcogenides (TMDs), such as the most known MoS₂, WSe₂, WS₂, has been heavily investigated over the last decade due to their intriguing and layer-tunable properties combined with their ease of fabrication¹⁻⁷. The bulk materials, made of atomic layers held together by van der Waals forces, can be easily exfoliated to obtain single- or few-layer nanosheets^{8,9} and the electrical and optical properties of these materials are strongly dependent on their thickness^{10,11}. Indeed, the modulation of bandgap via changing the number of layers enables the use of TMDs as field-effect transistor (FET) channels in optical sensors with high photo-response^{12,13}. Moreover, the intrinsic n- or p-type doping is useful for the construction of p-n heterojunctions¹⁴⁻¹⁶.

To date, in addition to the exfoliation methods, various scalable and controllable growth techniques, mostly based on chemical vapor deposition (CVD) and laser ablation, have been developed, enabling the synthesis of large-area flakes with fine thickness control and consequent tailoring of the chemical, optical and electrical properties¹⁷⁻¹⁹.

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Different from MoS₂, WSe₂ or other group-6 TMDs, dichalcogenides based on group-10 transition metals have just recently gained popularity and not been fully explored yet.

Platinum diselenide (PtSe₂) and ditelluride (PtTe₂), along with their palladium analogues, were theoretically predicted in the sixties of the last century but have only recently been isolated and investigated as 2D materials²⁰⁻²⁶. Numerical calculations of their electronic structure and properties encouraged their use in electronic applications. These materials crystallize in an octahedral lattice structure where the transition metal atoms are coordinated with six chalcogens. Each layer is a two-dimensional packed array of metal atoms sandwiched between two similar arrays of chalcogens, and bonded by van der Waals forces to form the multilayer structure. The main difference between TMDs of group 6 and the group 10 is that the presence of d-electrons in the group-10 transition metals gives rise to additional semiconductor bands²⁰.

PtSe₂, the material under study in this paper, is a semimetal in bulk form, with slightly indirect overlap of the conduction and valence bands, that undergoes a semimetal-to-semiconductor transition when it is thinned to a few atomic layers²⁷⁻²⁹. Monolayer PtSe₂ has an indirect bandgap of ~1.2 eV, which is expected to reduce to 0.3 eV for the bilayer^{22,30}; the bandgap of multilayer PtSe₂ is not well known, but according to Ansari et al. the extrapolated theoretical bandgap of 2.5 nm thick PtSe₂ is ~0.25 eV²⁹.

The variability of electrical properties, combined with environmental stability, is attracting growing attention from both fundamental and application standpoints. For instance, the bandgap covers the spectral range that is important for telecommunications and solar energy harvesting³¹, and the carrier mobility (theoretically predicted up to 4000 cm²V⁻¹s⁻¹³² and experimentally found to be around 200 cm²V⁻¹s⁻¹^{33,34}), competitive with black phosphorus, can enable fast electronic devices^{33,35-37}. Other interesting properties include the catalytic activity^{38,39} and the sensitivity to analytes such as NO₂, NO, NH₃ or ethanol^{40,41}. Furthermore, the direct selenization of platinum films on a chosen substrate, and the rather low temperatures (400 °C) required for the thermally assisted synthesis of PtSe₂, might give this material a boost for integration in semiconductor technologies for mass production^{21,22,28}.

In this work, we study the electrical conduction in multilayer PtSe₂ sheets along orthogonal directions, using back-gated field-effect transistors. The electrical behavior is analyzed over a wide temperature range demonstrating semiconducting p-type conduction, with reasonable gate modulation and relatively high hole mobility. The electrical conduction along orthogonal directions does not show any significant difference and the intrinsic p-type behavior of the transistors is stable under air exposure. Moreover, laser irradiation reveals an unusual phenomenon. Owing to their semiconducting nature, light exposure induces carrier photogeneration and increases the channel conductance in most TMD transistors, as largely reported in the literature^{42,14,43,6,44}. Conversely, irradiation of PtSe₂ transistors by laser pulses unexpectedly results in a reduction of the channel conductance, following an opposite trend. We explain such a negative photoconductivity (NPC) by a photogating effect, which arises from photocharge accumulation in the SiO₂ dielectric and at the Si/SiO₂ interface.

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The devices were fabricated over Si/SiO₂ substrate (85 nm thermally grown oxide on p-type silicon, $\rho \sim 0.001 - 0.005 \Omega\text{cm}$). The PtSe₂ film on SiO₂ was obtained by direct selenization of a previously sputtered Pt film (nominal thickness 0.7 nm) following procedures described elsewhere⁴⁰. Briefly, Pt films were placed in the center of the downstream zone of a two-zone furnace where they were heated to 400 °C. Se pellets were independently heated to 220 °C in the upstream zone and 150 sccm of Ar:H₂ (90%:10%) carried Se vapor from the upstream to the downstream zone, where it reacted with the Pt films. A growth time of 2 hours ensured that Pt was completely converted to PtSe₂. Natural cool-down followed the selenization process. During this process the film thickness increases by a factor of $\sim 3.5 - 4$ ²¹.

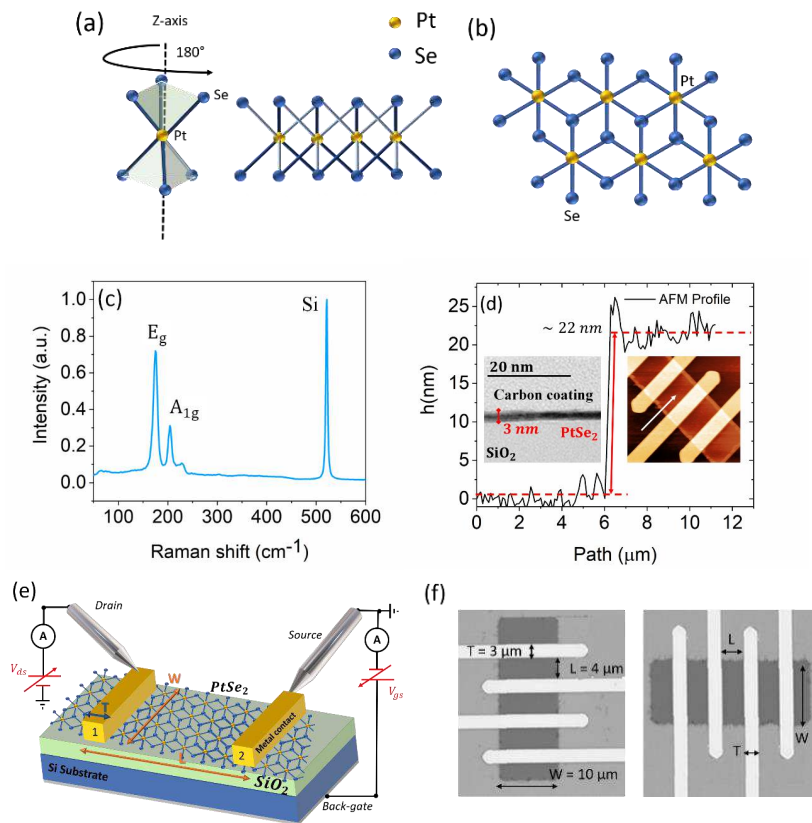


Figure 1. (a) Scheme of the octahedral basis and layered structure of PtSe₂ layer and (b) its hexagonal top viewed array. (c) Raman characterization of the prepared PtSe₂ sheet, showing the two PtSe₂ peaks at 176 cm⁻¹ and 205 cm⁻¹, and the Si peak. (d) AFM step height profile; the insets show a TEM and an AFM image of the PtSe₂ film. (e) Schematic of the measurement setup. (f) Optical images of two selected devices in the horizontal and vertical configuration.

The PtSe₂ film was transferred using a polymer-based process from the growth substrate to a fresh Si/SiO₂ substrate²⁹, and it was then patterned using photoresist masking and a SF₆-based inductively coupled plasma (ICP) etching process. This was followed by patterning Ni: Au (20 nm : 150 nm) metal contacts using a standard lift-off process.

The as-synthesized PtSe₂ film has a 1T crystal structure, where six selenium atoms are bonded to a platinum atom located at the center of an octahedral geometry, as shown in Figure 1(a)⁴⁵. The lattice in a top view (see Figure 1(b)) appears as two hexagonal arrays, shifted with respect to each other, with Pt (yellow spheres) and Se (blue spheres) atoms centering the basis^{40,46}.

Raman spectroscopy was used to characterize the as-synthesized films. The characteristic E_g ($\sim 176\text{ cm}^{-1}$) and A_{1g} ($\sim 205\text{ cm}^{-1}$) modes (see Figure 1(c)) were observed confirming the synthesis of PtSe₂. The position and relative intensity of these modes is consistent with the synthesis of multilayer PtSe₂⁴⁶. The step height measured by atomic force microscopy (AFM) is $\sim 22\text{ nm}$ (Figure 1(d)): it includes the thickness of the PtSe₂ film, that is about $\sim 3\text{ nm}$, and 19 nm SiO₂ over-etch applied during the PtSe₂ film patterning. The $\sim 3\text{ nm}$ PtSe₂ thickness is measured by transmission electron microscopy (TEM) (inset of Figure 1(d)) and corresponds to about 6 layers, being the PtSe₂ monolayer $\sim 0.5\text{ nm}$ thick⁴⁷.

The devices were measured in two- and four-probe configurations in a Janis Probe Station (Janis ST-500 probe station) equipped with four nanoprobe connected to a Keithley 4200 SCS (semiconductor characterization system), under different conditions. A scheme of the contacted back-gated device is shown in Figure 1(e): The metal contacts were used as the drain and source electrodes while the probe station chuck, connected to the silicon substrate, provided the gate voltage.

The following analysis was conducted on two main types of devices, having the same channel length (L) and width (W) and contact leads (T) in the direction of the channel. The devices were fabricated from the same PtSe₂ sheet, simultaneously patterned to form a horizontal and a vertical channel, an example is displayed by the optical microscope images of Figure 1(f). Identical contacts, differing only in orientation, allow the measurement of the channel conductance in the two perpendicular directions.

The electrical measurements were performed at a constant air pressure of 1 mbar and at different temperatures, as well as in dark conditions and under irradiation from a super-continuous laser source (NKT Photonics, Super Compact, wavelength ranging from 450 nm to 2400 nm, at 30 mW/cm²).

Using the two-probe configuration, we studied the variation of the PtSe₂ conductance G as a function of temperature T from 400 K to near liquid-nitrogen temperature. The G-T curves for the horizontal and vertical samples, reported in Figure 2(a), show similar trends with the channel conductance decreasing when T is slowly cooled down from 400 K to 90 K. The G-T behavior reveals the semiconducting nature of PtSe₂ ultrathin films.

The I-V current-voltage characteristics, at zero gate voltage, in two- and four-probe configurations, are reported in the inset of Figure 2(a), for both the two chosen horizontal- and the vertical-channel devices. The comparison of two- and four-probe measurements, at 290 K, with no appreciable difference, confirms the

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negligible effect of the metal contacts (with a contact resistance $\sim 1 k\Omega\mu m$) on the channel conductance as well as the ohmic behavior of the semiconductor/metal junctions, enabling the use of the two-probe configuration for further analysis.

To better understand the semiconducting nature of the material, we performed transfer characteristic measurements at two extreme temperatures, 290 K and 90 K. The gate voltage was intentionally limited between -10 and 10 V to avoid the breakdown of the 85 nm gate oxide. Figure 2(b) shows decreasing conductance for positive gate voltage, indicating p-type intrinsic channel doping and a modulation of a factor 2 in the narrow explored gate voltage range, consistent with the narrow bandgap of multilayer PtSe₂. The p-type doping can be attributed to Pt vacancies as we reported elsewhere²⁹. Furthermore, the use of Ni as the contact material facilitates hole injection as the Ni Fermi level aligns to the top of the valence band of PtSe₂.

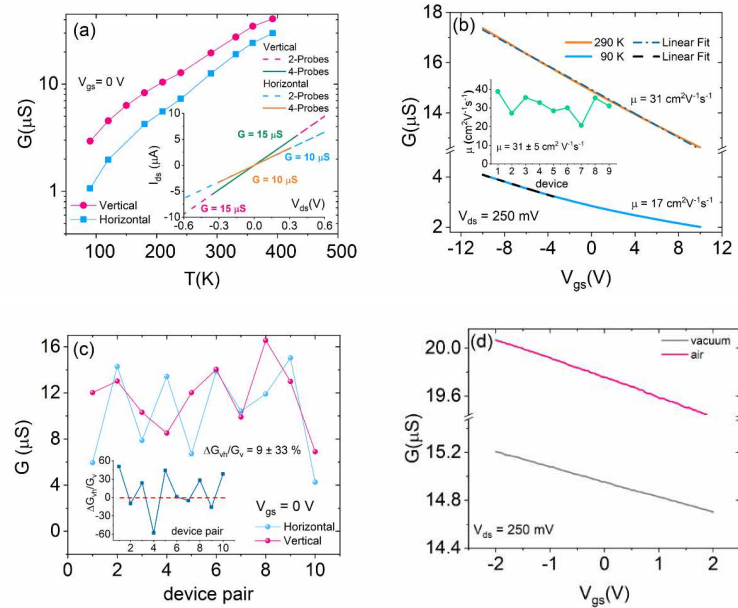


Figure 2. (a) G vs T curves for horizontal (cyan) and vertical (magenta) devices on semilogarithmic scale, at $V_{gs} = 0$ V; the inset shows the I - V characteristics for horizontal and vertical device in 2- (4-) probe configuration plotted in cyan dashed (orange straight) line and in magenta dashed (green straight) line, respectively. (b) Transfer characteristics at 90 K (cyan curve) and 290 K (orange) for the vertical device with the linear fits (black and blue dashed curves respectively) used to estimate the maximum hole mobility. The inset reports the hole mobility for different devices in an analog configuration, with a mean value of $\mu = 31 \pm 5 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ (1 sigma). (c) Room temperature conductance for horizontal (cyan) and vertical (magenta) device pairs. Each pair is formed by a horizontal and a vertical device fabricated next to one another to avoid potential impact of thickness. The inset shows the percentile variation $\frac{\Delta G_{vh}}{G_v} = \frac{G_v - G_h}{G_v} = 9 \pm 33 \%$ between the two conductances for each device pair. (d) Transfer characteristics in vacuum ~ 1 mbar (grey curve) and air (magenta curve), for the vertical device.

From the transfer characteristics, we evaluated the field effect mobility as $\mu = \frac{L}{WC_{ox}V_{ds}} \frac{dI_{ds}}{dV_{gs}}$ (I_{ds} and V_{ds} are the drain current and voltage, $C_{ox} = 3.11 \text{ nFcm}^{-2}$ is the SiO_2 capacitance per area, L and W are the channel length and width), finding values in the range $15 - 40 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at room temperature (as reported in the inset of Figure 2(b) for for the vertical devices) and $10 - 20 \text{ cm}^2\text{V}^{-1}\text{s}^{-1}$ at 90 K. The temperature dependence of the mobility is ascribed to Coulomb scattering due to fixed charges^{6,48}

Two competitive mechanisms, i.e. the ionized impurity scattering (Coulomb scattering) and the phonon scattering, determine the mobility versus temperature behavior^{49,50}. The first mechanism dominates at low temperatures, defining a mobility increase raising T, while phonon scattering prevails at higher temperatures (close to room temperature) causing a mobility decrease. Trapped charge in SiO_2 and intrinsic defects of the material contributes to Coulomb scattering, which can be reduced by the encapsulation with high-k dielectrics, such as HfO_2 , that are less prone to trap charge^{19,51}.

The measured mobility is up 5 times higher than the one measured in differently fabricated PtSe_2 devices^{21,34,52,53} and is even higher if compared to the mobility obtained in similar devices with or other TMDs such as PdSe_2 , MoS_2 or WSe_2 ^{6,54,55}.

The electrical conductance of several pairs of horizontal and vertical devices, at room temperature and grounded gate, has been investigated and is reported in Figure 2(c). The horizontal and vertical configurations were adopted to check for possible conduction anisotropy due to the 1T crystal phase of PtSe_2 . The device pairs were chosen as close as possible to each other (few μm) to avoid potential impact of thickness between distant zones of the wafer. The inset shows that the percentage difference between vertical and horizontal conductance, $\frac{\Delta G_{vh}}{G_v} = \frac{G_v - G_h}{G_v} = 9 \pm 33 \%$ (1 sigma), has large fluctuations but is consistent with the hypothesis of equal conduction in the two directions. The isotropic conduction is also consistent with the observation of randomly-oriented grains in ultrathin PtSe_2 films reported elsewhere^{40,46,56}.

Finally, Figure 2(d) shows that passing from vacuum ($\sim 1 \text{ mbar}$) to ambient pressure does not change the channel doping from p-type to n-type as observed with other TMD materials, strongly influenced by the environmental atmosphere^{6,24,57,58}. The p-type behavior of PtSe_2 is preserved when changing either the chamber temperature or pressure, and the conduction enhancement in air is attributed to the p-doping of oxygen molecules, which are electron acceptors.

To further investigate the behavior of PtSe_2 , we measured the electrical conduction of the device under pulsed laser irradiation, both in vacuum (1 mbar) and at room pressure.

Figure 3(a-b) show the device channel current under super-continuous laser irradiation, with the light switched on and off every two minutes for 12 cycles. The experiment was conducted with the laser source at 30 mW/cm^2 . After the on/off cycles, the laser is turned off and the device slowly returns to the initial conduction state. Surprisingly, each laser irradiation (on-pulse) induces a reduction of the current. This behavior, that is referred to as negative photoconductivity, is opposite to the current increase normally observed under light as an effect of photo-generation⁵⁹⁻⁶³.

It is important to note that the laser beam induces reversible changes in the irradiated device. The current lowering lasts for different periods of time, ranging from minutes to several hours. The comparison of Figure 3(a) and 3(b) shows that the restoration of the initial state is significantly faster in air at room pressure.

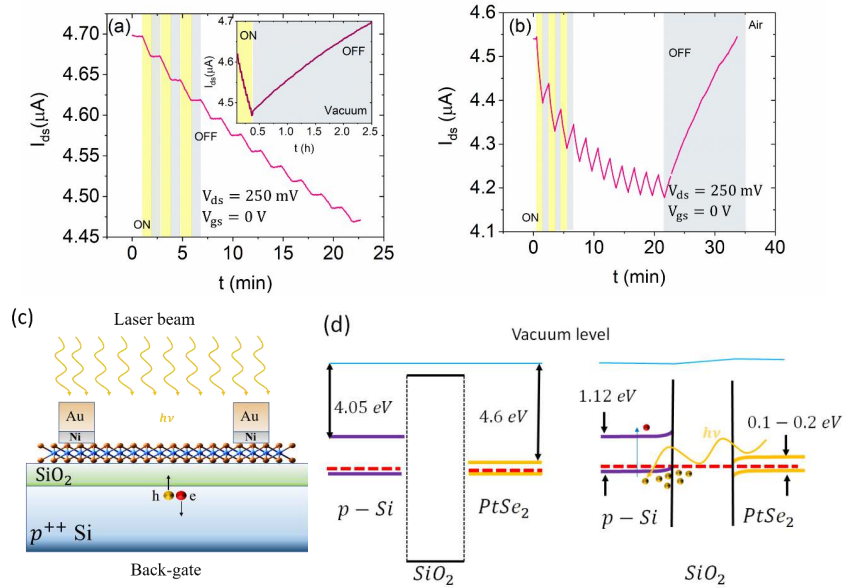


Figure 3. I_{ds} vs time characteristics under super-continuous laser beam irradiation, wavelength ranging from 450 nm to 2400 nm, at 1 mbar (a) and room pressure (b). The inset of panel (a) shows the recovery of the device current after several hours from the laser irradiation. (c) Schematic of the device under laser irradiation and (d) band diagram of the p-Si/SiO₂/PtSe₂ structure and charge accumulation under laser pulse.

The NPC mechanism can be explained by considering the dominance of the photogating effect caused by the electron-hole pair photogeneration in Si or PtSe₂ close to the SiO₂ interface⁶⁴. When the device is irradiated, the incident laser is mostly absorbed by the Si substrate leading to the creation of electron-hole pairs in the substrate (see Figure 3(c))⁶⁵. While the generated electrons are swept to ground, the photo-generated holes accumulate at the Si/SiO₂ interface or in SiO₂ trap states, favored by the vertical up-bending of the Si bands at the Si/SiO₂ interface due to the different electron affinity of Si (~4.05 eV) and PtSe₂ (~4.6 eV), as shown in Figure 3(d). When the measurements are performed in the air, photo-assisted desorption of O₂ or water molecules, can contribute to the negative photoconductivity, causing a reduction of the p-doping level of the channel⁶⁰.

The so-accumulated positive charge below the PtSe₂ channel plays the role of a positive gate voltage (photogating effect) which biases the device and decreases the channel current, according to the p-type behavior of the transistor.

The inset of figure 3(a) shows that the restoration of the initial state, in vacuum, is slower (about 2 hours) due to the limited diffusion of the positive photo-charge accumulated at the Si/SiO₂ interface or in the SiO₂ layer. The restoration becomes faster in air (15-20 min) because of the counterdoping effect of adsorbates, such as oxygen and water, which increase the p-doping of the PtSe₂ channel.

In conclusion, we investigated the electrical transport in PtSe₂ films along perpendicular directions. We used PtSe₂ as the channel material of field effect transistors in back-gated configuration. The conductance vs temperature behavior confirmed the semiconducting nature of ultrathin PtSe₂ films and the transistor characterization indicated intrinsic p-type conduction. The silicon back-gate enabled channel current modulation with field effect mobility up to $\sim 40 \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$ at room temperature. The comparison of the channel conductance along perpendicular directions for a large number of devices led to the conclusion that the conduction is isotropic being mainly dominated by the polycrystalline structure of the PtSe₂ film. Finally, we found a negative photoconductivity under laser irradiation, indicating a dominant photogating effect.

Acknowledgments: This work is partially supported by Science Foundation Ireland (SFI) through grants 15/IA/3131 and 12/RC/2278 and 15/SIRG/3329, 12/RC/2278_P2.

The authors thank MIUR, projects Pico & Pro ARS01_01061 and RINASCIMENTO ARS01_01088.

Availability of data: The data that support the findings of this study are available from the corresponding author upon reasonable request.

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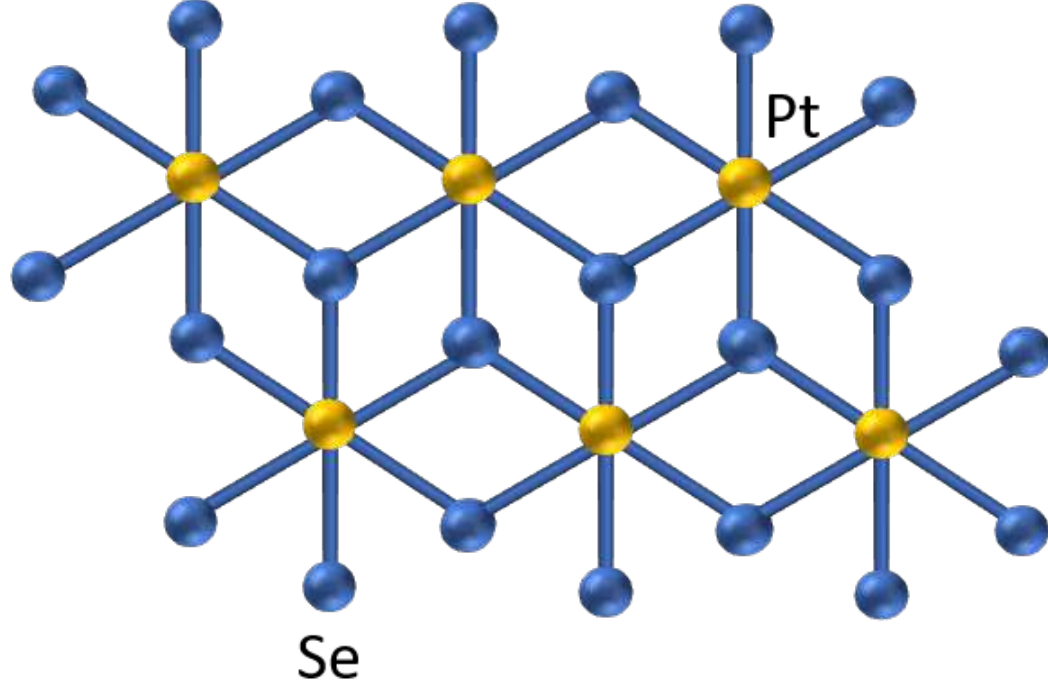
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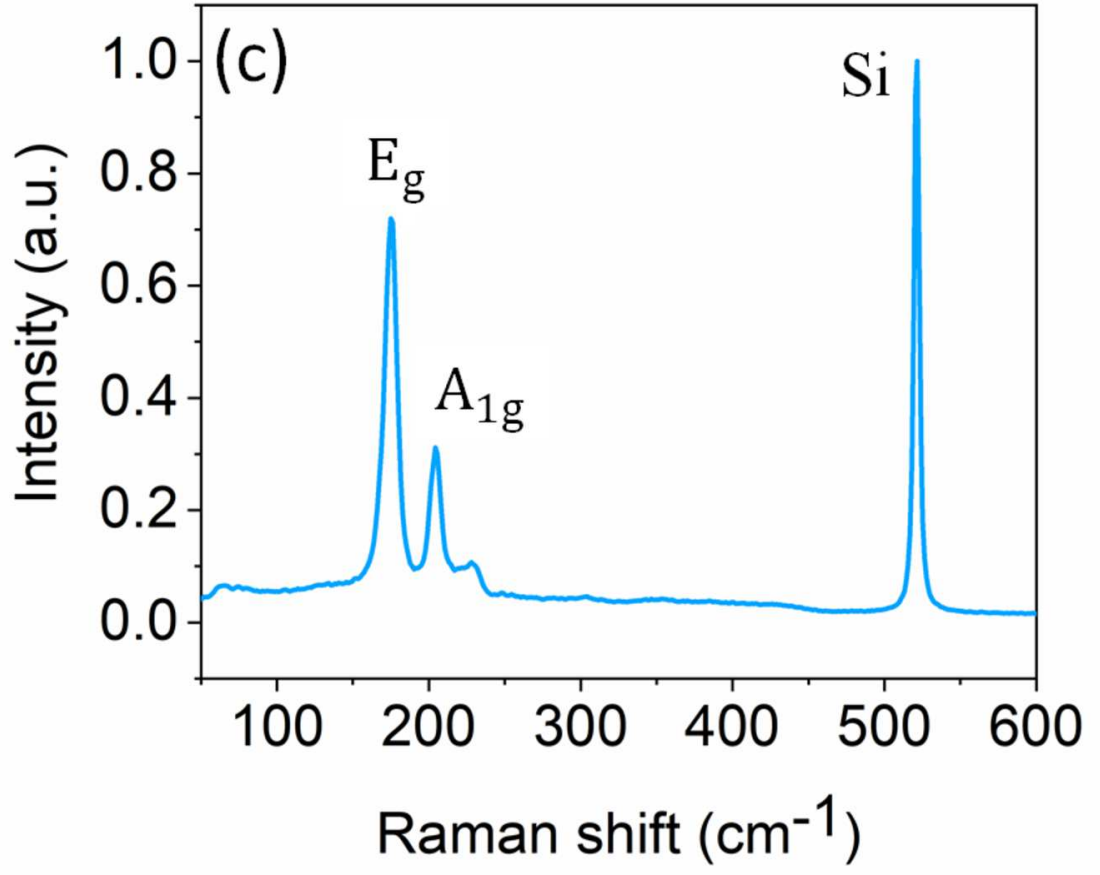
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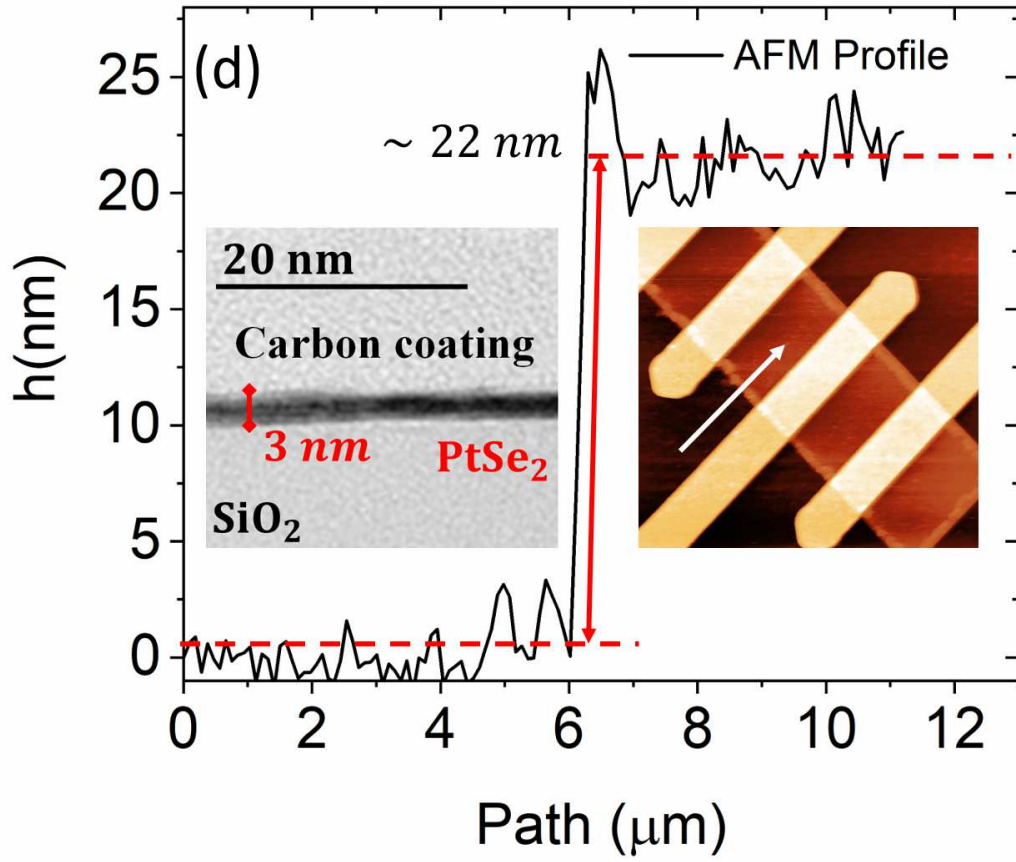


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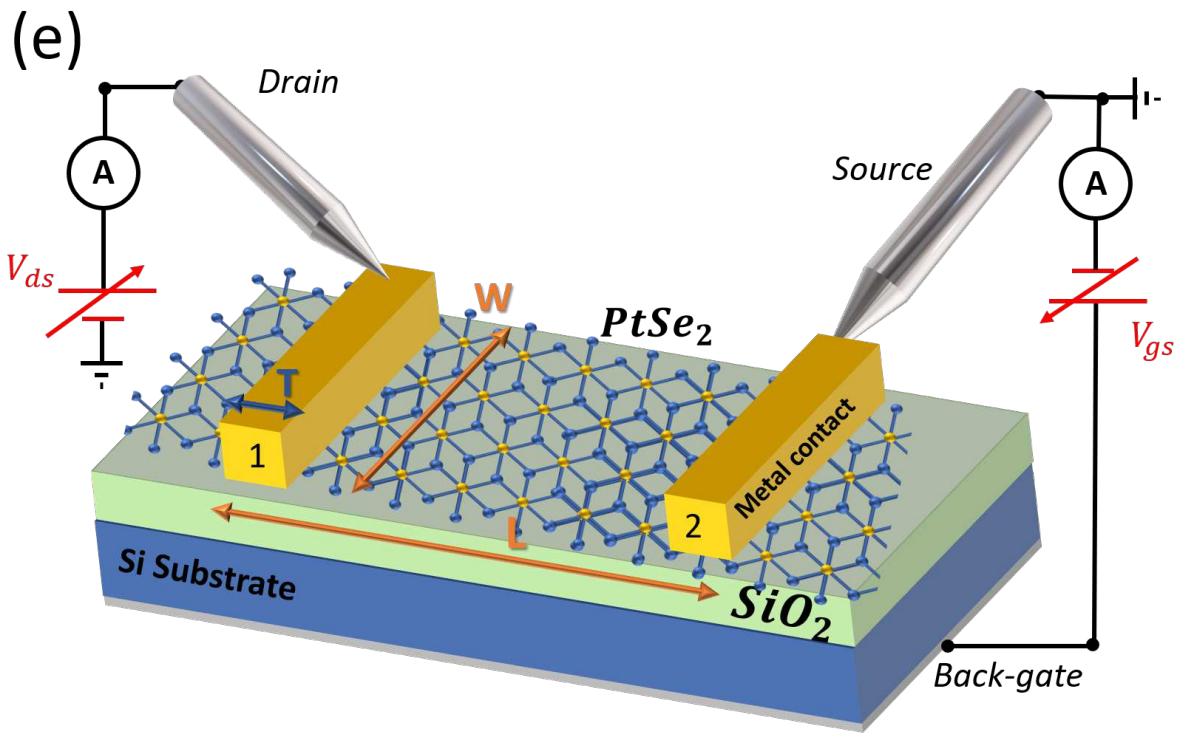


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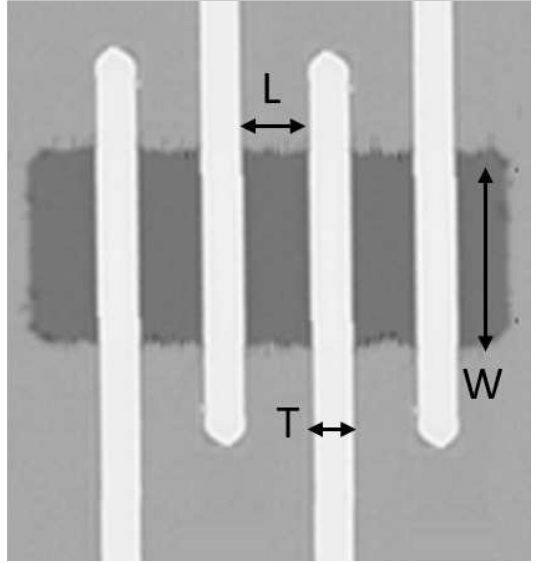
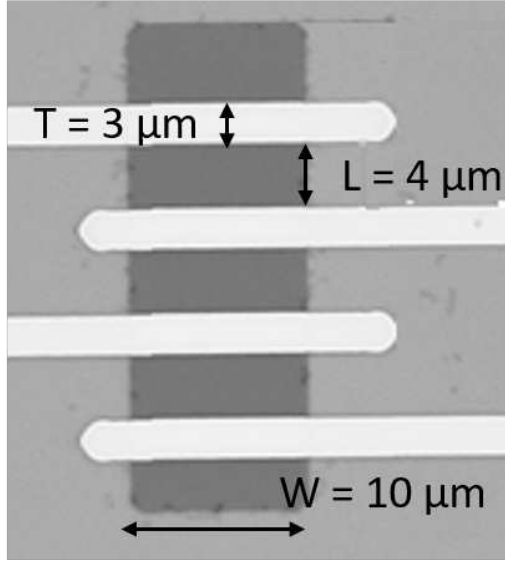
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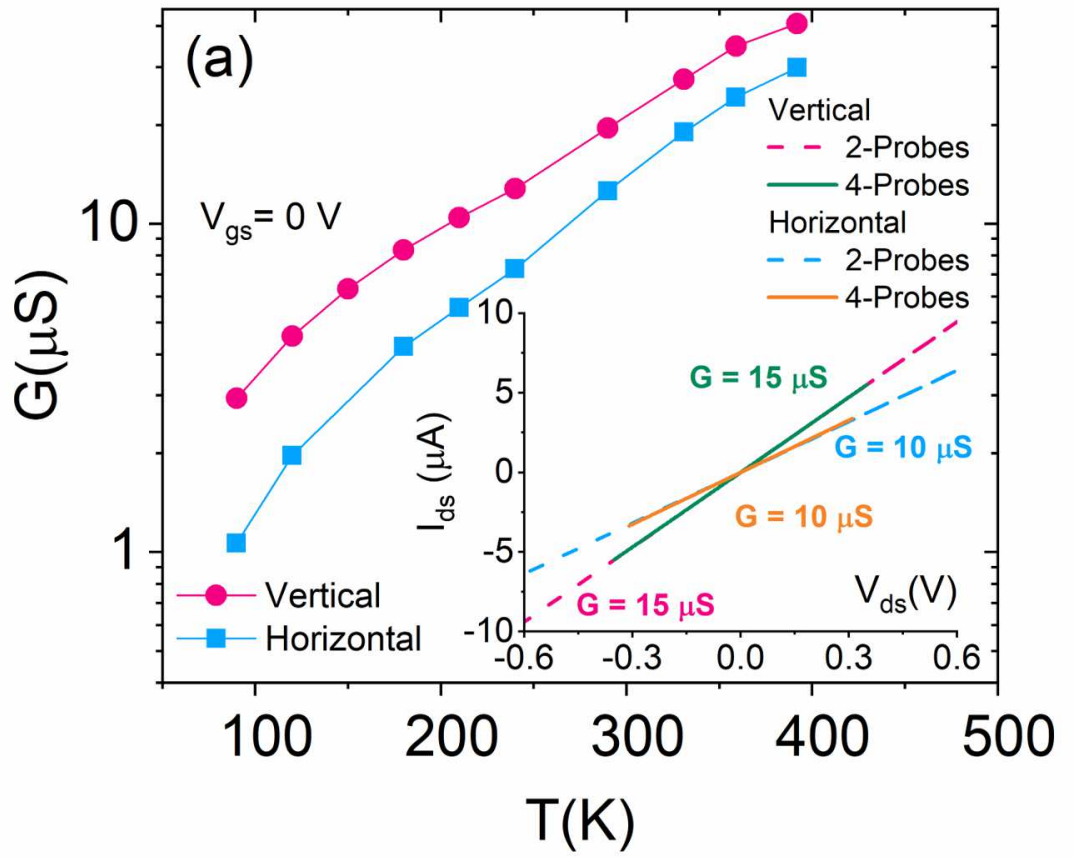
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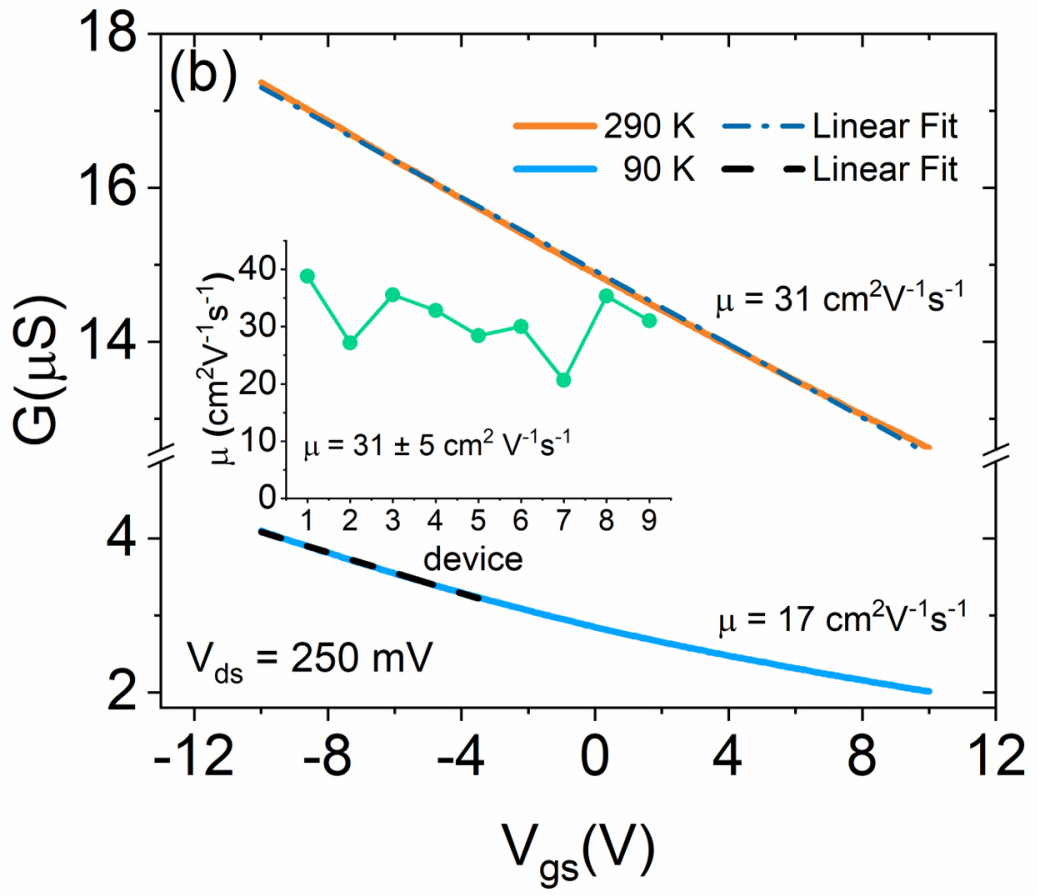
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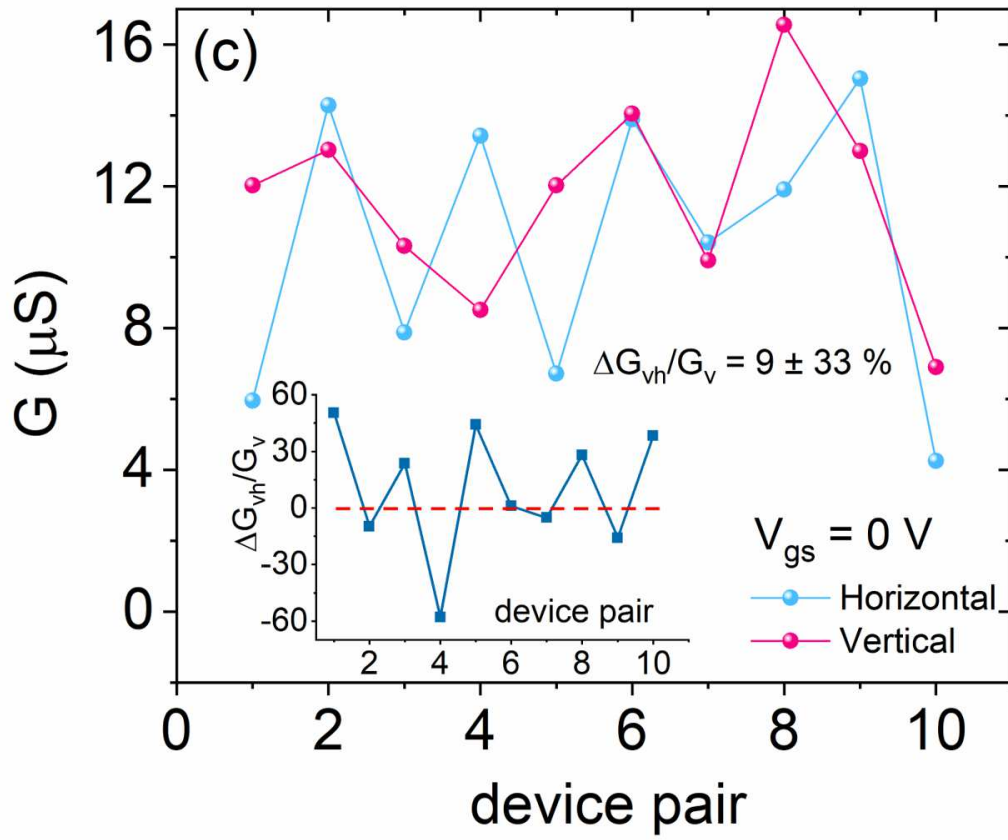
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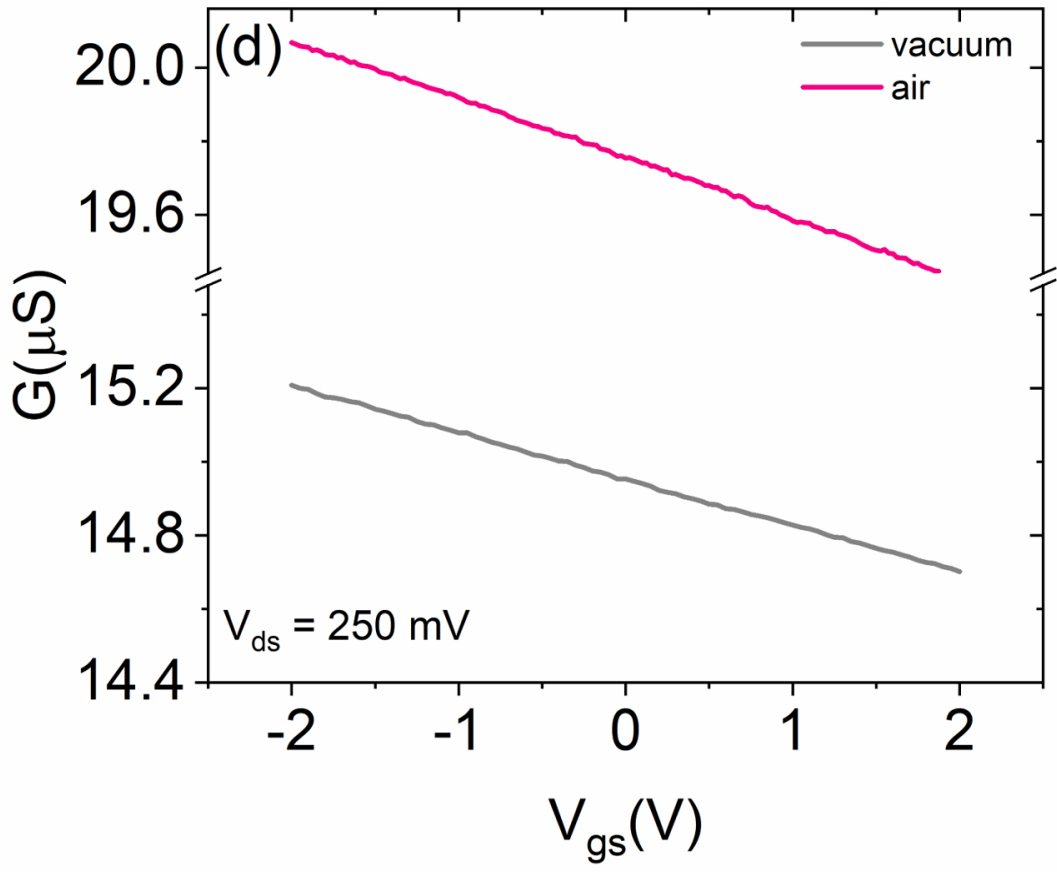
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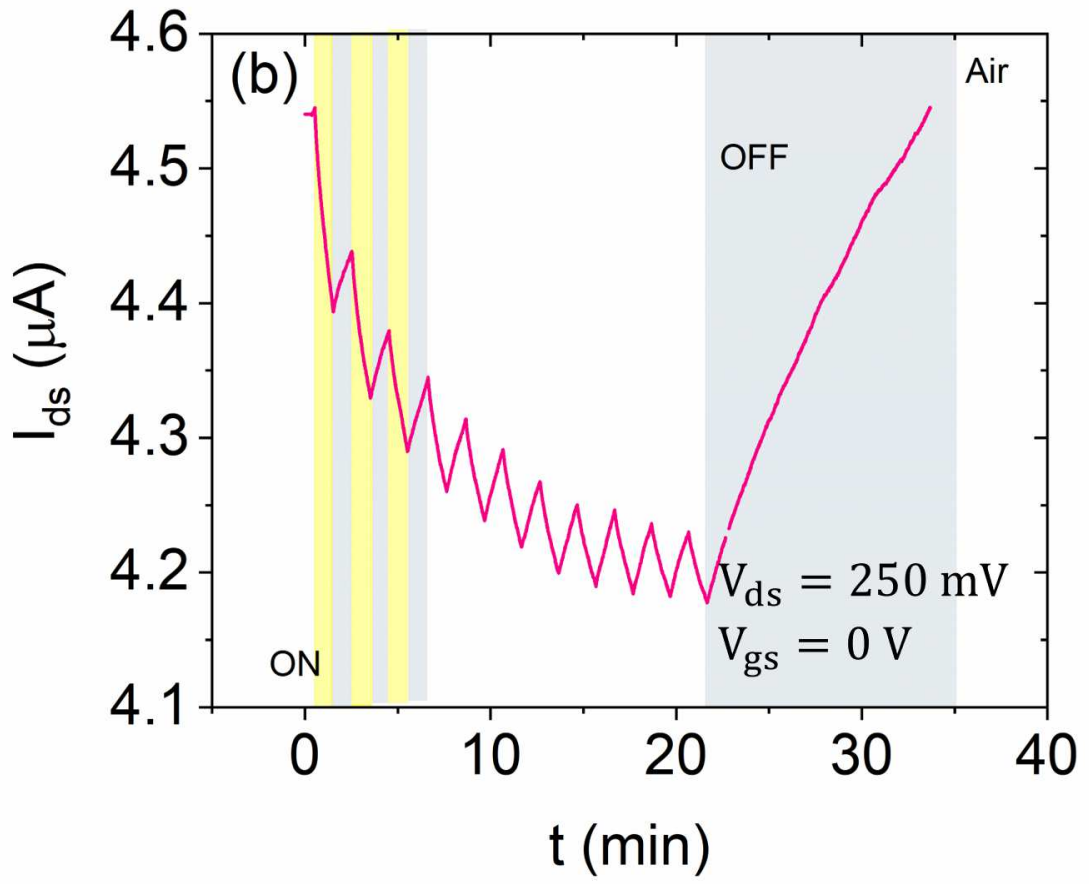


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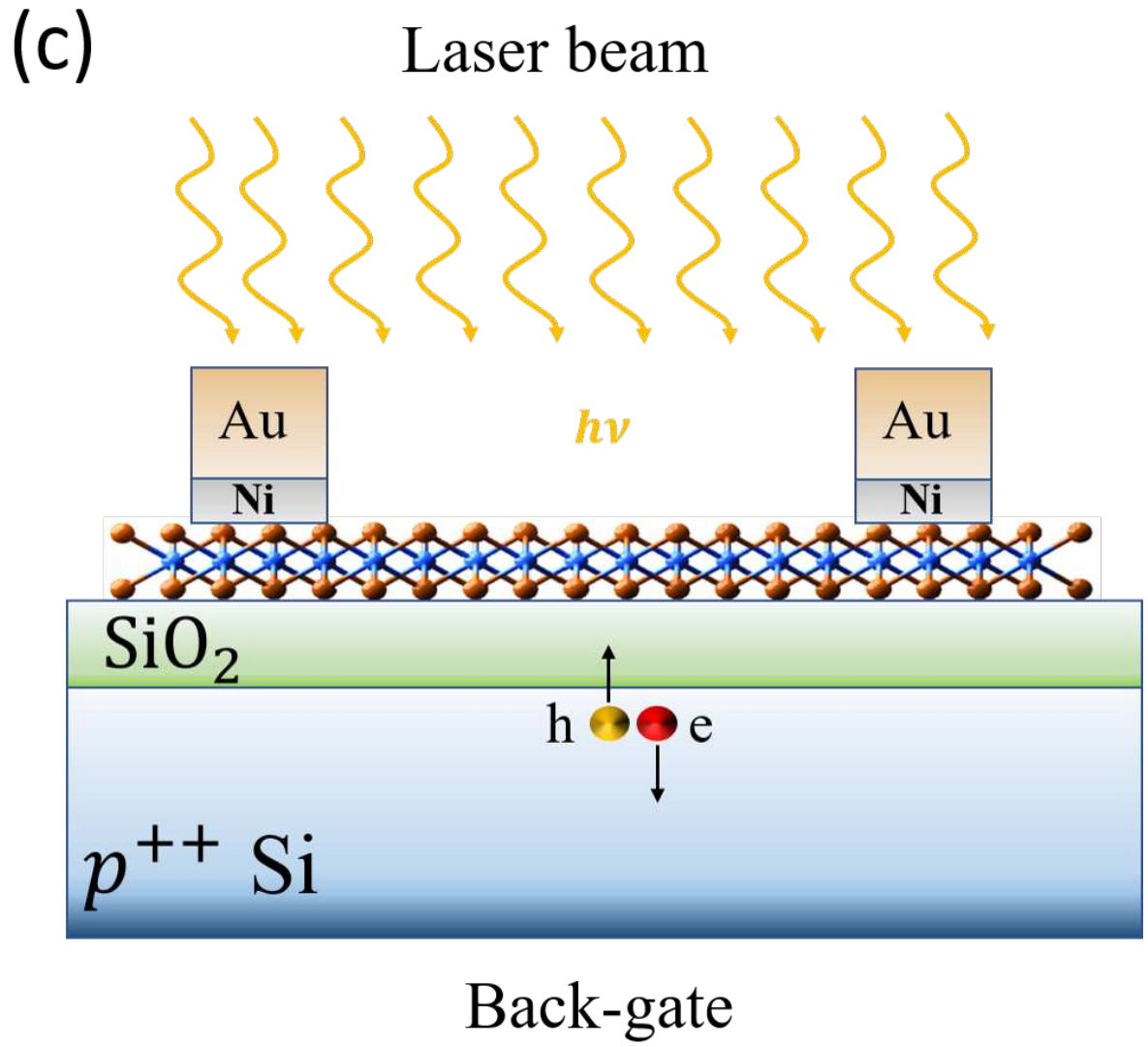


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