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Fast Functional Modelling of Diode-Bridge Rectifier Using Dynamic Phasors

T. Yang, S. V. Bozhko, G. M. Asher

The University of Nottingham, Nottingham NG7 2RD, UK

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Corresponding Author: Dr Tao Yang
Power Electronics, Machine and Control Group
Faculty of Engineering
The University of Nottingham
Nottingham NG7 2RD
United Kingdom

Phone: +44 (0)115 748 4739
Email: Tao.Yang@nottingham.ac.uk

Abstract— In this paper, a functional model for diode-bridge rectifiers is developed based on the dynamic phasor concept. The developed model is suitable for accelerated simulation studies of the electric power systems under normal, unbalanced and line faulty conditions. The high accuracy and efficiency of the developed model have been demonstrated by comparison against three-phase time-domain model and against the model employing synchronous space-vector representations. The experimental verification of the developed model is also reported. In addition, an error analysis shows that the error of the developed model is less than 10% at the most severe unbalanced conditions. The prime purpose of the model is for the simulation studies of more-electric aircraft power architectures at a system level; however it can be directly applied for simulation study of any other electrical power system interfacing with uncontrolled diode bridge rectifiers.

Index Terms – AC-DC power conversion, Aircraft Power Systems, Converters, Dynamics, Modelling, Rectifiers, Vectors

I. INTRODUCTION

There has been seen a significant penetration of power electronics into electric power systems (EPS) in recent years. For example, terrestrial EPS's particularly at distribution level, promise a multiplicity of power electronic converters to handle renewable sources, energy storage or EPS conditioning. A similar scenario pertains for isolated and mobile EPS, such as more-electric aircraft (MEA) and more-electric ship. In the MEA the electric power conversion is required to manage power distribution, landing gear, flight actuation and other functions [1].

The use of large numbers of power electronic devices brings significant modelling challenge at the EPS system level due to the system complexity and the wide variation in time constants. The challenge is to balance the simulation speed against the model accuracy and this is dependent on the modelling task. Four different modelling layers are defined according to the modelling bandwidths i.e. architectural models, functional models, behavioural models and component models [2], [3]. The architectural layer computes steady state power flow and is used for weight, cost and cabling studies. In the functional level, the system components are modelled to handle the main system dynamics up to 150Hz and the error should be less than 5% in respect of the behaviour model accuracy. The behavioural model uses lumped-parameter subsystem models and the modelling frequencies can be up to hundreds of kHz. The component models cover high frequencies, electromagnetic field and electromagnetic compatibility (EMC) behaviour, and perhaps thermal and mechanical stressing. The bandwidth of component models can be up to in MHz region if required.

Targeting for acceptable simulation times for system-level EPS modelling, a number of approaches have been investigated. Average state-space models [4] are a standard technique for considering only the fundamental wave converter behaviour. Average modelling of ac distribution systems involves transforming the three-phase ac signals to a synchronous rotating dq frame, henceforth termed the dq0 model. This method has been used in modelling MEA electric power systems and is proved to be an effective technique [3]. A model with three-phase ac variables is henceforth called an abc model.

One of the disadvantages of the dq0 approach is that under faulty and unbalanced conditions double-frequency components appear and the simulation time steps must be drastically reduced to maintain accuracy. An alternative approach that can address this problem is dynamic phasors (DPs) [5, 6]. The DP method has been applied to the modelling of terrestrial EPS systems including imbalanced regimes [7, 8]. Comparative study of a simple EPS with line faults carried out in abc, dq0 and DP domain is given in [9] where the efficiency of the DP approach is demonstrated. The DP method has also been applied for modelling of electrical machines [10, 11] and flexible ac transmission systems [12], including active filters [13] and static synchronous compensator (STATCOM) [14]. In [15] and [16], a DP model for thyristor-based high-voltage direct current (HVDC) transmission systems is reported, although only balanced conditions are considered.

Different models for uncontrolled diode-bridge rectifiers (DBs) have been developed in recent publications. A comprehensive analytical model is developed in [17] and [18], where different operation modes of DBs have been thoroughly studied. Another mathematical model for analysing DBs based on switching functions is introduced in [19]. An average model for DB is proposed in [20]. However, all these models are only for steady-state applications and the power supply is assumed to be balanced. This paper aims to develop a DP model for DBs. This model can be used for both steady-state and transient studies, under balanced and unbalanced conditions. At the time of this study development, no report on DB modelling in DP domain has been published. The most similar to DBs modelled using DP approach is a thyristor-based HVDC converter [16]. In addition, the model reported in [16] is suitable only for balanced operations. The DB functional model presented in this paper is based on the DB relations between ac and dc terminal variables. The negative sequence under unbalanced or fault conditions is treated as a disturbance in the model. Both the 2nd and 6th harmonic on the dc-link voltage are conveniently included in the reported DP model. This enables to cover the ac imbalanced voltage and dc ripple voltage for both continuous and discontinuous operation.

The main results of the paper are summarised as follows:

- The DP approach has been successfully applied to develop model an uncontrolled DB rectifier applicable for accelerated simulation studies of complex EPS, both balanced and unbalanced, and is consistent with the functional modelling layer specifications in [3]. The model is independent on other EPS devices and may be used as a library element interfacing within an extended three-phase EPS models
- The model is verified experimentally under both balanced and unbalanced operation
- The computational effectiveness of the developed model is proved through comparison against time-domain abc switching and functional non-switching dq0 models and a significant computational acceleration is demonstrated.

II. DYNAMIC PHASORS

This Section, in order to keep the paper self-containment and to assist readability, shortly revises the basic concepts that are employed for the development of the diode bridge model in DP domain. For the readers who are not familiar with the concept, it is recommended to refer to the basic DP theory [5] since it will be intensively used throughout the rest of the paper.

The DP concept assumes that a time-domain nearly-periodic waveform $x(\tau)$ can be represented on the interval $\tau \in (t-T, t]$ by a following Fourier series:

$$x(\tau) = \sum_{k=-\infty}^{\infty} X_k(t) e^{jk\omega_s \tau}, \quad \tau \in (t-T, t] \quad (1)$$

where $\omega_s = 2\pi/T$ and T the fundamental period of the waveform. $X_k(t)$ is the k^{th} Fourier coefficient in complex form referred to as a “dynamic phasor” and determined as follows:

$$X_k(t) = \frac{1}{T} \int_{t-T}^t x(\tau) e^{-jk\omega_s \tau} d\tau = \langle x \rangle_k \quad (2)$$

where k is the DP index and angular brackets $\langle \rangle$ are used to denote DP-domain variables. In contrast to the traditional Fourier Transformation (FT), these Fourier coefficients are time-varying as the integration interval (window) slides through time. As it follows, the DP represents the variation in specific frequency component over time. The required accuracy of the time-domain variable approximation can be achieved by appropriate selection of a DP set K for a particular modelling task. For example, for dc-like variables and signals the index set only includes the component $k=0$, and for purely sinusoidal ones $k=1$.

A key factor in developing dynamic models based on DP is the relation between the derivatives of the variable $x(\tau)$ and the derivatives of k^{th} Fourier coefficients [5]:

$$\left\langle \frac{dx}{dt} \right\rangle_k = \frac{dX_k(t)}{dt} + jk\omega_s X_k(t) \quad (3)$$

This can be verified using (1), (2), and may be used in evaluating the k^{th} phasor of time-domain model. Another important property of DP is that the k^{th} phasor of a product of two time-domain variables can be obtained via the convolution of corresponding DPs:

$$\langle xy \rangle_k = \sum_i \langle x \rangle_{k-i} \langle y \rangle_i \quad (4)$$

The properties (3) and (4) play a key role when transforming the time-domain models into DP domain. Algebraic manipulations in this paper will also exploit the following property of real functions $x(\tau)$:

$$X_{-k}(t) = X_k^*(t) \quad (5)$$

where the notation $*$ denotes a complex conjugate.

III. UNCONTROLLED DIODE-BRIDGE RECTIFIER

A key objective of this paper is development of a computationally efficient model that can represent the DB behaviour in both balanced and unbalanced supply conditions including line faults. The functional level modelling does not

require consideration of switching behaviour [3]. Hence, our study is based on the non-switching model that is reviewed in this Section.

A three-phase diode bridge rectifier is well-documented ac/dc converter and it is shown in Fig.1. In general case, it is supplemented by an output dc filter $L_{dc}C$ and a front-end inductance L_s representing feeding cables, ac chokes etc. The dc load is shown as an equivalent resistance R_L . This converter employs uncontrolled diodes; hence the switching instants are determined by the circuit condition exclusively. The time-domain non-switching DB model under balanced conditions is well developed and documented in many publications, for example [17, 21].

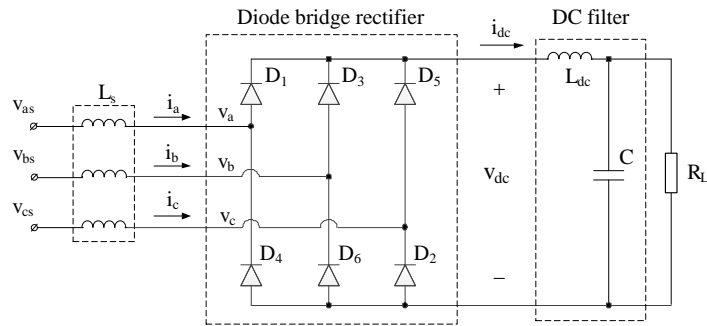


Fig. 1 Three-phase diode bridge rectifier

Under the symmetrical balanced supply the rectifier terminal voltages can be represented as

$$\mathbf{V}_{abc} = \begin{bmatrix} v_a \\ v_b \\ v_c \end{bmatrix} = v_m \begin{bmatrix} \cos(\omega t + \varphi) \\ \cos(\omega t + \varphi - 2\pi/3) \\ \cos(\omega t + \varphi + 2\pi/3) \end{bmatrix} \quad (6)$$

where v_m is the voltage magnitude, ω is the supply electrical frequency and φ is the initial phase angle. The fundamental of diodes switching function then is given as follows [22]:

$$\mathbf{S}_{abc} = \begin{bmatrix} S_a \\ S_b \\ S_c \end{bmatrix} = \frac{2\sqrt{3}}{\pi} \begin{bmatrix} \cos(\omega t + \varphi) \\ \cos(\omega t + \varphi - 2\pi/3) \\ \cos(\omega t + \varphi + 2\pi/3) \end{bmatrix} \quad (7)$$

The switching function (7) defines the input-output relations for the diode bridge:

$$v_{dc} = \mathbf{S}_{abc}^T \mathbf{V}_{abc} = \frac{3\sqrt{3}}{\pi} v_m \quad (8)$$

$$\mathbf{I}_{abc} = \mathbf{S}_{abc} i_{dc} \quad (9)$$

where $\mathbf{I}_{abc} = [i_a \ i_b \ i_c]^T$ - an input ac current in vector-matrix form, v_{dc} and i_{dc} - rectifier output dc voltage and current. As it follows from (9) and (7), the input current is in phase with the input voltage (6) and its magnitude is:

$$i_m = \frac{2\sqrt{3}}{\pi} i_{dc} \quad (10)$$

In a presence of front-end inductance L_s the dc-link voltage (8) reduces due to the commutation effect [22]. Normally, this effect is taken into account by introducing in (8) an additional term corresponding to a voltage drop across the resistance of r_f value:

$$r_f = 6fL_s \quad (11)$$

In the following paper sections we will also intensively use the voltage (6) expressed as a space vector:

$$\bar{v} = 2/3 \cdot (v_a + v_b e^{j2\pi/3} + v_c e^{-j2\pi/3}) \quad (12)$$

It is convenient to analyse three-phase power electronic circuits in synchronously rotating frame[3], [4]. Define the dq frame such that it rotates with the grid electrical frequency ω . Any three-phase variable \mathbf{f}_{abc} can be expressed in the dq frame such that:

$$\mathbf{f}_{dq} = \mathbf{T} \mathbf{f}_{abc} \quad (13)$$

where \mathbf{T} is a transformation matrix:

$$\mathbf{T} = \frac{2}{3} \begin{bmatrix} \cos \omega t & \cos(\omega t - 2\pi/3) & \cos(\omega t + 2\pi/3) \\ -\sin \omega t & -\sin(\omega t - 2\pi/3) & -\sin(\omega t + 2\pi/3) \end{bmatrix} \quad (14)$$

Combining (8)-(10), (12)- (14) yields the following:

$$v_{dc} = \frac{3\sqrt{3}}{\pi} \sqrt{v_d^2 + v_q^2} \quad (15a)$$

$$i_m = \sqrt{i_d^2 + i_q^2} = \frac{2\sqrt{3}}{\pi} i_{dc} \quad (15b)$$

where v_d and v_q are d and q components of voltage vector (12). Since the input current fundamental is in phase with the input voltage, its d and q components are as follows:

$$i_d = i_m \cos \varphi; \quad i_q = i_m \sin \varphi \quad (16)$$

Under the balanced supply, φ is equal to the initial phase of the input voltage and can be derived from voltage d- and q- components:

$$\varphi = \tan^{-1}(v_q/v_d) \quad (17)$$

Equations (15)-(17) comprise the relations between the DB input voltage/current expressed through their dq components and

the output dc voltage/current. These equations will be employed for the development of DB model in the DPs domain.

IV. DYNAMIC PHASOR DB MODEL DEVELOPMENT

In this Section the non-switching DP-domain DB model is developed. We will start with establishing how to map the generally unbalanced supply voltage vector (12) expressed in terms of synchronous dq frame from time domain into the frequency domain of DPs and how the corresponding DPs can be derived from time-domain values of individual phase voltages v_a, v_b, v_c for both balanced and unbalanced conditions. Then the DB input-output relations (15) will be transformed into DPs and the DP-domain DB model will be assembled using the derived relations.

A. Input voltages and current in dynamic phasors domain

In general cases, the DB terminal voltage can be represented by:

$$v_i = V_i \cos(\omega t + \varphi_i), \quad i = a, b, c \quad (18)$$

where V_a, V_b and V_c are phase voltage magnitudes, and $\varphi_a, \varphi_b, \varphi_c$ – their phase angles. Applying the Euler formulae, each of these components can be rewritten as:

$$v_i = \frac{V_i e^{j\varphi_i} e^{j\omega t} + V_i e^{-j\varphi_i} e^{-j\omega t}}{2}, \quad i = a, b, c \quad (19)$$

The DPs of phase voltages can be derived applying the definition (2) and selecting the DPs set as $k=1$ since (18) includes only the fundamental component:

$$\langle v_i \rangle_1 = \frac{1}{2} V_i e^{j\varphi_i}, \quad \langle v_i \rangle_{-1} = \langle v_i \rangle_1^* = \frac{1}{2} V_i e^{-j\varphi_i}, \quad i = a, b, c \quad (20)$$

Combining equations (12), (18), (19) and re-arranging the terms results in the following relation:

$$\vec{v} = \frac{2}{3} e^{j\omega t} \left[\langle v_a \rangle_1 + \langle v_b \rangle_1 e^{j2\pi/3} + \langle v_c \rangle_1 e^{-j2\pi/3} \right] + \frac{2}{3} e^{-j\omega t} \left[\langle v_a \rangle_1^* + \langle v_b \rangle_1^* e^{j2\pi/3} + \langle v_c \rangle_1^* e^{-j2\pi/3} \right] \quad (21)$$

It is clearly seen that the two right-hand side terms define the positive and the negative sequences of input voltage vector. The latter will appear under unbalanced phase voltages only. In terms of synchronous dq frame defined by (14), the voltage vector (21) can be derived as:

$$v_d + jv_q = \vec{v} e^{-j\omega t} = \frac{2}{3} \left[\langle v_a \rangle_1 + \langle v_b \rangle_1 e^{j2\pi/3} + \langle v_c \rangle_1 e^{-j2\pi/3} \right] + \frac{2}{3} e^{-j2\omega t} \left[\langle v_a \rangle_1^* + \langle v_b \rangle_1^* e^{j2\pi/3} + \langle v_c \rangle_1^* e^{-j2\pi/3} \right] \quad (22)$$

Re-arranging the right-side terms results in:

$$v_d + jv_q = (V_{d0} + jV_{q0}) + (V_{d2} + jV_{q2})e^{-j2\omega t} \quad (23)$$

where variables V_{d0} , V_{q0} , V_{d2} and V_{q2} can be calculated as:

$$V_{d0} = \frac{2}{3} \operatorname{Re}[\langle v_a \rangle_1 + \langle v_b \rangle_1 e^{j2\pi/3} + \langle v_c \rangle_1 e^{-j2\pi/3}] \quad (24a)$$

$$V_{q0} = \frac{2}{3} \operatorname{Im}[\langle v_a \rangle_1 + \langle v_b \rangle_1 e^{j2\pi/3} + \langle v_c \rangle_1 e^{-j2\pi/3}] \quad (24b)$$

$$V_{d2} = \frac{2}{3} \operatorname{Re}[\langle v_a \rangle_1^* + \langle v_b \rangle_1^* e^{j2\pi/3} + \langle v_c \rangle_1^* e^{-j2\pi/3}] \quad (24c)$$

$$V_{q2} = \frac{2}{3} \operatorname{Im}[\langle v_a \rangle_1^* + \langle v_b \rangle_1^* e^{j2\pi/3} + \langle v_c \rangle_1^* e^{-j2\pi/3}] \quad (24d)$$

The d- and q- axes components of the input voltage vector v_d and v_q can be derived from (23) by separation of real and imaginary parts:

$$\begin{aligned} v_d &= V_{d0} + V_{d2} \cos 2\omega t + V_{q2} \sin 2\omega t \\ v_q &= V_{q0} + V_{q2} \cos 2\omega t - V_{d2} \sin 2\omega t \end{aligned} \quad (25)$$

The following important conclusions should be made analysing the result given by (25):

- under balanced conditions the voltage dq-components become dc-like: $v_d=V_{d0}$ and $v_q=V_{q0}$;
- if the supply voltage is unbalanced, the dq frame components of the voltage vector will also include the second harmonics;
- the DP set K , in order to represent the supply voltage in dq frame under both balanced and unbalanced conditions, should include zero and 2nd harmonics, i.e.

$$K = \{0, 2\} \quad (26)$$

The mapping of v_d and v_q into DPs is established in Table I. The current i_d and i_q can be derived in the same manner.

TABLE I. DYNAMIC PHASORS FOR DB INPUT VOLTAGE AND CURRENT IN SYNCHRONOUSLY ROTATING FRAME

Variable	Dynamic phasors	
	k=0	k=2
v_d	$\langle v_d \rangle_0 = V_{d0}$	$\langle v_d \rangle_2 = (V_{d2} - jV_{q2})/2$
v_q	$\langle v_q \rangle_0 = V_{q0}$	$\langle v_q \rangle_2 = (V_{q2} + jV_{d2})/2$
i_d	$\langle i_d \rangle_0 = I_{d0}$	$\langle i_d \rangle_2 = (I_{d2} - jI_{q2})/2$
i_q	$\langle i_q \rangle_0 = I_{q0}$	$\langle i_q \rangle_2 = (I_{q2} + jI_{d2})/2$

B. DC-link Voltage in dynamic phasors domain

In this Section we will transform the DB voltage input-output relation (15a) into the DP domain. The main challenge is due to a non-linear nature of this equation that makes the direct application of DP definition (2) non-analytic. The approach we propose in this paper expands (15a) into a Taylor series with respect to v_d and v_q followed by the transformation of the truncated series into the DP domain. Considering the DB output voltage as a function of two variables v_d and v_q as follows:

$$f_1(v_d, v_q) = v_{dc} = \frac{3\sqrt{3}}{\pi} \sqrt{v_d^2 + v_q^2} \quad (27)$$

Approximating (27) by the Taylor series requires selection of the operation point. The natural choice is the pair $\{v_d, v_q\}$ under balanced conditions. Hence, the operating point is defined as $\{V_{d0}, V_{q0}\}$ according to (25). The Taylor expansion of (27):

$$v_{dc} = k_0 + \frac{k_1}{1!}(v_d - V_{d0}) + \frac{k_2}{1!}(v_q - V_{q0}) + \frac{k_3}{2!}(v_d - V_{d0})^2 + \frac{k_4}{2!}(v_q - V_{q0})^2 + \frac{k_5}{2!}(v_d - V_{d0})(v_q - V_{q0}) + \dots \quad (28)$$

where k_i are constants depending on the selected operation point. These can be calculated using (24), (27) and are given in Appendix I.

The series (28) can be converted into the frequency domain after suitable truncation. Since v_d and v_q include harmonics up to the second order in our model, we truncate 3rd-order and higher terms in (28). From (28), in the balanced condition ($v_d=V_{d0}$ and $v_q=V_{q0}$) the dc-voltage v_{dc} will be constant and equal to k_0 which is associated with the input voltage positive sequence. Under unbalanced conditions, the negative sequence will appear and disturb the diodes switching function (7). The impact of the negative sequence can be represented as a disturbance to v_{dc} (27) in a form of the second harmonic appearing according to (25) in both v_d and v_q . Applying the DP index set (26) and employing the convolution property (4) to the truncated Taylor series of (28), the DPs of dc-link voltage v_{dc} are derived as follows:

$$\langle v_{dc} \rangle_0 = k_0 + k_3 \langle v_d \rangle_2 \langle v_d \rangle_2^* + k_4 \langle v_q \rangle_2 \langle v_q \rangle_2^* + k_5 \left(\langle v_d \rangle_2 \langle v_q \rangle_2^* + \langle v_d \rangle_2^* \langle v_q \rangle_2 \right) \quad (29a)$$

$$\langle v_{dc} \rangle_2 = k_1 \langle v_d \rangle_2 + k_2 \langle v_q \rangle_2 \quad (29b)$$

The DPs $\langle v_d \rangle_0$, $\langle v_d \rangle_2$, $\langle v_q \rangle_0$ and $\langle v_q \rangle_2$ are given in the Table I in a previous Section. Hence, the DPs for dc-link voltage are fully defined.

C. Accounting for the dc-voltage ripple

The converter output voltage calculated using (29) exhibits only dc-component under balanced conditions and includes the 2nd harmonic under unbalances. For functional-level simulation studies this may be sufficient. However, there is a room for convenient model improvement by inclusion into a model the 6th harmonic on the dc side voltage that exhibits under balanced conditions in the rectifier under consideration [22]. This section demonstrates how this component can be introduced into the DP-based model if required.

Under the balanced operation, the 6th harmonic in the dc voltage is due to the 5th and 7th harmonic in the switching function [23] that can be given as:

$$S_{a,5,7} = -\frac{2\sqrt{3}}{5\pi} \cos(5\omega t + 5\varphi) + \frac{2\sqrt{3}}{7\pi} \cos(7\omega t + 7\varphi) \quad (30a)$$

$$S_{b,5,7} = -\frac{2\sqrt{3}}{5\pi} \cos(5\omega t + 5\varphi + \frac{2\pi}{3}) + \frac{2\sqrt{3}}{7\pi} \cos(7\omega t + 7\varphi - \frac{2\pi}{3}) \quad (30b)$$

$$S_{c,5,7} = -\frac{2\sqrt{3}}{5\pi} \cos(5\omega t + 5\varphi - \frac{2\pi}{3}) + \frac{2\sqrt{3}}{7\pi} \cos(7\omega t + 7\varphi + \frac{2\pi}{3}) \quad (30c)$$

Combining (30) with (8), (9) and (12), the magnitude of the 6th harmonic in dc voltage v_{dc6} and its phase angle φ_{dc6} are derived as follows:

$$v_{dc6} = -\frac{3\sqrt{3}}{5\pi} \sqrt{V_{d0}^2 + V_{q0}^2} + \frac{3\sqrt{3}}{7\pi} \sqrt{V_{d0}^2 + V_{q0}^2} \quad (31)$$

$$\varphi_{dc6} = 6 \tan^{-1}(V_{q0} / V_{d0}) \quad (32)$$

In a way similar to (20), one can derive the corresponding DP:

$$\langle v_{dc} \rangle_6 = \frac{1}{2} v_{dc6} e^{j\varphi_{dc6}} \quad (33)$$

This extra DP can be added to the previously derived set (29) to represent the DB dc-link voltage in the DP domain.

The time-domain value of dc-link voltage can be calculated using the definition (1):

$$v_{dc}(t) = \sum_k \langle v_{dc} \rangle_k(t) e^{jk\omega t} = 2 \left(\Re[\langle v_{dc} \rangle_k] \cos k\omega t - \Im[\langle v_{dc} \rangle_k] \sin k\omega t \right) \quad (34)$$

where $k=\{0,2,6\}$. Hence, the dc-link voltage (34) includes the dc component as well as the 2nd and the 6th harmonics. Under balanced conditions the 2nd harmonic is absent. However as the unbalance or line fault occurs, it becomes the dominant harmonic component.

D. Rectifier ac currents

The linear relationship between the magnitude of the current vector i_m and the dc-link current i_{dc} was given in (15b) and can be transformed into DP domain as follows:

$$\langle i_m \rangle_k = \frac{2\sqrt{3}}{\pi} \langle i_{dc} \rangle_k \quad (35)$$

The ac input currents of the rectifier are dependent on the dc load current which is determined jointly by the dc-link voltage v_{dc} and by the load itself. In the proposed model the dc current is derived from its time-domain value measured at the model output and then converted into the DP. The DP index for $\langle i_{dc} \rangle_k$ for linear loads should be chosen according to $\langle v_{dc} \rangle_k$, i.e. $k=\{0,2,6\}$. However, using the following equation

$$\langle i_{dc} \rangle_0 = i_{dc} \quad (36)$$

allows us to avoid calculating of $\langle i_{dc} \rangle_2$ and $\langle i_{dc} \rangle_6$ with the DP definition (1) and thus no cumbersome calculation of $\int_{t-T}^t x e^{-jk\omega t} dt$ is needed. With (36) all the dc-link current information will be included in $\langle i_{dc} \rangle_0$. The fluctuation of i_{dc} will be reflected to the fundamental DPs $\langle i_{a,b,c} \rangle_1$ through $\langle i_{dc} \rangle_0$ and this will be illustrated later in the paper. From (35), the same DP set $k=0$ will apply to i_m . The DP for the input current d- and q-axis components can be derived using (16) as follows:

$$\begin{aligned} \langle i_d \rangle_k &= \sum_{i=0}^k \langle i_m \rangle_i \langle \cos \varphi \rangle_{k-i} \\ \langle i_q \rangle_k &= \sum_{i=0}^k \langle i_m \rangle_i \langle \sin \varphi \rangle_{k-i} \end{aligned} \quad (37)$$

The main challenge in calculation of (37) deals with the establishing the DPs for non-linear functions $\sin\varphi$ and $\cos\varphi$. As in previous Section, the approximation is executed by a Taylor series. The non-linear terms are expressed via d- and q-axes voltage components:

$$\cos \varphi = f_2(v_d, v_q) = \frac{v_d}{\sqrt{v_d^2 + v_q^2}} \quad (38a)$$

$$\sin \varphi = f_3(v_d, v_q) = \frac{v_q}{\sqrt{v_d^2 + v_q^2}} \quad (38b)$$

Selecting the operation point $\{V_{d0}, V_{q0}\}$, using the same technique as that dealing v_{dc} in (27) derives,

$$\langle \cos \varphi \rangle_0 = h_0 + h_3 \langle v_d \rangle_2 \langle v_d \rangle_2^* + h_4 \langle v_q \rangle_2 \langle v_q \rangle_2^* + h_5 \left(\langle v_d \rangle_2 \langle v_q \rangle_2^* + \langle v_d \rangle_2^* \langle v_q \rangle_2 \right) \quad (40a)$$

$$\langle \cos \varphi \rangle_2 = h_1 \langle v_d \rangle_2 + h_2 \langle v_q \rangle_2 \quad (40b)$$

$$\langle \sin \varphi \rangle_0 = g_0 + g_3 \langle v_d \rangle_2 \langle v_d \rangle_2^* + g_4 \langle v_q \rangle_2 \langle v_q \rangle_2^* + g_5 \left(\langle v_d \rangle_2 \langle v_q \rangle_2^* + \langle v_d \rangle_2^* \langle v_q \rangle_2 \right) \quad (40c)$$

$$\langle \sin \varphi \rangle_2 = g_1 \langle v_d \rangle_2 + g_2 \langle v_q \rangle_2 \quad (40d)$$

where h_i and g_i are constant coefficients that depends on a selected operation point and given in Appendix I. Hence, the DPs for the non-linear functions (38) are derived. Then, the DPs for DB input current d- and q-components can be easily derived using (37). Finally, if the modelling task instead of d- and q- current components requires the current in a form of three-phase ac variables, then the DPs (37) have to be transformed into abc frame as described in the following Section.

E. DQ0 to ABC transformation in dynamic phasors domain

Applying the convolution property to the abc/dq transform given by (13) and (14) yields:

$$\begin{bmatrix} \langle i_a \rangle_1 \\ \langle i_b \rangle_1 \\ \langle i_c \rangle_1 \end{bmatrix} = \langle \mathbf{T}^{-1} \rangle_1 \begin{bmatrix} \langle i_d \rangle_0 \\ \langle i_q \rangle_0 \end{bmatrix} + \langle \mathbf{T}^{-1} \rangle_1^* \begin{bmatrix} \langle i_d \rangle_2 \\ \langle i_q \rangle_2 \end{bmatrix} \quad (41)$$

where, \mathbf{T}^{-1} is the generalized inverse matrix \mathbf{T} :

$$\mathbf{T}^{-1} = \begin{bmatrix} \cos \omega t, & -\sin \omega t \\ \cos(\omega t - 2\pi/3), & -\sin(\omega t - 2\pi/3) \\ \cos(\omega t + 2\pi/3), & -\sin(\omega t + 2\pi/3) \end{bmatrix} \quad (42)$$

Calculation of $\langle \mathbf{T}^{-1} \rangle_1$ requires the DPs for $\cos \omega t$ and $\sin \omega t$. As ω is constant, the DP index for these will include the only component $k=1$, hence employing (2) one can easily derive:

$$\langle \mathbf{T}^{-1} \rangle_1 = \begin{bmatrix} 0.5 & 0.5j \\ -1/4 - j\sqrt{3}/4 & -j1/4 + \sqrt{3}/4 \\ -1/4 + j\sqrt{3}/4 & -j1/4 - \sqrt{3}/4 \end{bmatrix} \quad (43)$$

Using (43), the DP of input currents in abc frame can be derived easily. Furthermore, applying definition (1), the corresponding time-domain current values can be calculated as well.

F. Model assembling

The equations derived in the sections above can be combined together in order to build the DP-domain model of the uncontrolled bridge rectifier as it is shown in in Fig.2.

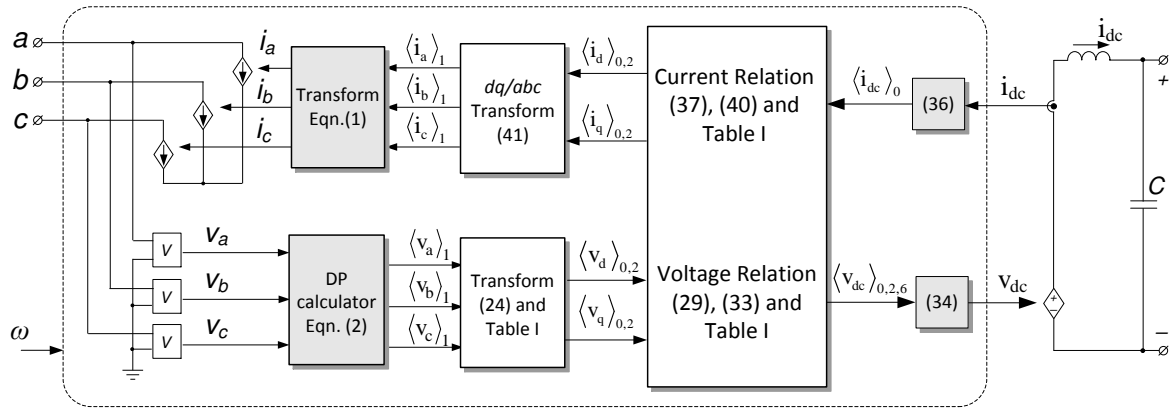


Fig. 2 DP model of the DB rectifier

The current flow shown in Fig.2 illustrates the mapping of i_{dc} into the AC currents $\langle i_{a,b,c} \rangle_1$. With (36), all the information in i_{dc} is reserved in the DP $\langle i_{dc} \rangle_0$. This makes $\langle i_{a,b,c} \rangle_1$ a function of the time-varying current i_{dc} and allows the harmonic characteristics in the ac currents to be represented by the fundamental DPs $\langle i_{a,b,c} \rangle_1$. The DP model shown in Fig. 2 can be used in EPS simulations with no need for the user to understand DP theory. In case studies when the DB rectifier is fed through an inductive line, a small capacitor should be added at input terminals to avoid model state redundancy and related numerical problems. For the user who wishes to build the entire EPS model in the DP domain, the model does not need the interface blocks (coloured in grey in Fig.2) since the DP variables are already available and the DB model can be directly interfaced to the other EPS model blocks.

V. MODEL ERROR ANALYSIS

In the diode-bridge DP-domain model derived in previous Sections we employed linearization of (27) and (38) around the operation point determined by the voltage positive sequence; the negative sequence was treated as a disturbance. With the increase of the negative sequence this consideration may result in some modelling error that is a subject of analysis in the current paper Section.

Consider the modelling error ε defined as:

$$\varepsilon = \frac{\int_T v_{dc_BM} dt - \int_T v_{dc_DPM} dt}{\int_T v_{dc_BM} dt} \times 100\% \quad (44)$$

where v_{dc_BM} is the dc-link voltage calculated by the benchmark model (this is a full switching model in three-phase coordinates) and v_{dc_DPM} is the same calculated by the DP model. The analysis has been performed for the discontinuous

mode case described in the previous Section with the set of phase voltages as:

$$v_a = 80 \cos(\omega t), v_b = V_b \sin(\omega t + \phi_b), v_c = 80 \cos(\omega t + 2\pi / 3) \quad (45)$$

Evaluation of the error (44) has been performed by series of simulations varying the magnitude of phase b voltage V_b from 0 to 120V and its phase ϕ_b from 0 to 2π . Thus all balanced and unbalanced conditions are covered. The results of the error analysis are depicted in Fig. 3 as a function of V_b , ϕ_b and the unbalance factor λ . The latter is defined as the ratio of between the magnitudes of negative-sequence voltage v_n and the positive-sequence one v_p and written as:

$$\lambda = v_n / v_p \quad (46)$$

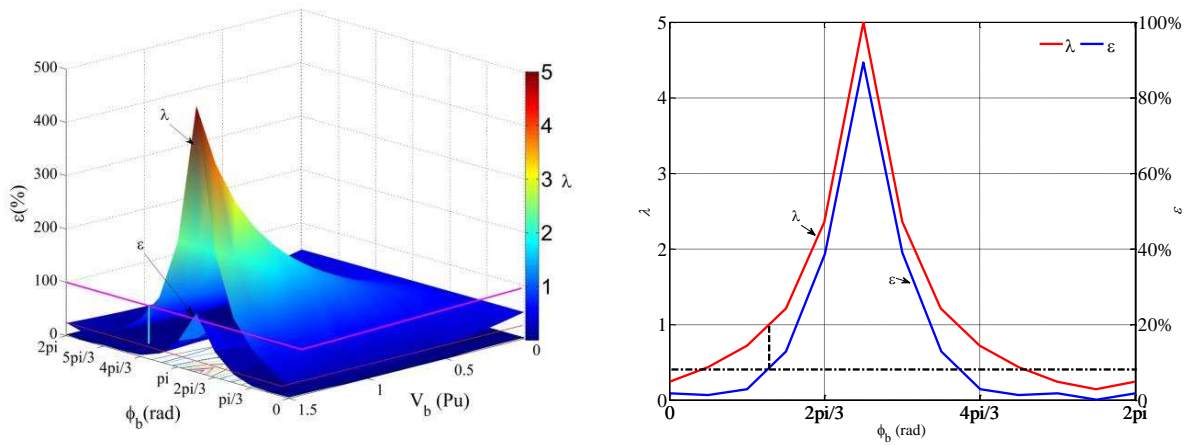


Fig. 3. Analysis of the DP model: (a) - 3D presentation of $\varepsilon = \varepsilon(V_b, \phi_b)$, $\lambda = \lambda(V_b, \phi_b)$; (b) - 2D graph showing $\varepsilon = \varepsilon(\phi_b, \lambda)$ with $V_b = 120V$

It is clearly seen that the model error greatly depends on unbalance factor λ . This is reasonable since with the increase of λ the disturbance from the negative sequence becomes more severe and thus the operation point shifts from $\{V_{d0}, V_{q0}\}$ as selected for linearization of (27) and (38). The modelling error has a maximum when $\phi_b = 5\pi/6$ and $V_b = 120V$ – at these values λ takes an extreme value. In practice, if the negative sequence becomes dominant, i.e. $\lambda > 1$, the operation point should be selected using the negative sequence $\{V_{d2}, V_{q2}\}$ considering the positive sequence as a disturbance. Thus, the model can be conveniently adopted for this case. Hence in the error analysis we only have to consider the cases when $\lambda \in [0, 1]$ (below the horizontal line $\lambda = 1$ in Fig. 3(b)). For the case of balanced operation $\lambda = 0$ the modelling error is less than 2%. The line-to-line fault can be considered as the case when $\lambda = 1$ and $V_b = 120V$; the modelling error for this event is less than 10% as follows from Fig. 3(b). For the case of phase-to-ground fault ($V_b = 0V$) the modelling error is very small and is always less than 2%.

The modelling accuracy analysis confirms that the developed DP model is well suitable for the functional modelling of uncontrolled diode-bridge rectifier. The accuracy of the DP model will be experimentally validated and discussed in the

following section.

VI. MODEL EXPERIMENTAL VALIDATION

A test rig shown in Fig.4 with parameters given in Appendix III has been employed for the model verification. A programmable source Chroma II has been used to apply balanced and unbalanced voltages to the DB. The model has also been verified in continuous and discontinuous current modes as reported below. In the simulation model, small capacitors ($C=1e-8F$) were added between the diode bridge and the line inductors as suggested in Section IV.F above.

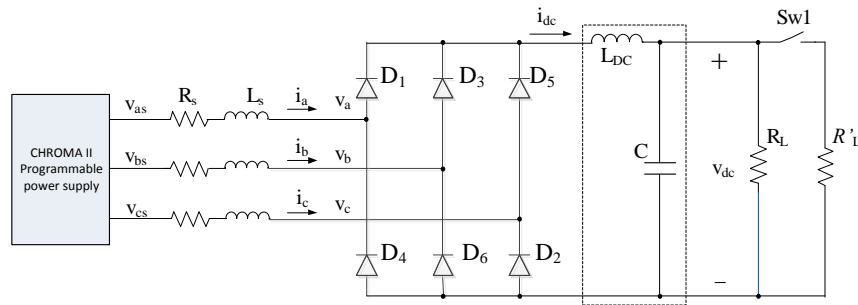


Fig.4. The experimental rig

A. Continuous mode

In the experiment, the initial value of balanced supply voltage was set to values $v_a=v_b=v_c=40V/50Hz$ to avoid source protection triggering when imitating the fault. The phase A voltage is set to zero at $t=0.2s$ in order to perform the line-to-ground fault to the system. A continuous current mode is ensured with a heavy load on dc-link side by switching on Sw1 and paralleling R_L and R'_L . The simulation and experiment values of dc-link voltage, input currents and the inductor current are compared in Fig. 5.

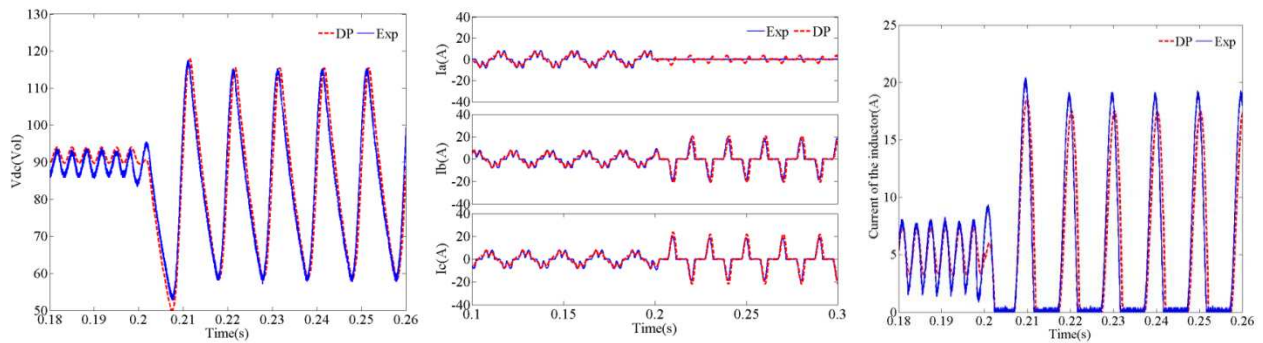


Fig. 5. The dc-link voltage (a), input current (b) and inductor current (c) in continuous mode with phase A loss at $t=0.2s$

As can be seen in Fig. 5(a), under the balanced condition $t < 0.2s$, v_{dc} has the 6th harmonic component. With the 6th

harmonic included in the DP model, the results from the experiment and DP model are well matched. After the line-to-ground fault, the dc-link voltage fluctuates at double frequency. In this case, the 2nd harmonic is included in the DP model and the results from the experiment and simulation are well matched. The DP model and experiment results for $i_{a,b,c}$ are shown in Fig. 5(b) and they are well matched before and after the fault occurs. The DC-link current i_{dc} is shown in Fig. 5(c) and it changes from a continuous current mode (CCM) to a discontinuous mode (DCM) when the fault occurs. It can be seen that under CCM and DCM conditions, the DP model demonstrates good performance in both cases.

B. Discontinuous mode

In this case the switch Sw1 is open hence a large load resistance R_L results in a small dc-link current which makes the inductor current discontinuous. The experiment starts with balanced voltages set $v_a=v_b=v_c=80V/50Hz$ followed the loss of phase A supply ($v_a=0$) at $t=0.2s$. The simulation and experiment values of dc-link voltage, input currents and the inductor current are compared in Fig. 6.

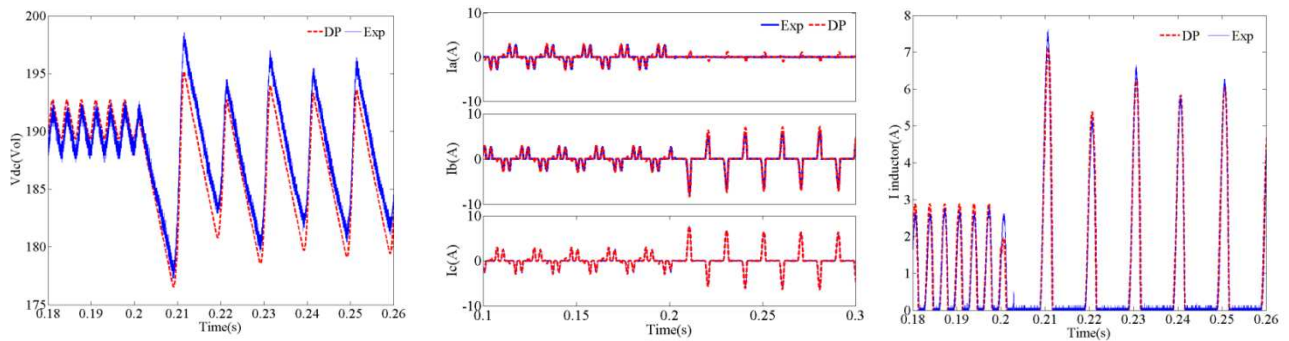


Fig. 6 The dc-link voltage (a), input current (b) and inductor current (c) in discontinuous mode with phase A loss at $t=0.2s$

As can be seen in Fig. 6(a), v_{dc} has a 6th harmonic under the balanced condition and a 2nd harmonic under the line fault condition. In both cases, the results from the DP model are well matched with experiment. The ac side currents from the experiment and the DP model, shown in Fig. 6(b), are well matched before and after the fault occur. The dc-link current i_{dc} , shown in Fig. 6(c), indicates that the rectifier works under the DCM under both normal and faulty conditions. The results from the experiment and DP model are well matched in both cases.

Following the results in this section, the proposed model successfully verified under balanced and unbalanced operation, in both continuous and discontinuous current mode.

VII. COMPUTATION TIME STUDIES

Since the accuracy of the DP model has been validated, this section will focus on the assessment of the computational performance of the developed DP model. For this purpose, different modelling techniques have been applied to an example EPS given in Fig.7 and the CPU time taken for simulation run has been compared. The simulated scenario includes balanced and unbalanced operation, as well as continuous and discontinuous DB modes as detailed below. The system parameters are given in the Appendix III.

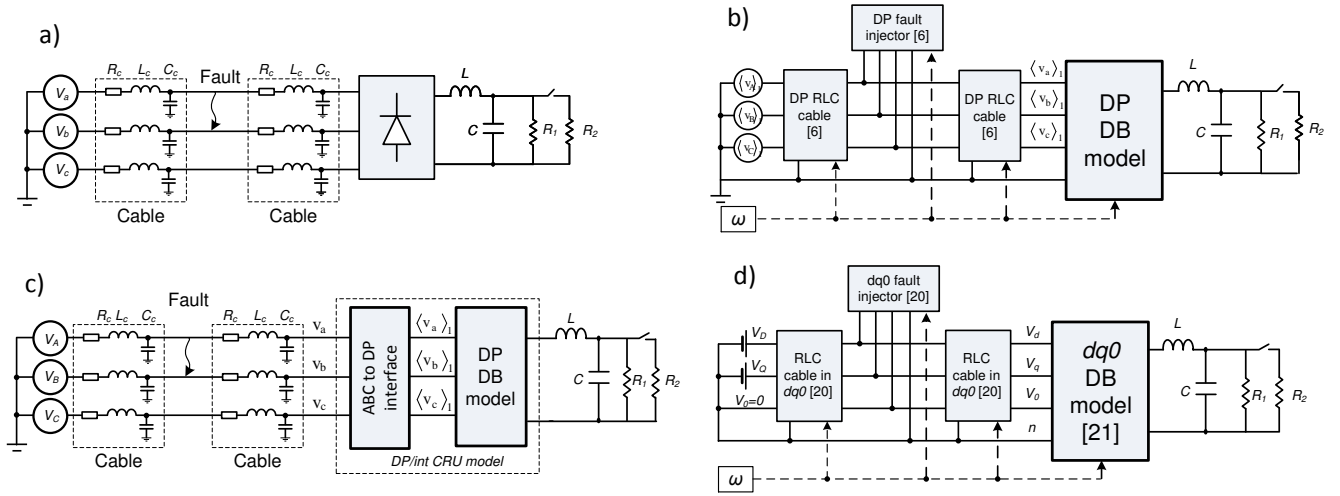


Fig. 7 EPS models for comparison: (a) abc model; (b) DP model; (c) DP/int model; (d) dq0 model.

The following EPS models have been developed and compared:

- A three-phase EPS with a switching DB rectifier model (ideal switches) illustrated by Fig.7(a) and referred to as an abc model;
- An EPS with all the elements are DP models as shown in Fig.7(b). There are no three-phase time-domain variables in this model and it is referred to as DP model;
- An EPS in which only the DB is modelled using DPs with three-phase interfaces as shown in Fig.7(c). The EPS is seen by the user as a three-phase system and the DP formulations are not visible. This is termed the DP/int model;
- An EPS with all the elements in dq0 frame [24, 25] as shown in Fig.7(d).

A. Models comparison under balanced operation

The simulation scenario in this case assumes the DB operation in discontinuous model followed by impact of dc load making the dc-link current continuous at $t=0.1s$. The dc-link voltage and current for all the models are shown in Fig.8(a). As one can see, the DP and DP/int models match very well to abc model in both discontinuous and continuous

mode. In contrast, the dq0 model delivers correct average v_{dc} and i_{dc} values of v_{dc} only in continuous mode, however in discontinuous mode it demonstrates a significant discrepancy compare to models. This is because in discontinuous mode when i_{dc} becomes zero the capacitor discharges to the load impacting the average value of v_{dc} , however the dq0 model does not include the effect of additional charging received due to the 6th harmonic.

The CPU time taken by different models is compared in Table II. The DP and DP/int models in balanced operation are more than 20 times faster than abc model, however the dq0 model is much faster. The cumulative CPU time during the simulation is shown in Fig.8(b). Note that when the continuous mode occurs the abc model slows down dramatically but DP and DP/int models maintain the simulation speed. This is because in discontinuous operation there are no commutating periods in DB and only two diodes conduct at any instant, however in continuous mode there are commutation periods requiring from the solver reduction of integration time-steps thus slowing down the simulation.

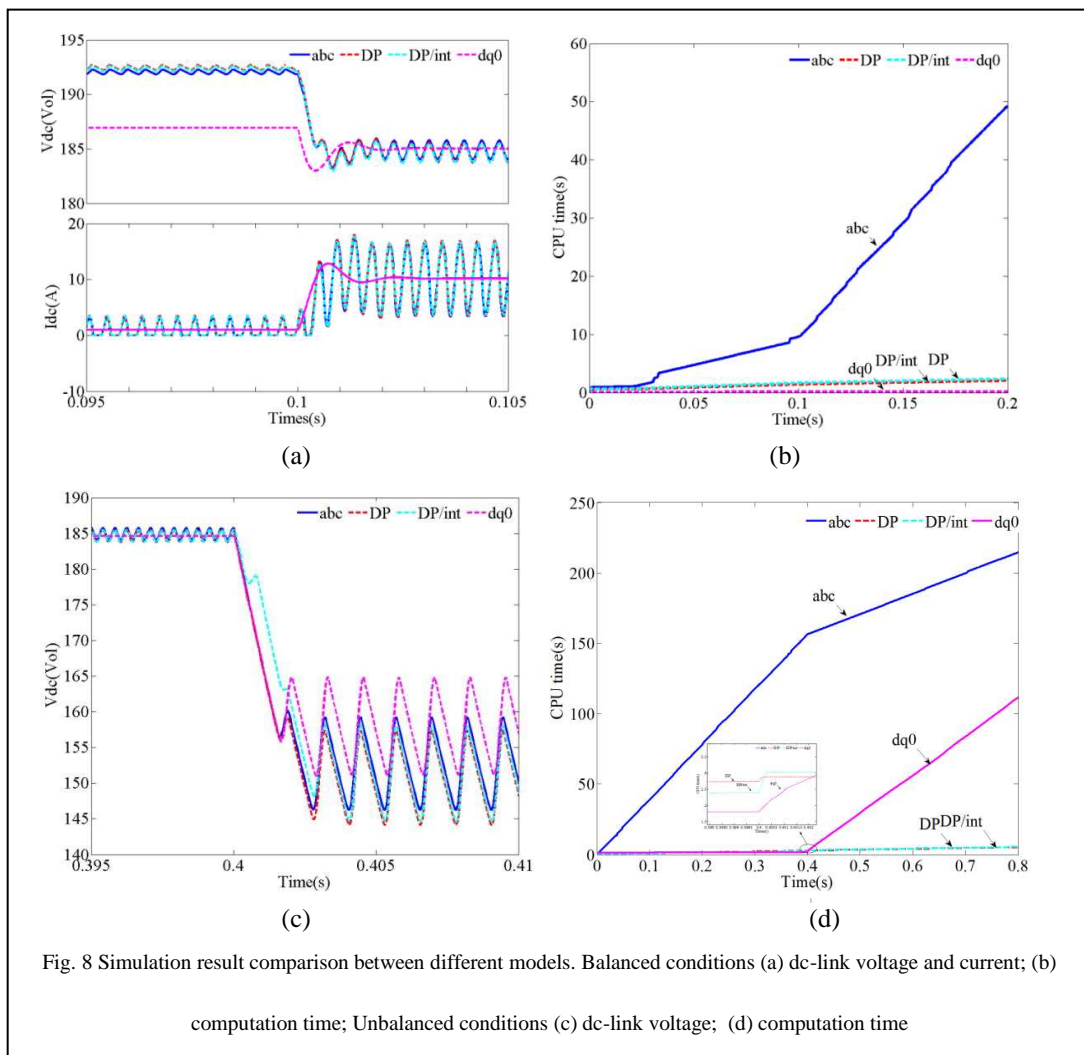


Fig. 8 Simulation result comparison between different models. Balanced conditions (a) dc-link voltage and current; (b) computation time; Unbalanced conditions (c) dc-link voltage; (d) computation time

TABLE II. CPU TIME TAKEN FOR BALANCED SCENARIO SIMULATION

Model:	abc	dq0	DP	DP/int
CPU time taken:	49.17	0.206	2.079	2.369
Performance index:	1	238	24	21

Summarizing, in balanced conditions the DP and DP/int models are slower than dq0 model due to complexity and inclusion of higher harmonics, however the dq0 model is much less accurate especially in discontinuous mode.

B. Line-to-line fault

The unbalanced regime is simulated as a line-to-line fault (phases A and B are shortened via the fault resistor $R_{\text{fault}}=1e-4\Omega$) at $t=0.4s$. The switch Sw1 is “on” to ensure the continuous mode prior the fault. The dc-link voltage is shown in Fig.8(c). As one can conclude, the DP and DP/int models match well to abc model under both balanced and unbalanced regimes. The dq0 model in the balanced condition ($t<0.4s$) reflects the voltage average value.

The CPU time taken by different models is compared Table III. As one can conclude, the efficiency of the dq0 model is lost in unbalanced operation. This is clearly seen from the cumulative CPU time graph in Fig.8(d): the slope corresponding to dq0 model is the largest when $t>0.4s$. In contrast, the DP-based models maintain their simulation speed and for the entire scenario they are 40 times faster of compare to the abc model and 20 times faster than the dq0 model.

Table III. CPU time taken for unbalanced scenario simulation

Model:	abc	dq0	DP	DP/int
CPU time taken:	214.4	111.4	5.405	5.448
Performance index:	1	2	40	40

Hence, the results above have confirmed a very good efficiency of the developed DB model compare to other modelling techniques, in particular – for simulation studies of EPS in unbalanced/line fault conditions.

VIII. CONCLUSION

This paper extends the DP concept into modelling a three-phase diode rectifier. Considering the fact that the 6th harmonic is the dominant harmonic on the DC-link side under balanced conditions and the 2nd harmonic for unbalanced conditions, the developed DP model achieved higher accuracy by embracing the 2nd and 6th harmonics in the DC-link variables. Compare to the traditional average model (dq0 model), the developed DP-based model is more accurate in the balanced condition and much more time-efficient in the unbalanced condition. The accuracy of developed more is validated using simulation and experiment. In addition, our analysis shows that the maximum error is less than 10% at the most severe

unbalanced conditions. The model can be conveniently interfaced with other standard three-phase time-domain model and thus can be widely used in the accelerated electric power system simulation studies.

APPENDIX I. TAYLOR SERIES COEFFICIENTS IN (28)

$$k_0 = f_1(V_{d0}, V_{q0}); k_1 = \frac{\partial f_1(V_{d0}, V_{q0})}{\partial v_d}; k_2 = \frac{\partial f_1(V_{d0}, V_{q0})}{\partial v_q}; k_3 = \frac{\partial^2 f_1(V_{d0}, V_{q0})}{\partial v_d^2}; k_4 = \frac{\partial^2 f_1(V_{d0}, V_{q0})}{\partial v_q^2}; k_5 = 2 \frac{\partial^2 f_1(V_{d0}, V_{q0})}{\partial v_d \partial v_q}.$$

APPENDIX II. TAYLOR SERIES COEFFICIENTS IN (39)

$$h_0 = f_2(V_{d0}, V_{q0}); h_1 = \frac{\partial f_2(V_{d0}, V_{q0})}{\partial v_d}; h_2 = \frac{\partial f_2(V_{d0}, V_{q0})}{\partial v_q}; h_3 = \frac{\partial^2 f_2(V_{d0}, V_{q0})}{\partial v_d^2}; h_4 = \frac{\partial^2 f_2(V_{d0}, V_{q0})}{\partial v_q^2}; h_5 = 2 \frac{\partial^2 f_2(V_{d0}, V_{q0})}{\partial v_d \partial v_q};$$

$$g_0 = f_3(V_{d0}, V_{q0}); g_1 = \frac{\partial f_3(V_{d0}, V_{q0})}{\partial v_d}; g_2 = \frac{\partial f_3(V_{d0}, V_{q0})}{\partial v_q}; g_3 = \frac{\partial^2 f_3(V_{d0}, V_{q0})}{\partial v_d^2}; g_4 = \frac{\partial^2 f_3(V_{d0}, V_{q0})}{\partial v_q^2}; g_5 = 2 \frac{\partial^2 f_3(V_{d0}, V_{q0})}{\partial v_d \partial v_q}$$

APPENDIX III. EXPERIMENTAL RIG PARAMETERS

Power source: Chroma, model 61511. DB rectifier: IRKD101-14. AC supply frequency – 50Hz. Input impedance (ac cable and chokes): $L_s=1\text{mH}$, $R_s=0.1\Omega$. DC-link: $C=2400\mu\text{F}$, $L_{dc}=120\mu\text{H}$. Loads: $R_L=200\Omega$, $R'_L=19\Omega$.

APPENDIX IV. EPS IN FIG.8 PARAMETERS

Cable: $R_c=0.1\Omega$, $L_c=2\mu\text{H}$, $C_c=20\text{pF}$; DC-link: $L=120\mu\text{H}$, $C=500\mu\text{F}$, $R_L=200\Omega$, $R'_L=19\Omega$; AC grid frequency – 400Hz.

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