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New Fuzzy Logic Based Switch-Fault Diagnosis in Three Phase Inverters

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Abstract— Power electronic systems such as inverters play a vital role in today's life serving various applications. It has a great impact on renewable power integration and energy savings techniques. Condition monitoring of these devices is challenging due to several factors like accessibility of physical components. There are various faults which affects the inverter performance and cause shutdown if not diagnosed and rectified early enough. Fault diagnosis is a critical reliability tool to minimize the inverter's operation downtime. There are several approaches of inverter fault diagnosis. However, this paper presents a new fault diagnosis technique for multi-switch open circuit faults using the load current average and rms, the method centred around using fuzzy logic based identifications technique to identify the faulty switch. The results show the capability of the developed technique in accurately identifying the faults in a single switch as well as multiple switches in different phases.

Index Terms—Inverter, Fault diagnosis, open switch fault, Fuzzy Logic

I. INTRODUCTION

Power devices are vital in today's growing industrial sector. It plays major roles especially in motor drive systems, made up of voltage source inverters (VSI) and induction motors [1]. The breakdown of these devices will lead to unexpected downtime which in turn leads to incurring economical losses. In motor drive systems, the VSIs are more susceptible to fault and statistically the power devices have a high failure rate in the VSI [1-3] among other components such as dc capacitors and gate drivers. These faults are caused by thermal stress, aging and environmental conditions such as temperature, and can be categorised into open and short circuit (O.C and S.C) faults [1, 4]. The latter is very destructive compared to the former because of the generation of high current. Protection systems are usually designed to tackle short circuit faults [2]. The O.C fault does not cause immediate shutdown but will accumulate and lead to secondary fault in other components. This will eventually lead to total shutdown of the inverter system [1, 2].

Fault diagnosis (FD) is a critical reliability tool to reduce downtime in inverters. Serval researches have been conducted on investigating the behaviour of power switches (IGBTs) during O.C faults. This has led to developments of FD techniques and fault tolerant schemes during O.C faults.

The following techniques have been used for FD in inverters; A park vector method was used in [5–7] for FD of O.C faults. Here, authors have monitored and used the average current trajectory for fault identification. In a healthy

condition the space trajectory will be a circle but changes during O.C faults. The major drawback to this technique was its load dependency. A new method called dc normalized current was presented in [8] to tackle the park vector drawback. This technique compares the normalised dc current for each phase with a universal standard threshold [4, 8]. Authors of [2] presented a pattern recognition technique that involves the use of current waveform characteristics in the time domain to extract fault signatures. The dc component and the average current polarity were used to define these faulty signature patterns. The ac actual voltage was used in [7] to compare a reference value, thus errors in this comparison is used for FD. This technique involved the use of extra voltage sensors which decreases the reliability of the entire system. Another pattern recognition technique which involves the use of the three phase current harmonics was introduced in [2]. It was detected that when there is an O.C fault in a phase, its zero order harmonics value will be the summation of the two non-faulty phases zero order harmonics values. Detection of changes in the three phase current using wavelet analysis and fuzzy algorithm was carried out in [9,10]. The fuzzy logic system's role was mainly for classification of faults, this method could classify single and multiple switch faults. Several forms of wavelet in combination with sophisticated algorithms have also been used for fault identification in [7, 10 - 11]. Major drawbacks include its high implementation effort and complexity. Authors of [12] proposed a technique based on current observation for rms and mean current values for fault diagnosis of open switch faults, the technique could only detect a single switch fault in a faulty arm, the main shortfall of this techniques was its lack of robustness and inability to identify multiple concurrent faults.

From the literature the complexity, false alarms, addition of extra hardware and the lack of robustness to identify single and multiple switches are the challenges in inverter fault detection. This paper presents a FD technique that uses the three phase load current average and root mean square (rms) values for fault identification and classification. The technique is split into two stages and is used successfully to identify single and multiple switch open circuit faults. This was achieved with no need of additional sensors and to mitigate false alarms.

II. INVERTER MODEL AND FAULT OBSERVATIONS

This paper investigates the fault behaviour of three phase power inverters under two control techniques, PWM and 180° conduction mode. The Matlab/Simulink inverter

model used can be seen in Fig.1, it consists of a two-level inverter system with six IGBT switches and the output of the inverter is connected to a RL load. For the PWM mode, the IGBTs are controlled by sinusoidal PWM control pulses. while in 180° conduction mode, the inverter is controlled with appropriate pulse generators. The parameters of both models can be seen in Appendix A.



Fig. 1 Inverter model

Under normal conditions, the three phase instantaneous currents have sinusoidal waveforms but this changes when a fault occurs. A typical waveform of the three phase currents during healthy, single and multiple O.C switch faults are illustrated in Fig.2-4. The simulation time is from 0.26s to 0.36s and an O.C switch fault is applied at 0.3s. It is observed during the healthy state (from 0.26s to 0.3s) the waveforms are a balanced sinusoidal set with equal magnitude. However, when a fault occurs at 0.3s, changes have occurred. Fig.2 shows the current waveform when S1 has O.C fault, it is observed that in phase A waveform, only the negative cycle is present. Furthermore, changes in the current waveform post fault can be seen during multiple switch faults for S1&S3 and S1&S6 in Figs 3 & 4 respectively.

Therefore, it is possible to extract fault indications (signatures) from the three phase current waveform characteristics. This paper makes use of the average and rms values of the three phase current for fault diagnosis.



Fig. 2 S1 Open circuit fault (single switch fault)



Fig. 3 S1& S3 open circuit faults (Double switch fault- two upper switches)



Fig. 4 S1& S6 open circuit faults (Double switch fault- one upper and one lower switch)

Figs 5 & 6 show the waveforms of the three phase currents as average and rms values under healthy and single faulty switch condition. As expected, the average current is zero under healthy condition however the sum of the average of three currents are zero under one faulty switch condition. It is noticeable that the rms current, for the faulty phase is significantly lower.



Fig. 5 Three phase average current for healthy and S1 O.C fault



Fig. 6 Three phase rms current for healthy and S1 O.C fault

Figs 7 & 8 show the average and rms current when two upper switches of different phases are O.C. In addition to the zero summation of the average currents, the polarity of the current identifies the faulty phases. The rms measurements for the faulty phases are the lowest as shown.



Fig. 7 Three phase average current for S1&S3 O.C fault condition



Fig. 8 Three phase rms current for S1&s3 O.C fault condition

Figs 9 & 10 show the average and rms current when two switches from different locations and different phases are suffering O.C fault conditions. Besides the zero summation & polarities observed in faults discussed earlier, it is also noticeable that the rms currents will give limited indication of the faulty switch.



Fig. 9 Three phase average current for S1&S6 O.C fault condition



Fig. 10 Three phase rms current for S1&S3 O.C fault condition

III. FAULT DIAGNOSIS SCHEME

Based on the observations of the average current measurement as discussed in Section II, it is possible to use this parameter as an indicator of the faulty switch. Therefore, a proposed knowledge-based diagnosis is introduced to identify the faulty switch/switches.

The challenge was discovered when two switches in the same phase are open circuit, it is observed that the average current calculated using (1) (continuous or discrete [13]) cannot be used to differentiate between the healthy case and one-phase faulty state, this can be clearly seen in Fig.11.

$$I_{avg} = \frac{1}{T} \int_{0}^{T} i(t) dt$$
(1-a)
$$I_{avg} = \frac{1}{N} \sum_{n=0}^{N-1} i(n)$$
(1-b)

Therefore, the authors of the paper are proposing the use of the three phase rms current as first stage of identifications to differentiate between faulty phase and healthy condition.



Fig. 11 Three phase average current for S1&S4 O.C fault condition

This is illustrated in Fig.12, the rms current that is calculated using (2) (continuous or discrete) for healthy and faulty states, it is used to differentiate between both states if one phase is completely open. Thus, can be also used to decrease possibilities of false alarms in the FD Technique.

$$I_{rms} = \sqrt{\frac{1}{T} \int_{0}^{T} i^{2}(t) dt}$$
(2-a)
$$I_{rms} = \sqrt{\frac{1}{N} \sum_{n=0}^{N-1} i^{2}(n)}$$
(2-b)

The flow chart of the proposed fault diagnosis system is shown in Fig.13. This was based on applying the measurement of the currents rms as first screening stage to identify the faulty phase from healthy condition and at the same time to inspect which phase of the three phases is faulty. The second stage is initiated with average current measurements into a fuzzy knowledge base and inference logic system, for further fault identification and classification to identify the faulty switch, for all single-switch or multipleswitches cases leading to development of correct faulty-case signatures.



Figure 12 Three phase rms current for S1&S4 O.C fault condition





IV. FUZZY LOGIC SYSTEM DESIGN

The system simulates human skills and thoughts in terms of rule-based actions [13]. Choosing the right membership functions (MFs) is critical as this affects its performance and the computational effort [13, 14]. In this research, the MFs were generated according to the inverter average currents for all possible single and double switch faults. The range of input MFs is (-1.6, 1.6) for 180° mode and (-3.3, 3.3) for PWM mode, triangular membership was selected due to its simplicity, as illustrated in Fig. 14 for 180° mode. This is the same for all the three phase average currents.



Fig. 14 Fuzzy MFs of input current variables

The fuzzy set includes: positive very big (PVB), positive big (PB), positive medium (PM), positive small (PS), positive very small (PVS), zero (ZE), negative very small (NVS), negative small (NS), negative medium (NM), negative big (NB), and negative very big (NVB). Table 1 shows the rule-based logic which the fuzzy system is structured upon, while the database provides the corresponding input MFs. Each row in the table shows the

fault condition that occurs when the input three phase current $(I_a, I_b \text{ and } I_c)$ satisfy the values attached to each MF. Table 1 Fault diagnosis and fuzzy rules

Rule #	Ia	I _b	Ic	Faulty Switch
1	NM	PVS	PVS	S1
2	PVS	NM	PVS	S3
3	PVS	PVS	NM	S5
4	PM	NVS	NVS	S4

Defuzzification: There are serval defuzzification methods to evaluate the output of the fuzzy system, in this case the weighted average of all rule outputs is used. The final crisp output is obtained from (3) and (4).

$$Z_i = f(x, y) \tag{3}$$

$$\mathbf{Z} = \frac{\sum_{i=1}^{N} W_{Ri} \times Z_i}{\sum_{i=1}^{N} W_{Ri}}$$
(4)

Where, N = total number of rules



5	NVS	PM	NVS	S6
6	NVS	NVS	PM	S2
7	PVB	NS	NS	S3S5
8	NS	PVB	NS	S1S5
9	NS	NS	PVB	S1S3
10	PS	NVB	PS	S2S4
11	NVB	PS	PS	S2S6
12	PS	PS	NVB	S4S6
13	NB	PB	ZE	S1S6
14	PB	NB	ZE	S3S4
15	NB	ZE	PB	S1S2
16	PB	ZE	NB	S4S5
17	ZE	NB	PB	S2S3
18	ZE	PB	NB	S5S6

The fuzzy rule extraction can be classified into the following;

Inference: The Takagi-Sugeno type fuzzy is used in this paper. The if then rule is used to express the relationship between the crisp input and output functions.

The rules for the fuzzy diagnosis scheme are presented in Table 1 for all scenarios of single and double switch faults. The following are examples of such rules from Table 1:

- 1. If $(I_a \text{ is } NM)$ and $(I_b \text{ is } PVS)$ and $(I_c \text{ is } PVS)$ then S1 is faulty
- 2. If $(I_a \text{ is } NS)$ and $(I_b \text{ is } PVB)$ and $(I_c \text{ is } NS)$ then S1&S5 are faulty.
- 3. If $(I_a \text{ is } NB)$ and $(I_b \text{ is } PB)$ and $(I_c \text{ is } ZE)$ then S1&S6 are faulty

 W_{Ri} = weighting factor from the ith rule

i = active rule number

V. ANALYSIS AND RESULTS

This section contains the steady state analysis and simulation results of the proposed technique used for a PWM and 180° conduction mode inverter. Table 2 shows a sample of the average and rms current values for both control techniques, during different fault conditions. These values are used to design the fuzzy MFs, the range of these MFs for the purpose of this simulation were given in Section IV and illustrated in Fig. 14. The system under study is shown in Fig. 15, where first stage block differentiates the healthy case from faulty phase, then the fuzzy system block is used to identify the faulty switch/switches.

	Phase current for 180° mode			Phase current for PWM mode			
	Faulty Switch	Ia	Ib	I_c	Ia	I_b	I_c
	S 1	-1.3	0.65	0.65	-2.939	1.47	1.47
average current (A)	S 3	0.65	-1.3	0.65	1.47	-2.939	1.47
	S3S5	1.6	-0.8	-0.8	3.675	-1.836	-1.836
	S1S5	-0.8	1.6	-0.8	-1.837	3.675	-1.838
	S1S6	-1.5	1.5	0	-3.306	3.309	0
rms (A)	Healthy	9	9	9	6	6	6
	S1S4	0	7.8	7.8	0	4.9	4.9
	S2S5	7.8	7.8	0	4.9	4.9	0

Table 2 Sample of average and rms current for different faults

A. PWM control mode

An O.C fault is applied to both S1 and S3 within the model under study for PWM control mode. As expected, the first identification stage is bypassed as fault is not in its remit, and the fuzzy system has demonstrated as illustrated in Fig. 16 that the output rule "9" is active, i.e. the S2 and S3 are faulty according to the knowledge base shown in Table 1 above.



Fig. 16 Fuzzy scheme for S1&S3 fault

B. 180° control mode

For the sake of verifying the proposed diagnostic scheme for 180° mode of inverter operation, several fault conditions have been tested and verified the success of the proposed scheme, due to space limitation, only one case has been shown in Fig. 17 for the case when S1 is subjected to an O.C fault. The fuzzy logic system is showing rule "1" is active, i.e. S1 is an open switch according to Table 1 knowledge base.



Fig. 17 Fuzzy scheme for S1 fault

VI. CONCLUSION

In comparison to literature works that can identify faults in a single switch, this paper presented a fault diagnosis technique for three phase dc-ac inverter using the average and rms values of the three phase current to estimate single and multiple switch status. Two stages of identification have been proposed, the first stage is to differentiate between the healthy inverter and only one phase is faulty. The second stage is to estimate correctly the faulty switch/switches. The results have shown that using both current measurements/parameters can identify an open switch fault, the combination of rms and average quantities in one diagnosis scheme will increase robustness of the fault detection technique.

The proposed technique is less immune to changes in the inverter loading and changes in the PWM modulation index, therefore, the work is continuing on developing a more dynamic diagnosis system that can take into consideration model changes.

Table A1				
Parameters	Values			
DC supply	100V			
Fundamental frequency	50Hz			
Carrier frequency	2kHz			
Load resistance	5Ω			
Load inductance	1e-3H			

VII. APPENDIX A

VIII. REFERENCE

0.8

Modulation index

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