First characterization of a superconducting filter-bank spectrometer for hyper-spectral microwave atmospheric sounding with transition edge sensor readout

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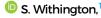
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ABSTRACT

We describe the design, fabrication, integration, and characterization of a prototype superconducting filter bank with transition edge sensor readout designed to explore millimeter-wave detection at frequencies in the range of 40-65 GHz. Results indicate highly uniform filter channel placement in frequency and high overall detection efficiency. The route to a full atmospheric sounding instrument in this frequency range is discussed.

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I. INTRODUCTION

Superconducting on-chip filter-bank spectrometers (SFBSs) are a promising technology for a number of scientifically important applications in astronomy and meteorology that require low-noise, spectroscopic measurements at millimeter and sub-millimeter wavelengths. SFBSs are capable of achieving high channel counts and the individual channel characteristics such as shape, width, and position in frequency, power handling, and sensitivity can be tuned to the application. Moreover, the micro-fabrication techniques used in the production of these thin-film devices mean that SFBSs are intrinsically low-mass, physically compact, and easily reproducible, making them well-suited for array applications for both ground-based and satellite-borne instruments. High signal detection efficiencies can be achieved up to the superconducting pair-breaking threshold of the superconductors used in the design, typically 680 GHz for Nb but higher for superconducting compounds such as NbN or NbTiN.

Applications in astronomy include surveys of moderate redshift galaxies (Z = 4-6) by precision determination of the frequencies of CO and [CII] rotational lines, multichroic pixels for cosmic

microwave background (CMB) observations (foreground subtraction by observing in the 24-30 and 30-48 GHz atmospheric windows and simultaneous observation in multiple CMB frequency bands^{2,3}), or observation of low-Z CO and O line emissions from nearby galaxies.4 Chip spectrometers coupling to superconducting kinetic inductance detectors (KIDs) are being developed by a number of groups and large multiplexing counts of order 1000s have been demonstrated.^{5,6} However, KIDs are difficult to design for detection at low frequencies $v \leq 100 \, \text{GHz}$, the pair-breaking threshold for a typical low superconducting transition temperature material such as Al,⁷ and can also be challenging to calibrate as regard their power-tooutput-signal responsivity. Superconducting transition edge sensors (TESs) are a type of bolometric detector where the power absorption is not limited by the pair-breaking threshold of a superconducting film. In addition, their (power-to-current) responsivity is straightforward to determine both theoretically and experimentally. TES multiplexing schemes are a mature technology using both time- and frequency-domain approaches giving multiplexing factors of order 100s,^{8,9} and microwave TES readout schemes promise to equal the multiplexing factors demonstrated for KIDs. 10,1

Vertical profiles of atmospheric temperature and humidity measured by satellite-borne radiometric sounders provide vital information for long-range weather forecasting. These sounders work by measuring the upwelling radiance from the atmosphere in a number of spectral channels, typically either at microwave (MW) or infrared (IR) wavelengths. Vertical resolution and measurement accuracy improve rapidly with greater channel number and radiometric sensitivity. 12,13 Significant progress has been made in IR sounder performance; the Infrared Atmospheric Sounding Interferometer (IASI), for example, provides over 8000 channels with sub-kelvin noise equivalent differential temperature (NETD).¹⁴ However, while able to provide high quality data, IR sounders can do so only under infrequent clear-sky conditions, as clouds absorb and interfere with the signal of interest. MW sounders, by contrast, are not affected by cloud cover, but their use has been hampered by poorer instrument performance. Channel number is a significant problem: the Advanced Microwave Sounding Unit-A (AMSU-A) in current use has, for example, only 15 channels, ¹⁵ while the planned Microwave Imaging Instrument (MWI) will offer 23.16 Sensitivity is also an issue and a recent study by Dongre et al. 13 has indicated that maintaining and/or improving sensitivity as channel count increases is vital. In this paper, we report on an SFBS with TES readout as a technology for realizing a MW sounding instrument with several hundred channels and sky-noise limited performance. This would represent a disruptive advance in the field, allowing measurements of comparable performance to IR sounders under all sky conditions.

The chip spectrometer reported here is a demonstrator for an atmospheric temperature and humidity sounder (HYper-spectra Microwave Atmospheric Sounding: HYMAS), that is, being developed to operate at frequencies in the range of $v=45-190\,\mathrm{GHz}.^{13,17}$ The demonstrator was designed to cover the very important O_2 absorption band at $v=50-60\,\mathrm{GHz}$ for atmospheric temperature sounding. We believe, however, that our prototype designs and initial characterizations are already relevant across the broad band of scientifically important research areas described above.

In Sec. II, we give a brief overview of SFBSs and the particular features of the technology that make them attractive for this application. We will then describe the design of a set of devices to demonstrate the key technologies required for temperature sounding using the O₂ absorption band at 50–60 GHz. In Sec. III, we describe the fabrication of the demonstrator chips and their assembly with electronics into a waveguide-coupled detector package. In Sec. IV, we report the first characterization of complete SFBS's with TES readout detecting at 40–65 GHz, considering the TES response calibration, measurements of overall detection efficiency, and measurements of filter response. Finally, we summarize the achievements and describe our future program and the pathway from this demonstrator to a full instrument.

II. SUPERCONDUCTING ON-CHIP FILTER-BANK SPECTROMETERS

In general, a filter-bank spectrometer uses a set of banddefining electrical filters to disperse the different spectral components of the input signal over a set of power detectors. In the case of an SFBS, the filters and detectors are implemented using microfabricated superconducting components and are integrated together on the same device substrate (the "chip"). The integration of the components on the same chip eliminates housings, mechanical interfaces, and losses between different components, helping to reduce the size of the system, while improving ruggedness and optical efficiency. In addition, it is easy to replicate a chip and, therefore, a whole spectrometer, making SFBSs an ideal technology for realizing spectroscopic imaging arrays.

Most mm- and sub-mm wave SFBSs that have been reported in the literature operate directly at the signal frequency, i.e., there is no frequency downconversion step. ^{5,6} There are two main benefits of this approach, the first of which is miniaturization. The size of a distributed-element filter is intrinsically inversely proportional to the frequency of operation: the higher the frequency, the smaller the individual filters and the more channels that can be fitted on the chip. The second benefit is in terms of instantaneous observing bandwidth, which is in principle limited only by the feed for frequencies below the pair-breaking thresholds of the superconductors.

Operation at the signal frequency is made possible by (a) the availability of superconducting detectors for mm- and sub-mm wavelengths, and (b) the low intrinsic Ohmic loss of the superconductors. Critically, (b) allows filter channels with scientifically useful resolution to be realized at high frequencies. In the case of normal metals, increases in Ohmic loss with frequency and miniaturization of the components quickly degrade performance. As an example, the Ohmic losses in Nb microstrips line at sub-kelvin temperatures are expected to be negligible for frequencies up to 680 GHz ¹⁸ (the onset of pair-breaking). The use of ultra-low noise superconductor detector technology such as TESs and KIDs, (a), in principle allows for extremely high channel sensitivities.

The SFBS test chips described here were developed with the target application of satellite atmospheric sounding and against the specification given in Table I. Figure 1 shows the system level design of the chips. Each chip comprises a feeding structure that couples signal from a waveguide onto the chip in the range of 45–65 GHz, followed by a filter bank and a set of TES detectors. The chip is then housed in a test fixture that incorporates additional

TABLE I. Table of detector specifications for a baseline temperature sounder using the O_2 absorption band. The background power loading and detector time constant have been calculated assuming the same optics and scan pattern as the MWI instrument. ¹⁶ The required NEP gives near sky-noise limited performance under these conditions. The efficiency is referenced to the feed to the chip.

Parameter	Specification	Units
Operating frequency range	50-60	GHz
Filter resolution	100-500	
Noise equivalent power	$\lesssim 10$	aW/\sqrt{Hz}
Detector time constant	5	ms
Detector absorption efficiency	50%	
Background power handling	60	fW
Operating temperature	>300	mK
Number of channels	35	

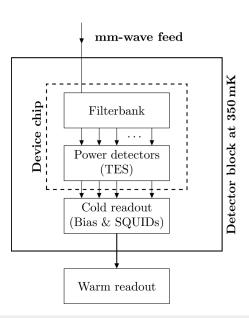


FIG. 1. Conceptual design of the test devices, also showing the architecture of a superconducting on-chip filter-bank spectrometer.

cold electronics and waveguide interfaces. In the sub-sections, we will describe the design of each of the components in detail.

Each of the test chips described has 12 detectors in total. This number was chosen for convenient characterization without multiplexed readout, but the architecture readily scales to higher channel count. In principle, the limiting factor is increasing attenuation on the feed line as more channels are added, but the losses on superconducting line are so low that other issues, such as readout capacity, are expected to be the main limit.

A. Feed and transition

RF signal input to the test devices is via a standard $1.88 \times$ 3.76 mm² WR15 waveguide, allowing full coverage of the O₂ band (WR15 has a recommended operating range of 50-75 GHz, singlemode operation from 39.875 to 79.750 GHz). The signal is coupled from the waveguide onto a $22.3\,\Omega$ microstrip line on the chip through a radial probe transition, 19 the design of which is shown in Fig. 2. A split block waveguide is used, the upper part of which has been rendered as transparent in the figure to allow the internal structure to be seen. A channel is machined through the waveguide wall at the split plane to accommodate a silicon beam extending from the chip, shown in green. This beam supports a fan-shaped radial probe, shown in blue, the apex of which connects to the upper conductor of the microstrip. The microstrip ground plane is shown in yellow. Not visible is an air channel under the beam, which raises the cut-off frequency of the waveguide modes of the loaded channel above the band of operation.

It is critical for performance that the ground plane is at the same potential as the waveguide/probe-channel walls at the probe apex; however, it is not straightforward to make a physical

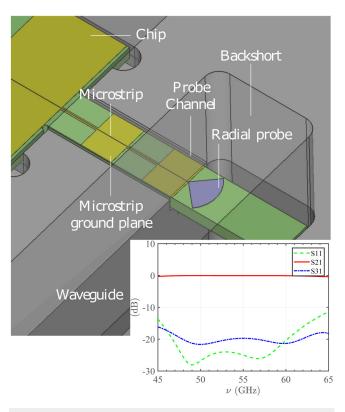


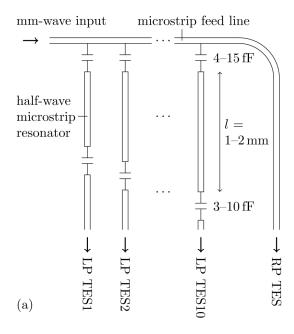
FIG. 2. Details of the waveguide to microstrip transition. The different components are described in the text. The inset shows the simulated scattering parameters, where ports 1, 2, and 3 are the waveguide input, microstrip output, and coupling to the chip cavity, respectively.

connection. The changes in ground plane width shown in Fig. 2 implement a stepped impedance filter in the ground plane/wall system to ensure a wideband short at the probe plane, assuming the ground plane is wire-bonded to the walls in the chip cavity. This filter also prevents power flow along the air channel due to the TEM mode.

The performance of the design was simulated using OpenEMS, a finite difference time domain (FDTD) solver. Insertion loss (S_{21}), reflection loss (S_{11}), and indirect leakage to the chip cavity (S_{31}) as a function of frequency are shown in the inset of Fig. 2. As can be seen, the design achieves better than -3 dB insertion loss over 45–65 GHz, corresponding to a fractional bandwidth of nearly 35%.

B. Filter bank

The filter-bank architecture employed on the test chips is shown schematically in Fig. 3(a). It comprises ten half-wave resonator filters driven in parallel from a single microstrip feed line. Each of the filters comprises a length of microstrip line, the ends of which are connected by overlap capacitors to the feed and an output microstrip line. The output line is terminated by a matched



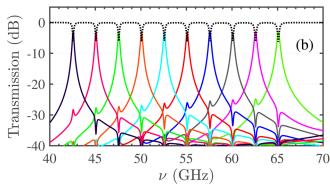


FIG. 3. Details of the filter-bank designs. (a) shows a schematic of the filter bank. (b) shows the power transmitted to individual channels for $\mathcal{R}=200$. The different channels are indicated by the different line colors. Transmission to the residual power detector terminating the feed is indicated by the black dotted line.

resistor that is closely thermally coupled to a TES bolometer. Both the termination resistor and TES are thermally isolated from the cold stage. The feed line itself is terminated on an 11th matched resistor coupled to a TES. This TES measures the power remaining after passing the filter bank and is subsequently described as the residual power detector. This TES was specifically designed to handle the expected higher incident power. A 12th detector on chip has no microwave connection and was used as a "dark" (i.e., nominally power-free) reference. A common superconducting microstrip design is used for all parts of the filter bank, consisting of a Nb ground plane, a 400-nm-thick SiO₂ dielectric layer and a $3\,\mu\mathrm{m}$ wide, 500 nm thick, Nb trace. The modeled characteristic impedance is $22.3\,\Omega.^{18}$

The spectral characteristics of each filter are determined by the resonant behavior of the line section and the strength of the coupling to the feed and detector. For weak coupling, the line section behaves as an open-ended resonator and transmission peaks sharply at resonance.

Each filter channel is designed to have its own fundamental frequency v_0 . For a filter of length l and phase velocity c, v_0 is given by

$$v_0 = \frac{c}{2l}. (1)$$

Tuning over the range of 40–70 GHz is achieved by values of l in the range of 2.225–1.000 mm. The channel resolution $\mathcal{R} = v_0/\Delta v$, where Δv is the 3-dB width.

The bandwidth of a filter and its peak-value of transmission are governed by losses in the resonator, as measured by the quality factor. Assuming power loss P from the resonator when the stored energy is U, the total quality factor Q_t of the resonator (and hence the resolution of the filter $\mathcal{R} \equiv Q_t$) is defined by $Q_t = 2\pi v_0 U/P$. P can be further decomposed into the sum of the power loss $P_{c,in}$ and $P_{c,out}$ to the input and output circuit, respectively (coupling losses) and the power dissipated P_{int} in the resonator itself through Ohmic and dielectric loss (internal losses). We can then define additional quality factors Q_{int} , $Q_{c,in}$, and $Q_{c,out}$ by $Q_n = 2\pi v_0 U/P_n$ These correspond to the Q-factor of the resonator in the limit where the individual losses are dominant, and $Q_t^{-1} = Q_{int}^{-1} + Q_{c,in}^{-1} + Q_{c,out}^{-1}$. With these definitions, the power gain of the channel as a function of frequency v can be shown to be

$$G(v, v_0) = \frac{2Q_{\rm t}^2}{Q_{\rm cin}Q_{\rm cont}} \frac{1}{1 + 4Q_{\rm cin}^2(v - v_0)^2/v_0^2}.$$
 (2)

 $Q_{c,in}$ and $Q_{c,out}$ are controlled by the input and output coupling capacitors, respectively. For maximum transmission of power to the filter channel detector (-3 dB), we must engineer $Q_{c,in} = Q_{c,out} = Q_c \ll Q_{int}$, in which case $Q_t = Q_c/2 \ll Q_{int}$. Therefore, the minimum achievable bandwidth is limited by Q_{int} . Under the same conditions, the power transmitted to the residual power detector is

$$T(\nu, \nu_0) = 1 + \frac{Q_t}{Q_{c,in}} \frac{[Q_t/Q_{c,in} - 2]}{1 + 4Q_t^2(\nu - \nu_0)^2/\nu_0^2},$$
 (3)

and the power is reduced by 6-dB on resonance as seen from the dotted black line in Fig. 3(b). In practice, the presence of the coupling capacitors causes the measured center frequency $v_{0,\text{meas}}$ of the filter to differ slightly from the resonant frequency of the open-ended line, as given by (1). The detuning is dependent on coupling strength and analysis of the perturbation of the circuit to first-order gives

$$v_{0,\text{meas}} \approx \left(1 - \frac{1 + \sqrt{2}}{2\sqrt{\pi R}}\right) v_0.$$
 (4)

Ohmic loss is small in superconducting microstrip lines at low temperatures. Instead, experience with superconducting

microresonators <10 GHz suggests dielectric loss due to two-level-systems will govern $Q_{\rm int}$. A main aim of the prototype devices was to investigate the loss mechanisms active at millimeter wavelengths, although extrapolation from the low-frequency data suggest spectral resolutions of a few hundred should be easily achievable for the microstrip design used here, and higher may be achievable for alternatives such as coplanar waveguide.

We report on two of the four different filter-bank designs that were fabricated on the demonstrator devices. Three of these have identically spaced channels, designed to be 2.5 GHz apart over the range of 42.5-65.00 GHz. The designs differ in spectral resolution of the channels, with target values of R = 250, 500, and 1000. These explore the achievable control of channel placement and resolution and provide experimental characterization of the millimeter-wave properties of the microstrip over a wide frequency range. However, the channels in these designs are widely spaced. The fourth design investigated an alternate possible mode of operation where the passbands of the filters overlap significantly, with nine $\mathcal{R}=500$ channels in the band 53-54 GHz. In this case, the spacing of the filters along the feed line becomes significant, as the filters interact with each other electrically. If the filters are arranged in order of decreasing frequency and placed approximately a quarter of a wavelength apart, part of the in-band signal scattered onto the feed by the filter is reflected back in phase from the next filter along, enhancing transmission to the detector. Consequently, the transmission of the filters can be increased by dense packing but at the expense of the shape of the response; we are investigating the implications for scientific performance. The modeled passbands for the widely spaced filter banks are shown in Fig. 3(b).

C. TES design

Electrothermal feedback (ETF) in a thermally isolated TES of resistance R, voltage-biased within its superconducting-normal resistive transition means that the TES self-regulates its temperature T very close to T_c , the TES transition temperature. When the superconducting-normal resistive transition occurs in a very narrow temperature range $\alpha \gg 1$, where $\alpha = T(dR/dT)/R$ characterizes the transition sharpness. Provided T, $T_c \gtrsim 1.5 \, \mathrm{T_b}$ the bath temperature, the small-signal power-to-current responsivity s_I is then given by $s_I = -1/(I_0R_0)$. Here, T_0 , I_0 , and R_0 are the TES temperature, current, and resistance, respectively, at the operating point, and R_L is the load resistance. All are simple to evaluate from measurements of the TES and its known bias circuit parameters meaning that s_I is straightforward to evaluate.

The TESs we report on were designed to operate from a bath temperature of $T_{\rm b} \simeq 300$ mK in order to be usable from a simple cryogenic cooling platform, to have a saturation power of order $P_{\rm sat} \simeq 2$ pW to satisfy expected power loading, and a phonon-limited noise equivalent power (NEP) of order $2\,{\rm aW}/\sqrt{\rm Hz}$ to minimize detector NEP with respect to atmospheric noise (see Table I). TES design modeling indicates that the required performance should be achievable with a superconducting-normal transition temperature $T_c \sim 550 \rightarrow 650\,{\rm mK}$. The detailed calculation depends on the value of the exponent n that occurs in the calculation of the power flow from the TES to the heat bath: $P_{\rm b} = K_{\rm b}(T_c^r - T_{\rm b}^n)$, where $K_{\rm b}$ is a material dependent parameter that includes the

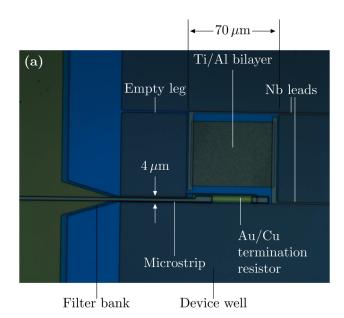
geometry of the thermal link between the TES and bath. For ideal voltage bias $(R_{\rm L}=0)$, the saturation power is $P_{\rm sat}=(1-R_0/R_{\rm N})P_{\rm b}$ and $R_{\rm N}$ is the TES normal-state resistance. 22 Under typical operating conditions, $R_0\sim 0.25R_{\rm N}$. The thermal conductance to the bath $G_{\rm b}=dP_{\rm b}/dT_{\rm c}$ determines the phonon-limited NEP, where NEP = $\sqrt{4k_{\rm b}\gamma G_{\rm b}T_{\rm c}^2}$, $k_{\rm b}$ is Boltzmann's constant, and $\gamma\leq 1$ takes account of the temperature gradient across $G_{\rm b}$. Our previous work suggests that at the operating temperature and length scales $n\sim 1.5\to 2.5$ should apply for 200-nm-thick SiNx at these temperatures with length of 50–1000 $\mu \rm m$. For the filter channels, thermal isolation of the TES was formed from four 200-nm-thick SiNx legs each of length 500 $\mu \rm m$, three of width 1.5 $\mu \rm m$, and one of 4 $\mu \rm m$ to carry the microstrip. The residual power detector had support legs of length 50 $\mu \rm m$ and the same widths as for the filter channels.

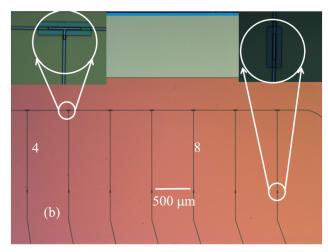
III. FABRICATION AND ASSEMBLY

The detector chips were fabricated on 225 µm thick Si wafers coated with a dielectric bilayer comprising 200-nm-thick low-stress SiN_x and an additional 50 nm SiO₂ etch stop. After Deep Reactive Ion Etching (DRIE), the SiN_x, SiO₂ films formed the thermal isolation structure. The detectors were made by sputtering metal and dielectric thin films in ultra-high vacuum. The superconducting microstrip was formed from a DC-sputtered 150 nm-thick Nb ground plane with 400 nm amorphous silicon dioxide (SiO₂) dielectric deposited by RF-sputtering. A second deposition of Nb 500 nm-thick 3 µm-wide completed the microstrip. The ground plane Nb also formed the TES wiring. The measured Nb residual resistance ratio (RRR) was 2.7-3.0. Coupling capacitors were made from SiO₂ and Nb. Thin-film AuCu resistors terminate the microstrip on the TES island. The TESs were fabricated from TiAl bilayers with the Ti and Al thicknesses of 150 nm and 40 nm, respectively, calculated to target a superconducting transition temperature of 600-650 mK.²³ DRIE removed the Si under the TES such that the SiN_x island and legs provide the necessary thermal isolation. The DRIE also released the individual chips from the host wafer and defined the chip shape.

Figure 4(a) shows an image of the residual power detector prior to DRIE of the Si. The 4 µm-wide leg supporting the microstrip is indicated in the lower left-hand region of the image. The $3\times35\,\mu\text{m}^2$ AuCu termination can be seen. Figure 4(b) shows a detail of the filter bank. The insets in the upper quadrants to the left and right show detailed images of the T-section coupling capacitors to the through line and the inline coupler to the TES detectors, respectively. In all cases, the microstrip was $3\,\mu m$ wide. Filter channels 4 and 8 are indicated. Filter lengths are determined by the line length between the T-section and inline capacitors. The lengths increase with channel number (left-to-right in the image) and correspondingly v_0 reduces. Figure 4(c) shows a composite high resolution photograph of a completed detector chip after DRIE. The component structures, waveguide probe, filter bank, TES detectors, and superconducting contact pads are indicated. The individual filter channels are also visible.

Figure 5 shows an image of a completed chip mounted in a detector enclosure. The probe can be seen extending into the lower half of the split waveguide. Al wirebonds connect the chip ground plane to the enclosure and on-chip electrical wiring to the





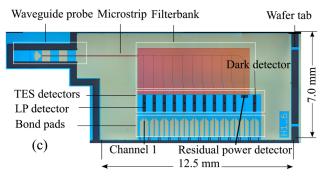


FIG. 4. (a) shows a high resolution optical image of a residual power detector. (b) shows a high resolution optical image of the filter bank. The expanded images in the upper left and right quadrants show details of a T-section and inline capacitive coupler. (c) is an optical image of a complete demonstrator chip.

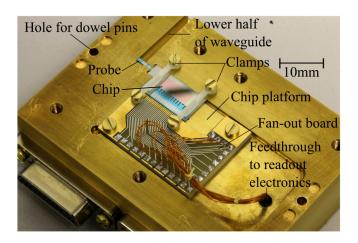


FIG. 5. Photograph of the assembled lower half of the detector enclosure. The upper part forms a light-tight enclosure and completes the upper section of the split waveguide.

superconducting fan-out wiring. Superconducting NbTi wires connect through a light-tight feedthrough into the electronics enclosure on the back of the detector cavity. The low-noise two-stage SQUIDs used here were provided by Physikalisch–Technische Bundesanstalt (PTB). The upper section of the detector enclosure completed the upper section of the waveguide and ensured that the package was light-tight. Completed detector packages were cooled using two separate cryogen-cooled ADRs both giving a base temperature $T_{\rm b} \lesssim 100$ mK.

Figure 6(a) shows a schematic of the blackbody power calibration scheme. A matched 50 Ω resistive load (Fairview Microwave

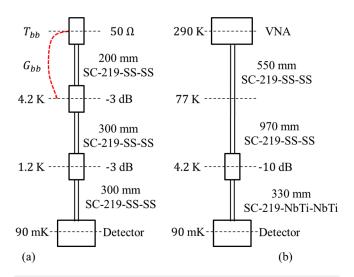


FIG. 6. Schematics of the measurement scheme. (a) For the blackbody power calibration. (b) For the measurement of filter spectral response.

SKU:ST6510)²⁴ terminated a length of Coax Co. Ltd SC-219/ 50-SS-SS coaxial line.²⁵ The load was mounted on a thermally isolated Cu plate within the cryostat 4K vacuum space. This termination-resistor stage was equipped with a calibrated LakeShore RuOx thermometer²⁶ and resistive heater and was connected to the refrigerator 4K stage by a length of copper wire $(l = 20 \text{ cm}, r = 250 \,\mu\text{m})$ that determined the dominant stage time constant which was estimated at $\sim 20\,\mathrm{ms}$. Furthermore, SC-219 connected the termination resistor to a 3-dB attenuator (Fairview Microwave SA6510-03) mounted on the 4K stage itself, to a further attenuator mounted on the 1 K stage, in order to minimize heat-loading to the cold stage by heat-sinking the inner coax conductor and then to the WR-15 coax-waveguide transition (Fairview Microwave SKU:15AC206), waveguide probe, and detector chip. The total coax length was 800 mm. Results of these measurements are reported in Sec. V.

The spectral response of the filters was measured using a continuous wave (CW) source. For these tests, a different cryostat was used, but it again comprised a two-stage (1 K/50 mK) ADR launched from a 4 K plate cooled by liquid cryogens. A different set of cold electronics with nine single-stage SQUIDs provided by PTB was used that allowed simultaneous readout of nine devices.

Figure 6(b) shows a schematic of the scheme. A Rohde and Schwarz ZVA-67 VNA capable of measurements up to 67 GHz was used as a frequency-tunable, power-leveled, CW source and the signal coupled down to the detector block on coaxial cable. A KMCO KPC185FF HA hermetic feedthrough was used to enter the cryostat, and then 1520 mm of Coax Co. Ltd SC-219/ 50-SS-SS coaxial cable was used for the connection between room temperature and the 4K plate, anchoring on the liquid nitrogen tank, liquid helium tank, and finally, the 4K plate itself. The final connection from 4K to the waveguide adapter on the test block was made using 330 mm of Coax Co. Ltd SC-219/50-NbTi-NbTi superconducting cable for thermal isolation. A 10 dB attenuator (Fairview Microwave SA6510-10) was inserted in line at the 4K plate to provide thermal anchoring between the inner and outer conductors of the coaxial cable and isolation of the detectors chip from short-wavelength power loading. All connections were made with 1.85 mm connectors to allow mode free operation up to 65 GHz. Measurements indicated the expected total attenuation of order 68 dB between room temperature and the WR-15 waveguide flange. In operation, the frequency of the CW source was stepped through a series of values and the output of the devices logged in the dwell time between transitions, exploiting the normal mode of operation of the VNA. Results of these measurements are reported in Sec. VI.

Two chips were characterized in this series of tests. Both chips were fabricated on the same wafer but have different designed filter resolving powers. Chip 1 has filters designed with $\mathcal{R}=500$ was used for the blackbody measurements with measurements on filter channels 4–6, 11 (the residual power detector), and 12 (the dark detector). Chip 2 with $\mathcal{R}=200$ was used for the spectral measurements and we report measurements on channels 3–8, 10, and 12. Both chips were designed to cover the 42.5–65 GHz range.

IV. RESULTS

A. TES characteristics

Table II shows values for the DC characteristics of five channels from Chip 1 calculated from measurements of the current-voltage response of the devices with bath temperature. T_c was close-to but somewhat lower than designed, and R_N was higher. The critical current density was also reduced from our previously measured values even for pure Ti films. This may indicate that these TESs demonstrate the thin-film interdiffusion effects between Ti-Nb thin-film interfaces recently reported by Yefremenko et al.27 The superconducting-normal resistive transition with $T_{\rm b}$, as inferred from measurements of the TES Joule power dissipation as a function of bias voltage, appeared somewhat broad. Hence, Gb was determined from the power dissipated at constant TES resistance $R_0 = 0.25R_{\rm N}$, close to the bias voltage used for power measurements. The exponent in the power flow was $n=2.0\pm0.1$ for all TESs. The calculated phonon-limited NEP was $6.1 \pm 0.1 \,\mathrm{aW}/\sqrt{\mathrm{Hz}}$ assuming a noise-modifier $\gamma = 0.7$, extrapolating to $9.2 \pm 0.1 \, \mathrm{aW}/\sqrt{\mathrm{Hz}}$ at the target T_c of 550 mK.

B. TES ETF parameters and power-to-current sensitivity estimate

For a TES modeled as single heat capacity C with conductance to the bath G_b , the measured impedance Z(f) is given by $\frac{1}{2}$

$$Z(f) = R_{\rm L} + j2\pi f L_{\rm in} + Z_{\rm TES}(f), \tag{5}$$

where $R_{\rm L}$ is the load resistance, $L_{\rm in}$ is the input inductance of the SQUID plus any stray inductance, $Z_{\rm TES}(f)$ is the TES impedance, and f is the measurement frequency. The TES impedance is given by

$$Z_{\text{TES}}(f) = R_0(1+\beta) + \frac{R_0 L_I}{1 - L_I} \frac{2 + \beta}{1 + 2\pi f \tau_I},\tag{6}$$

where $L_I = P_0 \alpha/G_b T_0$. $P_0 = I_0 R_0^2$ is the Joule power at the bias point, $T_0 \simeq T_c$ is the operating temperature, $\tau_I = C/[G_b(1-L_I)]$, and $\alpha = T(dR/dT)/R$ characterizes the sharpness of the resistive transition at the bias point. $\beta = I(dR/dI)/R$ measures the sensitivity of the transition to changes in current I. At measurement frequencies much higher than the reciprocal of the

TABLE II. Summary of DC TES characteristics for Chip 1.

Channel	$R_{\mathrm{N}}\left(\Omega\right)$	T_c (mK)	n	$G_{\rm b}\left({\rm pW/K}\right)$	Notes
4	2.6	457	2.0 ± 0.1	4.7	Filter
5	2.5	452	2.0 ± 0.1	4.75	Filter
6	2.4	454	2.0 ± 0.1	4.8	Filter
12	2.4	455	2.0 ± 0.1	4.7	Dark
11	2.4	459	2.0 ± 0.1	65	Residual power

effective TES time (here taken to be $f \ge 5 \,\mathrm{kHz}$), Re $(Z(f)) = R_0(1+\beta)$, and so β can be determined with minimal measured parameters. At low frequencies, here taken to be $f \le 5 \,\mathrm{Hz}$, Re $(Z(f)) = R_\mathrm{L} + R_0(1+\beta) - (R_0L_I(2+\beta))/(L_I-1)$ so that L_I and hence α can be found. The low-frequency power-to-current responsivity is given by

$$s_I(0) = -\frac{1}{I_0 R_0} \left[\frac{R_L + R_0 (1 + \beta)}{R_0 L_I} + 1 - \frac{R_L}{R_0} \right]^{-1}. \tag{7}$$

For a TES with good voltage bias $R_L \ll R_0$, and a sharp transition α , $L_I \gg 1$, then provided $\beta \ll L_I - 1$, $s_I(0) = -1/(I_0R_0)$ and the power-to-current responsivity is straightforward to calculate. For the TiAl TES reported here the measured power-voltage response suggested a fairly broad superconducting-normal transition. Here, we use the full expression given by Eq. (7) with $s_I(0) = -k_s/(I_0R_0)$ and k_s the calculated value of the reciprocal within the brackets.

The inset of Fig. 7 shows the measured real and imaginary parts (blue and green dots, respectively) of the impedance of the residual power detector, channel 11. The solid red and dashed black lines show the modeled impedance. Table III shows the derived ETF parameters and heat capacity. The main figure shows the measured normalized TES current response (blue dots) to a small change in TES bias voltage (i.e., a small modulation of instantaneous Joule power), and (red line) the calculated response using the parameters determined in modeling the impedance. No additional parameters were used.

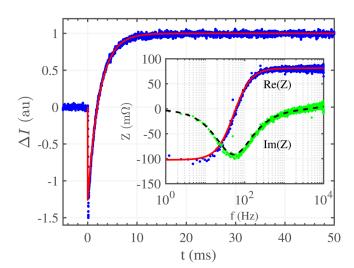


FIG. 7. The inset shows the measured real and imaginary parts (blue and green dots, respectively) of the impedance of the residual power detector (channel 11). The solid red and dashed black lines are the modeled values (real and imaginary, respectively) using the parameters given in Table III. The main plot shows the measured (blue dots) and calculated (red line) response of the TES current to a small step change in the bias voltage for the same channel with no additional parameters.

TABLE III. Summary of calculated ETF parameters.

Channel	α	β	C (fJ/K)	k_s	Notes
4	66	1.3	220	0.77	Filter
5	77	2.9	200	0.74	Filter
6	114	4.4	200	0.80	Filter
12	128	1.3	180	0.88	Dark
11	114	1.9	390	0.66	Residual power

The inset of Fig. 8 shows the measured real and imaginary parts (blue and green dots, respectively) of the impedance of the dark detector, channel 12. The solid red and dashed black lines show the modeled impedance. Table III shows the derived ETF parameters and heat capacity. The main figure shows the measured normalized TES current response (blue dots) to a small change in TES bias voltage (i.e., a small modulation of instantaneous Joule power), and (red line) the calculated response using the parameters determined in modeling the impedance. As in the modeling shown in Fig. 7, no additional parameters were used. Comparable correspondence between measured impedance and current step response using impedance-derived ETF parameters and heat capacity was found for all measured channels.

Derived values for α , β , the heat capacity, and k_s for Chip 1 used in the modeling of Figs. 7 and 8 are given in Table III. We see that α is low compared to our MoAu TESs, where $\beta \sim 0.05\alpha$, and the responsivity is reduced from the high- α value $k_s = 1$. The

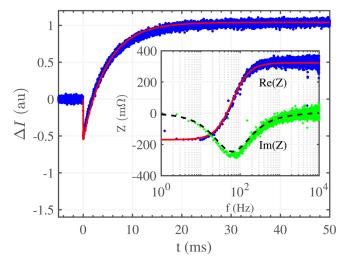


FIG. 8. The inset shows the measured real and imaginary parts (blue and green dots, respectively) of the impedance of the dark detector (channel 12). The solid red and dashed black lines are the modeled values (real and imaginary, respectively) using the parameters given in Table III. The main plot shows the measured (blue dots) and calculated (red line) response of the TES current to a small step change in the bias voltage for the same channel without additional parameters.

large variation in α may-well be a result of the same materials problem discussed in Sec. IV A.

V. POWER CALIBRATION

A. Available in-band power

We assume that the blackbody load, coax connection, 3-dB attenuators, 1.85 mm connectors , waveguide, probe, and matched termination load behave as an ideal wideband single-mode blackbody source at temperature $T_{\rm bb}$. The optical bandwidth is determined by the components in the source rather than the detector itself. The lower band edge is determined by the cut-off frequency of the lowest order mode in the WR15 waveguide section at $v_{\rm min}=39.9\,{\rm GHz}$. The upper cut-off frequency is determined by the 1.85 mm ('V–') connectors in the coaxial section (8 being used), which have a low-pass response with a 3-dB point at 65 GHz. Raising the lower edge to account for the width of the cut-on region and performance of the waveguide transitions, the estimated total bandwidth is 45–65 GHz.

Fitting a model to the manufacturer's data, we find that loss in the coax can be described by $\alpha_{\rm coax}(l)$ [dB] = 0.2648 l [mm]/ $\sqrt{\nu}$ [GHz], where l is the total line length. Additional losses arise from the 3-dB attenuators that form the coax heat-sinks (2 being used), and the 1.85 mm connectors (8 in total), each contributing an additional loss of $\alpha_{\rm c}=0.223$ dB. The total attenuation $\alpha_l(\nu)$ (in dB) is then

$$\alpha_l(v) = \alpha_{\text{coax}}(l) + 6 + 8\alpha_c. \tag{8}$$

The change in available power at the probe is $\Delta P_{\max}(T_{bb}) = (P_0(T_{bb}) - P_0(T_0))10^{-\alpha_l(\nu)/10}$, with T_0 the lowest operating temperature for the blackbody and

$$P_0(T) = \int_{v_{\min}}^{v_{\max}} hvn(v, T)dv, \tag{9}$$

where $n(v, T) = [\exp(hv/(k_bT) - 1)]^{-1}$ is the Bose distribution and h is Planck's constant.

To calculate the power transmission to the residual power detector, we assume each of the filter channels of center frequency $v_{0,i}$ (where i identifies the channel and $i \in 1, ..., 10$) taps its maximum fraction of the incident power given by Eq. (2) and the transmitted power is given by Eq. (3), with 6—dB attenuation at the residual power detector due to each filter at its resonant frequency. The total available power at the residual power detector $P_r(T_{\rm bb})$ is

$$P_r(T) = \eta \int_{V_{\min}}^{V_{\max}} hv n(v, T) \prod_i T_i(v, v_{0,i}) 10^{-\alpha_l(v)/10} dv, \qquad (10)$$

where Π is the product and η is the overall efficiency referenced to the input to the probe.

B. Coupled power measurement

Measurements of the detection efficiency were made on Chip 1 with the cold stage temperature maintained at 90 mK using a PID

feedback loop to regulate the ADR base temperature. Under typical (unloaded) conditions, the cold stage temperature is constant at $T_{\rm b}=\pm 100\,\mu{\rm K}$. $T_{\rm bb}$ was increased by stepping the blackbody heater current at 2 Hz with the change in measured TES current and T_{bb} digitized at 2 kHz. Changes in detected power in both the residual power and dark detectors, as T_{bb} was increased, were calculated from the change in their respective measured currents power-to-current assuming the simple responsivity, $s_I(0) = -1/(I_0R_0)$, with the TES operating points calculated from measured and known electrical circuit parameters. At the highest $T_{\rm bb}$ used, ~ 10 K, $T_{\rm b}$ increased by ~ 1 mK suggesting power loading onto the cold stage. Under the same blackbody loading, and even after subtracting the expected dark detector power response due to $T_{\rm b}$, $\Delta P_{\rm dark} = G_{\rm b,dark} \Delta T_{\rm b}$, the dark detector indicated a current (hence power) response that we interpret here as additional incident power. We modeled this residual response as a change in the temperature of the Si chip itself T_{chip} , finding an increase of order 3 mK at the highest $T_{\rm bb}$. The modeled chip response $\Delta T_{\rm chip}$ closely followed a quadratic response with $\Delta T_{\rm bb}$. Although the origins of this additional power loading could not be determined in this work, it may have arisen from residual short-wavelength loading from the blackbody source. At 10 K, the peak in the multimode blackbody spectrum is around 1 THz implying that there may be a significant available detectable power at high frequencies. Even a very small fraction of this power would be sufficient to produce the inferred chip heating. Changes in detected power for the residual power detector with T_{bb} were accordingly reduced to include the effect of both the measured $\Delta T_{\rm b}$ and the modeled $\Delta T_{\rm chip}$.

Figure 9 shows the change in detected power ΔP_r as a function of ΔT_{bb} , for T_{bb} in the range 4.2–9.4 K. The power response is close

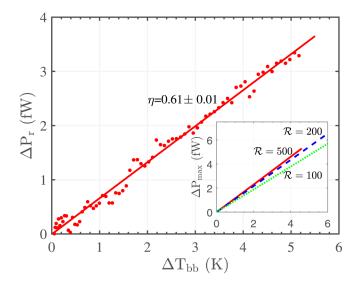


FIG. 9. Power detected for the residual power detector as a function of change in blackbody temperature. The inset shows the maximum available power P_{max} for three values of the filter-bank resolution. $\mathcal{R}=500$ is the designed resolution. The indicated efficiency η assumes $\mathcal{R}=200$ and was calculated with the maximum TES responsivity $s_i=-1/(l_0R_0)$.

to linear with temperature (the correlation coefficient $R_{cor} = 0.98$). The inset shows the maximum available power P_{max} calculated for three values of the filter resolution $\mathcal{R}_i = 100$, 200, and 500—the designed value. The residual power increases as \mathcal{R}_i increases. As discussed in Sec. VI, the designed value may represent an overestimate of the achieved R for the measured chip. Assuming $\mathcal{R}=$ 200, we estimate an efficiency of $0.61^{+0.01}_{-0.03}$ and the lower error is calculated assuming the designed filter resolution $\mathcal{R}=500$. The measured power and hence efficiency estimate were calculated assuming the maximum power-to-current responsivity $s_i = -1/(I_0 R_0)$. From the measured response of the residual power detector described in Sec. IV B and Table III, we see that this overestimates the sensitivity by a factor $1/k_s \simeq 1.5$. Taking account of this correction, our final estimate of overall efficiency referred to the input to the probe is increased to $\eta = 0.91^{+0.015}_{-0.05}$.

VI. FILTER SPECTRAL RESPONSE

Measurements of the spectral response were made on Chip 2. Figure 10 shows(dots) the normalized power detected and (solid lines) the modeled response using Eq. (2) (i.e., a Lorentzian) for filter channels 3–8 and 10 as labeled. Colors and annotation identify the individual channels. The individual filter responses \mathcal{R}_i and $v_{0,i}$ are shown in Table IV along with the nominal design filter center frequencies. Errors in $v_{0,i}$ were calculated from the fit to the data. The frequency sampling of channel 10 was insufficient to determine the filter response. For this channel, the model fit shown was calculated assuming $\mathcal{R}_{10} = 90.2$ (the mean of the other channels) and is plotted only as a guide to the eye. For this series of measurements, within measurement noise, there was *no* observable response in the dark channel and we estimate $\Delta P_{\rm dark} < 100 \, {\rm aW}$. The absence of any detectable dark response using a *power-leveled*, *narrow-band*, source operating at a fixed temperature of $\sim 300 \, {\rm K}$ —

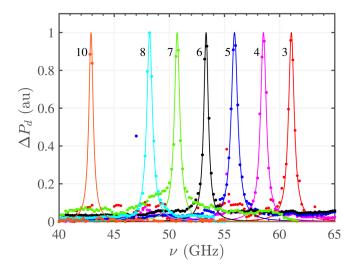


FIG. 10. Normalized power detected for filter channels 3–8 and 10 for Chip 2 (dots) and the calculated filter profiles (full lines). Colors and annotation identify the individual channels.

TABLE IV. Summary of designed and measured filter channel characteristics.

Channel	v _{design} (GHz)	<i>v</i> _{0,<i>i</i>} (GHz)	\mathcal{R}_i
3	60.0	61.06 ± 0.013	99.0 ± 6.5
4	57.5	58.53 ± 0.008	92.8 ± 3.4
5	55.0	55.90 ± 0.013	79.3 ± 4.2
6	52.2	53.33 ± 0.003	106.5 ± 2.0
7	50.0	50.71 ± 0.010	85.3 ± 5.5
8	47.5	48.21 ± 0.005	78.4 ± 1.9
10	42.5	42.89 ± 0.001	

in which case we might expect no, or only very small changes in additional loading—gives weight to our interpretation and analysis that the blackbody and efficiency measurements should indeed be corrected for heating from a *broad-band*, *temperature modulated* source. In those measurements, the dark detector showed an unambiguous response. The shapes of the individual filter responses were close to the expected Lorentzian as shown in more detail in Fig. 11 for channels 3 and 5.

Figure 12 shows the measured filter center frequencies v_0 as a function of reciprocal filter length l. The correlation is close to unity ($R_{\rm cor}=0.9998$) as predicted by Eqs. (1) and (4), demonstrating the precision with which it is possible to position the individual filter channels. From the calculated regression line v_0 (GHz) = $(66.15 \pm 0.20)/l$ (mm) – (1.14 ± 016) , and assuming a modest lithographic precision of $\pm 1\mu m$ for the fabrication process, we calculate $\delta v = \pm 37$ MHz for a 50 GHz filter, or $\delta v/v = 0.0007$. The finite value for the intercept is unexpected and is an area for future investigation. Uncertainties in parameters such as the realized coupling capacitance and the exact permittivity of the dielectric constant are expected, but from Eqs. (1) and (4),

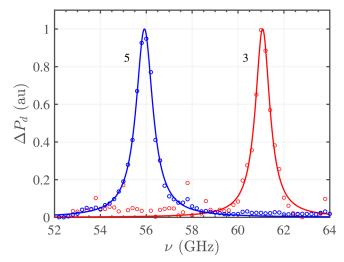


FIG. 11. Power detected from filter channels 3 and 5 with Lorentzian fits.

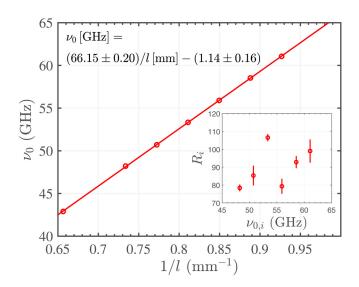


FIG. 12. Plot of measured filter center frequency as a function of inverse filter length. The inset shows the measured channel resolutions \mathcal{R}_i with frequency.

should only affect the constant of proportionality in the relationship $v_0 \propto l^{-1}$, rather than generate an offset. Instead, the offset suggests an issue with some detail of the underlying circuit model.

VII. SUMMARY AND CONCLUSIONS

We have described the design, fabrication, and characterization of a superconducting filter-bank spectrometer with transition edge sensor readout. We have described the design of a waveguidemicrostrip radial probe transition with wide bandwidth. We have demonstrated the detection of millimeter-wave radiation with frequencies from 41 to 64 GHz with characteristics that are already suitable for atmospheric temperature sounding using the O2 absorption band. The temperature of a cryogenic blackbody was modulated to determine the power detection efficiency of these prototype devices, finding an efficiency $\eta = 0.91^{+0.015}_{-0.05}$ using the measured impedance of the TES to determine the detection responsivity. Filter profiles were determined on a separate device. The measured channel profiles were Lorenztian as expected. We measured extremely good predictability of relative channel placement and also in individual channel placement $\delta v/v = 0.07\%$. We found somewhat lower filter resolutions \mathcal{R} than the designed values, possibly arising from our use in the filter design of dielectric constant measurements at lower frequencies compared to those explored here or, perhaps, unexpected dielectric losses. From the measured RRR of our Nb films, we estimate an effective penetration depth $\lambda_{\rm eff} = 100 \, \rm nm$ so that the influence of coupling and loss into the underlying SiNx cannot be discounted. This will be an area for future investigation. In the next phase of this work, we will continue to investigate this question but also investigate alternative filter architectures—particularly coplanar structures where dielectric losses may-well be lower. Guided by the measurements reported, we will refine our designs to increase channel density for the O₂-band observations and also include higher frequency channels particularly aimed at observation of the 183 GHz water-vapor line to enable a complete on-chip atmospheric temperature and humidity sounding instrument. Finally, we emphasize that, although we have presented and discussed these measurements in the context of an enhanced atmospheric temperature sounding demonstrator, we believe we have already shown impressive spectroscopic and low-noise power detection at technically challenging millimeter-wave frequencies with applications across a broad spectrum of scientific enquiry.

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DATA AVAILABILITY

The data that support the findings of this study are openly available in the Cambridge Research Repository Apollo at https://doi.org/10.17863/CAM.52378.

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