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## Plasma Damage on Low-k Dielectric Materials

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#### Abstract

Low dielectric constant (low-k) materials as an interconnecting insulator in integrated circuits are essential for resistance-capacitance (RC) time delay reduction. Plasma technology is widely used for the fabrication of the interconnects, such as dielectric etching, resisting ashing or stripping, barrier metal deposition, and surface treatment. During these processes, low-k dielectric materials may be exposed to the plasma environments. The generated reactive species from the plasma react with the low-k dielectric materials. The reaction involves physical and chemical effects, causing degradations for low-k dielectric materials. This is called "plasma damage" on low-k dielectric materials. Therefore, this chapter is an attempt to provide an overview of plasma damage on the low-k dielectric materials.

**Keywords:** plasma, low dielectric constant, porosity, plasma damage, Cu interconnects, back end of line, damascene

## 1. Introduction

To improve the performance of integrated circuits (ICs), the device dimensions are continuous scaling down. However, as the technology node of ICs is advanced to 0.25  $\mu$ m, the interconnect-induced delay outpaces the gate delay, becoming the main obstacles for the downscaling [1–3]. This interconnect-induced delay is so-called resistance-capacitance (*RC*) delay, which is produced by the conductors and insulators in the interconnects [4–6]. With decreasing the device dimensions, both the resistance and the interline capacitance increase due to the decrease of the conductor cross section, the increase of the wire length, and the reduction of interline spacing. Hence, the *RC* delay is significantly increased with the advance of the technology node.

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In order to slow down the increase of *RC* delay, the introduction of new materials to the back-end-of-line (BEOL) interconnects is needed. Aluminum (Al) had been replaced by copper (Cu) as a conductor dielectric because Cu can provide a lower resistivity ( $\rho$ ) [7]. In the case of the interconnecting insulator, the traditional SiO<sub>2</sub> dielectric had been replaced by the low-*k* materials with the relative dielectric constant (*k*) lower than 4.0 (SiO<sub>2</sub> *k* value) [8–10].

Additionally, the integration method for Cu/low-k interconnects must be changed because Cu etching is very challenging due to nonvolatile by-products. Traditional metal etching approach had been replaced by a damascene process [11]. In a damascene process, plasma technology is widely used because it can provide an isotropic process and a fast rate. Thus, these changes make the low-k materials to direct contact with the plasma, such as dielectric etching, photo strip, barrier metal deposition, and surface treatment. Under the plasma irradiation, low-k materials are sensitive to chemical modification, resulting in an increased k value. This is so-called plasma damage [12–15], becoming the main impediment to a successful integration of low-k materials into ICs.

In this connection, this chapter is an attempt to provide an overview of plasma damage on the low-*k* materials. This chapter is organized as follows: in Section 3, we introduce the low-*k* materials and plasma. Next, in Section 5, the processing with plasma damage on the low-*k* materials during interconnects fabrication is identified. Then, in Section 4, the results of plasma damage on the low-*k* materials based on our group's investigation are summarized. Finally, short conclusion is provided in Section 5.

### 2. Low-k materials and plasma

#### 2.1. Low-k materials

The dielectric constant of materials can be typically described by Clausius-Mossotti Equation [16]:

$$\frac{k-1}{k+2} = \frac{4\pi N}{3} \alpha \tag{1}$$

where  $k = \varepsilon/\varepsilon_0$ ,  $\varepsilon$ , and  $\varepsilon_0$  are the dielectric constants of the material and vacuum, N is the number of molecules per unit volume (density), and  $\alpha$  is the total polarizability, including electronic ( $\alpha_e$ ), distortion ( $\alpha_d$ ), and orientation ( $\alpha_o$ ) polarizabilities. According to Eq. (1), the dielectric constant of materials can be reduced by two strategies: decreasing the total polarizability ( $\alpha$ ) and density (N). Reducing the polarizability can be achieved by the use of low polar bonds (like C–C, C–H, Si–CH<sub>3</sub>, etc.), and reducing the film's density can be obtained by means of the introduction of porosity. **Table 1** summarizes the classification of low-k materials and their corresponding dielectric constants.

Low-*k* materials can be divided into several categories: silica-based, silsesquioxane (SSQ)-based, organic polymers, and amorphous carbon low-*k* materials [17–20]. The last three categories have integration issue due to the weak mechanical strength; therefore, they are not officially production in the semiconductor industry.

Classification	Material	Dielectric constant (k)		
O'lling have d	SiOF (FSG)	~3.5		
Silica based	SiCOH	~2.8		
Silsesquioxane (SSQ) based	HSQ	~3.0		
	MSQ	~2.5		
Polymer	Ploy(arylene ether) (PAE)	~2.6		
	Polyimide	~2.3		
	Parylene-N(-F)	~2.4		
	Teflon (PTFE)	~2.0		
Amorphous Carbon	C-C (-F)	~2.0		
Porous	Porous SiCOH, MSQ, PAE	< 2.0		
Air gaps	Air	~1.0		

 Table 1. Low-k dielectric classification.

The silica-based low-*k* materials have been successfully integrated in a microprocessor due to high chemical and thermal stability. The silica-based materials have the tetrahedral basic structure of  $SiO_2$ . Silica has a molecular structure, in which each Si atom is bonded to four oxygen atoms and each oxygen atom is bonded to two silicon atoms (SiO<sub>4/2</sub>). Each silicon atom is at the center of a regular tetrahedron of oxygen atoms.

The first-generation low-*k* material in semiconductor production line was fluorinated silicon glass (FSG or SiOF), in which the Si–O bond is replaced by the less polarizable Si–F bond. FSG materials were used at the 0.18  $\mu$ m technology node with the dielectric constant from 3.5 to 3.8, depending on the concentration of Si–F bond [21, 22].

Next, the second-generation low-*k* material was the organosilicate glass (SiCOH), in which the Si–O bond is replaced by the less polarizable Si–CH<sub>3</sub> bond. The *k* value of the SiCOH material is in the range of 2.6–3.0, depending on the number of CH<sub>3</sub> groups built into the structure. So, SiCOH materials were successfully integrated in some 130 nm and 90 nm products [23, 24]. Generally, FSG and SiCOH materials were deposited by plasma-enhanced chemical vapor deposition (PECVD). Moreover, both fluorine and carbon increase the interatomic distances or "free volume" of silica. This provides an additional decrease of dielectric constant but decreases the film density. Since the CH<sub>3</sub> group has a larger volume and is hydrophilic, SiCOH materials have a lower density (~1.2–0.4 g/cm<sup>3</sup>) and tend to be hydrophilic.

The limitation of k value for SiCOH materials is ~2.6. To prevent or limit an increase in the BEOL capacitance in the advanced technology nodes (65 nm or below), it requires a new low-k material with a further lower-k value (< 2.5). To meet this goal, the introduction of porosity in the low-k SiCOH materials is required because air can provide the minimum k value of ~1.0. The produced low-k material is a so-called porous SiCOH dielectric, which can be fabricated either by the structural or the subtractive method [25–27]. The latter method is widely accepted because the produced film is more thermally stable and can provide

a lower-*k* value. In the subtractive method, the films are deposited as a dual-phase material, using a mixture of a SiCOH skeleton precursor with an organic porogen precursor. The popularly used skeleton precursor is diethoxymethylsilane (DEMS). The used organic porogen precursor must have sufficient volatility for easy removal. The used molecules are alpha-terpinene (ATRP), bicycloheptadiene (BCHD), or cyclooctane ( $C_8H_{16}$ ). Hence, in order to remove the labile organic fraction in the as-deposited films, curing process has to be done after the deposition [8, 10, 27]. By this way, a porous film can be formed. Thermal curing, electron beam, or ultraviolet (UV) irradiation can be used to achieve this work. Generally, UV-assisted curing for the fabrication of porous SiCOH dielectrics is widely adopted by the semiconductor industry because it can also rearrange the film's structure and enhance the cross-linking of the skeleton. This provides a big help to improve the mechanical strength for porous SiCOH dielectrics.

The *k* value of porous SiCOH dielectrics can be scaling down by increasing the porosity and pore size simultaneously. However, this makes materials to become softer. Moreover, both the dielectric breakdown field and leakage current are degraded. Furthermore, as the porosity or pore size increases to a critical value, the pores can be connected each other to form so-called open pores. The open pores can be served as the easier penetration path into the bulk of the low-*k* material for active reactants [28]. Thus, more challenges will be addressed as porous SiCOH dielectrics are integrated in the advanced technology nodes.

#### 2.2. Plasma

In a vacuum system, plasma can be produced by introducing the process gas and applying the power. The process gas can be underwent ionization, excitation/relaxation, and dissociation under the power. Therefore, energetic ions, electrons, light (from deep vacuum ultraviolet (VUV) to infrared (IR)), and highly reactive radicals are produced in the plasma [29, 30]. In semiconductor processing, plasma technology can be used for ion implantation, etching, and deposition. The ion implantation processing is achieved by the energetic ions. The etching processing involves both physical and chemical reactions, which are related to the energetic ions and the highly reactive radicals, respectively. The deposition processing only relies on the highly reactive radicals for chemical reaction.

To produce the plasma, three main reactors are used: capacitively coupled plasma (CCP), inductively coupled plasma (ICP), and remote or downstream plasma (RP or DSP) [31]. The energy transfers are through capacitive coupling by parallel electrodes, inductive coupling by a coil, and microwaves for CCP, ICP, and RP systems, respectively. In the CCP and ICP systems, light from VUV to IR, energetic ions, electrons, and highly reactive radicals are presented. In the RP reactors, however, the plasma generation region is usually separated from the processing region. Additionally, a grid between the plasma and the substrate is used for charge neutralization, and a special measure is designed to minimize the photon flux. As a result, only reactive radicals or dissociated molecules or atoms can reach the surface of the wafer. This minimizes the damage from light and/or high-energy species. Due to the absence of ions, the RP reactors cannot provide patterning etching.

In the ICP systems, there are two applied RF power: one is source power (top power), and the other is bias power (bottom power). Therefore, plasma density and ion energy can be controlled separately. Additionally, the ICP system has the highest plasma density with 10<sup>11</sup>–10<sup>12</sup> electrons/cm<sup>3</sup> [31]. The plasma density of CCP system is 10<sup>9</sup>–10<sup>10</sup> electrons/cm<sup>3</sup>. The RP system has the lowest plasma density. Due to anisotropic etching property provided by ion bombardment, ICP and CCP systems are usually used for pattern etching. Since dielectric films are very sensitive to ion bombardment and ICP reactors lack passivating species required by typical dielectric etching, CCP reactors are mostly used for dielectric patterning etching. On the other hand, ICP reactors are often used for conductor patterning etching due to the etching rate consideration. To avoid damage by ion bombardment and UV light irradiation or no need anisotropic etching in the plasma process, RP reactors are the best choice. So, cleaning and resist stripping processes during semiconductor fabrication can be done by RP reactors.

#### 2.3. Plasma damage mechanism

The plasma-induced damage on the low-*k* dielectrics is a complex phenomenon involving both physical and chemical effects. Ion bombardment on the low-*k* dielectrics represents the physical effect. This effect depends on the energy distribution and flux for each ionic species. The chemical effect involves photochemistry induced by the UV radiation and chemical reaction between the radicals and low-*k* constituents. Under physical and chemical reactions in the plasma, the surface of low-*k* dielectrics is modified. The modification depth is related to the ion energy, diffusion of active radicals (O, H, F, etc.), and porosity and constituents in the low-*k* material [32, 33].

The plasma damage on low-*k* dielectrics makes the increase of the dielectric constant, the changes in bonding configuration, the formation of carbon-depleted layer, film shrinkage, and surface densification.

The depletion of carbon is mainly caused by active radicals through chemical reactions. Due to the loss of hydrophobic  $CH_3$  groups, the surface of low-*k* dielectrics becomes hydrophilic and adsorbs moisture. Therefore, a drastically increase in the *k* value and leakage current and a degradation in the dielectric breakdown were detected for plasma-treated low-*k* dielectrics.

## 3. Low-k plasma damage during interconnects fabrication

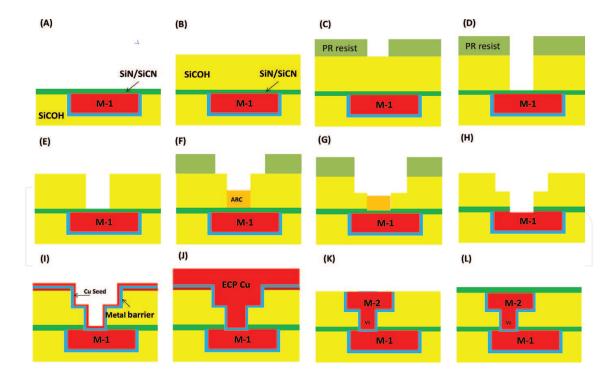
As Al/SiO<sub>2</sub> interconnects had been transferred to Cu/low-*k* interconnects, the fabrication method was also changed. "Damascene" process has been used to fabricate Cu/low-*k* interconnects because Cu cannot be easily patterned by reactive ion etching (RIE) due to the low volatility of Cu etching by-products, such as Cu chlorides and Cu fluorides [34]. Generally, "dual-damascene" process, in which both via and trench are patterned simultaneously, is widely used. The sequence of via and trench patterning can be changed. Via-first dual-damascene process, in which via is first patterned, is preferred [35]. The process flow of via-first dual damascene is plotted step by step, as shown in **Figure 1**.

The induced plasma damage on low-*k* dielectrics during the fabrication of Cu/low-*k* interconnects by the use of via-first dual-damascene process is described below:

After processing of Metal-1 (M-1), the etching stop layer (Cu barrier dielectric layer) is firstly deposited by PECVD method. The used material can be SiN, SiC, or SiCN. Before deposition,  $NH_3$  or  $H_2$  plasma clean is performed to remove copper oxide (CuO<sub>x</sub>) for adhesion improvement [36, 37]. Both these two steps would damage the underlying low-*k* dielectric. Then, a PECVD SiCOH low-*k* dielectric film is deposited for the Via-1 (V-1)/Metal-2 (M-2) patterning. Due to the presence of the etching stop layer, the plasma damage is seldom occurred in this step. Next, Via-1 and Metal-2 trench are subsequently patterned. Via-1 patterning is stopping on the etching layer. Then, before Metal-2 trench patterning, the plug is filled into the Via-1 to avoid etching during Metal-2 trench etching. Finally, resist removal and etching stop layer opening are subsequently performed to complete the dual-damascene patterning.

In the Via-1 and Metal-2 trench patterning, the etching process induces plasma damage not only on the horizontal surfaces but also on the vertical surfaces (sidewall).

The damaged layer on the horizontal surfaces can be removed as the etching proceeds. Therefore, the resulted damage on the low-*k* dielectric is the result of a competition between the etching rate and the diffusion rate of active species causing the damage. The non-damaging process can be achieved by using higher etching rate process. However, for vertical surfaces, the damage is still remained after etch. The damage is more minor due to the absence of ion



**Figure 1.** Via-first dual-damascene process flow for Cu/low-*k* interconnects. (A) Etching stopping layer deposition (SiN, SiC, SiCN, SiCOH, SiO<sub>2</sub>). (B) Low-*k* dielectric (SiCOH) deposition. (C) Via-1 lithography. (D) Via-1 etching. (E) Photoresist ashing or stripping. (F) Via-1 ARC plug and M-2 trench lithography. (G) M-2 trench etching. (H) ARC plug removal, photoresist ashing, and stripping etching stop layer opening. (I) Cu metal barrier and seed layer deposition. (J) Cu ECP deposition. (K) Cu CMP. (L) Etching stopping layer deposition (repeated (A)).

bombardment. The detrimental effect on the low-*k* dielectrics is caused by photoresist process because oxygen ( $O_2$ ) is widely used as plasma gas due to high reactivity of O radicals [38, 39]. To minimize the plasma damage during the photoresist process, H<sub>2</sub>-based plasma in the RP reactor is a viable alternative. To facilitate the removal rate of photoresist, the operation temperature can be elevated [40, 41].

Then, metallization process is preceded in the dual-damascene structure. Cu barrier layer, Cu seed layer, and bulk Cu layer are subsequently deposited. Finally, Cu chemical-mechanical polishing (CMP) process is used to remove the excess metal over the field regions. Thus, a layer of Cu dual-damascene structure (via and trench) is finished. In these steps, Cu barrier layer and seed layer are performed by PVD sputtering with using plasma. The former step would cause damage on the low-*k* dielectrics due to the direct contact with the dielectric. The purpose of this step is to prevent Cu from diffusing into the dielectric, and the typically used material is a TaN/Ta barrier layer. It should be mentioned that plasma cleaning before Cu barrier layer deposition is necessary because the underlying Cu film is opening. This plasma cleaning can be done either by Ar physical bombardment or  $H_2$  chemical reaction. However, low-*k* dielectrics are damaged under such plasma cleaning.

After completing the Cu metallization fabrication, the above steps are repeated for each metal level. After the last metal layer is fabricated, thick dielectric passivation layer (e.g.,  $SiO_2/SiN$  bilayer) is deposited, and via is opened to the bond pads.

## 4. Low-k plasma damage

#### 4.1. Plasma damage characterization

To characterize the plasma damage on the low-*k* dielectrics, several methodologies can be used to detect the physical and chemical changes of low-*k* dielectrics after irradiation of plasma. The plasma induces a dense, hydrophilic,  $SiO_2$ -like layer at the top surface of the low-*k* dielectric. The thickness of this layer can be measured using spectral reflectivity or ellipsometry with bilayer model, scanning electron microscope (SEM), or transition emitting microscopy (TEM). **Figure 2** displays TEM image of the porous low-*k* dielectrics after  $O_2$  plasma treatment. A distinct layer is formed at the top surface of the film.

X-ray reflectivity (XRR) is another method to determine the density, thickness, and roughness of both pristine and damaged low-*k* layers through software data fitting [42]. **Figure 3** shows the XRR density profile of the low-*k* film after He plasma. The result demonstrates that He plasma creates a thin densification layer in the top part of the low-*k* film. The thickness of this densification layer is close to 17 nm. The density of the bulk layer in the pristine material density is constant and remained unchanged. However, the top of the densification layer has a higher density [43].

"HF decoration" method [44] can be used to detect the modification layer induced by plasma. This method is based on the fact that a pristine low-k dielectric is usually not dissolved or



**Figure 2.** TEM image of porous low-*k* dielectric after O<sub>2</sub> plasma treatment.

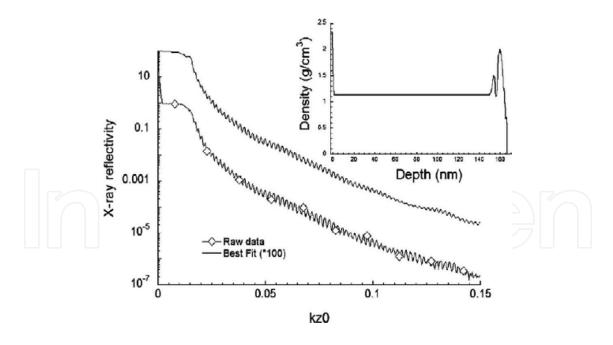


Figure 3. XRR spectrum of low-k dielectrics after He plasma treatment [43].

slowly dissolved in the diluted HF solution. In contrast, a plasma-induced damaged layer is attacked by HF very quickly. Therefore, following the HF decoration, the thickness loss is equal to the thickness of the damaged layer.

A given system of solid and liquid (or vapor) at a given temperature and pressure has a unique equilibrium contact angle. The measured angle is water contact angle (WCA). It can be used to quantify the wettability of a solid surface by a liquid via the Young equation. If the used liquid molecules are strongly attracted to the solid molecules, the liquid drop then will completely spread out on the solid surface, corresponding to a WCA of 0°. This case can be occurred at bare metallic or ceramic surfaces for water liquid. As an oxide layer or contaminant is on the solid surface, WCA value significantly increases. Generally, the solid surface tends to be hydrophilic if WCA value is smaller than 90°, while if WCA value is larger than 90°, the solid surface is considered to be hydrophobic. For low-k dielectrics, WCA measurement is a power method to determine the films' hydrophobicity. If the used low-k dielectrics are hydrophilic, they tend to absorb moisture in the air, increasing the dielectric constant. Moreover, as the plasma is treated on low-k dielectrics, Si-OH/H-OH bonds can be formed because the plasma-generated dangle bonds absorb moisture. Figure 4 compares the WCA values and images of the pristine and plasma-treated SiCOH low-k dielectrics. The WCA value of the as-deposited SiCOH low-k dielectrics is larger than  $85^{\circ}$  due to the presence of hydrophobic Si-CH<sub>3</sub> groups. After plasma irradiation, the loss of Si-CH<sub>3</sub> groups and the formation of Si–OH/H–OH bonds result in a decreasing WCA value, making the low-k dielectric to be more hydrophilic.

Fourier transform infrared (FT-IR) spectroscopy is a common technique to characterize the structure of SiCOH low-*k* dielectrics [45, 46]. **Figure 5** compares the FTIR spectrum of the pristine and plasma-treated low-*k* dielectrics. Absorption bands located at ~950–1250 and ~1273 cm<sup>-1</sup> correspond to Si–O–Si and Si–CH<sub>3</sub> groups, respectively, which are the main representative. Additionally, Si–H bending and C–H<sub>x</sub> stretching located at 2200–2250 and 2850–3100 cm<sup>-1</sup> are detected. The appearance of the absorbance of the Si–OH and H<sub>2</sub>O groups at 3200–3500 cm<sup>-1</sup> depends on hydrophobic properties of the film. For the pristine SiCOH low-*k* dielectrics, no peak at 3200–3500 cm<sup>-1</sup> is detected, representing that no moisture is present in the film, which is consistent with WCA result. After NH<sub>3</sub> plasma irradiation, the intensities

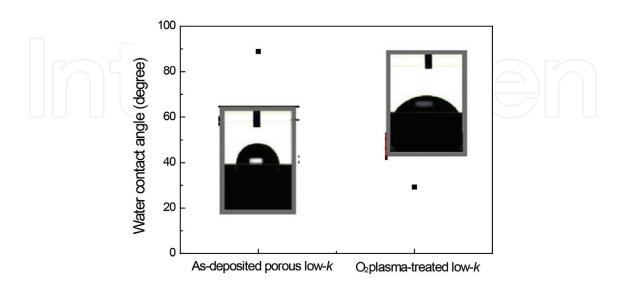


Figure 4. WCA values and images of porous low-k dielectrics after O<sub>2</sub> plasma treatment.

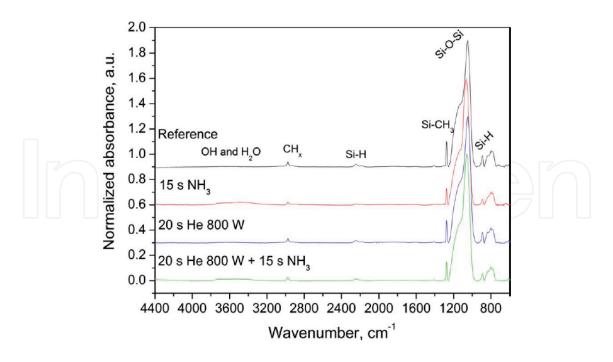


Figure 5. FT-IR absorption spectra of low-k dielectrics before and after plasma treatment in the range of 4400–400 cm<sup>-1</sup> [47].

of the Si–CH<sub>3</sub> and Si–H absorbances are decreased, while the absorbances of the Si–OH/H<sub>2</sub>O groups are increased. However, the low-*k* dielectric is pretreated with He plasma, and it can suppress the formation of the Si–OH/H<sub>2</sub>O groups [47].

The Si–O–Si bridging in the 900–1250 cm<sup>-1</sup> can be deconvoluted into three peaks centered at 1129, 1063, and 1023 cm<sup>-1</sup>, corresponding to the Si–O–Si cage-like structure with a bond angle of approximately 150°, Si–O–Si network with a bond angle of 140°, and Si–O–Si suboxide structure with a bond angle of less than 140°, respectively. Other contributions from C–O–C and Si–O–Si asymmetric stretching will also be overlapped with the Si–O–Si asymmetric stretching in the broadband at 1000–1200 cm<sup>-1</sup> [45]. The intensity of Si–O–Si bonds slightly increases, and this peak shifts to a higher wavelength after O<sub>2</sub> plasma treatment.

X-ray photoelectron spectroscopy (XPS) is a surface-sensitive spectroscopic technique to quantitatively measure a material's elemental composition. XPS can also be operated in a "depth-profiling mode" to analyze the elemental composition throughout the film by using ion etching/sputtering technology. For SiCOH low-*k* dielectrics, C, O, and Si elements can be detected, while H element cannot be detected.

In the pristine SiCOH low-*k* dielectrics, a homogeneous chemical composition was expected, but the ratio of these elements depends on the used materials. For the plasma-treated sample, the top surface exhibits a high initial oxygen concentration coupled to a very low carbon concentration. A gradual increase in carbon content and a concomitant decrease in oxygen concentration were observed with the film depth, as shown in **Figure 6**. As the atomic concentrations are back to a level same with the pristine low-*k* dielectric, the depth is corresponding to the plasma-damaged layer. Moreover, this plasma-damaged layer is not a homogeneous layer.

The dielectric constant (k), the leakage current, the breakdown voltage (or field), and the breakdown time of low-k dielectrics are measured using metal-insulator-semiconductor (MIS) capacitor structures, which can be fabricated by evaporation of aluminum through a metal shadow mask to form Al dots on the film. Before measurements, the samples are required to remove the physically absorbed water by annealing at 100–150°C. The k value of the low-k dielectric is determined from the measured capacitance by capacitance-voltage (C-V) measurements at a frequency of 10 kHz. The film thickness and the dot area must be precisely measured in order to obtain the reliable k value. The leakage current and the breakdown voltage (or field) are determined by current-voltage (I-V) measurements. The breakdown field is calculated by the measured breakdown voltage divided by film thickness. The leakage current J (A/cm<sup>2</sup>) is recorded as a function of field strength until the breakdown field is reached. It is usually reported at a low field of 1–2 MV/cm. The breakdown time is measured by using time-dependent dielectric breakdown (TDDB) tests. In a TDDB test, a constant voltage (field) is applied to the MIS capacitor structure with a low-k dielectric, and the leakage current is monitored with stress time. The dielectric breakdown time is recorded as the stress time at a sudden rise of the leakage current density. The applied voltage (field) must be lower than the measured breakdown voltage (field). In a real Cu/low-k interconnects, comb/serpentine (also called meander fork) or comb/ comb (fork/fork) patterns are typically used to measure the interline capacitance, the leakage current, the breakdown field, and the dielectric breakdown time.

#### 4.2. Plasma damage on the electrical characterization and reliability

In this section, the results of plasma damage on the low-*k* dielectrics from our group's investigation are reported in terms of the effects on the electrical characterization and reliability. The experimental detail deposition can be found elsewhere [39, 48–50].

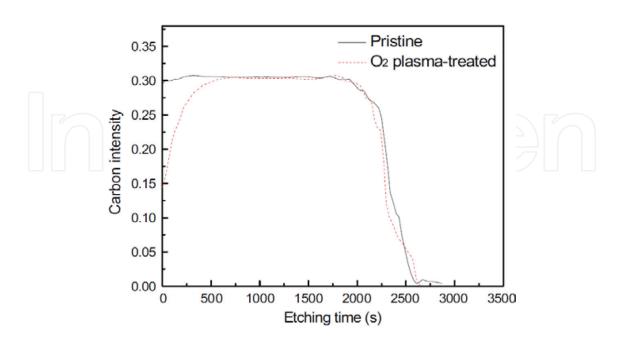


Figure 6. Carbon concentration of XPS depth-profiling for pristine and O<sub>2</sub> plasma-treated low-k dielectrics.

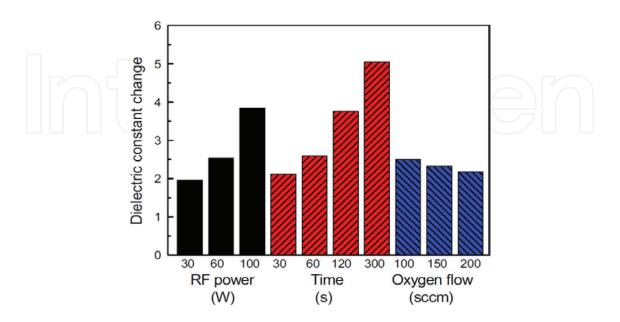
#### 4.2.1. O, plasma damage

#### 4.2.1.1. Plasma process dependence

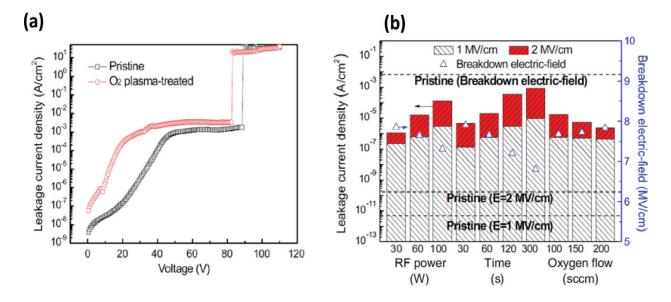
**Figure 7** shows the variation in the *k* value of a porous low-*k* dielectric after  $O_2$  plasma treatments with various plasma conditions (power, treatment time, and  $O_2$  flow rate). The *k* value of the pristine porous low-*k* dielectric was 2.56. The *k* values of porous low-*k* dielectrics increased after plasma treatment. The increasing magnitude increased with increasing the RF power and the treatment time but slightly deceased with increasing the  $O_2$  flow rate. More reactive oxygen species (ions and radicals) formation and a deeper penetration depth for a higher RF power and a longer treatment time in an  $O_2$  plasma treatment can be responsible for a larger change in the *k* value. The negative dependence on the  $O_2$  flow rate can be attributed to the decreased dissociation rate of  $O_2$  gas due to a fixed RF power, leading to a decreased reactive oxygen species (ions and radicals) in the plasma.

**Figure 8(a)** compares the leakage current density and the stress electric field for the pristine and  $O_2$  plasma-treated low-*k* films. At a lower electric field, the leakage current density increases with increasing the electric field (region I). Then, the leakage current density reaches a plateau without significant variation (region II). Finally, the leakage current density suddenly jumps, whose value is over  $10^{-2}$  A/cm<sup>2</sup>. This electric field is defined as the dielectric breakdown electric field (region III). For  $O_2$  plasma-treated low-*k* dielectrics, a higher leakage current in region I, a longer duration in region II, and a lower breakdown electric field in region III were detected.

**Figure 8(b)** compares the leakage current densities at 1 MV/cm and 2 MV/cm and the breakdown electric field of the  $O_2$  plasma-treated low-*k* dielectrics with various treatment conditions. Similar to the result of *k* value change, a higher RF power, a longer treatment time, and a lower  $O_2$  flow rate can result in the largest increase of the leakage current density for



**Figure 7.** Dielectric constant change of porous low-*k* dielectrics under various  $O_2$  plasma treatment conditions (standard condition: RF power = 60 W; time = 60 s; oxygen flow = 100 sccm).



**Figure 8.** (a) Leakage current density versus applied voltage for pristine and  $O_2$  plasma-treated low-*k* dielectrics. (b) Leakage current densities at 1 and 2 MV/cm and dielectric breakdown field of porous low-*k* dielectrics under various  $O_2$  plasma treatment conditions.

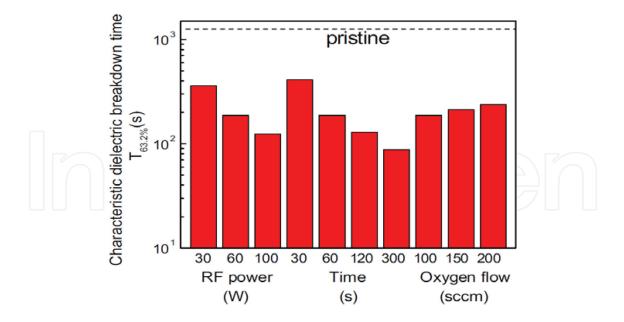
 $O_2$  plasma-treated low-*k* dielectrics. A higher leakage current density also leads to a lower breakdown electric field for the  $O_2$  plasma-treated low-*k* dielectrics. The degrading electrical performance for  $O_2$  plasma-treated low-*k* dielectrics can be attributable to more absorption of moisture, which provides ionic conduction pathways by releasing mobile ions (H<sup>+</sup>, OH<sup>-</sup>) [51].

**Figure 9** compares the characteristic dielectric breakdown times  $(T_{63.2\%})$  for  $O_2$  plasma-treated low-*k* dielectrics with various treatment conditions. The characteristic dielectric breakdown time was determined from Weibull distribution, representing the time as 63.2% of the sample failed [52]. The degradation in the characteristic dielectric breakdown time becomes serious with an increase of RF power and treatment time or a reduction of  $O_2$  flow rate. All process parameters in the  $O_2$  plasma process would degrade low-*k* dielectric properties. The RF power and the treatment time of  $O_2$  plasma treatment cause a more significant degradation.

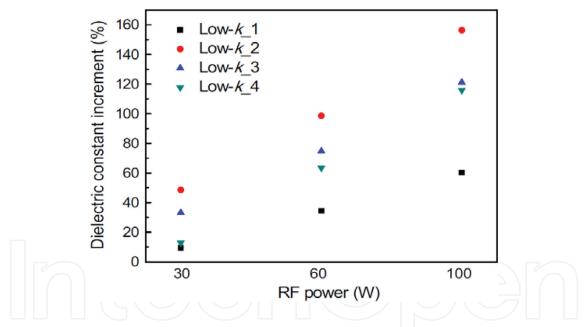
#### 4.2.1.2. Low-k dielectric dependence

Four kinds of low-*k* dielectrics were treated by  $O_2$  plasma with various RF powers. The used low-*k* dielectrics were dense low-*k* (k = 3.02; called low- $k_1$ ), porogen low-*k* without UV (k = 2.92; called low- $k_2$ ) and with UV curing (2.56; called low- $k_3$ ), and porogen low-*k* with UV curing and followed by a RP H<sub>2</sub>/He plasma treatment (k = 2.48; called low- $k_4$ ).

**Figure 10** compares the change percentage of the *k* value as a function of RF power in  $O_2$  plasma process for four different low-*k* films. The increasing magnitude is enlarged with an increase of RF power for all low-*k* dielectrics. The porogen-containing low-*k* dielectrics (low- $k_2$  and low- $k_3$ ) have a higher increase in the *k* value as compared to low- $k_1$ . This suggests that porogen plays an important role for the low-*k* dielectrics under plasma irradiation. Furthermore, the highest increase in the *k* value is occurred in the low- $k_2$  (porogen-containing low-*k* film without UV curing). The UV irradiation on the low-*k* dielectrics not only removes the porogen to form pores but also strengthens the bonding strength of the low-*k* dielectrics.



**Figure 9.** Characteristic dielectric breakdown times of porous low-*k* dielectrics under various  $O_2$  plasma treatment conditions (standard condition: RF power = 60 W; time = 60 s; oxygen flow = 100 sccm).



**Figure 10.** Change percentage in dielectric constant of different low-*k* dielectrics as a function of RF power in O<sub>2</sub> plasma treatment.

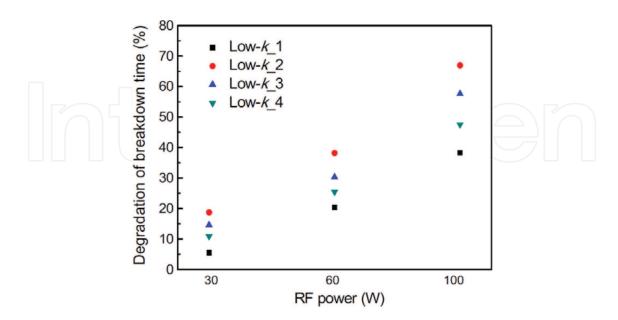
[53]. Therefore, the resistance to  $O_2$  plasma damage can be reinforced. To enhance plasma resistance for porous low-*k* dielectrics, a RP H<sub>2</sub>/He plasma treatment seems to be a possible method to alleviate the increase in the *k* value upon  $O_2$  plasma process. The RP H<sub>2</sub>/He plasma treatment can form a densification layer on the low-*k* dielectric's surface without damaging film's properties. This formation densification layer can effectively resist  $O_2$  plasma damage and prevent active oxygen species to penetrate into the film. However, as the RF power further increases in  $O_2$  plasma treatment process, the difference in the *k* value in low-*k*\_3 and low-*k*\_4

becomes smaller. This implies that the post-remote  $H_2/He$  plasma treatment on the porous low-*k* dielectric is becoming ineffective in preventing  $O_2$  plasma damage as a higher RF power. As the RF power is increased in  $O_2$  plasma treatment process, the more active oxygen species are produced, and these active species get more energy so as to penetrate the densification layer induced by remote  $H_2/He$  plasma treatment into a deeper region within the low-*k* dielectric, causing the bonding breakage and reaction with moisture.

**Figure 11** compares the degradation in the characteristic dielectric breakdown times relative to those of the pristine low-*k* dielectrics as a function of RF power. The stress electric field was 6.8 MV/cm for all low-*k* dielectrics. The reliability performance continuously degrades with RF power. Additionally, for the same RF power, the degradation order is low-*k*\_2 > low-*k*\_3 > low-*k*\_4 > low-*k*\_1. This means that porogen, rather than pore, within a low-*k* dielectric is a key issue to cause the reliability degradation under O<sub>2</sub> plasma treatment. Since two phases (matrix and porogen) coexist in the low-*k* dielectrics, a weaker bonding strength can be deduced, resulting in a weaker resistance against O<sub>2</sub> plasma damage. By means of UV irradiation or the post-deposition plasma treatment, using remote H<sub>2</sub>/He plasma to form a surface densification layer can alleviate the reliability degradation for the porous low-*k* dielectrics under O<sub>2</sub> plasma treatment.

#### 4.2.1.3. Plasma component dependence

A "roof" structure, consisting of a top optical mask, is designed to isolate the ions, photons, and radicals to reach the porous low-*k* dielectrics [54]. Si, MgF<sub>2</sub>, or CaF<sub>2</sub> was used as a mask in this study. The height of the gap was fixed at 1 cm. Under O<sub>2</sub> plasma treatment with different masks, the plasma species penetrating into the porous low-*k* dielectric through the gap is summarized in **Table 2**. For the porous low-*k* dielectric under plasma using various masks,



**Figure 11.** Degradation in characteristic dielectric breakdown time of different low-*k* dielectrics as a function of RF power in O<sub>2</sub> plasma treatment.

the thickness reduction, the Si–CH<sub>3</sub> group extraction, the Si–OH/H–OH bond formation, the top modification layer formation, the WCA value declination, the dielectric constant increment, and the dielectric breakdown field degradation were detected. The results indicate that all ions, photons, and radicals in the plasma cause negative impact on the porous low-*k* dielectrics, but they have different contributions. The maximum change is in the case of without mask. In Si mask case, the photons and the ions are blocked so that only oxygen radicals can react with the porous low-*k* dielectrics. Its plasma damage is less minor. In MgF<sub>2</sub> mask or CaF<sub>2</sub> mask, photons can penetrate but depends on the wavelength. With an addition of photon effect, the changes in the physical and electrical properties for the porous low-*k* dielectrics slightly increase. Furthermore, as ions are added in the plasma to react with the porous low-*k* dielectrics (without mask case), the changes become significant. This implies that the synergy between the radicals, the photons, and the ions in the plasma induces the highest degradation in the porous low-*k* dielectrics.

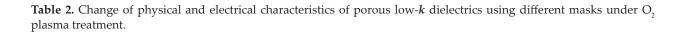
The plasma-induced damage mechanism is that the bonds inside the porous low-*k* dielectrics are broken by ion bombardment and then easily react with radicals to form the new bonds or Si–OH/H–OH bonds with a higher *k* value (~80). As for the contribution of photons, photons can weaken or broken the low-*k* dielectric's bonds, assisting the chemical reaction of radicals. The photons with a higher energy cause more bonding breakage, inducing a more degradation. Therefore, a higher degradation in the porous low-*k* dielectric underneath MgF<sub>2</sub> mask during O<sub>2</sub> plasma treatment was detected due to extra photon transmission with 120–250 nm wavelength.

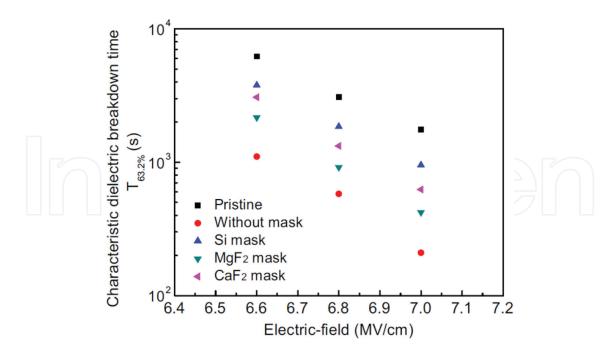
**Figure 12** plots the characteristic dielectric breakdown times ( $T_{63.2\%}$ ) versus the applied electric field for O<sub>2</sub> plasma-treated low-*k* dielectrics with various masks. In a fixed electric field, the order of  $T_{63.2\%}$  is pristine > Si mask > MgF<sub>2</sub> mask > CaF<sub>2</sub> mask > without mask, indicating that all ions, photons, and radicals in the plasma cause the dielectric reliability degradation. For example, in an electric field of 6.8 MV/cm, the dielectric lifetime degradation ratios are 43.17, 66.41, and 82.18% for Si mask, MgF<sub>2</sub> mask, and without mask cases, respectively, corresponding to radical, radicals + photon, and ions + photons + radical effects. By simple calculation, the contributions of radicals, photons, and ions were 43.17, 23.24, and 15.77%, respectively,

Optical mask	Plasma components on low- <i>k</i> film	Thickness reduction (%)	Top- modified layer (nm)	Etching rate of top-modified layer (nm/min)	Water contact angle (°C)	Methyl depletion $(\Delta Si - CH_3/Si - O)$ (%)	Moisture uptake (ΔSi – OH/Si – O) (%)	Dielectric constant increase $(\Delta k)$	Leakage current density at 2 MV/cm (A/cm <sup>2</sup> )	Breakdown field (MV/cm)
Without	lons, radicals, photons (VUV, UV, Vis, IR)	8.36	58.63	80.295	27.03	2.91	8.7	3.52	1.25 × 10 <sup>-5</sup>	8.42
MgF <sub>2</sub>	Radicals, photons (VUV <sup>a</sup> , UV, Vis, IR)	7.31	40.01	54.3	35.22	2.42	6.4	1.5	$5.37 \times 10^{-7}$	8.98
CaF <sub>2</sub>	Radicals, photons (VUV <sup>b</sup> , UV, Vis, IR)	6.92	38.27	51.82	36.4	2.28	6.1	1.23	$3.46 \times 10^{-7}$	9.03
Si	Radicals, photons (IR)	6.12	33.35	45.68	42.26	1.85	5.3	0.73	$1.76 \times 10^{-7}$	9.23
Remark:	Pristine low-k:	-	-	2.86	88.96	-	-	2.56 <sup>c</sup>	$9.39 \times 10^{-9}$	9.82

<sup>b</sup> 250–350 nm.

<sup>c</sup> The dielectric constant of the studied low-k film.



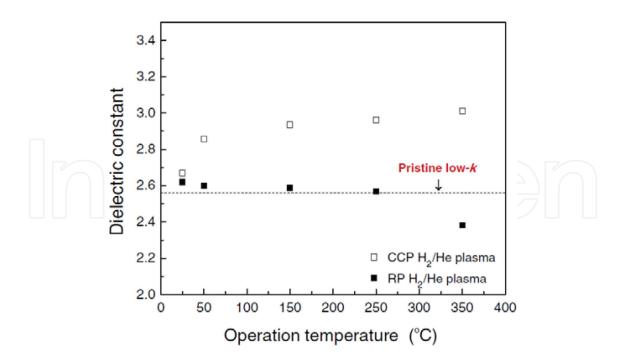


**Figure 12.** Characteristic dielectric breakdown time versus electric field for porous low-*k* dielectrics under  $O_2$  plasma treatment with various masks.

indicating that radicals cause the greater degradation in the dielectric reliability than the other two plasma components. However, this finding is still needed to be demonstrated by more experiments which can be treated under the individual plasma component. The synergy between radicals, photons, and ions causes a considerable degradation. Getting rid of one or two components from the plasma environment is a workable strategy for the low-*k* dielectric's reliability improvement.

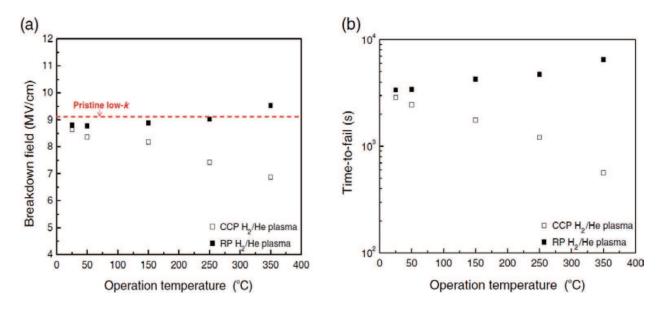
#### 4.2.2. Hydrogen $(H_2)$ /helium (He) plasma damage

H<sub>2</sub>/He plasma treatments on the porous low-k dielectrics (k = 2.56) using CCP and RP systems at various operation temperatures (25–350°C) were investigated. The k value of porous low-k films after H<sub>2</sub>/He plasma treatment in CCP and RP systems as a function of the operation temperature is presented in Figure 13. An increased k was detected after H<sub>2</sub>/He plasma treatment in CCP system, and the increasing magnitude increased with increasing the operation temperature. However, as the operation temperature is raised above 250°C, the increasing rate of the k value tends to alleviate. This phenomenon can be explained by transforming Si–OH bonds to Si–O–Si bonds at an elevated temperature above 200°C. For porous low-k dielectrics treated by H<sub>2</sub>/He plasma in RP system, the increase in the k value was lower owing to a relatively small Si–CH, depletion and Si–OH formation. Additionally, the k value was slightly reduced with an increase of the operation temperature. Furthermore, as the operation temperature is elevated to  $350^{\circ}$ C, the *k* value was reduced to be lower than 2.56. The result suggests that the effect of H<sub>2</sub>/He plasma treatment in RP system on porous low-k dielectrics turns to be positive by raising the operation temperature to 350°C. As a consequence, a "damage-free" resist strip processing can be obtained by using H<sub>2</sub>/He plasma treatment in RP chamber at elevated temperatures.



**Figure 13.** Dielectric constant of  $H_2/He$  plasma-treated low-*k* dielectrics operated in CCP and RP systems as a function of operation temperature.

Figure 14(a) and (b) shows the breakdown field and the dielectric breakdown time, respectively, of porous low-k dielectrics under H<sub>2</sub>/He plasma treatment in the CCP or RP systems at various operation temperatures. Both results indicate that H<sub>2</sub>/He plasma-treated low-k dielectrics in RP system exhibited a higher breakdown field and a longer breakdown time as compared to those in CCP system, indicating that deep UV light radiation and ion bombardment induced from H<sub>2</sub>/He plasma treatment in CCP system on the low-k dielectric can accelerate the degradation of reliability. The trends of temperature dependence of reliability characteristics were different for H<sub>2</sub>/He plasma treatments in the CCP and RP systems. The breakdown field and the breakdown time of  $H_2$ /He plasma-treated low-k dielectrics in CCP system were decreased, while those in CCP system were improved as the operation temperature is raised. Furthermore, H<sub>2</sub>/He plasma-treated low-k dielectrics operated in CCP system displayed a strong temperature dependence of reliability, implying that the reaction induced by radicals is not enhanced by increasing the temperature. However, with the assistance of deep UV light radiation and ion bombardment, the reaction becomes stronger at a higher operation temperature. As the operation temperature of H<sub>2</sub>/He plasma treatment in RP system was raised to 350°C, the reliability performance of the plasma-treated low-k dielectrics exceeded that of the pristine samples. A better reliability for H<sub>2</sub>/He plasma-treated low-k films operated in RP system at 350°C can be attributable to another mechanism because the scission of Si-CH<sub>3</sub> bonds was still detected although the decreasing ratio was reduced. H<sub>2</sub>/He plasma treatment on porous low-k dielectrics in RP system at evaluated temperatures reportedly removes carbon-based porogen residues, which are formed inside the porous lowk structure due to non-optimized incorporation of porogen molecules and non-optimized UV curing [55]. The removal of porogen residues from porous low-k dielectrics has also been demonstrated to promote reliability for low-k dielectrics. Therefore, H<sub>2</sub>/He plasma treatment



**Figure 14.** (a) Breakdown field. (b) Dielectric breakdown time of  $H_2$ /He plasma-treated low-*k* dielectrics operated in CCP and RP systems as a function of operation temperature.

on porous low-*k* dielectrics in RP system at 350°C efficiently removes porogen residues from porous low-*k* dielectrics, resulting in a better reliability.

The mechanism about the reaction between  $H_2$  reactive plasma species and porous low-*k* dielectrics can be described as [56]:

$$\equiv \text{Si-CH}_3 + 2\text{H} \rightarrow \equiv \text{Si-H} + \text{CH}_4 \quad \Delta \text{H}_r = -411 \text{ KJ/mole}$$
(2)

$$\equiv \text{Si}-\text{O}-\text{Si}\equiv +2\text{H} \rightarrow \equiv \text{Si}-\text{H} + \equiv \text{Si}-\text{OH} \quad \Delta\text{H}_{r} = -325 \text{ KJ/mole}$$
(3)

where  $\Delta H_{\rm a}$  is the estimated enthalpy. The reaction temperature of these two reactions is assumed to be 25°C. The negative enthalpies of reactions (2) and (3) represent that the reactions are exothermic and presumably occurred at room temperature [57]. Assuming that the amount of H atoms remains unchanged at an elevated temperature, these two reactions would become less favored with an increase of the reaction temperature according to Chatelier's principle. Therefore, the scission of Si-CH<sub>3</sub> and Si-O-Si groups should be stronger at a lower temperature. However, FT-IR analysis revealed that the losses of CH<sub>3</sub> and Si–O–Si groups were higher for porous low-k dielectrics treated by H<sub>2</sub>/He plasma in CCP system at a higher temperature. The discrepancy can be explained by the fact that only H radical is considered to react with the low-k dielectric for reactions (2) and (3). However, H ions and VUV photons can be produced in H<sub>2</sub>/He plasma operated in CCP system. Due to the presence of H ions and VUV photons, the above two reactions become possible because the bonding energies of Si-CH<sub>2</sub> and Si-O-Si bonds are weaken. Furthermore, at an elevated temperature, ions and photons can gain more energy and easily break Si-CH<sub>3</sub> and Si-O-Si bonds, causing a violent response for reactions (2) and (3). In the case of H<sub>2</sub>/He plasma treatment in RP system, only H radicals can react with the porous low-k dielectric. According to FT-IR result, only Si–CH<sub>3</sub> group was found to reduce, and the concentration of Si–O–Si bond almost kept unchanged for  $H_2/He$  plasma-treated low-*k* dielectrics, implying that reaction (2) is favored over reaction (3) because of a lower dissociation energy of Si–CH<sub>3</sub> bond. Additionally, the reduction amount of Si–CH<sub>3</sub> bond is relatively small and no temperature dependence effect, indicating that reaction (2) is relatively weak even at a higher temperature for  $H_2/He$  plasma treatment in RP system.

In addition to the above reactions (2) and (3),  $H_2$  plasma can break Si–CH<sub>3</sub> and Si–O–Si bonds to create Si dangling bonds. The subsequent air exposure makes these Si dangling bonds transform to Si–OH bonds. If OH- bonds are weak or physically bonded, dehydroxylation of Si–OH bonds can occur to form Si–O–Si bonds at a higher temperature [58]. This can be explained by the reduction of Si–OH bonds for  $H_2$ /He plasma-treated low-*k* films operated at temperatures above 250°C.

#### 4.2.3. Ammonia (NH<sub>3</sub>)/nitrogen (N<sub>2</sub>) plasma damage

The effect of the NH<sub>3</sub>/N<sub>2</sub> ratio in plasma treatment on the porous low-k dielectrics (k = 2.56) was investigated. The reaction mechanism between the porous low-k dielectric and  $NH_3/N_2$ plasma can be described as follows: in pure N<sub>2</sub> gas plasma, only N, N<sub>2</sub>, and N<sub>2</sub>\* active species are generated, and no hydrogen species is produced. Physical bombardment by N radicals is favorable, roughing the film's surface. Moreover, the weak bonds in the low-k dielectric, such as Si-H, Si-CH<sub>3</sub>, and C-H<sub>x</sub> bonds, can be broken by these active species in the plasma, forming Si–N and C–N bonds. As NH<sub>3</sub> gas was added into the plasma, other active species in addition to the N,  $N_{2'}$  and  $N_{2}^{*}$  active species, such as H,  $NH_{2'}$ ,  $NH_{4'}$ , and  $N_{2}H$ , may be generated. The Si–CH<sub>3</sub> group in the low-k dielectric is broken to form Si dangling bonds. This dangling bond easily absorbs H or NH, species to form Si–H or Si–NH, bonds due to a lower reaction energy, which is thermodynamically favorable [59–62]. The Si–H and Si–NH, bonds are not stable in air and easily react with ambient air to form Si–OH, which is more hydrophobic and has a higher k value. As the portion of  $NH_3$  in the plasma increases, the number of H and NH, active species increases accordingly. At the same time, the amount of the generated N,  $N_{\gamma}$ , and  $N_{\gamma}^{*}$  active species is limited because more energy is required to generate these active species. These changes in the plasma result in the significant replacement of -CH<sub>3</sub> groups by H and NH<sub>2</sub> active species, the formation of more Si-OH bonds, and the reduction of Si-N and C-N bonds.

**Figure 15** shows the changes in the *k* value of  $NH_3/N_2$  plasma-treated low-*k* dielectrics upon  $O_2$  plasma treatment. After  $NH_3/N_2$  plasma treatment, the *k* value of the plasma-treated low-*k* dielectrics increases. Under pure  $NH_3$  or pure  $N_2$  gas plasma treatment conditions, the increase is larger. This can be attributed to more formation of Si–OH bonds or Si–N/C–N bonds on the surface layer for pure  $NH_3$  or pure  $N_2$  gas plasma treatment, respectively. Treatment with  $O_2$  plasma increases the *k* values of all  $NH_3/N_2$  plasma-treated low-*k* dielectrics by the replacement of Si–CH<sub>3</sub> and Si–H bonds with Si–O bonds [63]. The increase in the *k* value becomes larger with the  $NH_3/N_2$  gas ratio. The pure  $N_2$  gas plasma-treated sample exhibits a smaller increase in the *k* value owing to the formation of protective Si–N/C–N layer. This layer suppresses the penetration of oxygen radical into the low-*k* dielectric.

**Figure 16(a)** plots the measured dielectric breakdown fields of  $NH_3/N_2$  plasma-treated low*k* dielectrics. All  $NH_3/N_2$  plasma-treated samples had a poorer dielectric breakdown performance than the pristine low-*k* dielectric. Moreover, the breakdown field of the  $NH_3/N_2$ plasma-treated low-*k* dielectrics decreases as the  $NH_3/(N_2 + NH_3)$  ratio increases. The low-*k* dielectric that was plasma treated with pure  $NH_3$  gas has the lowest breakdown field and the highest leakage current.

**Figure 16(b)** compares  $T_{63.2\%}$  values as a function of applied electric field for the pristine and plasma-treated low-*k* dielectrics. All plasma-treated samples had shorter dielectric break-down times and a wider distribution as compared to the pristine low-*k* dielectric. The reduction of the dielectric breakdown time may be caused by an accumulation of defects owing to

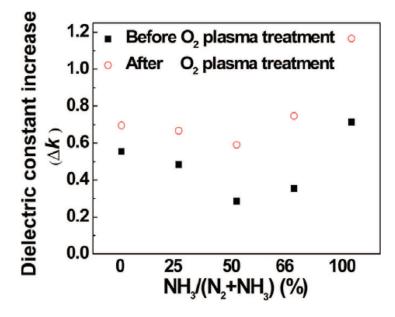
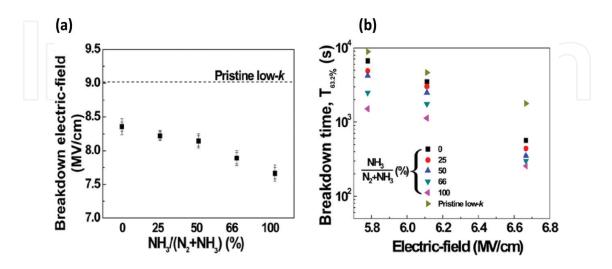


Figure 15. Change in dielectric constant of porous low-k dielectrics after NH<sub>3</sub>/N<sub>2</sub> and O<sub>2</sub> plasma treatments.



**Figure 16.** (a) Breakdown field. (b) Dielectric breakdown time as a function of electric field of porous low-*k* dielectrics after various NH<sub>3</sub>/N, plasma treatments.

plasma-induced damage. Furthermore, the reductions in the dielectric breakdown time were significant in stronger stressing electric fields. Additionally, the  $T_{63.2\%}$  values of plasma-treated low-*k* dielectrics decreased as the NH<sub>3</sub>/(N<sub>2</sub> + NH<sub>3</sub>) ratio increased, which is correlated well with the moisture contents in the plasma-treated dielectrics. This indicates that the moisture content in a low-*k* dielectric plays an important role in reducing the dielectric breakdown time. The low-*k* dielectric that was plasma treated with pure N<sub>2</sub> gas had the longest low-*k* dielectric because the formed amide-like or nitride-like layers on the surface retard low-*k* dielectric breakdown.

### 5. Conclusions

Low-*k* dielectric materials are essential for *RC* delay reduction to improve the performance of ICs. However, plasma-induced damage on the low-*k* dielectric materials during Cu/low-*k* interconnects fabrication is a critical issue to influence the low-*k* integrity. Plasma damage on the low-*k* dielectrics is a complicated phenomenon involving physical and chemical reactions. The resulting main negative impact is an increased dielectric constant due to water adsorption after plasma irradiation on the low-*k* dielectrics. The plasma damage depends on the used low-*k* dielectrics, plasma gases and conditions, and reactors. Therefore, the optimization in the plasma process is required to minimize the plasma damage. Additionally, the damaged low-*k* dielectrics can be recovered by the removal of adsorbed water. This turns the hydrophilic materials to be hydrophobic. Currently, complete recovery cannot be obtained; consequently, this area requires a lot of effort to make in the future.

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