

**UNIVERSITY OF OSLO**  
**Department of Physics**

**Development of a  
full-size low price  
Automatic Survey  
Vessel (ASV) for  
hydroacoustic  
work**

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# Abstract

This thesis describes a pilot project for the development of an Automatic Survey Vessel (ASV) platform useful for hydroacoustic work. An inexpensive control unit, suitable for installation in an arbitrarily selected full-sized boat, has been developed. The control unit includes a motor control system, communication equipment, and a simple autopilot. This enables an operator to either remotely control the vessel during a survey or pre-program a route for the vessel to follow. Pre-programmed routes can be utilized to increase the efficiency of hydroacoustic surveys or to create complex driving patterns. The autopilot incorporates a GNSS-receiver and a self-developed tilt-compensated compass. The compass calculates the heading using sensor fusion from a 3-axis accelerometer, a 3-axis gyroscope and a 3-axis magnetometer. The motor control system, the compass and the autopilot have all been tested in either controlled- or field environments.



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# Preface

My master thesis is naturally divided into separate parts, and is therefore written as multiple papers. The introduction covers the overview of the whole project.

I assume that the reader is familiar with basic concepts regarding electrical engineering, computer science and hydroacoustics.





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# List of Acronyms

<b>AC</b> alternating current .....	4
<b>AHRS</b> Attitude and Heading Reference System	
<b>AP</b> Access Point .....	73
<b>ASCII</b> American Standard Code for Information Interchange	
<b>ASV</b> Automatic Survey Vessel .....	87
<b>AWeX</b> Advanced Waveform eXtension .....	19
<b>BJT</b> bipolar junction transistor .....	8
<b>BLDC</b> brushless DC electric .....	4
<b>BOOTP</b> Bootstrap Protocol	
<b>CEP</b> Circular Error Probable .....	69
<b>CGI</b> computer-generated imagery	
<b>CNC</b> computer numerical control	
<b>COTS</b> commercial off-the-shelf .....	3
<b>CPU</b> central processing unit	
<b>CRC</b> Cyclic Redundancy Check .....	40
<b>CSS</b> Cascading Style Sheets	
<b>DAC</b> digital-to-analog converter .....	12
<b>DC</b> direct current .....	4
<b>DFU</b> Device Firmware Upgrade .....	46
<b>DGPS</b> Differential GPS .....	66
<b>DHCP</b> Dynamic Host Configuration Protocol .....	73
<b>DMP</b> Digital Motion Processor .....	43
<b>EEPROM</b> Electrically Erasable Programmable Read-Only Memory	
<b>EGNOS</b> European Geostationary Navigation Overlay Service .....	66
<b>EMF</b> electromotive force .....	5
<b>EMI</b> electromagnetic interference .....	5
<b>ESD</b> electrostatic discharge .....	49

<b>FPU</b> floating-point unit.....	84
<b>FTDI</b> Future Technology Devices International .....	49
<b>GCC</b> GNU Compiler Collection.....	44
<b>GDB</b> GNU Debugger .....	69
<b>GNSS</b> Global Navigation Satellite System .....	66
<b>GNU</b> GNU's Not Unix!	
<b>GPL</b> General Public License .....	103
<b>GPS</b> Global Positioning System.....	69
<b>GPT</b> General Purpose Transceiver.....	33
<b>HDOP</b> horizontal dilution of precision.....	79
<b>HP</b> Hewlett-Packard.....	33
<b>HTML</b> HyperText Markup Language.....	84
<b>HTTP</b> Hypertext Transfer Protocol .....	84
<b>I<sup>2</sup>C</b> Inter-Integrated Circuit.....	72
<b>IC</b> integrated circuit .....	43
<b>IETF</b> Internet Engineering Task Force.....	66
<b>IGBT</b> insulated-gate bipolar transistor .....	15
<b>IMU</b> Inertial Measurement Unit	
<b>IO</b> input/output	
<b>IP</b> Internet Protocol .....	72
<b>IR</b> infrared .....	59
<b>ISR</b> interrupt service routine	
<b>JTAG</b> Joint Test Action Group	
<b>LAN</b> local area network	
<b>LED</b> light-emitting diode	
<b>LFPAK</b> Loss-Free Package .....	23
<b>MAC</b> Media Access Control .....	73
<b>MCU</b> microcontroller unit.....	69
<b>MEMS</b> microelectromechanical systems.....	42
<b>MOSFET</b> metal-oxide-semiconductor field-effect transistor.....	8
<b>MPL</b> Motion Processing Library.....	46
<b>MSB</b> most significant bit .....	20
<b>NLLS</b> Non-linear least squares.....	56
<b>NMEA</b> National Marine Electronics Association	
<b>NTP</b> Network Time Protocol.....	56

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<b>OSI</b> Open Systems Interconnection	
<b>OSMC</b> Open Source Motor Control.....	15
<b>OS</b> operating system.....	43
<b>OpenOCD</b> Open On-Chip Debugger	
<b>PCB</b> printed circuit board.....	89
<b>PCM</b> pulse-code modulation.....	7
<b>PHY</b> physical	
<b>POE</b> Power-over-Ethernet.....	52
<b>PPM</b> pulse-position modulation.....	7
<b>PPP</b> Precise Point Positioning.....	67
<b>PWM</b> pulse-width modulation	
<b>QFN</b> Quad Flat No-leads.....	47
<b>RADAR</b> RADio Detection And Ranging	
<b>RC</b> radio control.....	72
<b>RF</b> radio frequency	
<b>RMS</b> root mean square	
<b>ROV</b> remote operated vehicle	
<b>RPM</b> revolutions per minute.....	73
<b>RTC</b> real-time clock.....	44
<b>RTOS</b> real-time operating system.....	72
<b>SBAS</b> satellite-based augmentation system.....	66
<b>SBC</b> single board computer.....	44
<b>SD</b> Secure Digital.....	75
<b>TCP</b> Transmission Control Protocol	
<b>TFTP</b> Trivial File Transfer Protocol	
<b>TI</b> Texas Instruments.....	69
<b>TVS</b> transient-voltage-suppression.....	26
<b>TWI</b> Two Wire Interface.....	132
<b>UART</b> universal asynchronous receiver/transmitter	
<b>UDP</b> User Datagram Protocol.....	73
<b>USB</b> Universal Serial Bus.....	75
<b>US</b> United States of America.....	93
<b>UUT</b> unit under test	
<b>WAAS</b> Wide Area Augmentation System.....	66
<b>WLAN</b> wireless LAN.....	81



# Introduction

There is a growing need to study fish and water quality in lakes. This is reflected in political programs such as the EU Water Framework Directive [1]. Norway is through this directive obliged to examine the condition of all lakes over a certain size. This will not be economically feasible with the vast number of lakes matching this criteria and the available survey methods. These are largely based on the use of boats equipped with echosounders and other measuring equipment.

Hydroacoustic surveying on lakes is different from oceanic surveying because of the size of the lake, accessibility, ecology and economy. One must primarily use small boats and do the recordings at night. A small boat requires the driver to sit quietly to avoid reducing the quality of the recordings. Night work is also impractical, demanding, expensive and complicated to conduct in the dark because one must follow certain driving patterns for proper collection statistics. The echosounder will only see a limited portion of the water, since the sound beam is formed like the cone from a flashlight. At shallow lakes one can increase the coverage by doing longer surveys or use more boats. Another option is to direct the sound beam horizontally. Horizontal driving is however subjected by a number of problems related to reflection, refraction, determination of the orientation of the fish in the water and converting echo strength to fish size.

Many of these issues could be solved with a larger number of affordable automatic and self-steering survey vessels. Several boats can be launched at night and retrieved in the morning. We get greater coverage per task force and researchers will be able to analyze data by day instead of driving around the lakes at night. Much research remains on horizontal use of echosounders before this method can provide usable results. An automatic boat will make a significant contribution to this research in terms of providing a stable quiet platform with possible pre-programmed or dynamic driving patterns that is difficult to emulate with manned boats.

The main task in this project is the development of such a vessel. There already exists a few such boats, but they are either developed for oceanic use, with a size and price [2] which makes them totally unsuitable for lake research, or they are small model boats [3] unable to carry the necessary research equipment and energy to conduct a longer survey.

We therefore want to develop an Automatic Survey Vessel (ASV) platform. We will in practice accomplish this by developing a control unit. It should be possible to install this control unit in an arbitrarily selected boat, powered by two arbitrarily selected electric outboard motors. This will enable research institutions to choose boats and motors they already have available, and which are adapted to the actual lake.

The control unit in this project will include a motor control system, communication equipment and a simple autopilot. The autopilot uses a Global Navigation Satellite System (GNSS)-receiver and a self-developed compass for navigation.

## Thesis outline

The development of the motor control system with simple communication is covered by the paper «Construction of a remote controlled work platform for hydroacoustic work» on page 3. The development of the compass is covered by the paper «Construction of an Attitude and Heading Reference System (AHRS)» on page 41. The development of the simple autopilot is covered by the paper «Construction of a primitive autopilot for hydroacoustic work» on page 65.

# Paper 1

## Construction of a remote controlled work platform for hydroacoustic work

### Abstract

This paper presents the construction and implementation of the steering mechanism and motor control of a remote controlled work platform for hydroacoustic work. Multiple high current brushed DC-motor controllers, based on H-bridge circuits, have been developed. After a disappointing attempt with a commercially available H-bridge driver, a custom H-bridge driver has shown good results. The final system has, at multiple occasions, proved it can remote-control full-sized boats. The driver can also be extended for further automation.

**Keywords:** H-bridge, ROV, motor control

### 1.1 Introduction

Mobile hydroacoustic surveys in small boats requires the crew to sit quietly to avoid reducing the quality of the recordings. By using a remote controlled vessel, an operator can move freely on shore while conducting the survey at a distance.

Further development can extend the remote control functionality, and let the vessel follow pre-programmed patterns, making it even more useful for researchers. Finally a fully autonomous system can be built, based on the foundation developed in this project.

This paper will present the development of a motor control system with remote-control functionality. One of the cornerstones of the project was to build our system using only

commercial off-the-shelf (COTS) parts. This later enables us to build a fleet of survey vessels, and makes it inexpensive for other institutions to adopt. The system will also build upon boats and motors the user already may have available.

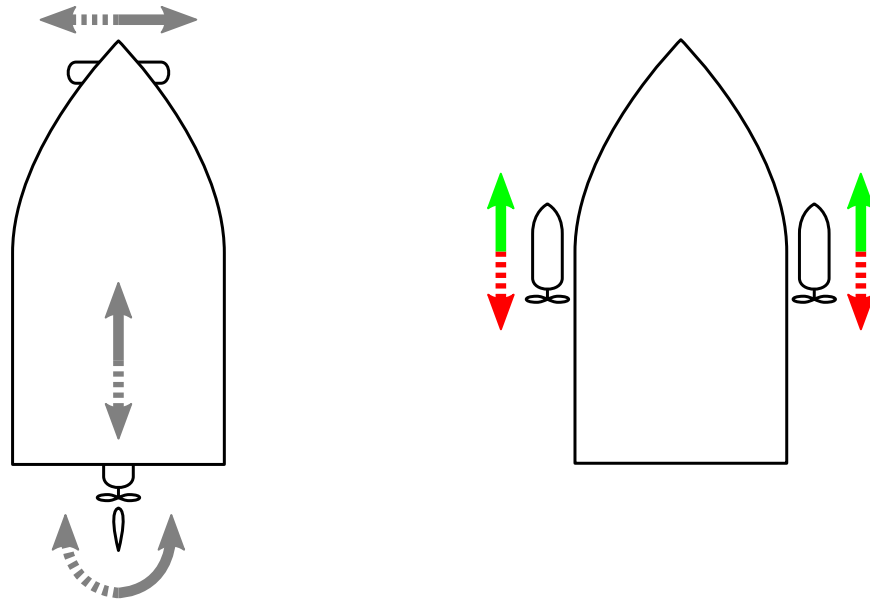
Similar remote-controlled systems have already been created, but they are too small [4] to carry the necessary research equipment or too expensive [2].

#### 1.1.1 Motor placement

There are multiple possible setups that can be used to steer a boat, and it was especially two different setups we were considering.

The first alternative is to control the throttle of a rear mounted outboard motor, together with a steering mechanism. The steering mechanism could be the turning of a rudder, turning of the whole motor or the throttle of a thruster<sup>1</sup>

<sup>1</sup>A thruster is a propulsion device mounted across the bow or stern of a boat to increase its maneuverability



(a) Motor placement using a rear mounted engine, (b) Motor placement using two engines, one mounted together with a thruster or a rudder. on each side.

Figure 1.1: Motor placement options.

mounted across the boat at the bow. This configuration is shown in figure 1.1a.

The second alternative is to control the throttle of two outboard motors. With one mounted at the port side and one mounted at the starboard side, as shown in figure 1.1b.

The first alternative would either have required the use of a servo motor to turn the rudder or the engine, or an expensive thruster. The second alternative, on the other hand, can be accomplished without any moving parts (besides the movement of the propeller). The second motor placement alternative will also make it possible to «turn 360° on-the-spot».

### 1.1.2 Motor type

The main motors needs to be powerful enough to drive the boat, while being easy to control. We were considering the use of either a fuel/petrol engine or an electric engine.

Electric engines are often less complicated to control compared to a fuel/petrol engine. This is because it is possible to control the thrust by

simply controlling the electric current running through them. A brushed direct current (DC)-motor can also with ease be used to drive a boat in reverse. Switching the polarity of the current will result in the movement direction of the propeller to be switched as well. A fuel/petrol engine requires mechanics to achieve this ability. On the other hand, an electric engine usually provides less thrust than a fuel/petrol engine. Since the vessel will only move at slow speed, and in low (water-)current environments, we still believe an electric motor is sufficient for our requirements.

Other possible electric motor types besides brushed DC-motors, are alternating current (AC) and brushless DC electric (BLDC) motors. AC motors were not considered because of the lack of an AC source in the vessel. BLDC motors were considered as they have multiple good features.

BLDC motors do not have any mechanical connection between the stator and the rotor. They rely on multiple stator coils



being alternatively magnetized at the correct rotor angle to drive the shaft. These motors require less maintenance because of the lack of brushes, gives high torque regardless of speed, and overall higher efficiency. Brushed DC motors have arcing by their brushes which generates electromagnetic interference (EMI), while BLDC do not. Unfortunately they require an advanced motor controller, as the timing of when the coils should be driven must be exact and depends on the rotor angle. The angle of the rotor can be found using hall effect sensors, or sensing the back-electromotive force (EMF) from the undriven coils.

Despite some of the good characteristics with a BLDC motor, we chose to use brushed DC motors. The reason being high availability of inexpensive brushed DC outboard engines and the requirement of a much simpler motor controller. Fuel/petrol engines were taken out of consideration, because they are more complex to control, and we did not need the high thrust they can deliver.

We also selected the second motor placement alternative, with one engine mounted on each side of the boat. Together with electric engines, this combination requires no movable parts (except for the propeller).

We found two inexpensive 12 V electric outboard engines with a pulling force of 15.4 kg, shown in figure 1.2a. The engines had a purchase price of about 1500 NOK each, and are available from Biltema (product nr. 25235). Any 12 V brushed DC engine the user already may have available should be suitable (as long as the current draw is limited). We were also considering Endura C2 from Minnkota, but chose the one from Biltema because it was easier for us to source. Our engines are rated to draw up to 32 A of electric current, but the motor control system is designed to handle 40 A.

If two different engines are used, their difference in thrust can be compensated in software. This is, however, currently not implemented.

The inside of the Biltema outboard engine is shown in figure 1.2b. The engine has a control mechanism in the handle where multiple speeds can be selected. The handle can also set the engine in reverse. We considered bypassing

this control mechanism, connecting straight to the motor. After reverse-engineering the connections inside the control system, however, we found that we have a direct connection to the motor when the maximum speed is selected. Our motor control system can therefore connect to the battery terminals of the engine, without modifying or breaking the warranty of the device.

To control the direction and thrust of the engines, we were in need of a high current brushed DC-motor controller.

### 1.1.3 H-bridge

With the requirement of running the electric engine both forward and backward, we chose to use an H-bridge. This will also make it possible to adjust the thrust of the engine. An H-bridge solves these challenges by directing the current to run through the motor in the wanted direction. We can control (pulse-width modulate) the amount of power the motor receives by turning this current rapidly «on» and «off».

The principal operation of an H-bridge is covered by section «H-bridge operation» on page 8. The implemented operation of our H-bridge is covered by section «Second H-bridge drive modes» on page 20.

We considered buying a commercial H-bridge motor control system, but chose to develop our own. The reason being lower cost and full control of the whole system. Our motor control system consists of an H-bridge controller, an H-bridge drive-stage and an H-bridge. In order to form a complete remote controlled motor control system, a radio control (RC)-receiver and a steering unit is included as well. Two attempts have been conducted; the first described in section «First attempt on H-bridge construction» on page 12 and the second in section «Second attempt on H-bridge construction» on page 19.

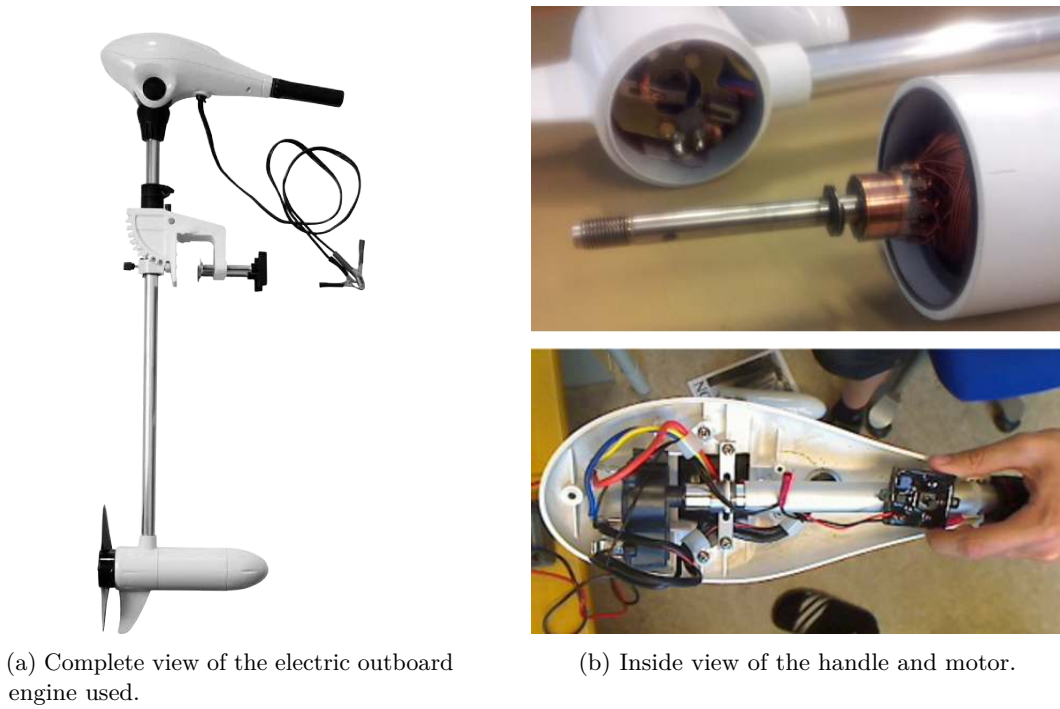


Figure 1.2: Electric outboard engine from Biltema.



Figure 1.3: Photographs of RC-communication equipment.

### 1.1.4 Communication

We need a way for the operator to send wireless commands to the boat. For the remote control operation described in this paper, the commands will be simple control signals to propel and steer the vessel. We considered different radio communication systems, such as IEEE 802.11 wireless LAN (WLAN) or hobby-radio control (RC) with pulse-position modulation (PPM) or pulse-code modulation (PCM) encoding.

It became apparent that using a standard RC-servo control signal, commonly used by hobby enthusiasts, would be a good choice. This system is widely used, simple to implement and would fulfill our requirements of long range, low price and sufficient bandwidth for sending simple control signals. It does not include any form for encryption or authentication, but security regarding control over the vessel is currently not vital as the vessel will be strictly operated under human supervision.

Further development, such as the implementation of an autopilot (paper 3), may use another radio communication system with encryption and higher bandwidth. The RC-control signal could still be used as a redundant communication channel, to override any automatic steering system and regain manual control over the vessel.

We chose a 9-channel 2.4 Ghz RC-transmitter named Turnigy 9X, with a matching 8-channel receiver<sup>2</sup>.

This RC-transmitter was chosen because it *a*) is inexpensive; *b*) is reliable (less interference, compared to a 27 MHz system); *c*) employs frequency hopping; *d*) has good range; *e*) is easily available (under multiple brand names); and *f*) has alternative firmware. The transmitter contains an Atmel ATmega64 8-bit microcontroller unit (MCU), two sticks, three potentiometers, seven switches and a 128x64 dot display. Several developers have created custom and free<sup>3</sup> firmwares for this unit, e.g. er9x [5] and th9x [6]. Since the stock firmware already loaded into the transmitter fulfilled our needs,

we did not use any of these custom firmwares. One of the alternative firmwares may, however, be customized for the project in the future. For instance to show information about how to use and/or setup the vessel on the display, or manage the switches and potentiometers to give additional control possibilities. The RC-transmitter is shown in figure 1.3a.

Each of the 9-channels is connected to an output on the receiver, and may have different purposes depending on the setup of the transmitter. The setup of our transmitter sends the position of the left stick to the first channel and the position of the right stick to the second. None of the other channels were used. The RC-receiver is shown in figure 1.3b.

The output of the RC-receiver is a standard servo control signal with a constant frequency of 50 Hz, but varying pulse-width depending on the position of the stick on the transmitter. A pulse-width of 1.5 ms corresponds to a stick in its center position, while 1 ms and 2 ms corresponds to minimum and maximum stick position respectively. These pulse-widths are shown in figure 1.4.

The user may use any RC-system he or she has available, as long as it is outputting these de facto standard servo signals.

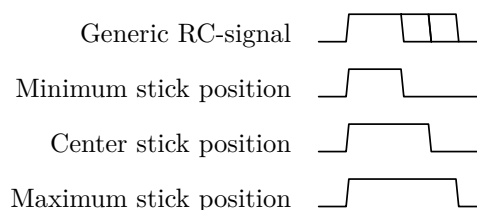
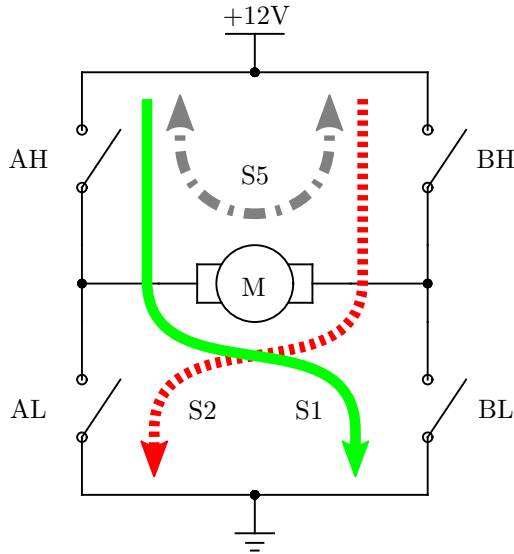


Figure 1.4: Illustration of RC-servo signals. Different pulse-widths corresponds to different stick positions.

<sup>2</sup>Also sold under other brand names, such as Eurgle, FlySky and Imax. Available from HobbyKing at about 50 EUR, [http://www.hobbyking.com/hobbyking/store/\\_\\_8991\\_\\_Turnigy\\_9X\\_9Ch\\_Transmitter\\_w\\_Module\\_8ch\\_Receiver\\_Mode\\_1\\_v2\\_Firmware\\_.html](http://www.hobbyking.com/hobbyking/store/__8991__Turnigy_9X_9Ch_Transmitter_w_Module_8ch_Receiver_Mode_1_v2_Firmware_.html)

<sup>3</sup>as in free speech



(a) Drawing of H-bridge with currents.

(b) H-bridge operating states.

#	H-bridge state	AH	AL	BH	BL
S1	Motor runs cw	on	off	off	on
S2	Motor runs ccw	off	on	on	off
S3	Motor runs free	off	off	off	off
S4	Motor brakes	off	on	off	on
S5	Motor brakes	on	off	on	off
S6	Shoot-through	on	on	off	off
S7	Shoot-through	off	off	on	on
S8	Shoot-through	on	on	on	on

Figure 1.5: Illustration and table for H-bridge principal operation.

## 1.2 Theory

### 1.2.1 H-bridge operation

An H-bridge is a circuit topology where four switches and a load are connected in the shape of an «H», as shown in figure 1.5a. These switches can either be mechanical or electronic, and can turn each leg of the H-bridge «on» or «off».

An H-bridge is operated in different states, by setting its switches in different configurations (table in figure 1.5b). By turning the switches AH and BL «on», while AL and BH are «off», a current is flowing through the motor as indicated by the green arrow in figure 1.5a. This makes the motor shaft turn clockwise, as shown in table 1.5b. Subsequently AL/BH may be «on», while AH/BL are «off», to make the motor shaft turn counter-clockwise<sup>4</sup> (red arrow). If all the switches are «off», the motor will run free. If the top (or bottom) switches are «on», the motor will brake (gray arrow).

Some configurations will result in a short circuit between the power rail and ground, and must be avoided by all means. This is called a shoot-through, and happens if two switches

on the same side are «on» simultaneously. To prevent this, the H-bridge controller must make sure there is a time delay between turning two transistors on the same side «on».

Transistors are often used as the electronic switches in an H-bridge, such as two pairs of NPN and PNP bipolar junction transistors (BJTs) or two pairs of P- and N-channel metal-oxide-semiconductor field-effect transistors (MOSFETs). Since these are complementary, they will turn «on» and «off» inversely of each other. Alternately, just N-channel MOSFETs may be used, as P-channel MOSFETs have much higher  $R_{ds_{on}}$ , lower current rating and higher price. This requires, however, a drive-stage which can supply a voltage higher than the motor's power supply to turn the N-channel MOSFETs «on». For a N-channel MOSFET to turn «on», it must have a high potential on its gate with respect to its source.

The states listed in table 1.5b shows how the H-bridge is controlled during static operation. E.g. when all the current is flowing continuously in the wanted direction and the motor is running

<sup>4</sup>What direction the motor is turning (cw/ccw) is of course depending on which way its wires are connected.

at full speed. To change the speed of the motor, the drive voltage can be altered, as the speed of the motor is proportional to the drive voltage. A higher voltage will make the motor draw a larger current and consume more power, and vice versa.

Another solution is to turn the motor rapidly «on» and «off». It is easier to turn the drive current «on» and «off», than to change the drive voltage, since we already have the necessary switches in place. The ratio between the «on» and «off» time of the drive current will set the speed, as the motor receives different amounts of energy. To control the ratio, the switches are driven with a pulse-width modulated PWM signal. A PWM signal has constant frequency, but varying pulse-width<sup>5</sup>, effectively changing the ratio between «on» and «off», as shown in figure 1.6. The motor is then duty-cycle controlled.

Further discussion about how the H-bridge drive modes is actually implemented is covered by section «Second H-bridge drive modes» on page 20. A more detailed analysis about H-bridge operation is covered by a series of articles beginning with [7].

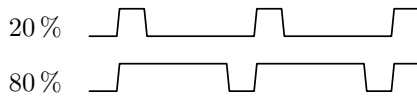


Figure 1.6: Illustration of (two) PWM signals. Top signal has a pulse-width (and thus ratio) of 20%, while the bottom signal has a pulse-width of 80%.

### 1.2.2 Calculated heat dissipation in MOSFETs

The current flowing through the MOSFETs in the H-bridge will dissipate heat in two ways.

When they are in their saturated region (completely turned on), their resistance between drain and source ( $R_{ds}$ ) will lead to a constant power consumption depending on the current running through them. This power is given by

<sup>5</sup>The RC-signal described in section 1.1.4 is thus also a PWM signal.

Ohm's law (1.2) and the electric power equation (1.3), which yields equation (1.1).

$$P_{\text{on}} = R_{ds} \times I_{\text{max}}^2 \quad (1.1)$$

where  $P$  is electric power,  $R$  is resistance and  $I$  is current.

Ohm's law is given as

$$U = R \times I \quad (1.2)$$

where  $U$  is the voltage,  $R$  is the resistance and  $I$  is the current.

When the MOSFETs are changing between their «on»- and «off»-state on the other hand, they dissipate power during the switching-transition. We want to find an expression of this dissipated power. To disregard the power consumed while the MOSFETs are saturated, we look at a MOSFET with  $R_{ds} = 0$ . The current through the MOSFET while switching is shown in figure 1.7 and the voltage over it is shown in figure 1.8. By using the electric power equation (1.3) we get the power shown in figure 1.9.

Electric power is given as

$$P = U \times I \quad (1.3)$$

where  $P$  is electric power,  $U$  is voltage and  $I$  is current.

Figure 1.9 shows us the power dissipated as heat in one switching-transition. We can sum this power by solving the integral of the power over time, and get the energy lost as heat by each switching-transition. The length of each switching-transition is denoted by the time  $\Delta t$ .

Energy lost during a switching-transition is thus given as

$$E = \int_0^{\Delta t} P(t) dt \quad (1.4)$$

where  $E$  is energy,  $P$  is electric power and  $t$  is time.

This energy is converted to heat twice in each period. First on the rising edge and then on the falling edge. We can now determine how many times a second this energy is converted to heat. This is where the switching frequency

gets involved. The energy from two switching-transitions can be multiplied with the frequency, and will then give the time-averaged energy per second. This is the definition of power and we have thus found an expression for the dissipated heat caused by the switching.

Average power lost in heat due to all switching-transitions is therefore given as

$$P = f \times 2E \quad (1.5)$$

where  $P$  is electric power,  $f$  is the switching frequency and  $E$  is energy.

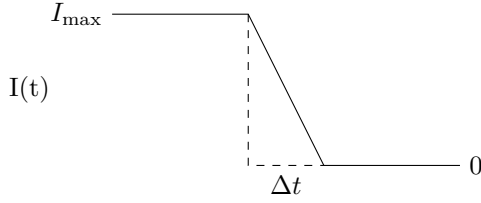


Figure 1.7: Current through a MOSFET during a switching-transition.

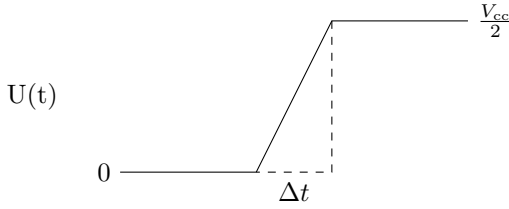


Figure 1.8: Voltage over a MOSFET during a switching-transition.

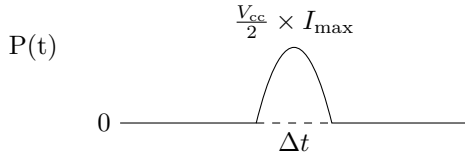


Figure 1.9: Power dissipation in a MOSFET during a switching-transition.

We can find the heat loss if we apply a linear approximation for the current and voltage during the switching-transition (as shown in figure 1.7 and 1.8).

The current during the switching-transition can be described as

$$I(t) = I_{\max} - \frac{I_{\max}}{\Delta t} t \quad (1.6)$$

which can be seen directly from figure 1.7.

Similarly, the voltage can be described as

$$U(t) = \frac{\Delta t}{\frac{V_{cc}}{2}} t = \frac{2\Delta t}{V_{cc}} t \quad (1.7)$$

$\frac{V_{cc}}{2}$  originates from the total voltage ( $V_{cc}$ ) being divided amongst the two MOSFETs.

By combining equation (1.6) and (1.7) with equation (1.3) we get equation (1.8).

$$P(t) = \frac{2\Delta t}{V_{cc}} t \left( I_{\max} - \frac{I_{\max}}{\Delta t} t \right) \quad (1.8)$$

The energy lost during one switching-transition ( $E$ ) is, as earlier discussed, the power ( $P$ ) lost over the time  $\Delta t$ , expressed in equation (1.9).

$$E = \int_0^{\Delta t} \frac{2\Delta t}{V_{cc}} t \left( I_{\max} - \frac{I_{\max}}{\Delta t} t \right) dt \quad (1.9)$$

After solving the integral in equation (1.9) we get equation (1.10).

$$E = \frac{I_{\max} \Delta t^3}{3V_{cc}} \quad (1.10)$$

Using equation (1.5), the average power lost in heat due to all switching-transitions is then given by equation (1.11).

$$P = \frac{2f \times I_{\max} \Delta t^3}{3V_{cc}} \quad (1.11)$$

These calculations show that the switching loss increases with the switching frequency ( $f$ ), current ( $I_{\max}$ ) and transition time ( $\Delta t$ ). The current is governed by the load, and the transition time is determined by the choice of MOSFETs and driver. The switching frequency can be adjusted, and should be kept low to minimize switching losses.

There is also switching loss involved with the charge and discharge of parasitic capacitances within the MOSFET. Another new MOSFET model for estimating switching loss is presented in [8].

### 1.2.3 Calculated copper track width

When routing printed circuit boards (PCBs) for the H-bridges, a suitable width for the tracks conducting the drive current to the motors must be found. If the track are too narrow, the copper laminate will get too hot and burn. By setting a limit on dissipated power (e.g. 1.5 W) for a given length (50 mm), we can calculate the needed track width. The dissipated power will increase with the track length, so the width needed to keep a constant temperature is usually independent of track length. The allowed dissipated power is dependent on the ambient temperature, allowed temperature rise, and thermal resistance between the track and the environment. IPC-2221 [9] can be used to find these factors.

The resistance  $R$  of a wire with cross sectional area  $A$ , length  $l$  and resistivity  $\rho$  is given by Pouillet's law

$$R = \rho \frac{l}{A} \quad (1.12)$$

Cross sectional area  $A$  is given as the width  $w$  of the track multiplied with its height  $h$  (thickness). We can then solve Pouillet's law (1.12) for  $w$  and get an expression for the width  $w$  needed to have a resistance  $R$ . We can use equation (1.1) to get an expression for the allowed resistance  $R$  to not exceed the power  $P$ . By combining Pouillet's law (1.12) with equation (1.1) we get equation (1.13).

$$w = \frac{\rho I^2 l}{h P} \quad (1.13)$$

where  $w$  is track width,  $h$  is track thickness,  $l$  is track length,  $P$  is allowed dissipated power,  $\rho$  is resistivity and  $I$  is current.

The resistivity  $\rho$  of copper is 17 nΩm [10, p. 658]. The thickness  $h$  of the outer copper sheets of a standard PCB is 35 μm. With a current of 40 A, and a track length of 50 mm, we need a minimum width of 25.9 mm to have a power dissipation below 1.5 W. A power dissipation of 1.5 W over 50 mm with this width corresponds to a temperature rise of about 25 ° (very high) for external layers in air and an ambient temperature of 25 ° [9].

An interesting result of Pouillet's law is that (when the thickness is fixed) the resistance of any square (width = length) will be equal, regardless of its size. A 1 mm × 1 mm square has the same resistance as a 1 m × 1 m square. The resistance will only change when the length-to-width ratio is altered.

## 1.3 Material and methods

Two attempts have been made to create a full motor control system. Both motor control systems would have incorporated *a*) one RC-receiver for communication; *b*) one or two steering controller(s); *c*) one or two H-bridge controller(s); *d*) two H-bridge drive-stages; and *e*) two H-bridges.

The RC-receiver is responsible for receiving the stick position of the remote control unit used by the operator. The steering controller is responsible for translating the stick position to driving commands suitable for the H-bridge controller. The H-bridge controller is responsible for controlling the H-bridge drive-stage, so the correct transistors are turned «on» and «off», and without creating a shoot-through condition. The H-bridge drive-stage is responsible for driving the gate of every transistor in each leg of the H-bridge, with the correct voltage and with enough current to minimize the transition time ( $\Delta t$ ). The H-bridge is responsible for conducting the drive current to one of the motors in the wanted direction. H-bridge driver is the name for the H-bridge controller and H-bridge drive-stage combined.

The first attempt was designed around the HIP4081 integrated circuit (IC) which is both an H-bridge controller and a drive-stage. This IC was placed on a PCB together with an H-bridge. The steering controller was first made using only analog components. The full motor control system, using these components is shown in figure 1.10a. Two analog control signal decoders are used as steering controllers. Both are needed to control the boat, with each connected to an H-bridge (with controller and drive-stage). Each H-bridge will then drive one of the electric

engines. The left stick on the RC-transmitter controls the left engine, while the right stick controls the right engine.

Since the analog steering controller was troublesome (described later), a new digital control signal decoder was developed. By using the new digital control signal decoder as the steering controller, both sticks on the RC-transmitter can be combined to create a more sophisticated steering mechanism (described in section 1.3.1). A block schematic of the new motor control system (still using the first H-bridge) is shown in figure 1.10b.

After problems with the first H-bridge solution were encountered (described in section 1.3.1), a new H-bridge drive-stage was constructed together with a new H-bridge. This new system is based on the previous digital steering controller, but incorporates the H-bridge controller as well. The new motor control system is shown in figure 1.11.

### 1.3.1 First attempt on H-bridge construction

The first attempt on an H-bridge construction started by creating an analog control signal decoder, which constitutes the steering controller (link between the RC-receiver and the H-bridge).

#### Analog steering controller

To get the stick position of the RC-transmitter, the pulse-width from the RC-receiver needs to be measured. This should in turn drive the H-bridge accordingly.

My first attempt was to accomplish this using only analog components. I used the circuit listed in appendix C based on a circuit from [11], and created the PCB shown in figure 1.12. This board would connect the RC-receiver to an H-bridge, by converting the servo-signal to two PWM pulses needed to drive the H-bridge. One to drive the motor forward, and one to drive it backward.

The circuit works by charging capacitor C2 and C3 with a buffered servo-signal. The charged voltage is sampled when the pulse ends. This sampled voltage is compared with U2A

and U2C, against a triangle wave generated by U2D. This results in two PWM pulses which can drive the H-bridge. One is output when the stick is positioned forward and the other when the stick is positioned backward.

#### Digital steering controller – proof of concept

The analog steering controller has a non-linear relation between the pulse-width and the voltage to the comparator. It was also difficult to adjust the timing so the analog switches opened and closed when they should. For these reasons, I began looking for another way to measure the pulse-width.

Multiple options were considered. One solution was to integrate the pulse with an operational amplifier and a capacitor to create a voltage proportional to the pulse-width. Another solution was to let a counter run for as long as the pulse is high and send this digital output to a digital-to-analog converter (DAC). A circuit with many analog components often requires a lot of ICs, which takes a lot of time to build and give many sources of error. I realized that the problem of reading the servo signal and output it to the H-bridge is straightforward to accomplish using a MCU. Everything can be done in the same IC – measuring the pulse-width with an internal counter, possibly communicate with another MCU, and generating a PWM drive signal along with a direction bit.

I used an MCU named ATtiny85 by Atmel which I had by hand, to create a proof of concept for this solution. This MCU has a counter and PWM output capabilities. I wrote the program listed in appendix D. The program receives an interrupt on the edges of the servo signal, and resets the counter if it is rising or reads the counter if it is falling. Depending on the value of the counter, a PWM signal will be output along with a direction indicator. The system was tested as shown in figure 1.13, and gave promising results, as it could successfully dim a LED depending on the stick position of the RC-transmitter.



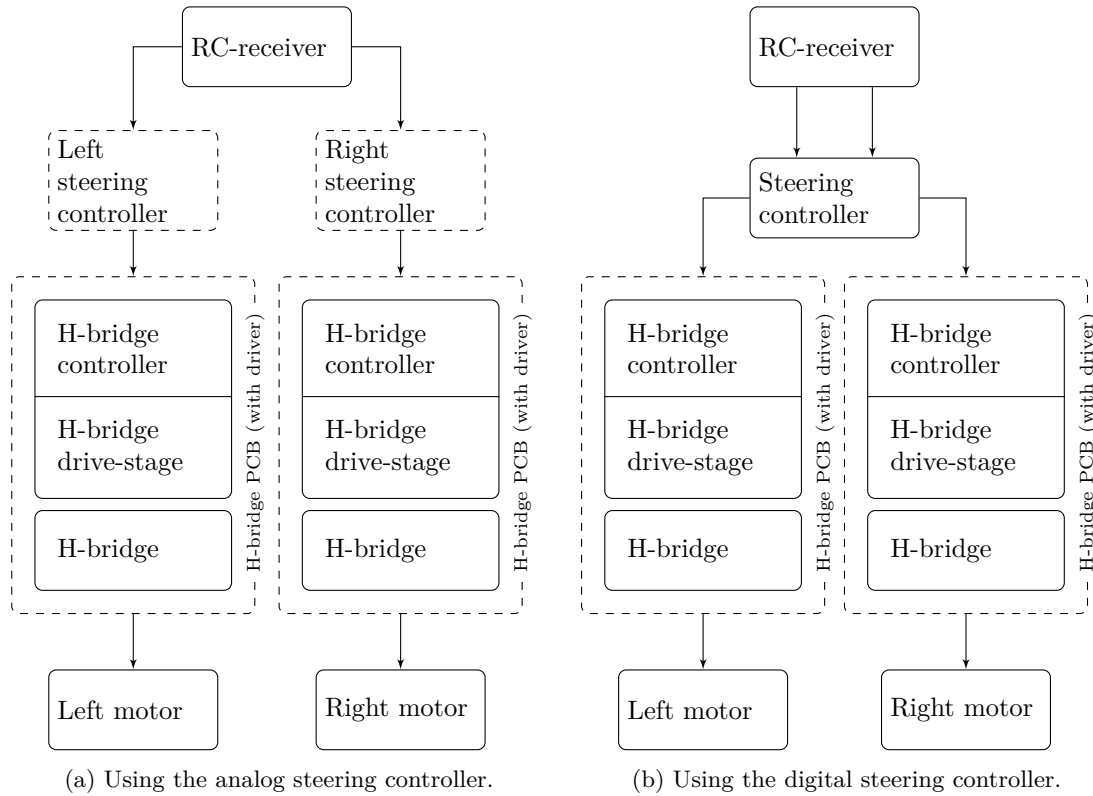


Figure 1.10: Block schematic of a full motor control system using a pair of the first H-bridges, with either analog or digital steering controller. Dashed boxes indicates self-made PCBs.

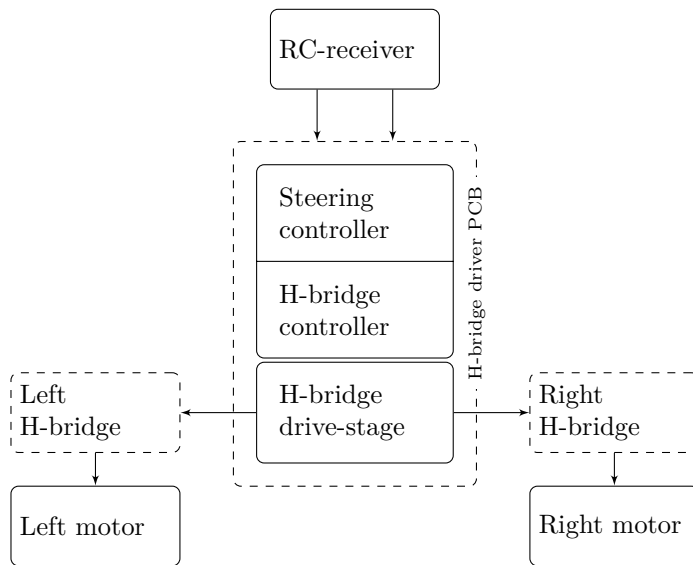
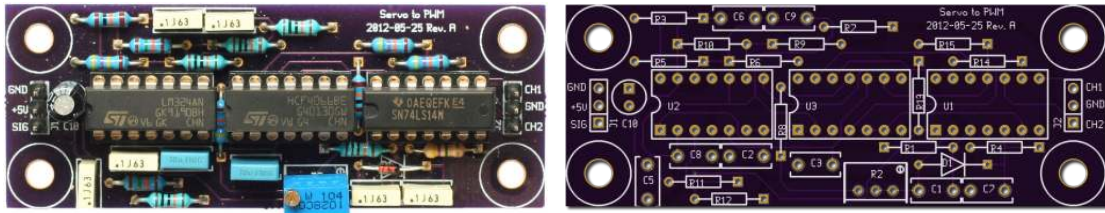


Figure 1.11: Block schematic of a full motor control system using the second H-bridge. Dashed boxes indicates self-made PCBs.



(a) Photograph of an assembled PCB of the analog steering controller. (b) CGI of the analog steering controller PCB.

Figure 1.12: Images of the PCB for the analog steering controller.

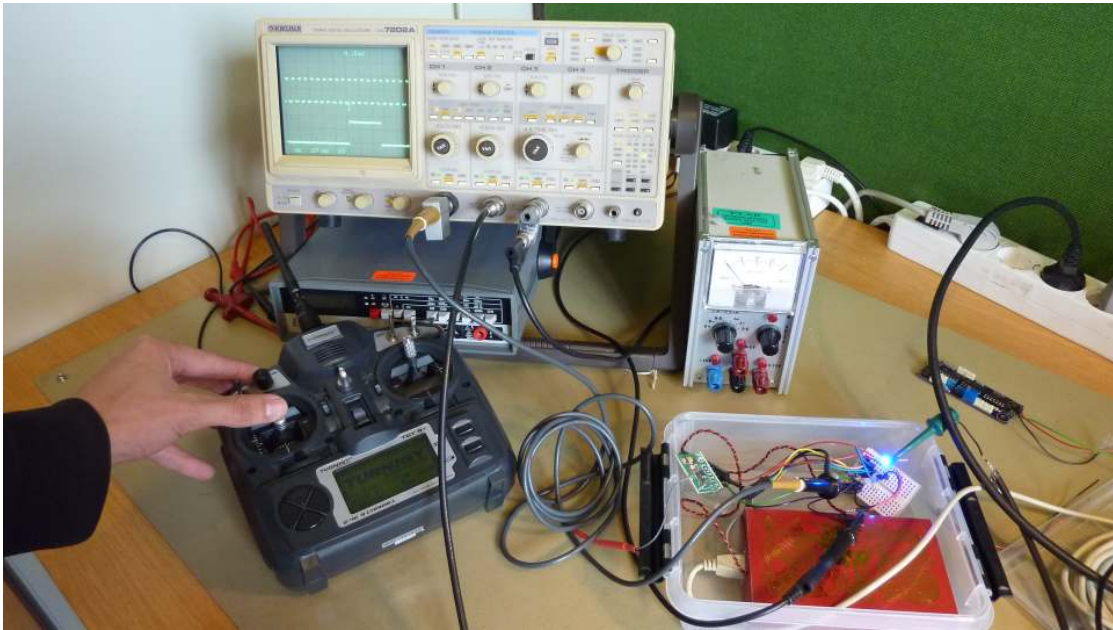


Figure 1.13: Test of the prototype digital steering controller running on ATtiny85.

### Digital steering controller – dual channel

Some of Atmel's other MCUs supports a special interrupt called Input Capture. This interrupt have the ability to timestamp events, and can give out information such as frequency or pulse-width directly. ATtiny10 has Input Capture and was considered for the job. With its one Input Capture channel it could have done the same job as the analog control signal detector. In the end an ATmega128A1 was chosen because it has multiple Input Capture channels, making

it easier to create a system which incorporates two control sticks.

Development of the ATmega128A1 code was originally done on a breakout board containing an ATmega128A1 MCU, a crystal, and other necessary components. The breakout board can be seen in the middle of figure 1.15. The code was initially developed in Atmel Studio, but Atmel Studio was eventually replaced by a stand alone compiler (GNU Compiler Collection (GCC)), build utility (make), flasher/programmer (avrdude) and a text editor (vim), for a more transparent workflow. I wrote the program shown in appendix E. This program

uses Input Capture interrupts to find the pulse-width of each channel. This pulse-width is scaled and modified with a dead band (around center) to find the stick position.

The program then calculates the thrust to each engine by combining the stick position of both sticks. The total thrust of both engines is controlled by the left stick. The difference between the thrust to each engine is controlled with the right stick. In other words, the left stick sets the speed of the boat, while the right stick sets the direction. The steering mechanism is presented in table 1.1.

It is also possible to choose a more primitive steering mechanism, by the flip of a switch. Each stick would then control the thrust of one motor, and give the same behavior as using two single steering controllers.

Finally it creates four PWM signals. Two for the left motor (forward and backward), and two for the right motor.

The switching (PWM) frequency was set to 1 kHz as this is high enough for the motor to run smoothly, while low enough to avoid excessive switching losses (as described in section 1.2.2). The chosen switching frequency lies in the audible spectrum for humans, but we do not consider this a problem since the vessel will usually be located away from people. It may be necessary to change the switching frequency, to stop its harmonics from interfering with (the frequency of) the echosounder used during a survey. The switching frequency can easily be altered in software.

### First H-bridge controller and drive-stage

After researching different H-bridge constructions, multiple options were found. It is possible to buy all assembled H-bridge-motor drivers as modules or as monolithic ICs with H-bridge controller, drive-stage and power transistors all embedded. ICs with just the controller and drive-stage are also available.

As described in 1.1.3, we chose not to buy an all assembled H-bridge-motor driver module (assembled PCB with enclosure). ICs with everything included were then considered. The SN754410 is an all-in-one H-bridge and

can drive up to 36V at 1A continuous output current, but this is far from enough for our application. NCV7729 is a similar IC from ON semiconductor that can withstand up to 8 A, but this is still is not enough.

Due to the high current required, it was decided that discrete power transistors were necessary. A drive-stage for these transistors, as well as an H-bridge controller were still needed.

Multiple pre-made H-bridge driver ICs were found, e.g. LT1162 and HIP4081. They have embedded logic for driving top and bottom transistors, protection against shoot-through conditions and generation of the high voltage needed to turn «on» the top N-channel MOSFETs. The HIP4081 from Intersil was finally chosen because it is widely used and have showed good results in the Open Source Motor Control (OSMC)-project [12].

### First selection of power transistors

We needed a minimum of four power transistors which each should handle the high currents needed to drive the electric engines. The possible transistor types were BJT, MOSFET or insulated-gate bipolar transistor (IGBT).

An IGBT is a kind of cross between a MOSFET and a BJT, as it is voltage controlled like a MOSFET, but has the power driving capabilities of a BJT. It can typically withstand much higher voltages ( $> 1000$  V), has lower  $R_{on}$  and less stray capacitances than a MOSFET.

BJTs were not considered because of their poor switching performance, such as longer turn-on and turn-off times. We chose MOSFETs over IGBTs, because we do not need the (very) high voltage and (very) high power capabilities of the IGBTs. MOSFETs will then have a larger selection at our specifications of 12 V and about 40 A.

The FDP3651U, a 100 V 80 A N-channel MOSFET, was considered among others, but was finally rejected in favor of IRF1404z. IRF1404z has a lower  $R_{ds,on}$  of only 3.7 m $\Omega$ , compared to 18 m $\Omega$  for FDP3651U. IRF1404z can withstand a continues current of 75 A at 10 V and pulsed currents up to 750 A. Maximum

Table 1.1: Steering mechanism. Vessel behavior at various stick combinations.

Left stick $\updownarrow$	Right stick $\leftrightarrow$	Vessel behavior
Forward	Center	Drive straight forward
Forward	Left	Drive forward, while turning left
Forward	Right	Drive forward, while turning right
Backward	Center	Drive straight backward
Backward	Left	Drive backward, while turning left
Backward	Right	Drive backward, while turning right
Center	Center	Stand still
Center	Left	Turn counter-clockwise on the spot
Center	Right	Turn clockwise on the spot

$V_{ds}$  is 40 V. We chose to use a TO-220 through-hole package for easy attachment to a heatsink.

### Heatsink

To cool the discrete transistors, we initially thought that a heatsink was required. I chose SK 61/100 SA from Fischer Elektronik, because it has mounting brackets and room for the PCB in the middle, as shown in figure 1.17a.

### Protection circuitry

Extra flyback diodes are connected in addition to the internal suppression diodes. These diodes protect the transistor from the sudden voltage spike the inductive load (motor) induces when its current is suddenly removed. They provide a path for the energy stored in the motor to escape back to the power supply, if no other path is available.

An RC-snubber circuit (consisting of a resistor and capacitor in series) connected in parallel with the load, was considered to be incorporated as well to help suppress voltage transients. In the end, this was not found to be required.

### Design and production of first H-bridge

A PCB based on the HIP4081 was designed and manufactured. The schematic is shown in appendix F, the PCB in figure 1.14 and the assembled PCB in figure 1.16 and figure 1.17b (with heatsink).

The PCB was designed using Zuken Cadstar, and etched in-house at the electronics laboratory. After etching, a thin coat of SUR-TIN<sup>6</sup> was applied to help soldering, as visible in figure 1.16. A lot of solder paste was applied to make the tracks thicker, before it was soldered in a vapor oven.

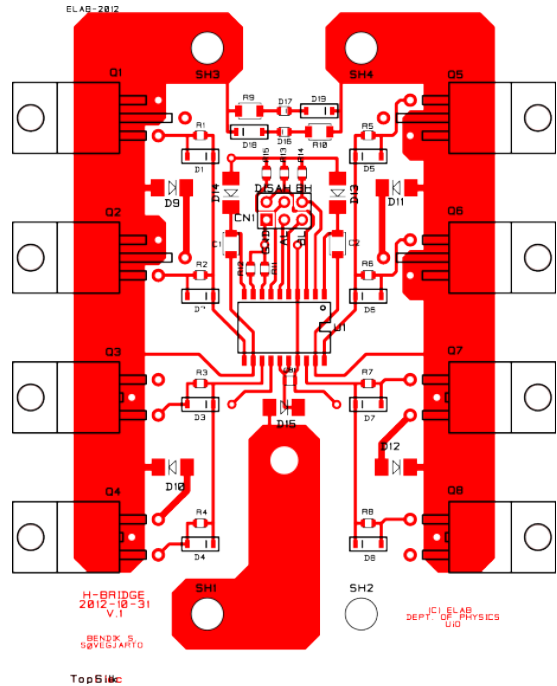


Figure 1.14: PCB layout for the first H-bridge.

<sup>6</sup>Electroless immersion tinning system

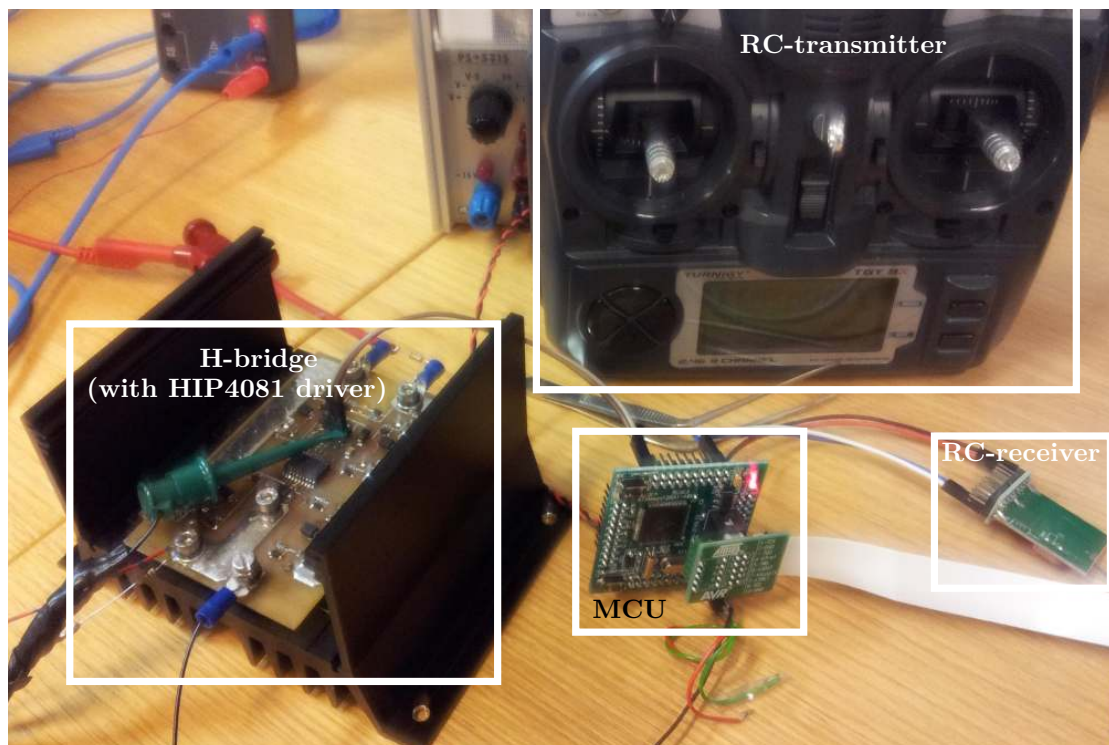


Figure 1.15: Test setup for the first H-bridge.

The PCB incorporates eight MOSFETs, with two connected in parallel for each leg in the H-bridge. This was done to lower the effective  $R_{ds_{on}}$ , and distribute the heat.

Gate discharge diodes were connected in parallel to the gate resistors, to empty the gate faster, and thus decrease the turn-off time. Two LEDs are connected in parallel to the load to give a visual indicator of the polarity and the (effective) potential of the voltage applied to the load.

The traces conducting the drive current to the motors were carefully placed, to keep them short and utilize most the available board area. Together with quite wide and thick (because of the solder applied) traces we achieved low resistance between the transistor and the load.

It was difficult to find suitable connectors for the load and power supply, due to the high currents involved. Our solution was to use a nut and bolt for each connection.

### Testing and abandonment of first H-bridge

The first H-bridge functioned well when tested with a light bulb as load or when driving an electric engine with slow changes in speed. It died multiple times, however, when the speed was changed rapidly. We first suspected that the power transistors had died, but after changing them, it was apparent that it was the HIP4081 driver that had stopped working. The test setup is shown in figure 1.15.

We believe it was large transients across the motor that killed the driver. The HIP4081 driver is in fact connected directly to the motor terminals as it is using the switching frequency of the PWM-signal to generate the high voltage needed to turn «on» the top N-channel MOSFETs.

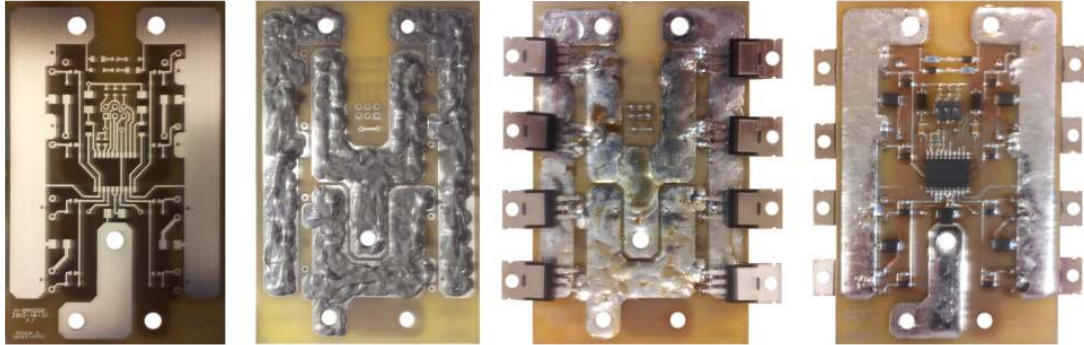
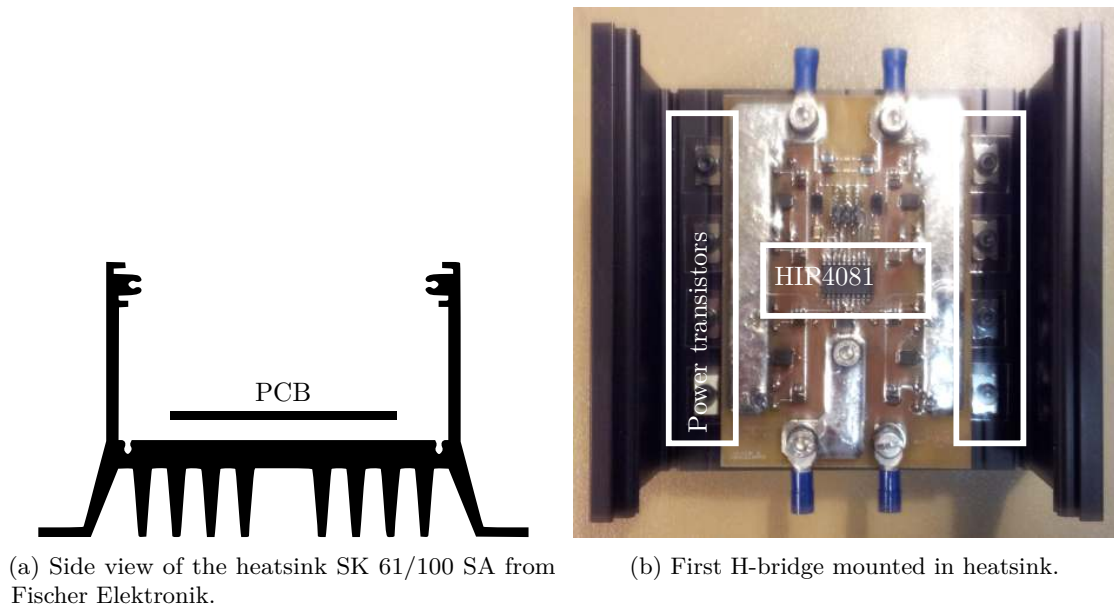


Figure 1.16: First H-bridge with solder paste and after soldering.



(a) Side view of the heatsink SK 61/100 SA from Fischer Elektronik.

(b) First H-bridge mounted in heatsink.

Figure 1.17: Heatsink for the first H-bridge.

### 1.3.2 Second attempt on H-bridge construction

Due to the low robustness of the first H-bridge, we set out to build a new version with our own controller and drive-stage. In this way we would have total control over the whole system, and we could distance the driver from the high voltage spikes from the motor.

The second motor control system incorporates one steering controller, one H-bridge controller, two H-bridge drive-stages and two H-bridges. The steering- and H-bridge controller, along with two H-bridge drive-stages are combined into one (H-bridge driver) PCB. Each of the H-bridges uses their own separate PCB. Three PCBs are thus needed for a full motor control system.

#### Motor control system stack

To interconnect the full motor control system, two solutions were considered; *a*) connecting the PCBs to a backplane; or *b*) stacking the PCBs. Even though a backplane construction gives easier access to each card, stacking the PCBs was chosen. The reason being the requirement of bulky and hard to source connectors needed to conduct the large currents through a backplane connection. This was chosen because the large currents involved would have required bulky and hard to source connectors for a backplane connection. The stack lets the current be distributed by bolts.

The stack also allows for multiple H-bridges to be connected in parallel. This makes it theoretical possible to control even higher currents than one H-bridge could handle. The H-bridge drive-stage limits how many transistors that can be driven at once, however.

The motor control system stack (with the prototype driver) is shown in figure 1.23b and (with finished driver) in figure 1.29.

An assembled stack is occasionally referred to as a «control unit».

#### Second steering- and H-bridge controller

The new steering controller is based on the previous digital dual channel steering controller. Since we used a multipurpose MCU, it can also be programmed to behave like an H-bridge controller.

Originally the same MCU breakout board containing an ATxmega128A1 was used, as shown in figure 1.23a. Later on, a custom PCB was designed (described in section 1.3.2) containing a smaller and newer version of the ATxmega128A1, namely the ATxmega32A4U (cost; ~3 USD each at a quantity of 100). In the new version (marked with a trailing «U» character), the manufacturer has corrected many of the silicon errors previously present and added Universal Serial Bus (USB)-functionality. These MCUs were chosen because of their motor driving capabilities, as well as the Input Capture interrupts described in section 1.3.1. They have specialized hardware for controlling half-bridges, as an extension of their PWM functionality. The extension is called Advanced Waveform eXtension (AWeX) and gives the ability to insert a dead-time and invert one of the outputs, so the top and bottom transistors never are turned «on» at the same time. Shoot-through conditions should not be possible if this is set up correctly.

The firmware for the MCU is available at [https://github.com/epsiro/ASV\\_H-bridge\\_controller](https://github.com/epsiro/ASV_H-bridge_controller) and is listed in appendix G. Git<sup>7</sup> is used for version control of the software. The firmware reads the pulse-widths, finds the stick position, calculates the appropriate motor thrust and creates eight (high and low side) PWM-signals with dead time. It will skip the first commands (pulse-widths) received from the RC-receiver, to wait for the RC-transmitter and -receiver to initialize. If the connection with the RC system is lost, a timeout will automatically stop the motors within a short amount of time.

<sup>7</sup>Git is a free (as in free speech) distributed revision control system

A block schematic of the program flow is shown in figure 1.18. Some of the blocks are outlined with pseudo-code in the following listings.

Listing 1.1: `normalize_stick_position`

```
1 center pulse-width around 1.5 ms
2 insert dead band
3 scale to -127..127
4 round up 125..127 to 127
```

Listing 1.2: `combo_sticks`

```
5 if (left_stick == 0 && \
6     right_stick != 0)
7     turn on the spot
8     motor_thrust_left = right_stick;
9     motor_thrust_right = -right_stick;
10 else
11     normal drive
12     motor_thrust_left = left_stick +
13         right_stick;
13     motor_thrust_right = left_stick -
14         right_stick;
15 remove overflows
16
17 drive left motor through a low pass
18     filter (drive_motor_lp)
18 drive right motor through a low pass
19     filter (drive_motor_lp)
```

Listing 1.3: `drive_motor_lp`

```
19 calculate moving average
20 drive motor (drive_motor)
```

Listing 1.4: `drive_motor`

```
21 scale the pwm-value
22 if (motor_thrust > 0)
23     drive forward // set timer reg.
24 else
25     drive backward // set timer reg.
```

The firmware can also receive steering commands over UART. This can be used to communicate with another control system, e.g. an autopilot. The steering commands consist of one byte, where the most significant bits (MSBs) indicate which motor to control, the second MSB indicates the direction and the remaining 6 bits sets the speed (in 64 steps), as shown in table 1.2. Each motor will run at the given speed until a new command is received or a timeout is encountered (after missing 20 UART commands). The timeout will stop the motors. This simple binary format was found to be sufficient, and was chosen for simplicity over a more complicated ASCII or multi-byte binary format.

## Second H-bridge drive modes

Static operation of an H-bridge is described in section «H-bridge operation» on page 8. The following section will describe how the second H-bridge controller adjusts the speed and direction of the motors.

As described earlier, the H-bridge are controlled by multiple PWM signals, where their pulse-width set the speed of the motor. These PWM signals are generated by the MCU on the H-bridge driver PCB and amplified by the H-bridge drive-stage's push-pull circuit. The signals generated by the H-bridge driver (connected to one of the H-bridges) is shown in figure 1.19. By comparing this figure with table 1.5b on page 8, we can see which H-bridge states we are using. We are alternating between the state «motor runs cw» (S1) and «motor brakes» (S5), when we drive forward, and «motor runs ccw» (S2) and «motor brakes» (S5), when we drive backward. The reason why we «brake» in between each cycle is described in the following paragraph.

Since our load is inductive, energy will be present even after the switches are turned «off» and the current have stopped. To release this energy the inductive load drives a current back (back-EMF). We need to provide a path for this current to flow. If we do not provide this path, the inductive load will increase the voltage across itself, as the energy it harvests has nowhere else to go. This will result in a high voltage that is harmful to the switching transistors. To provide this path, we turn both the top transistors «on» so the energy can escape back to the power supply. Large capacitors, which can consume this sudden backward current, are connected in parallel with the supply rail to protect the power supply.

By turning both top transistors «on» we risk entering a shoot-through state, since one of the bottom transistors was already «on» when the motor was actively driven. To prevent two transistors on one side from being «on» at the same time, the MCU inserts a time delay after the bottom transistors have been turned «off», and before the top transistor is turned «on». This delay is called a dead-time. The dead-time



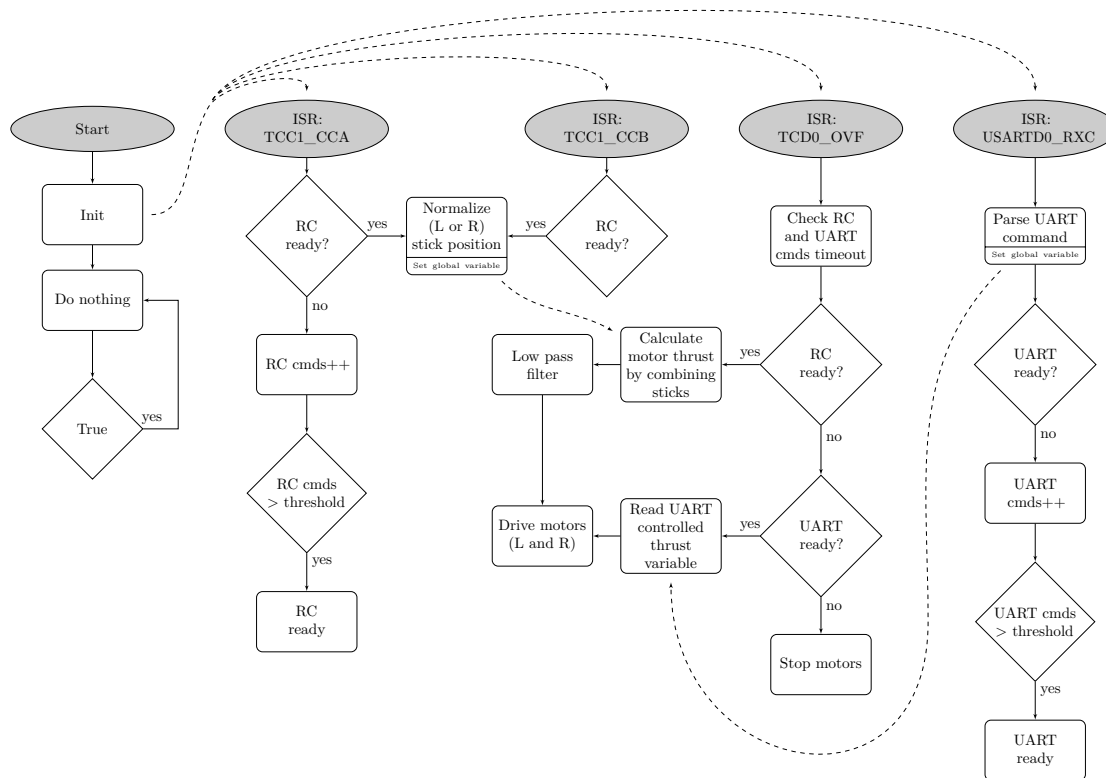


Figure 1.18: Block schematic over the H-bridge controller program flow. The ISR TCC1\_CCA and TCC1\_CCB is triggered every time a RC-control pulse is received. The ISR TCD0\_OVF is triggered every time a timer overflows (100 Hz). The ISR USARTD0\_RXC is triggered every time a byte is received over UART.

Table 1.2: Example of selected UART steering commands.

	Motor	Direction	Speed	Behaviour
	0b	0	000000	Left motor 0 % forward (stop)
	0b	0	111111	Left motor 100 % backward
	0b	1	011111	Right motor ~49 % forward
	0b	1	001111	Right motor ~24 % backward

is long enough to last through the turn-on and turn-off time of both transistors, along with an added safety margin. We insert a dead-time of  $30 \mu\text{s}$ .

The flyback diodes provide the necessary path back to the power supply during the dead-time (when both transistors are «off»). They are however not capable of handling the back-current for a long time, as they would overheat<sup>8</sup>.

### Second H-bridge drive-stage

To minimize the time it takes to turn the MOSFETs in the H-bridge «on» and «off», their gates needs to be quickly charged and discharged. This is accomplished by using a BJT push-pull stage to drive the MOSFETs, as shown in figure 1.21. The push-pull drive-stage is again driven by a single BJT, to amplify the drive signal from the MCU, as the MCU only can source a few milliamperes itself. This inverts the drive signal which must be compensated for in the MCU, either in software or by inverting the IO-pins.

The push-pull drive-stage was prototyped on a wooden plank to drive a half-bridge using the IRF1404z power transistors, as seen in figure 1.20. A half-bridge is a set of just one top and bottom transistor, simply half of the H-bridge, with the other side of the load always connected to ground. This setup could successfully change the brightness of a lamp or the speed of an electric engine quickly without dying. Since this was only a half-bridge, it could not change the direction of the current.

### Voltage multiplier

To turn «on» the high N-channel MOSFETs in the H-bridge, a high voltage source is needed. A voltage multiplier is used to create this voltage.

It consists of an oscillator (astable multivibrator) and a diode/capacitor-ladder. From figure 1.22 you can see the 12 V input flow through diode D1 and charge capacitor C3. When the oscillator goes high, the voltage below C3 goes up to 12 V. Since C3 still has a voltage of almost 12 V across itself, the voltage after D1 is lifted up to almost 24 V. This high voltage can now go through the second diode D2, and charge capacitor C2.

We now get almost twice the input voltage out, minus the two diode drops. Although the effective voltage can be much lower, as the voltage doubler only can source a limited current. Multiple ladders can be cascaded to further multiply the voltage, however the current sourcing capability will be even lower.

### Second H-bridge driver prototype

After the push-pull drive-stage was shown to successfully drive a half-bridge and the voltage multiplier was designed, a complete driver for two H-bridges was prototyped on a veroboard. The prototype is shown in figure 1.23a. A full motor control system using this prototype, is shown in figure 1.23b.

<sup>8</sup>Since their constant voltage drop multiplied with the current results in a high power dissipation

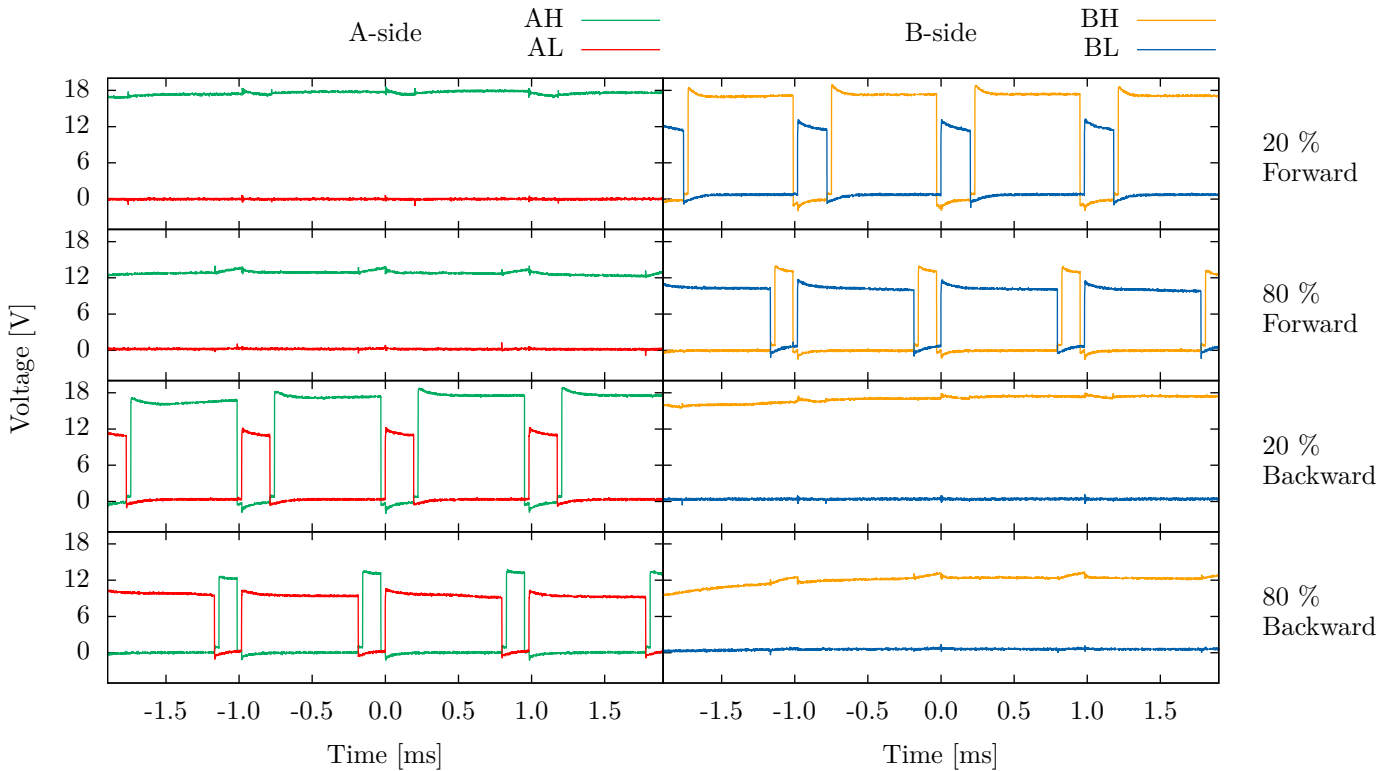


Figure 1.19: Drive signals from the H-bridge driver to the four legs of one of the H-bridges. AH, AL, BH, BL denotes the drive signal for the A- and B-side high (top) and low leg. The two top rows shows the signals used to drive the motor forward at two different speeds. Likewise, the two bottom rows shows backward driving. Forward and backward-driving is clearly very similar, with just the A- and B-side switched.

### Second selection of power transistors

Since the transistors and the heatsink of the first H-bridge never got noticeable warm while driving the electric engine, I decided to replace them with surface mounted transistors and use the PCB itself as the heatsink. This would make manufacturing considerable easier, and lower the cost as the external heatsink is no longer needed.

I chose PSMN2R6-40YS from NXP, because it *a*) has a low  $R_{ds(on)}$  of only  $2.8\text{ m}\Omega$  (at  $V_{GS} = 10\text{ V}$ ,  $I_D = 25\text{ A}$  and  $T_j^9 = 25^\circ\text{ C}$ ); *b*) is built in a package with a high power dissipation ( $\sim 130\text{ W}$  at  $25^\circ\text{ C}$ ); *c*) has a low gate charge; *d*) can withstand a continuous current of  $100\text{ A}$

(peak  $\sim 650\text{ A}$ ); *e*) is inexpensive ( $\sim 0.9\text{ USD}$  each at a quantity of 100); *f*) has a drain-source voltage of  $40\text{ V}$ ; and *g*) has embedded flyback diodes. The package used by this transistor is the Loss-Free Package (LFPAK) (figure 1.24), a type of Power-SO8. LFPAK eliminates the bonding wire commonly used in Power-SO8 packages, by soldering a copper clip directly to the silicon die, to connect the source and the gate with very low resistance. This allegedly results in superior electrical and thermal performance as well as higher reliability [13], according to the manufacturer. The bottom of the silicon die is soldered to a drain tab to provide a low thermal resistance path down to the PCB.

<sup>9</sup> $T_j$  is the junction temperature

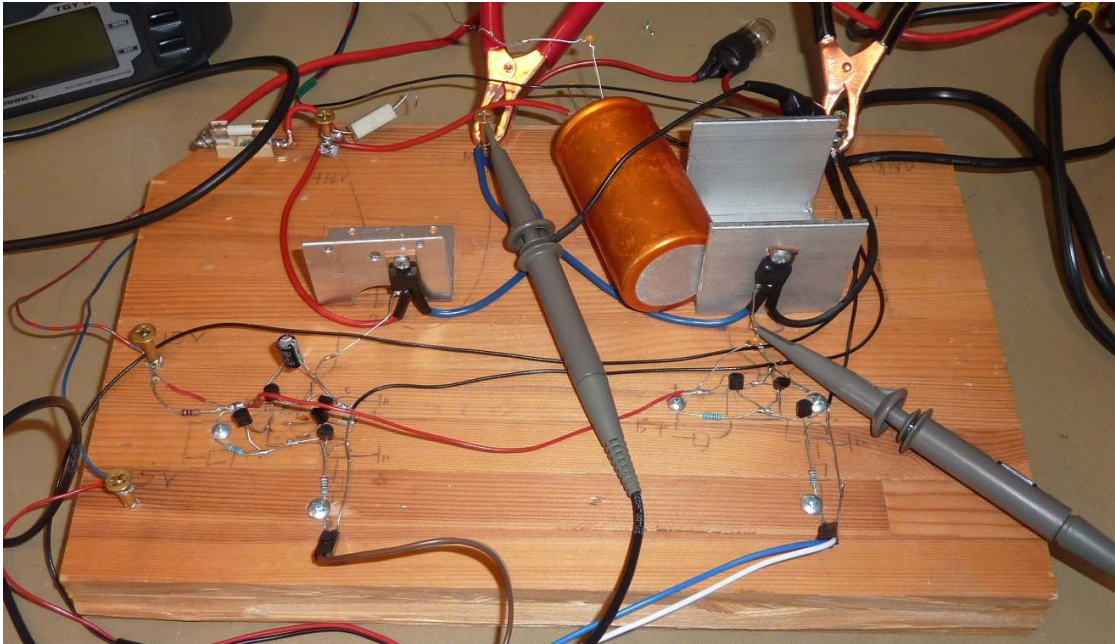


Figure 1.20: Photograph of the half-bridge prototype.

Table 1.3: Power transistors families compatible with the universal Power-SO8 footprint.

Manufacturer	Device Family
NXP	LFPAK (SOT669/SOT1023)
Infineon	PG-TDSON-8
Fairchild	Power 56
Vishay	PowerPAK SO-8
NEC	8-pin HVSON
ON Semi	SO-8 FL
STM	PowerFLAT (6x5)
Renesas	LFPAK

I have used the recommended universal Power-SO8 and LFPAK footprint which according to [13] allows all the device families listed in table 1.3 to be soldered. If another transistor is found to be better suited, it should thus be possible to solder it to the same PCB. The footprint is shown in figure 1.25.

Some of the later H-bridge PCBs has used the 60 V drain-source version (PSMN7R0-60YS), since PSMN2R6-40YS was temporarily out of

<sup>10</sup> $R_{ds_{on}}$  at  $V_{GS} = 10\text{ V}$ ,  $I_D = 25\text{ A}$  and  $T_j = 100^\circ\text{ C}$ . Although a higher current of 40 A does not influence the  $R_{ds_{on}}$ , when the  $V_{GS}$  is as high as 10 V.

stock. PSMN7R0-60YS has a  $R_{ds_{on}}$  of  $6.4\text{ m}\Omega$  (at  $V_{GS} = 10\text{ V}$ ,  $I_D = 15\text{ A}$  and  $T_j = 25^\circ\text{ C}$ ).

### Second H-bridge design and production

After the push-pull drive-stage was successfully prototyped with a half-bridge, I wanted a bare H-bridge PCB to continue development. I designed the PCB shown in figure 1.27, which only contains the power transistors and no driver. This was done to maximize the copper usage of the PCB, as the copper planes serve as the heatsink for the power transistors. I used four power transistor in each leg of the H-bridge, to distribute the heat and current.

Four transistors in parallel gives an effective  $R_{ds_{on}}$  of only  $0.9\text{ m}\Omega$  (even with a high junction temperature)<sup>10</sup> and with a current of 40 A equation (1.1) yields a static power dissipation of about 1.5 W.

As suggested by the application note [14], I placed a lot of copper around each transistor, maximized the distance between them, and used multiple layers with thermal vias to transfer

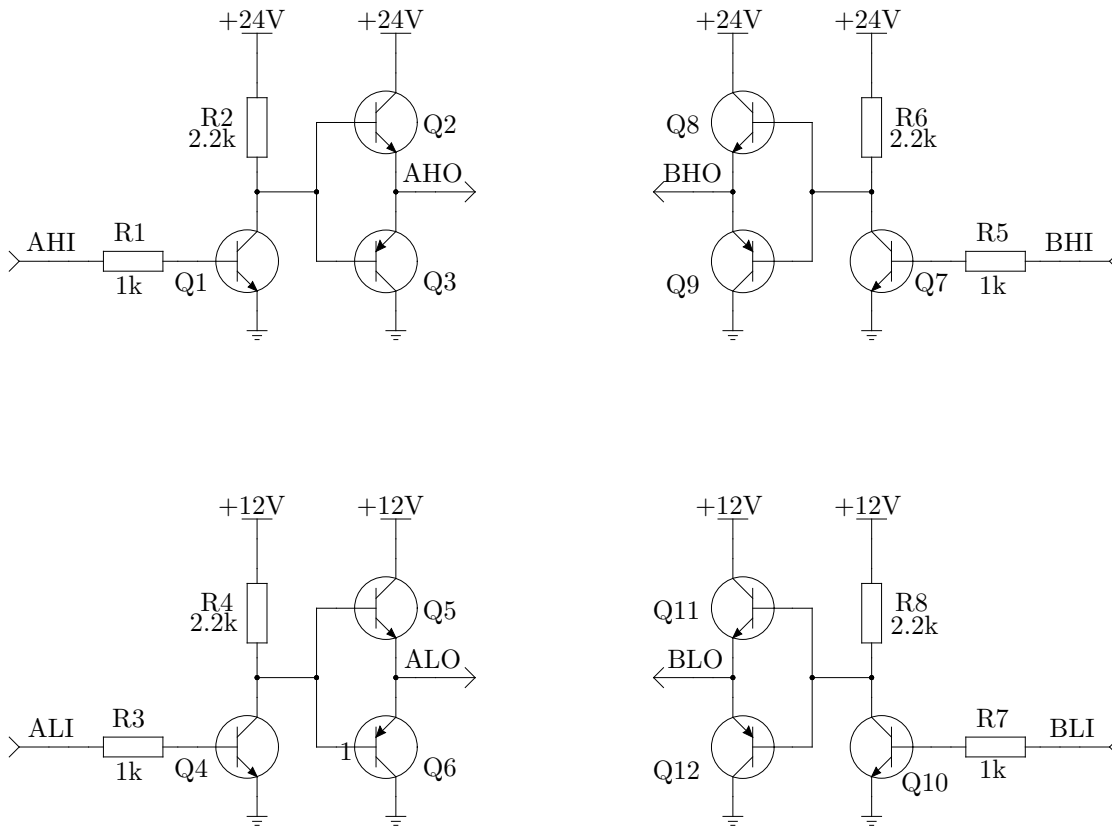


Figure 1.21: Schematic of the push-pull stage of the H-bridge drive-stage.

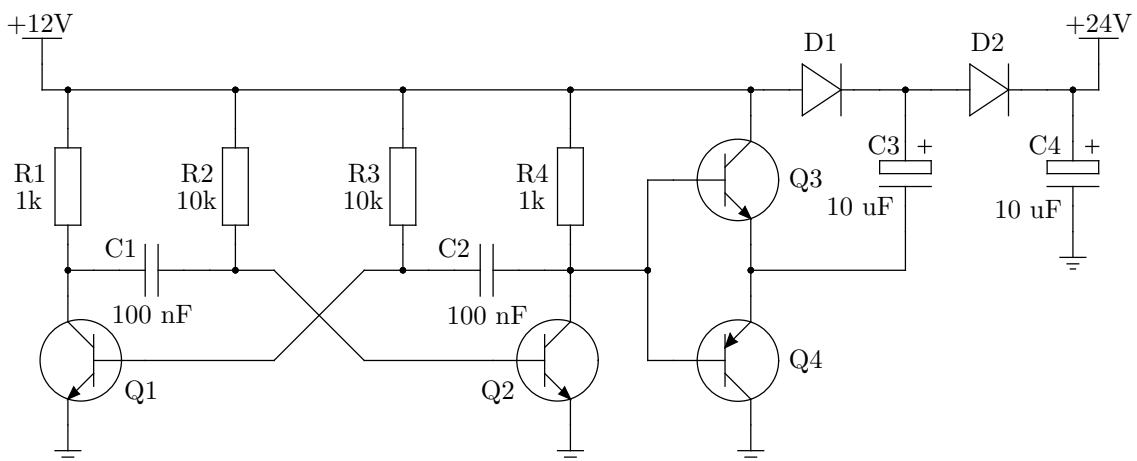
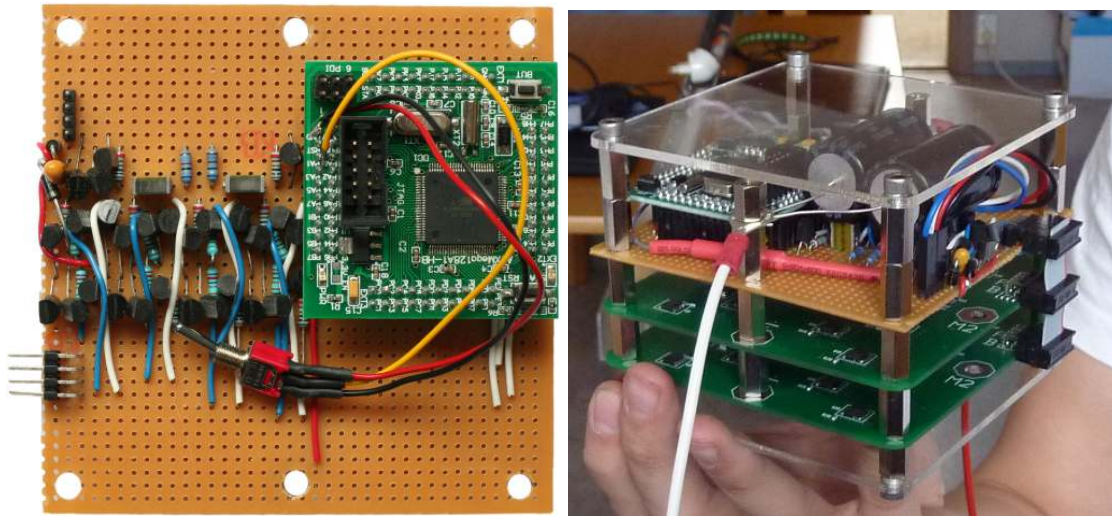


Figure 1.22: Schematic of the voltage multiplier. The oscillator is to the left, while the capacitor/diode ladder is to the right.



(a) Prototype of the full H-bridge driver for two H-bridges. With the steering and H-bridge controller to the right (green board), and the drive-stage to the left. The voltage multiplier is located above the drive-stage.

(b) Prototype of the full motor control system stack (control unit). With H-bridge driver for two H-bridges (top), and H-bridge for left motor (middle) and right motor (bottom). The RC-receiver is located in the back of the top PCB, and not visible. Laser-cut acrylic sheets form a very simple cover.

Figure 1.23: Prototype of H-bridge driver and full motor control system.

heat away. We used a 2-layer standard PCB with a copper thickness of  $35\ \mu\text{m}$ . Based on the equations described in section 1.2.3 and the use of copper planes that are nearly square, the resistance for the drive current will be low even with  $35\ \mu\text{m}$  copper. The current is also distributed on two layers and between multiple transistors, so the power dissipation is low enough for the copper sheets to not overheat during normal use.

As done in the first attempt, additional flyback diodes and indicator LEDs are included. Series resistors are connected to each gate of the power transistors, to limit the gate current (and turn-on and turn-off times) and dampen ringing between the gate's capacitance and wire's inductance. A value of  $220\ \Omega$  was initially chosen, but this can be decreased. The gate discharge diodes were not included, because they were deemed unnecessary with the new drive-stage. There is room on the PCB for a transient-voltage-suppression (TVS) diode.

<sup>11</sup>Not to be confused with the A- and B-side of the H-bridge

The gates of the power transistors are connected to a 8-pin connector. Each leg in the H-bridge (four transistors) can be connected to two pins on this connector. By soldering  $0\ \Omega$  resistors, the PCB can be configured to use the top (marked A) or bottom (marked B) side of the connector<sup>11</sup>. By doing so, the same PCB layout can be used to create two H-bridges, which can be controlled separately. This is needed since we wish to control two electric engines.

An assembled H-bridge is shown in figure 1.26a.

### Second H-bridge driver design and production

After the second H-bridge driver had been prototyped on a veroboard, I designed a PCB containing the same circuit. The PCB consists of an ATxmega32A4U MCU, a voltage multiplier, two H-bridge drive-stages and connectors.

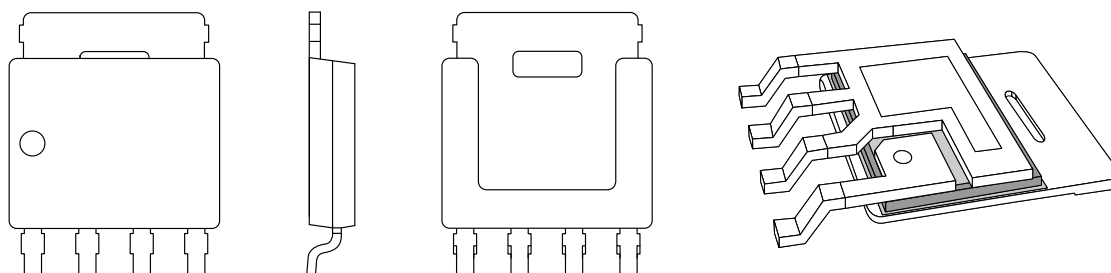


Figure 1.24: Illustrations of the LPAK package. From the left: Front view, side view, back view, internal view.

The ATxmega32A4U is a smaller and cheaper version of the previously used ATxmega128A1, and the same code can run on both (although they have different peripherals available). The bare PCB for the H-bridge driver is shown in figure 1.28, and an assembled board is shown in figure 1.26b.

### 1.3.3 Light version of the final H-bridge construction

Since the second motor control system worked very well, but used a lot of space, a light version was constructed as well. This can be useful for operating smaller motors. This unit incorporates two H-bridges with two drivers on one PCB, measuring only  $5 \times 10$  cm. It is capable of driving loads of up to 8 A. The PCB is shown in figure 1.30.

There was an error with the pinout of some of the transistors, so the board is currently not functional.

## 1.4 Testing and results

The complete motor control system, using the prototype of the H-bridge driver, has been tested at Lysaker, Norway and on lake Římov, Czech Republic. A temperature test has also been performed.

### 1.4.1 Test at Lysaker

A complete motor control system (shown in figure 1.23b), with two of the second version H-bridges and the prototype of the H-bridge driver, was tested May 2th, 2013 at Lysaker, Norway. The control unit (assembled motor control system) was placed in an 8 feet boat, together with two electric outboard engines and a lead-acid battery. The boat is shown in figure 1.31 and 1.32b, and the control unit in figure 1.32a. The engine's steering handles were mounted in a fixed position using wooden bars, to keep them from rotating.

Both steering mechanisms were tested. The most primitive steering mechanism (with each stick individually controlling one motor) was very difficult to use. The combination steering mechanism, however, (with one stick controlling the thrust and the other controlling the direction) was surprisingly easy to master. The primitive steering mechanism was thus abandoned in favor of the combination steering.

After testing the system with a person on board, the boat was released without a driver, although with a fishing line for security. The boat was successfully driven in multiple patterns by an operator at shore, and showed great maneuverability. The speed of the boat was also rapidly changed from stand-still to full-speed, and halted again, without damaging the control unit.

<sup>12</sup>Fish Ecology Unit of the Department of Fish and Zooplankton Ecology of the IH BC CAS. <http://www.fishecu.cz/>

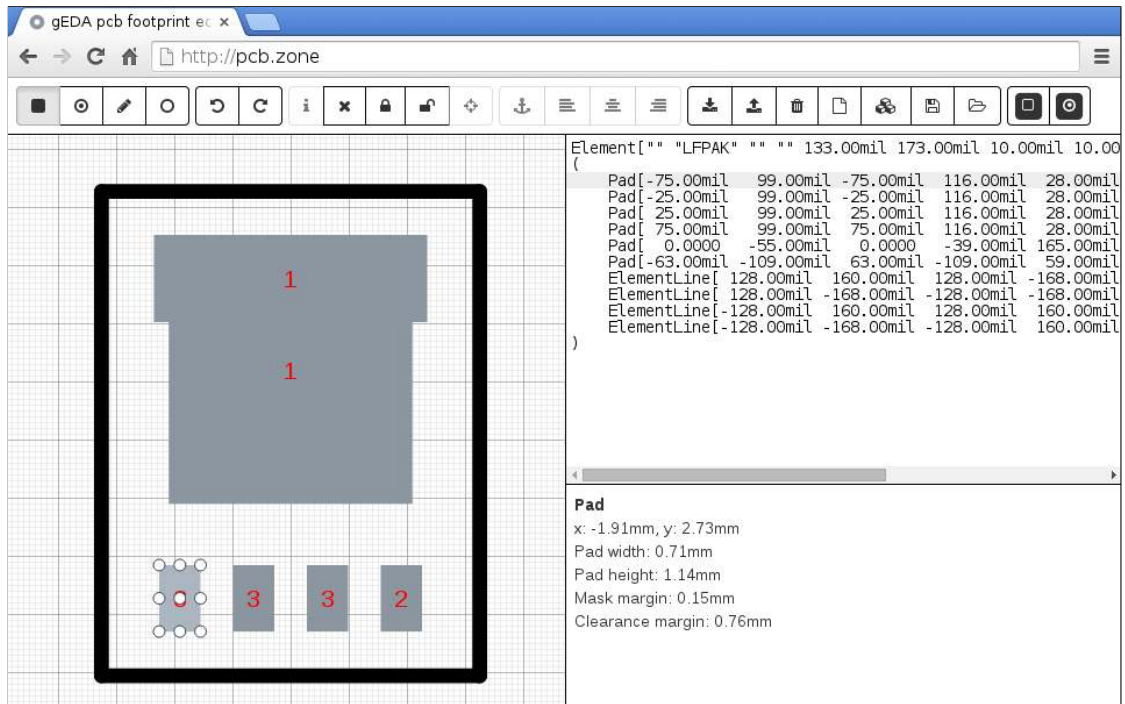
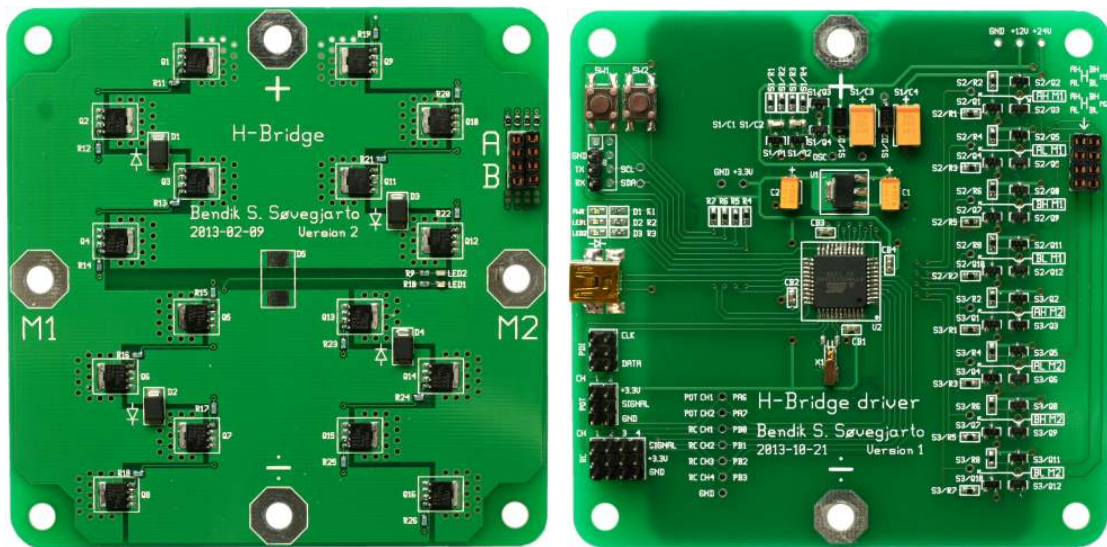


Figure 1.25: Universal Power-SO8 footprint, although especially made for LFPAK. Displayed using my self-developed footprint editor. Code available at <https://github.com/epsiro/pcb-footprint-editor>. Demo available at <http://pcb.zone> in 2015.



(a) Assembled PCB of second H-bridge.

(b) Assembled PCB of second H-bridge driver.

Figure 1.26: Photographs of assembled PCBs for the second H-bridge and H-bridge driver.



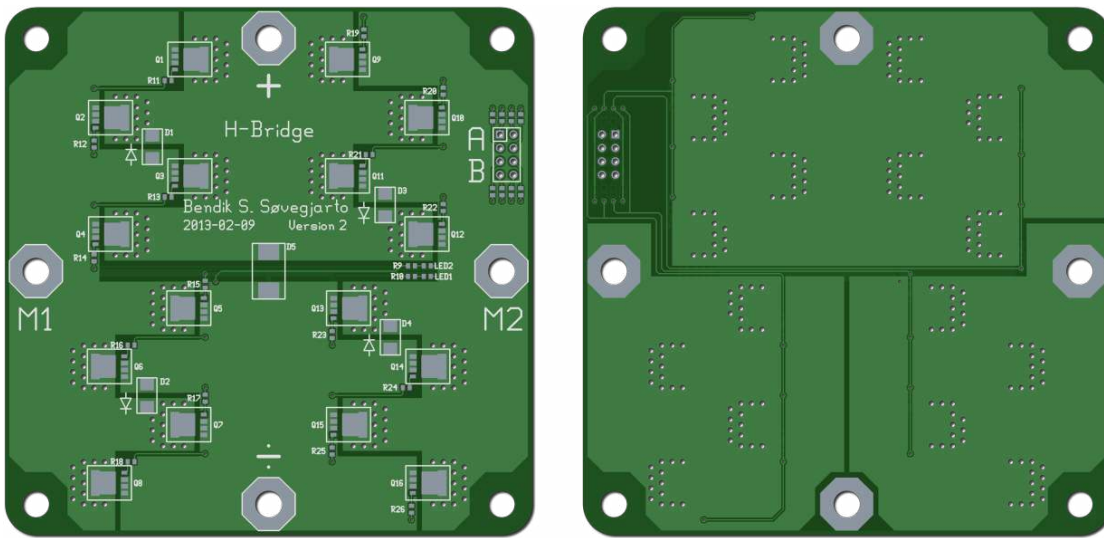


Figure 1.27: CGI of the PCB for the second H-bridge. Left image shows the top side, right image shows the bottom.

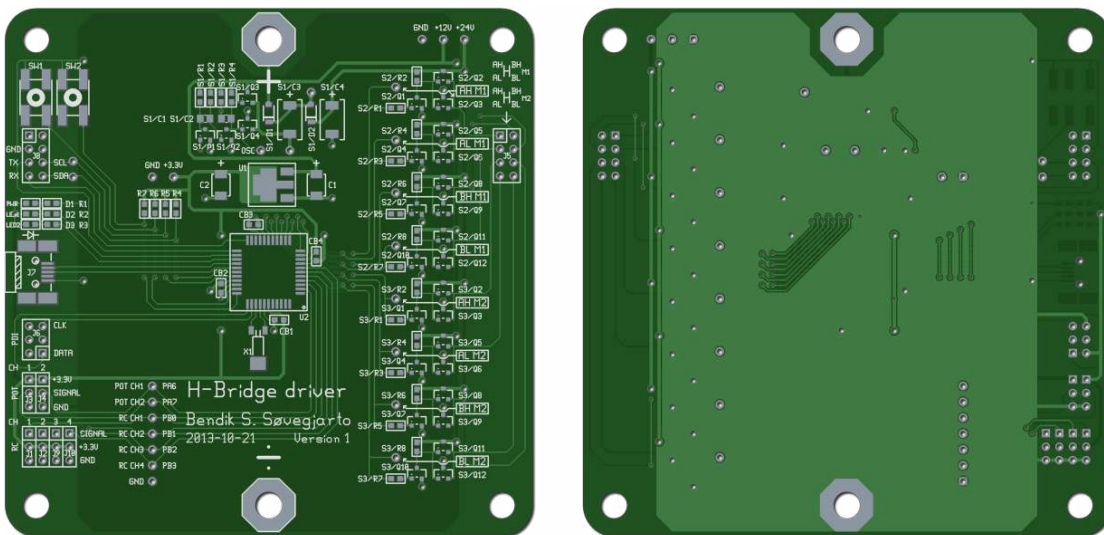


Figure 1.28: CGI of the PCB for the H-bridge driver. Left image shows the top side, right image shows the bottom.

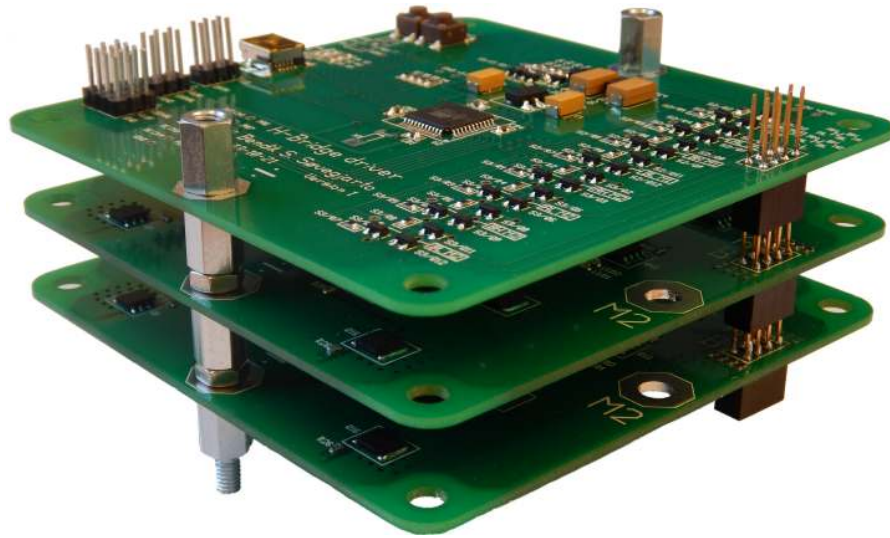
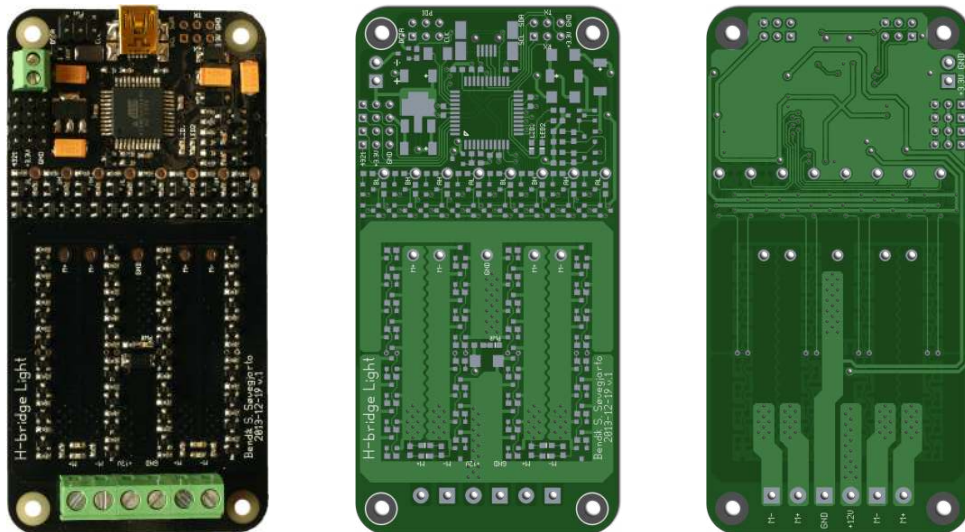


Figure 1.29: Photograph of finished motor control system stack (control unit).



(a) Photograph of assembled H-bridge light PCB. (b) CGI of H-bridge light PCB top side. (c) CGI of H-bridge light PCB bottom side.

Figure 1.30: Assembled and CGI of H-bridge light PCB.



Figure 1.31: Photograph of boat, with electric engines and control unit, tested at Lysaker in May 2013.



(a) The control unit mounted in the boat.

(b) The boat during deployment, with dr. Helge Balk as scale.

Figure 1.32: Detailed image and image to scale, of the boat used at the Lysaker test.



Figure 1.33: Photograph of aluminium boat used on lake Římov.



(a) Photograph of boat with a Simrad EK60 GPT.



(b) Simrad EK60 GPT directly connected to a Ubiquity PicoStation M2 (through a POE-injector).

Figure 1.34: Simrad EK60 GPT with boat and PicoStation.

## 1.4.2 Test on Římov

The same control system as tested at Lysaker was tested on lake Římov in August 2013. A suitable vessel was provided, along with two (different) electric outboard engines, by FishEcU<sup>12</sup>. The vessel was a 12 feet long aluminium boat, shown in figure 1.33.

The boat was successfully driven in different patterns, up to about 100 meters from the operator. It was also tested at full speed, and used for longer periods of time (hours).

Some of the journeys were conducted with a Simrad EK60 400kHz General Purpose Transceiver (GPT), as shown in figure 1.34a. The data from the echosounder was transferred wireless to the shore using a pair of Ubiquity PicoStation M2. To view the echogram live we first attempted to connect by remote desktop to a computer located in the boat. The result was not usable, as the refresh rate was very low. I therefore configured the radio link and the GPT to forward the User Datagram Protocol (UDP) packages from the GPT directly through the link and to a computer located at the shore. The setup with the echosounder and the link is shown in figure 1.34b. This worked very well, and gave the same usability as sitting in the boat. The data from these test surveys were viewed and recorded to the computer at the shore. We now had a working remote controlled survey vessel, as shown in figure 1.35.

I wrote a C-application to parse the recordings, along with a web-based (JavaScript/HTML/CSS) echogram viewer<sup>13</sup>. The parser was later ported to JavaScript as well. One of the recordings taken with the remote controlled survey vessel is shown in figure 1.36.

The boat was also remotely controlled to move a hydroacoustic standard target at a fixed depth along a straight path. This was done to inspect oscillating phenomena occurring during horizontal beaming. This worked successfully, although the larger boat was more challenging to maneuver than the boat used at Lysaker.

The tests conducted on lake Římov showed that the control system could successfully be used as a remote work platform, carrying dif-

ferent payloads. It was also capable of working with the hardware available at hand.

## 1.4.3 Temperature test

A temperature test was performed to find out which parts of the H-bridge would get hot under high loads. The Biltema electric outboard engine was mounted in a big bucket with water as shown in figure 1.39a. An H-bridge and H-bridge driver was connected and the RC-transmitter was used to control the thrust. A self-made power resistor and a Hewlett-Packard (HP) 3455A voltmeter was used to measure the current, as well as a Kyoritsu 2300R fork current tester, as shown in figure 1.38. The power resistor was made using multiple turns of  $2.3 \frac{\Omega}{m}$  resistance wire. Its resistance was measured to be  $14.3 m\Omega$  using an Agilent 4263B LCR meter. A Fluke Ti25 infrared (IR) camera was used to monitor the temperature.

The RC-transmitter was used to keep the thrust about  $\sim 90\%$ , which corresponded to a current draw of  $\sim 20 A$ .  $90\%$  was chosen because this yields switching losses as well as a high «static» loss. The battery could unfortunately not keep up the current of  $20 A$  for longer than five minutes. The figure 1.37a shows the current over the course of these five minutes, while figure 1.40 shows the temperature after one minute and after three and a half minutes.

The battery had enough energy for a new test with the motor running at a lower speed. The cable was securely reattached to the screw terminal, and the motor was run for another three minutes. The duty-cycle was adjusted with the RC-transmitter to keep a current draw of  $5 A$ . The figure 1.37b shows the current over the course of these three minutes, while figure 1.41 shows thermal images of the whole H-bridge and a close up of one leg.

The results of the temperature test is discussed in section 1.5.3.

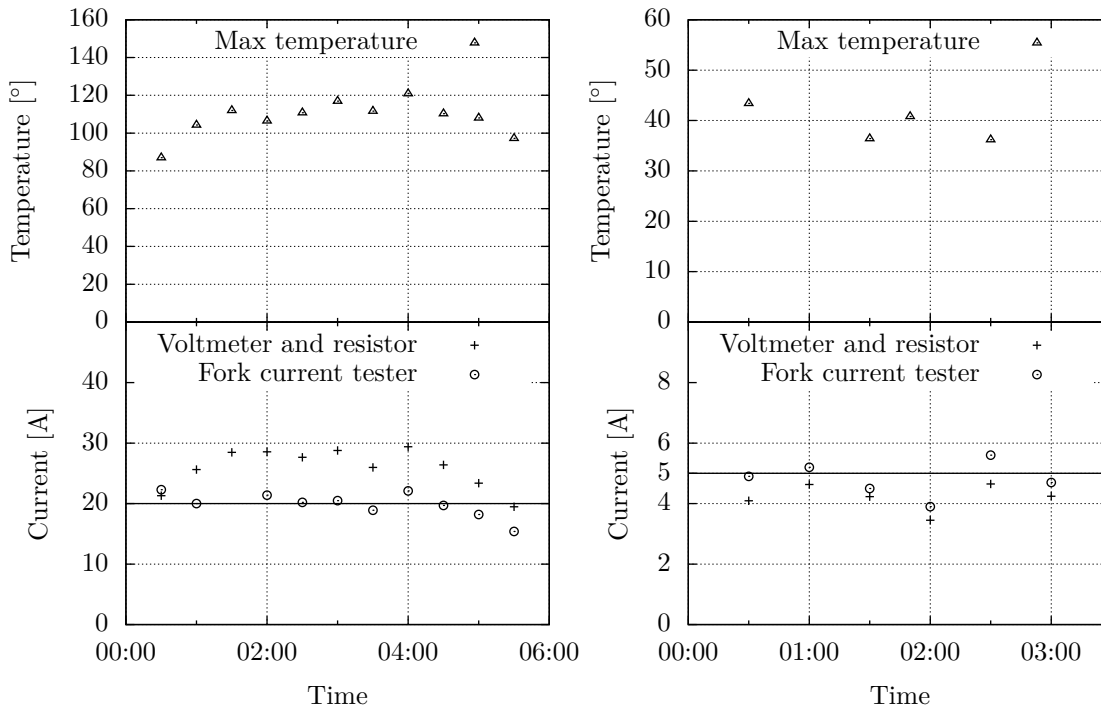
<sup>13</sup>Demo available at <http://opensonar.net> in 2015.



Figure 1.35: Remote controlled survey vessel supervised by operators at the shore.



Figure 1.36: Recording from one of the test surveys conducted on lake Římov. Noise is visible as horizontal lines.



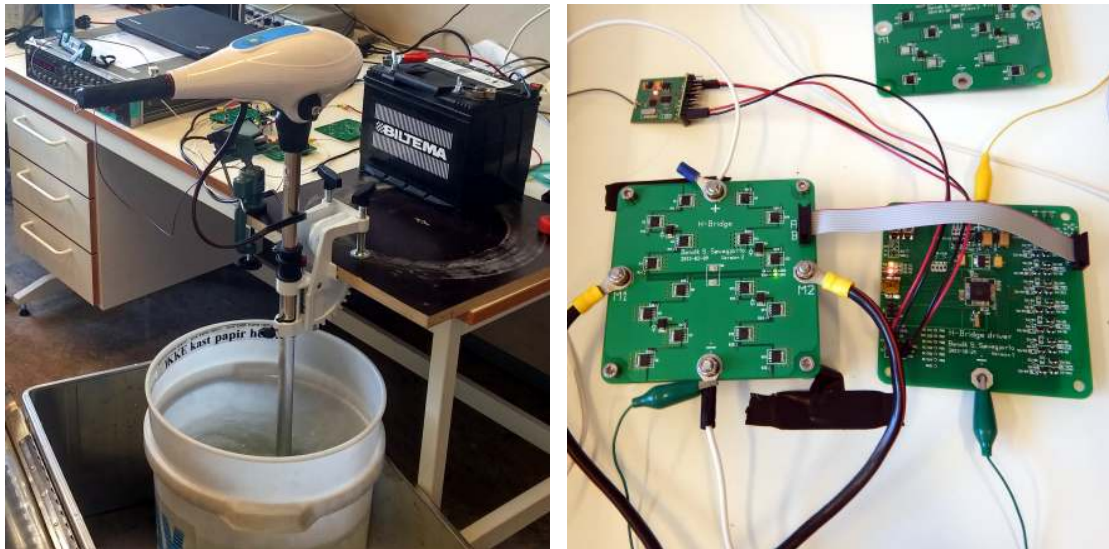
(a) Current and maximum temperature during the 20 A temperature test. We attempted to keep the current at a constant 20 A. The max temperature was caused by the screw terminal, and does not give any information about the performance of the H-bridge.

(b) Current during the 5 A temperature test. We attempted to keep the current at a constant 5 A. The max temperature was caused by the series resistor for one of the indicator LEDs.

Figure 1.37: Current measured during the temperature test.



Figure 1.38: Current measurement setup for the temperature test. The power resistor is visible to the right.



(a) Electric outboard engine in a bucket of water, used as the load.

(b) H-bridge and H-bridge driver.

Figure 1.39: Temperature test setup.

## 1.5 Discussion

The following paragraphs discuss design choices, test results, problems encountered and possible enhancements for the development and use of the final motor control system. Potential future work is presented in the last sections.

### 1.5.1 Motor control system reliability

The motor control system has been successfully used in Lysaker and on lake Římov. Not a single second version H-bridge has died on its own over the course of the two years it has existed. Although multiple H-bridge PCBs have become defective as a result of a user error. E.g. short circuit by a foreign object or shoot-through during flashing (reprogramming) of the H-bridge controller MCU.

### 1.5.2 Motor switching noise

Testing conducted on lake Římov with the motor control system and an echo sounder showed that the switching of the motor current

created a lot of noise (visible in figure 1.36). To minimize this noise multiple measures can be taken; *a)* the motor cables should be kept away from the transducer cable; *b)* separate batteries should be used for the echosounder and the motor control system; *c)* the switching frequency can be changed to not be a harmonic of the echosounder frequency; *d)* the motor cables can be shielded; *e)* ferrite chokes can be added to the motor cables; or *f)* the whole motor control system can be placed further away from the echosounder. The motor cables were lying across the transducer cable during the recording on lake Římov.

### 1.5.3 Discussion of temperature test results

The resistance of the power resistor used to measure the current may have changed, as the high current running through it may have altered its temperature. We therefore trust the fork current tester instead, for the high current (20 A) test.

A bad connection between the cable and the cable crimp at the top screw terminal led to



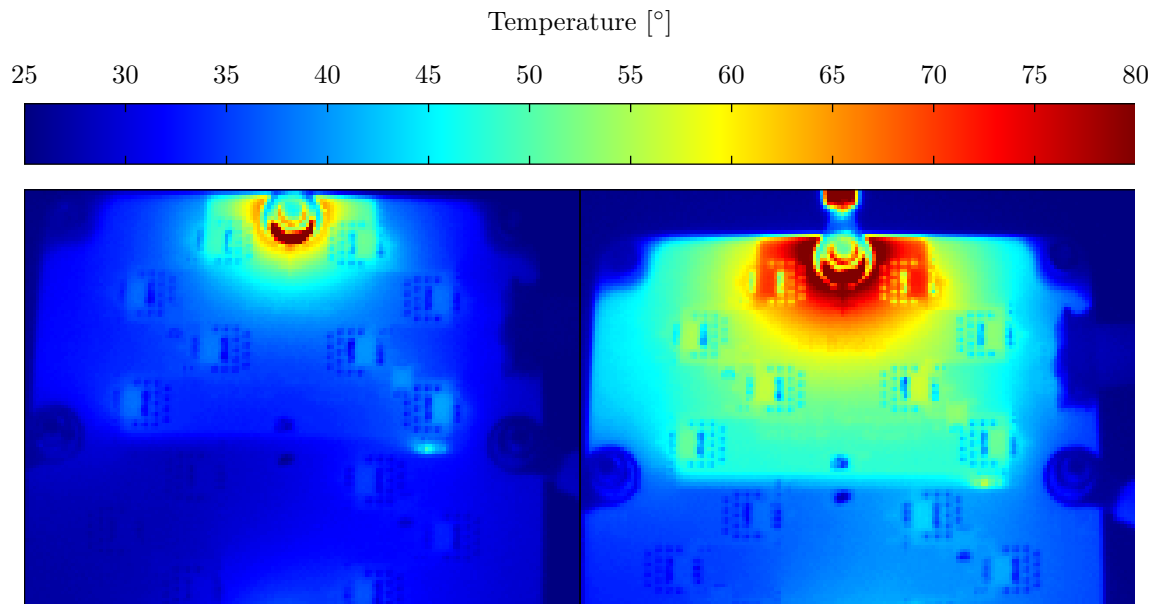


Figure 1.40: IR thermography of the 20 A temperature test. The left image shows the temperature after one minute, while the right image shows the temperature after three and a half minutes. A loose connection resulted in high thermal activity at one of the screw terminals.

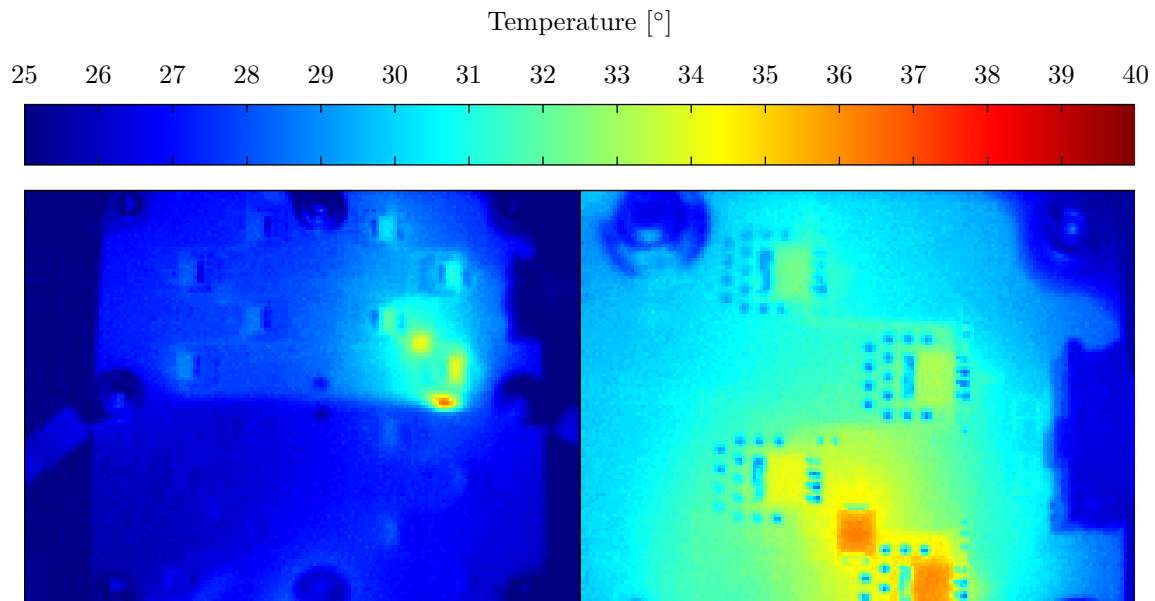


Figure 1.41: IR thermography of the 5 A temperature test. The left image shows the temperature of the whole H-bridge (after one minute), while the right image shows a close-up of the upper right corner (after two minutes). A series resistor for one of the indicator LEDs and a flyback-diode shows thermal activity.

high resistance and high thermal activity for the 20 A temperature test. This obscured the temperature monitoring of the rest of the H-bridge, but showed that parts of the PCB has good heat distribution and that the thermal vias are working. The heat distribution can be seen in figure 1.40, where the temperature difference is small over a large area, even outside the top copper plane. Figure 1.27 shows the location of the copper planes.

Thermal images from the 5 A test shown in figure 1.41 reveals multiple interesting points. The series resistor for one of the indicator LEDs is the hottest component on the PCB. This is caused by the high voltage drop over the resistor ( $560\ \Omega$ ), and the relative high current consumption (18 mA) of the LED (governed by the resistor). The result being a higher power dissipation (180 mW) than the small 0603 resistor (100 mW) can handle. The resistance should be increased to decrease the current through the LED and resistor. Alternately, a physically bigger resistor must be used, e.g. 1206 (250 mW).

The upper left and lower right leg (corner) is used to conduct the drive current to the motor (in the forward direction). We expected the transistors in these legs to be the hottest components in the H-bridge. They are, however, only slightly hotter than the surrounding PCB.

The upper right leg gets hotter than the other legs of the H-bridge, and the flyback diode in this corner gets hotter than the transistors. The flyback diode in this leg conducts the back-EMF from the motor back to the power supply (capacitor and battery), when the drive current is removed. The constant voltage drop over the diode combined with this current results in a significant power dissipation. We believe the heat originating from the diode (and resistor) increases the ambient temperature around the transistors in the upper right corner, making them appear hot as well. Switching the current direction (through the H-bridge) should cause the upper left corner to become the hottest, and this could be investigated in a new test. Decreasing the dead-time may help offload the diode even more, and reduce its heat dissipation.

The motor cables were switched, so we drove the motor backward (when we thought we drove it forward). Driving it forward will result in a higher current consumption.

#### 1.5.4 Stress test

The temperature test was originally planned to be a stress test, running the H-bridge until something broke or it reached a steady-state with constant temperature. This was unfortunately not possible, as the battery ran out, and there was not enough time for a new attempt. I also planned to perform multiple tests, with different H-bridges. Some with a different number of transistors, and some with a different type. For instance just using four transistors, or using the higher drain-source voltage version PSMN7R0-60YS, which have a higher  $R_{ds_{on}}$ . A new stress test, using an ample power source, could be performed by others if the maximum ratings of the H-bridge is needed.

#### 1.5.5 Power dissipation

We have not yet measured and calculated the actual power dissipation of the assembled (final) motor control system. This was not deemed important, as the system functioned well during field use and managed to drive the engines (and boat) we required. If needed, the static power loss can be found by measuring the  $R_{ds_{on}}$  or the voltage drop over the transistors and using equation (1.1) or (1.3) (with a constant load/current and switching disabled). Similar, the power loss during the switching transitions can be estimated by measuring the switching-transition time ( $\Delta t$ ) and using equation (1.11).

#### 1.5.6 Cable connections and -thickness

One of the nuts used to fasten the motor cable (through a bolt) to the H-bridge loosened during one of the tests on lake Římov. This resulted in the bolt getting very hot. This was later discovered as the bolt had permanently changed its color. Lock washers have since been used to prevent nuts from loosening. A bad connection

was also encountered while conducting the temperature test. It is important to securely attach any wires used for conducting the large currents.

Appropriate wire thickness should also be used in a production setting. Some of the wires used when testing the H-bridge (and shown in some of the pictures) were relative thin. Connecting multiple wires in parallel was considered, but no noticeable temperature rise was found when using only one.

### 1.5.7 Power source

The Biltema engines states an operating time of 80 – 120 minutes at full speed and up to 4 hours at half speed, when using a standard 75 Ah lead-acid battery (for each engine). Multiple high capacity batteries may be connected in parallel to increase the operating time. A battery bank may not be feasible if very long surveys needs to be conducted. A diesel generator can then be used to supply power to electric engines.

### 1.5.8 Abandonment of HIP4081

The problems we experienced using the HIP4081 could perhaps be fixed with a RC-snubber suppressing the transients occurring at the load. By developing our own H-bridge controller and -driver, however, we were left with total control over the whole system and we had no problems with transients whatsoever.

### 1.5.9 Jump start problems

Earlier, we experienced a short jump start of both motors when the RC-transmitter was turned on after the motor control system had been connected to power. In other words, both engines tried to run at full speed for a fraction of a second the moment power was connected to the motor control system. We were therefore cautious to always turn the RC-transmitter on before power was applied to the motor control system. We suspected that the cause of this behaviour was that the MCU had its pins configured as inputs or low outputs during power-up. Pull-up resistors as described

in the section 1.5.11 were thought to be a possible solution. In the end, we discovered that it was the RC-receiver which outputted invalid commands (pulse-widths) during power-up. By skipping the first RC commands (as described in section 1.3.2), the jump start problems disappeared. The motor control system can now be turned on, without problems, regardless of whether the RC-transmitter is turned on or off.

### 1.5.10 TVS diode

We did test the use of a TVS diode (SMCJ12CA) on the second H-bridge, but it did not seem to make any difference. This was however not examined in detail, and we did not use TVS-diodes further, since we did not have any problems with transients anymore (when using our H-bridge driver).

### 1.5.11 Drive-stage pull-up resistors

Pull-up resistors should be added on the control lines from the MCU to the drive-stage. Since these resistors are currently missing, the MCU must have its IO pins configured as outputs at all times. If they are configured as inputs, the control line will be left floating and could be registered as a low signal by the drive-stage. This would then turn «on» one of the transistors in the H-bridge, and possibly create a shoot-through condition. Pins are fortunately always configured as outputs (as long as the firmware is not modified), but pull-up resistors should be added nonetheless as a safety feature.

### 1.5.12 Number of power transistors

We initially decided to use four power transistors in each leg of the second H-bridge to distribute the heat and the electric current. One power transistor alone is, however, rated high enough to withstand the current drawn by our electric engines. It could therefore be possible to reduce the cost of the system, by reducing the number

of power transistors. A stress test (as described earlier) may dictate if this is feasible.

### 1.5.13 PCB heat and current distribution enhancements

We only used a 2-layer standard PCB with a copper thickness of  $35\mu\text{m}$  for the H-bridge PCBs. If higher heat and current distribution is needed, the copper thickness can be increased and/or more layers can be used.

### 1.5.14 Firmware reorganization

The firmware for the second steering- and H-bridge controller (listed in appendix G) is currently doing all its calculation in interrupt service routines. The time used handling a interrupt should be kept minimal, as not to block other interrupts from being handled. The priority of the interrupts are programmable, so the interrupt with the long interrupt handler should at least have a low priority. An improvement could be to set a flag when a RC or UART command is received, and let the main program do the calculations currently performed in the timer interrupt. The timer could instead be used to wake the MCU from sleep, if lower power consumption is wanted.

### 1.5.15 UART communication

The UART steering command protocol does not include any error-detection system, since losing some commands are not critical. When connecting or disconnecting another control system (e.g. an autopilot) over UART I have experienced that some steering commands (bytes) have been misread. A simple 1-bit Cyclic Redundancy Check (CRC) parity bit or a new protocol similar to NMEA 0183 (with checksum) can be implemented to combat these problems.

## 1.6 Summary

A functional motor control system has been developed, consisting of a RC-receiver (communication), a steering controller, two H-bridge controllers with drive-stages and two H-bridges. It is designed to drive two arbitrarily selected 12 V brushed DC electric motors at up to 40 A, although the max current rating has not been tested. The motor control system can also be used as a foundation for an autopilot. The use of a digital steering controlled proved to work better and give greater capabilities than an all analog version. We experienced problems when using a pre-made H-bridge driver, but a self-developed H-bridge controller and -driver worked successfully. A temperature test indicates that the power MOSFETs do not get very hot during normal use. Testing of the control unit (assembled motor control system), on two different boats with two different sets of engines, has shown that the motor control system is capable of controlling the electric engines we need. It has also shown that it is possible to use the equipment available, and that the complete cost of the system is low. The steering mechanism of combining both sticks provided very good control of the vessel, with a small boat (8 feet) giving especially high maneuverability. Noise problems caused by the motor switching were encountered during fieldwork, and these must be addressed before the motor control system can be truly usable for hydroacoustic work. Nevertheless, testing of the control unit together with a Simrad EK60 echosounder and a set of Ubiquity PicoStations show that the vessel can be used as a remotely controlled work platform.

## Paper 2

# Construction of an Attitude and Heading Reference System (AHRS)

### Abstract

This paper presents the construction of an Attitude and Heading Reference System (AHRS) useful for hydroacoustic work. The AHRS outputs calculated tilt, roll and heading measurements based on sensor fusion from a 3-axis accelerometer, a 3-axis gyroscope and a 3-axis magnetometer. These measurements can be used to stabilize hydroacoustic recordings, or as a part of the autopilot for an Automatic Survey Vessel (ASV). Three attempts were done to create a functional AHRS, and both hard- and software for each of these will be described. The final attempt resulted in a unit which fulfilled our requirements of accuracy and reliability.

**Keywords:** AHRS, IMU

## 2.1 Introduction

This paper presents the construction of an Attitude and Heading Reference System (AHRS). The AHRS was needed for the following three tasks.

As part of the autopilot in my thesis covering the development of a «full-size low price Automatic Survey Vessel (ASV)» I need a compass to steer the vessel. A compass measures the magnetic field of the Earth, and outputs its heading relative to the magnetic poles. We wanted to create an AHRS which could function as a tilt-compensated<sup>1</sup> compass.

Horizontal hydroacoustic surveying is very susceptible to angular movement, since a small displacement at the face of the transducer results in a large shift far from the transducer. This movement can be caused by surface waves or human activity in the vessel conducting the survey. To compensate for this displacement, the angular movement can be recorded and a stabilization algorithm can be applied in post-process. This works similar to the optical image stabilization currently available in various consumer cameras. We wanted to create an AHRS to record the angular movement, and do so with a (tilt) accuracy of at least  $\pm 0.5^\circ$ . This corresponds to a displacement of about  $\pm 25$  cm for a target (e.g a fish) located 30 meter away from the transducer.

<sup>1</sup>A tilt-compensated compass does not need to be oriented parallel to the magnetic field for it to calculate the correct heading.

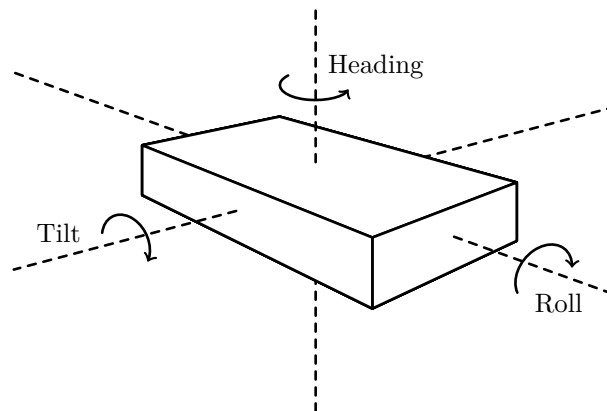


Figure 2.1: Illustration of tilt, roll and heading angles.

For some hydroacoustic experiments it is crucial to know the true tilt of the transducer. For instance when investigating surface-interaction during horizontal beaming. We wanted to create an AHRS which could function as an inclinometer, providing a reliable and accurate static orientation of hydroacoustic equipment.

We will describe how we solved these challenges by combining data from a 3-axis magnetometer, a 3-axis accelerometer and a 3-axis gyroscope.

Tilt, roll and heading angles are shown in figure 2.1.

A fellow master student, Johan Kleiberg Jensen, was at the time I needed a compass, working on his master thesis «Attitude Estimation for Motion Stabilization in Sonar Systems» [15]. He used a sensor with an embedded magnetometer. Unfortunately, he did not manage to read the magnetometer-data from the sensor, so I continued his work. Not only to get a compass for my own autopilot, but also to create an improved unit for experiments planned at lake Římov (České Budějovice, Czech Republic) the summer of 2013, where information about accurate tilt was essential.

The sensor Jensen found, contained three microelectromechanical systems (MEMS), a 3-axis accelerometer, a 3-axis gyroscope and a 3-axis magnetometer, all in one package. It is produced by InvenSense and called MPU-9150

(cost; ~8 USD each at a quantity of 100). The sensor is actually a combination of two chips, a MPU-6050 accelerometer and gyroscope and an AK8975 magnetometer. The gyroscope does not measure angular displacement, but angular velocity. The term gyroscope is thus technically not correct, and the term angular rate sensor would be more fitting. Since the term gyroscope is widely used when referring to an angular rate sensor, we will still use this term throughout the paper. Some specifications for MPU-9150 is listed in table 2.1. Current consumption varies depending on features enabled, sample rates, and sleep modes. More details about the MEMS can be found in [15–17].

Three attempts on creating a functional AHRS have been made. These are described in section 2.3. I have not developed any new algorithms for the sensor fusion, but merely adopted the work of others to create a device that fulfilled our requirements. Our main criteria was to get a unit which is *a)* low cost; *b)* sufficiently accurate; *c)* watertight; *d)* has good repeatability; and *e)* can calculate the heading. Jensen’s unit did not fulfill criteria *e)* and *d)* (with our clone of his unit).

x-IMU from x-io Technologies<sup>2</sup> is an inexpensive AHRS platform, which could have been used instead. We chose to make our own because we wanted to continue the work of

<sup>2</sup><http://www.x-io.co.uk/products/x-imu/>

Table 2.1: Key specifications for the MPU-9150.

Parameter	Specification
Gyroscope full-scale range	$\pm 250^\circ/\text{s}$ , $\pm 500^\circ/\text{s}$ , $\pm 1000^\circ/\text{s}$ , $\pm 2000^\circ/\text{s}$
Gyroscope sensitivity	$131 \frac{\text{LSB}}{^\circ/\text{s}}$ at $\pm 250^\circ/\text{s}$
Gyroscope total RMS noise	$0.06^\circ/\text{s}$
Accelerometer full-scale range	$\pm 2 \text{ g}$ , $\pm 4 \text{ g}$ , $\pm 8 \text{ g}$ , $\pm 16 \text{ g}$
Accelerometer sensitivity	$16384 \frac{\text{LSB}}{\text{g}}$ at $\pm 2 \text{ g}$
Accelerometer total RMS noise	$4 \text{ mg}$
Operating temperature range	$-40^\circ\text{C} - 85^\circ\text{C}$
Operating voltage range	$2.375 \text{ V} - 3.465 \text{ V}$

Jensen and create a custom solution using a single integrated circuit (IC).

The AHRSs developed in this paper are named HydroAHRS, although they were initially known as «IMU» (Inertial Measurement Unit) or anglemeter.

## 2.2 Theory

### 2.2.1 Angle representation

There are multiple ways of representing spatial orientation, and Euler angles, quaternions and rotation matrices are widely used for this purpose. They are just briefly mentioned in the following paragraphs. An in-depth presentation can be found in [18].

Euler angles describes a spatial orientation using three parameters, each representing an elemental rotation about one of the axes in a 3-dimensional Euclidean space. Euler angles suffers from singularities (gimbal lock) around its poles.

Quaternions is an extension of complex numbers, making non-singular representation of spatial orientation and rotation possible. It is in other words capable of representing all rotations and angles, even around the poles.

Rotation matrices is another way to represent spatial orientation relative to a reference-axis set. They can avoid the singularities around the poles, but is more computational expensive than quaternions as they contain more parameters.

## 2.3 Material and methods

In order to find the orientation in space, data from all three sensors must be combined. The gyroscope is used to measure the angular velocity and find the angular displacement, while the accelerometer and magnetometer provides a reference. Angles calculated by simply summing the angular displacement (gyroscope), would suffer from a large drift over time. This method can also just calculate relative angles. To suppress the drift and achieve absolute angle measurements (referenced to the Earth), the accelerometer and magnetometer is needed.

Jensen developed a complimentary filter to combine the accelerometer and gyroscope data. This sensor fusion calculated the tilt and roll angles. I wanted to calculate the heading as well.

The first attempt on my own AHRS was to port Jensen's complimentary filter to an 8-bit microcontroller unit (MCU), instead of running it on top of a full operating system (OS). This port is described in section 2.3.1. The port was successful, but failed to fulfill our requirements of good repeatability. This was experienced during fieldwork at lake Římov, described in section 2.4.1.

I then gave it a second attempt using firmware given by the manufacturer of the sensor (InvenSense). This firmware enables the sensor itself to do the accelerometer and gyroscope fusion in its embedded Digital Motion Processor (DMP). Fusion of magnetometer data was based

on work done by a company called Pansenti<sup>3</sup>. This implementation is described in section 2.3.2. The result was good tilt and roll measurements, however the heading calculation was unreliable (drifting and incorrect).

While struggling with the magnetometer fusion, InvenSense released a motion processing library which promised to do fusion of all its sensors. Accelerometer and gyroscope fusion was still done in the DMP, while the magnetometer data fusion was done in a pre-compiled library running on a MCU. Since I needed a fully functional compass for the Automatic Survey Vessel (ASV), I restarted the project yet again. Now based on both new hard- and software. The new software is described in section 2.3.3. The new hardware incorporated a 32-bit ARM Cortex-M MCU, which could handle the sensor fusion at the maximal rate of the sensor, 200 Hz.

The HydroAHRS outputs its calculated angles in the same (NMEA 0183 similar) format as originally used by Jensen [15]. The format is shown in figure 2.2 and table 2.2. The data format theoretically allows 71 bytes (maximum NMEA 0183 sentence length) to be output up to 200 Hz using 115200 baud over UART. A binary format, however, should be used if high throughput is needed, to reduce the amount of data transferred. For instance the binary protocol created by InvenSense to communicate with its OpenGL cube visualizer.

As the development of the firmware progressed, several hardware revisions were needed. The enclosures were also changed, to compromise between ease of assembly and water resistance. I will first present the development of the firmware, and afterwards the development of the hardware. Calibration and client side software will be described at the end.

### 2.3.1 First software attempt – Complimentary filter

The AHRS was originally based on the work of Jensen [15]. Jensen used a Raspberry Pi single board computer (SBC) running GNU/Linux to

talk to the sensor and run his complimentary filter. I felt the use of a SBC with a complex OS was overkill for such a relatively easy task. I wanted to create an alternative implementation that was *a)* physically smaller; *b)* easier to make watertight; *c)* more power efficient; *d)* even more low cost; and *e)* easier to build multiple units of. I therefore decided to port his code so it could run on a 8-bit MCU. I chose an ATxmega AU-series MCU from Atmel, because it had all the peripherals I needed and was easy to work with. Atmel's toolchain is based on the GNU Compiler Collection (GCC) toolchain, which is very mature and well supported on multiple platform, including GNU/Linux<sup>4</sup>.

His sensor fusion was built around a complimentary filter and showed promising results in testing [15]. I cleaned and restructured his single code file into appropriate functions over multiple files. I also wrote support code for Inter-Integrated Circuit (I<sup>2</sup>C) and UART communication. I used a Bus Pirate<sup>5</sup> to debug the I<sup>2</sup>C communication. Jensen's code is listed in [15, p. 99]. My code is shown in appendix J, licensed under GNU General Public License (GPL) version 2 or later.

I assembled my own prototype version to develop on, as described in section 2.3.4. I also built my own setup with an Raspberry Pi for reference, due to problems occurring while porting the code.

The AHRS is controlled by single character commands over a serial link (115200 baud), as listed in table 2.3.

One challenge I faced was the use of floating point numbers. I consider changing all calculations to use integer numbers, or take advantage of the implementation of fixed point math in version 4.8 of `avr-gcc`. Eventually, testing showed that the MCU could handle the floating point calculation at the low sample rate of 20 Hz.

I use a real-time clock (RTC) crystal of 32.768 kHz to calibrate the internal radio control (RC) oscillator and to timestamp samples with low drift.

<sup>3</sup><https://www.linkedin.com/company/pansenti-llc>

<sup>4</sup>clarification; to develop on – I run Debian GNU/Linux on my workstation

<sup>5</sup>Bus Pirate: [http://dangerousprototypes.com/docs/Bus\\_Pirate](http://dangerousprototypes.com/docs/Bus_Pirate)



```

      1      2      3 4      5      6      7 8 9 10 11 12
      |      |      | |      |      |      | | | | |
$PASHR, hhmss.sss, hhh.hh, M, rrr.rr, ppp.pp, xxx.xx, a.a, b.b, c.c, d, e*hh<CR><LF>

```

Figure 2.2: HydroAHRS data format.

Table 2.2: HydroAHRS data format field description.

Field	Placeholder	Description
1	hhmss.sss	Time (relative)
2	hhh.hh	Heading [degrees]
3	M	Flag indicating true or magnetic north heading
4	rrr.rr	Roll angle [degrees]
5	ttt.tt	Tilt angle [degrees]
6	xxx.xx	Heave (not used)
7	a.a	Roll angle accuracy (not used)
8	b.b	Tilt angle accuracy (not used)
9	c.c	Heading angle accuracy (not used)
10	d	Aiding Status (not used)
11	e	Accuracy indicator
12	hh	Checksum

Table 2.3: Control commands for HydroAHRS mk.I.

Command (ASCII)	Command (hex)	Function
s	0x73	Start sending angles
S	0x53	Stop sending angles
g	0x67	Set gyro bias
a	0x61	Set angle reference
1	0x31	Send one measurement

### 2.3.2 Second software attempt – DMP

Since the complimentary filter did not give the level of response and repeatability that we needed (described in section 2.4.1) and because it was difficult to get a readout from the magnetometer embedded in the MPU-9150, I began looking for other sensor fusion solutions. InvenSense had released firmware which could be downloaded into MPU-9150 to enable its embedded Digital Motion Processor (DMP). The firmware is available for download at their website if you register as a developer<sup>6</sup>. A company called Pansenti had also developed a software library for Arduino and a library for Linux based platforms to work with the MPU-9150. The libraries included magnetometer data fusion. I chose to port the Linux based library to the ATxmega MCU I already used. The library was available at their GitHub<sup>7</sup> website in 2013, but has since been removed. The library was released under a MIT-license.

The code gets quaternions from the DMP and converts these into Euler angles before fusing them together with the magnetometer data. The magnetometer data is tilt-compensated and mixed together with the gyroscope heading to set the new heading angle.

After some struggling, I also managed to implement native Universal Serial Bus (USB) communication using the LUFA<sup>8</sup> library from Dean Camera (which had experimental support for ATxmega). This enabled me to use a Device Firmware Upgrade (DFU) bootloader to program (flash) the MCU over USB. The DFU bootloader is described in an application note from Atmel [19]. The HydroAHRS could now be uploaded with new firmware even after it had been casted in an enclosure.

The AHRS is controlled by single character commands, as listed in table 2.4.

The firmware for HydroAHRS mk.II is listed in appendix K.

<sup>6</sup><http://www.invensense.com/developers/>

<sup>7</sup><https://github.com/Pansenti/linux-mpu9150>

<sup>8</sup><http://www.fourwalledcubicle.com/LUFA.php>

### 2.3.3 Third software attempt – MPL and DMP

Due to the problems described in section 2.4.5 regarding the non-functional and drifting compass, and other problems occurring with the use of Euler angles, I was looking for yet a new solution. I then discovered that InvenSense had released a library during the summer of 2014, which promised 9-axis fusion. This library is called Motion Processing Library (MPL) and is available for download if you register as a developer. It is pre-compiled and the source code is not available. They had some example code for use with an ARM Cortex M4 MCU and recommended a 32-bit CPU if 200 Hz sensor integration where needed. This would however mean I had to redo the project from scratch a third time. I chose an ARM Cortex M4 MCU from Texas Instruments (TI) named TM4C129 (cost; ~15 USD each at a quantity of 100), and decided to try again with ARM. This MCU was selected because it has hardware Ethernet PHY and MAC-layer embedded. The USB communication previously used has a short maximum length of only 5 m [20]. Ethernet enables up to 100 meter range [21].

Communication through Ethernet with TM4C129 has not yet been implemented for HydroAHRS, but has been done for the TM4C129 used in the autopilot described in paper 3. Although an Ethernet bootloader has been adapted and used successfully to program the HydroAHRS. For this, a separate BOOTP- and TFTP-server running on a nearby GNU/Linux computer was applied.

I had hardware kits from Texas Instruments (TI) which incorporated a MCU from the same family as TM4C129, the TM4C123. The TM4C123 is a smaller and cheaper version of the TM4C129 (without Ethernet). The kit was named EK-TM4C123GXL. I also had a matching sensor hub from TI, called BOOSTXL-SENSHUB, which had various sensors embedded, including the MPU-9150. TI also had code to do 6-axis fusion using their own

Table 2.4: Control commands for HydroAHRS mk.II.

Command (ASCII)	Command (hex)	Function
s	0x73	Start sending angles
S	0x53	Stop sending angles
r	0x72	Reset timestamp counter
l	0x31	Send one measurement
m	0x6D	Calibrate magnetometer
a	0x61	Calibrate accelerometer
M	0x4D	Reset magnetometer calibration
A	0x41	Reset accelerometer calibration
b	0x62	Run bootloader
w	0x77	Write mag. or accel. calibration
e	0x65	Read calibration data from EEPROM
i	0x69	Read serial number from EEPROM

complimentary filter. I decided to combine these two projects (from TI and InvenSense) into a functional 9-axis AHRS running on the TM4C123.

Receiving commands and saving calibration data to non-volatile memory is currently not implemented for HydroAHRS mk.III.

The firmware is unfortunately not listed in the appendix because of licensing restrictions, but is available in-house at the Department of Physics, University of Oslo.

### 2.3.4 Hardware for HydroAHRS prototype

The port of Jensen’s work was initially developed on a prototype hardware platform. The prototype platform consisted of a veroboard with a breakout board for an ATxmega128A1 and a breakout board for the MPU-9150, shown in figure 2.3. Both were purchased from SparkFun<sup>9</sup>.

<sup>9</sup>ATxmega128A1 breakout: <https://www.sparkfun.com/products/9546>, MPU-9150 breakout: <https://www.sparkfun.com/products/11486>

<sup>10</sup>Box: Deltron 483-0020

<sup>11</sup>IP68 certifies that the enclosure is dust tight and watertight in immersion beyond one meter of water.

<sup>12</sup>Connector: Bulgin PX0843/B, cable: Bulgin PX0840/B/5M00

### 2.3.5 Hardware for HydroAHRS mk.I

When the port of Jensen’s code was functional I decided to design a printed circuit board (PCB) incorporating both the MCU and the sensor, along with necessary communication and support circuitry. The PCB consists of an 8-bit ATxmega32A4U, the MPU9150 sensor, status LEDs and additional components such as a crystal and various headers. The communication was done with UART from the MCU. Since I wanted to have USB connectivity for easy user handling, I used a FT232RL IC from FTDI as an UART-to-USB bridge. By doing so I was able to communicate and power the device using a single cable. It was also directly compatible with all new computers (which have USB). The FT232RL was also used as the power regulator. A standalone steel stencil was used to distribute solder paste for the Quad Flat No-leads (QFN)-package of the sensor. The PCB is shown in figure 2.4b and assembled in figure 2.4a.

The PCB was designed to fit into a water-tight case. I chose a casted aluminium case from Deltron<sup>10</sup> because it was the smallest case I could find that would fit the PCB and a

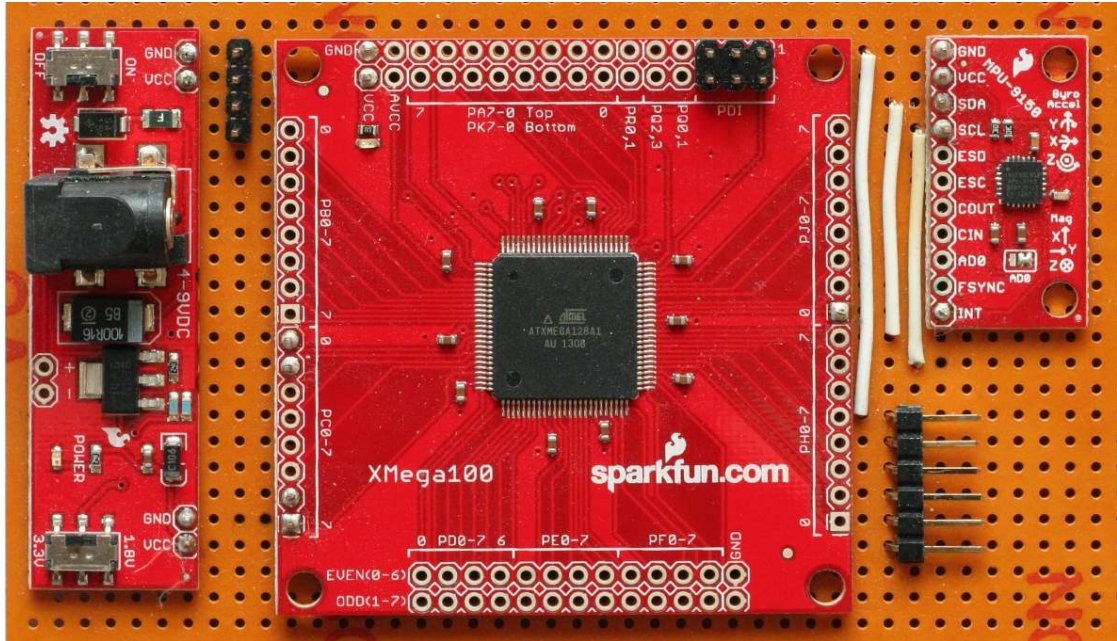
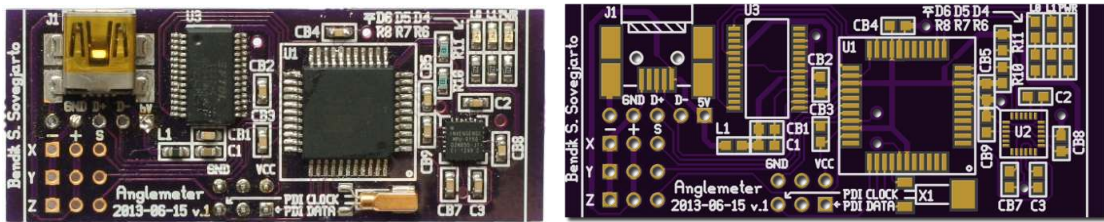


Figure 2.3: Prototype setup for porting the complimentary filter. In the middle is the ATxmega128A1 breakout board and to the right is the MPU-9150 breakout board.



(a) Assembled HydroAHRS mk.I.

(b) CGI of HydroAHRS mk.I PCB.

Figure 2.4: Images of HydroAHRS mk.I PCB.

USB connector while being rated IP68.<sup>11</sup> I found watertight USB connectors and cables from Bulgin<sup>12</sup>.

I made two HydroAHRs mk.I units and the instrument workshop at the department of Physics helped me drill holes for the connectors. These units were built to examine a hydroacoustic phenomenon when doing horizontal beaming. One of the HydroAHRs mk.I units assembled in the enclosure is shown in figure 2.5a.

The gasket along the edges of the enclosure was covered in silicon grease to further increase the water resistance. The PCB was also placed away from the bottom of the enclosure, to allow some water intake to be tolerated. Nevertheless, fieldwork in Czech Republic showed that the casing and the connector was not watertight after rough handling. When the USB cable got a tug, water escaped through the gasket. Water was also leaking in along the edges of one of the enclosures, probably caused by the front plate being bent during drilling of the connector hole.

### 2.3.6 Hardware for HydroAHRs mk.II

For the HydroAHRs mk.II a new PCB was designed and two new enclosures were built.

#### HydroAHRs mk.II – PCB

The new PCB was designed to fit into the first of the new enclosures, and was quite compact. It had the Future Technology Devices International (FTDI) chip removed as I now used the MCU for native USB communication. I added an external power regulator and electrostatic discharge (ESD)-protection for the USB signals. The PCB is shown in figure 2.6b and assembled in figure 2.6a.

A total of eleven HydroAHRs mk.II has been assembled.

#### HydroAHRs mk.II – First casing revision

Due to the problems with the box used by HydroAHRs mk.I, I wanted a smaller enclosure

with shorter edges. I also wanted to fill the enclosure so there was no place for the water to go. Since the USB connector was creating difficulties, we opted for a non-removable cable instead. I built and used my home-assembled CNC-milling machine and milled acrylic plastic sheets in different shapes, as shown in figure 2.7a. Multiple shapes were tested, and a version of the final shape is shown in figure 2.7b. I wrote a Python script to create the G-code needed to mill this shape.

The PCB with components was placed in one of the milled sheets. The USB cable was wrapped with self-amalgamating tape to fill the stress relief chamber. The main chamber was filled with liquid epoxy. The other sheet was placed on top, with a coat of acetone on all connecting faces. The acetone dissolved the surface of the acrylic sheets and fused the two sheets together. An assembled unit with this casing is shown in figure 2.5b. This enclosure proved to be watertight, but required a complicated manufacturing process and as later testing have indicated, was prone to inflicting ESD damage.

#### HydroAHRs mk.II – Second casing revision

A new revision of the enclosure for the same PCB was developed using another casted aluminium case. We went back to using aluminium as this would safely conduct static discharge from other objects away from the PCB. The casing would not create a static field itself neither. Since the USB connector was removed, the case could be quite compact. The PCB's mounting brackets and the USB cable was submerged in Tec 7<sup>13</sup>, to attach the PCB to the case and provide stress relief for the cable. Instead of using epoxy to fill the air cavity, we used canning wax. Later on, the wax can be melted away if the PCB needs reparation. Self-amalgamating tape was used for cable bend protection.

The process of mounting HydroAHRs mk.II in the aluminium enclosure is shown in figure 2.9

<sup>13</sup>MS polymer based sealant and adhesive, <http://www.tec7.ie/products/tec7-sealant>

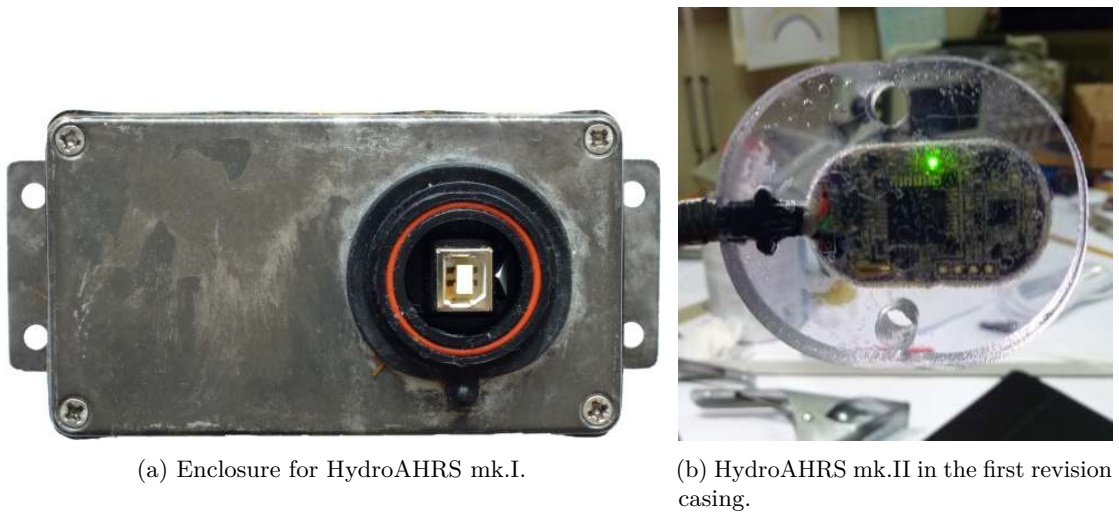


Figure 2.5: Enclosures for HydroAHRS mk.I and mk.II.

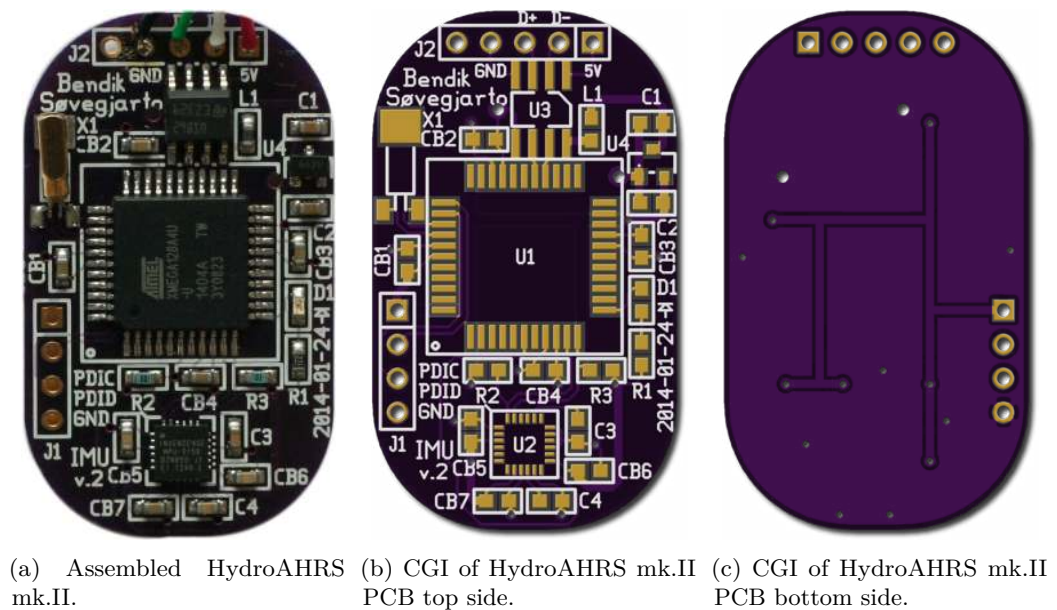
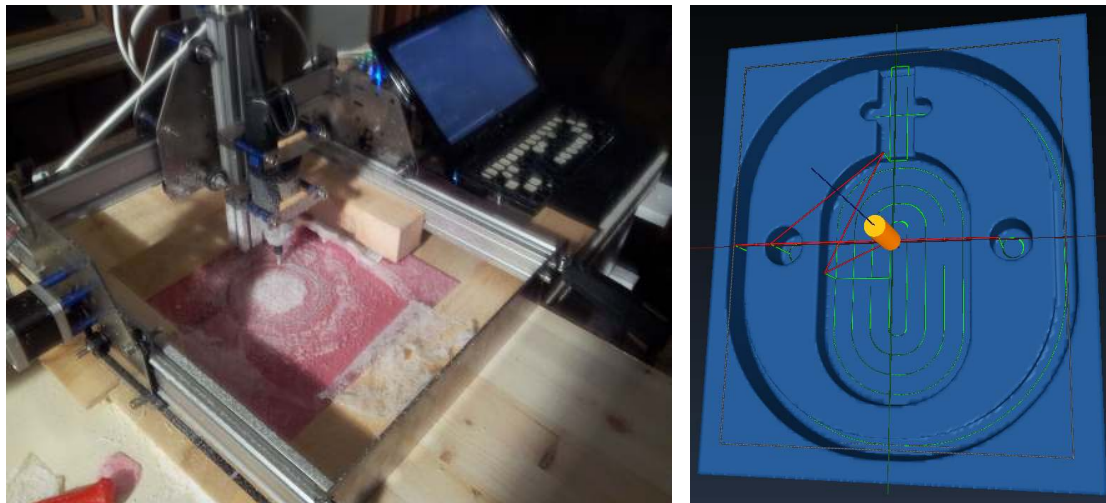


Figure 2.6: Images of HydroAHRS mk.II PCB.



(a) eShapeOko milling acrylic sheets.

(b) Final shape of HydroAHRs mk.II (first revision) casing.

Figure 2.7: Milled HydroAHRs mk.II (first revision) casing.



Figure 2.8: Four HydroAHRs mk.II assembled in their (first revision) casings.



(a) HydroAHRs mk.II in aluminium enclosures, before mounting.

(b) HydroAHRs mk.II in aluminium enclosures, after being submerged in Tec 7.

(c) HydroAHRs mk.II in aluminium enclosures, after being filled with wax.

Figure 2.9: The mounting process for HydroAHRs mk.II, with the second revision casing.

### 2.3.7 Hardware for HydroAHRS mk.III

With the change to a new MCU architecture, yet another hardware revision was needed. The new PCB incorporates the TM4C1294NCPDT from TI, the MPU9150 sensor, a new power regulator, SD-card connector, Ethernet connector, status LEDs and support circuitry. The SD-card connector is added for future use, if logging to SD-card is needed. Since the new MCU has embedded Ethernet MAC and PHY layer support, Ethernet has also been added as a possible communication option, with passive Power-over-Ethernet (POE) as the power supplier. The other option is UART (possible to convert to RS-232 or a current loop) with 5–24 V power.

The PCB is shown in figure 2.10b and assembled in figure 2.10a.

A total of 13 HydroAHRS mk.III has been assembled.

The PCB is designed to be mounted on a metal plate, with a protective layer moulded over it to make it watertight. The mould will have a metal mesh inside to protect the PCB from ESD damage. This enclosure has not yet been built or tested.

### 2.3.8 Calibration

Multiple calibration methods has to be applied for the HydroAHRS to be accurate. Each of the three sensors must be calibrated individually, and this has been done in different ways for the various HydroAHRS revisions. The tilt offset and slope error is then calibrated.

#### Magnetometer calibration

The HydroAHRS mk.I did not use the magnetometer, so no calibration was necessary.

The HydroAHRS mk.II magnetometer calibration is initiated by the user. The user then moves the unit around, while the MCU finds the maximum and minimum raw magnetometer measurements for all three axes. This is stored in EEPROM, and the subsequent raw magnetometer measurements is offset and scaled according to the mean and range (spread) of the max./min. values. The spread of the raw

magnetometer measurements after calibration from a HydroAHRS mk.II is shown in figure 2.11.

The HydroAHRS mk.III uses the MPL to automatically calibrate the magnetometer when a good figure-eight movement is detected. This is currently not saved to a non-volatile memory, and is lost after a power-cycle.

#### Accelerometer calibration

The HydroAHRS mk.I did not calibrate the accelerometer.

The HydroAHRS mk.II accelerometer calibration works similar to its magnetometer calibration. The unit is moved around, and the maximum and minimum accelerometer measurements are found. This must be done with very slow movements, as not to apply erroneous acceleration. We only want to measure the gravitational acceleration of the Earth. The max./min. values are stored to EEPROM, and the subsequent raw accelerometer measurements is offset and scaled accordingly.

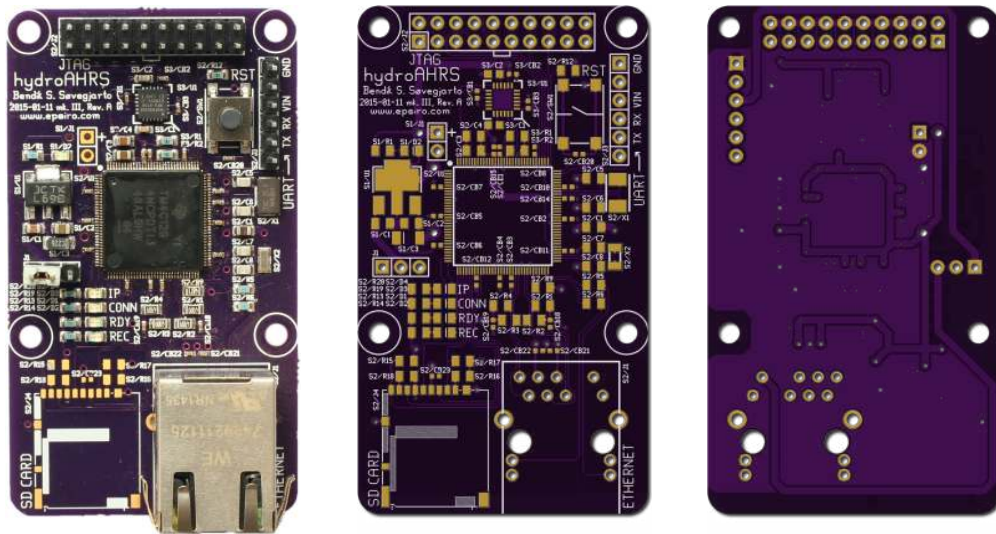
The HydroAHRS mk.III uses the MPL to perform a self-test and calibrate the accelerometer. The unit must be face-up or face-down during the calibration. This self-test is at the moment automatically performed on power-up or after a reset. The calibrated values is saved to (non-volatile) registers in the MPU-9150. The self-test can be modified to only run after a user request (and make use of the non-volatile nature of these registers), if a command interface for HydroAHRS mk.III is implemented.

#### Gyroscope calibration

The HydroAHRS mk.I uses a very primitive method to calibrate the gyroscope. A single measurement is taken while the unit is motionless, and this is subtracted from all subsequent measurements to remove the bias.

Both HydroAHRS mk.II and HydroAHRS mk.III uses the DMP to automatically calibrate the gyroscope if it senses that it has been motionless for eight seconds. HydroAHRS mk.III also runs a self-test on power-up or after a reset, which calibrates the gyroscope. It must therefore be motionless on power-up.





(a) Assembled HydroAHRs mk.III. (b) CGI of HydroAHRs mk.III PCB top side. (c) CGI of HydroAHRs mk.III PCB bottom side.

Figure 2.10: Images of HydroAHRs mk.III PCB.

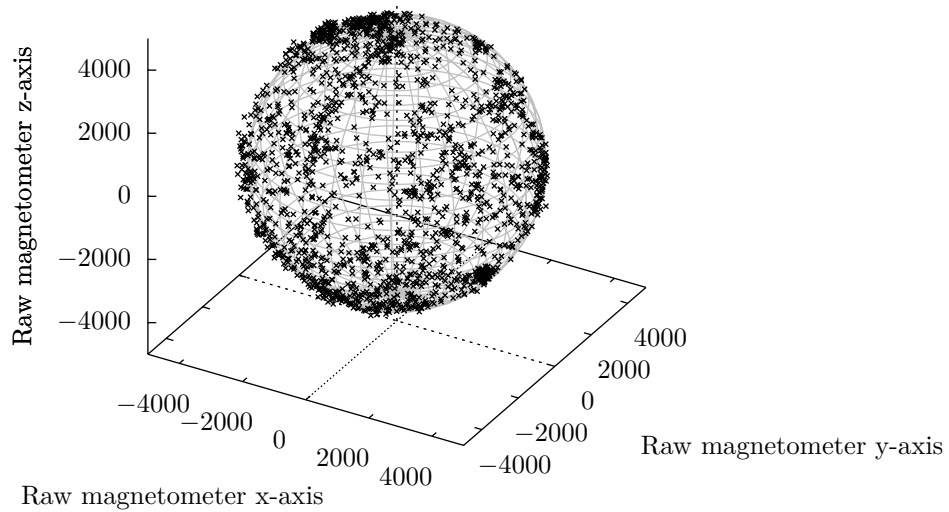


Figure 2.11: Calibrated raw magnetometer measurements from HydroAHRs mk.II.

### Tilt offset calibration

There exists an offset error of every angle calculated from a HydroAHRS, originating from offsets in the IC assembly to the PCB, and the PCB mounting to the case. We are most interested in getting rid of the tilt offset. The total tilt offset can be found by placing the unit parallel to a true vertical (or horizontal) reference. The difference between the calculated tilt and the true tilt of 90 (or 0) degrees, is the tilt offset. The HydroAHRS mk.II was calibrated against a vertical reference as described in section 2.4.4.

### Tilt slope calibration

To correct for the relative error between each tilt measurements, the slope must be calibrated. If the tilt is changed from 10 to 20 degrees, we expect the unit to change just as much as if it was changed from 40 to 50 degrees. The tilt measurements should be linear and have a slope of 1. To check if the tilt measurements are linear and find the slope, the unit is tilted while a different reference setup measures the relative tilt. The slope of several HydroAHRS mk.II units were found using the setup described in section 2.4.4.

#### 2.3.9 Front-end software

Multiple different software solutions has been used to communicate with HydroAHRS.

#### Serial terminal emulator

GNU `screen` has been widely used as a serial terminal emulator during development. Under GNU/Linux you can simply write `screen /dev/tty<device> 115200` to connect to a HydroAHRS at 115200 baud (press 's' and start receiving data from HydroAHRS mk.I or HydroAHRS mk.II). The data can be logged to file with `cat /dev/tty<device> >> log.nmea`.

#### Windows application

Helge Balk wrote a windows application in Pascal using Lazarus for communication, cal-

ibration and logging with HydroAHRS. The application is shown in figure 2.12.

#### Python- and web-based front-end

I created a web-based front-end in JavaScript/HTML/CSS to visualize the data from HydroAHRS. A Python server talks to the HydroAHRS over USB and redirects the data to the web-app. The application is shown in figure 2.13.

Another Python-application using `matplotlib` for the realtime-plotting has also been developed.

#### InvenSense cube

InvenSense has released a Python application which renders the spatial orientation of an AHRS as a cube using OpenGL. The application uses a binary format to transfer data from the AHRS and can thus handle very high sampling rates.

## 2.4 Testing and results

The HydroAHRS mk.I and mk.II have been tested in both controlled environments and in fieldwork, however, the last version (HydroAHRS mk.III) has not.

### 2.4.1 Fieldwork in Czech Republic 2013

HydroAHRS mk.I was created for its use in fieldwork in Czech Republic at lake Římov in August 2013. The experiments conducted revolved around phenomenas occurring during horizontal beaming with hydroacoustic echo sounders. The mounting angle of the equipment had to be known for the experiment to be successful.

Two HydroAHRS mk.I units were built and brought to the Czech Republic. To test their water resistance they were suspended in a bucket of water for multiple hours. No leakage was observed.

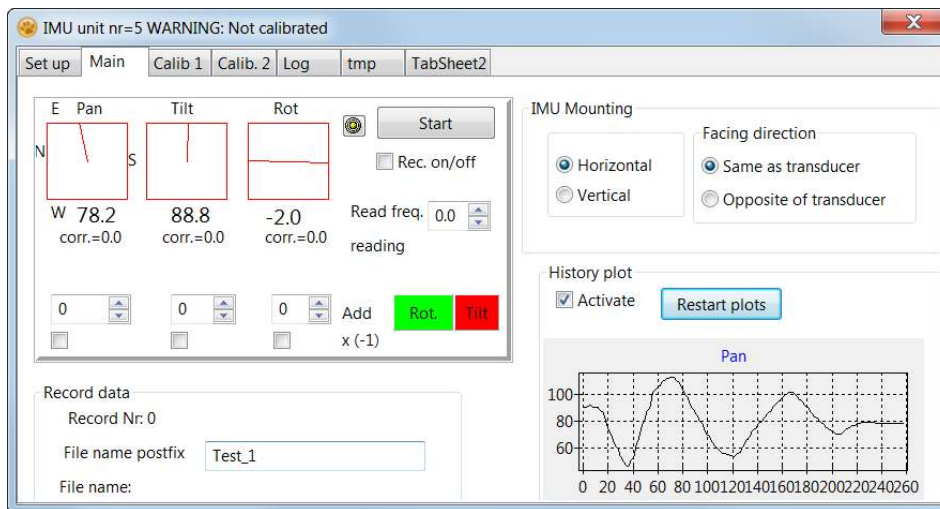


Figure 2.12: Windows-based front-end for HydroAHRs.

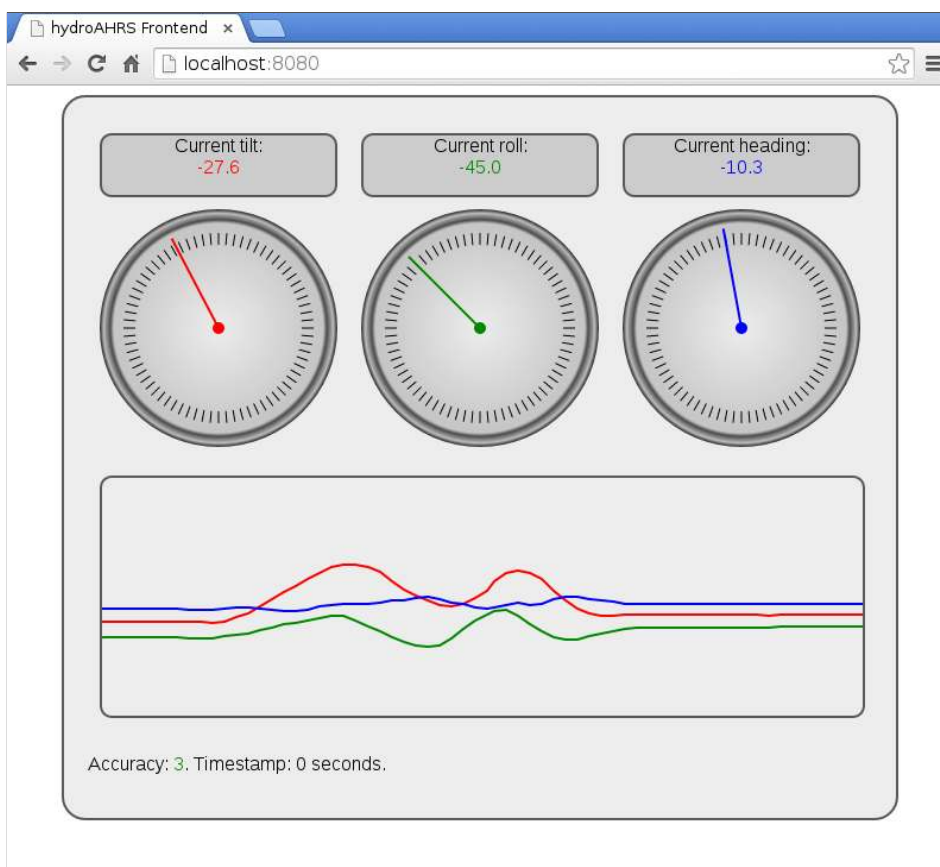


Figure 2.13: Web-based front-end for HydroAHRs.

The units were then calibrated using a Leica DISTO D5 as reference. The Leica has an accuracy of  $\pm 0.3^\circ$ . It is shown in figure 2.14.

A bash script was used to record the tilt from the HydroAHRS mk.I and note the reference value from the Leica. Gnuplot was then used to plot the results and to fit the data to a linear function using a Non-linear least squares (NLLS) Marquardt-Levenberg algorithm (available in Gnuplot). The resulting linear regression gave the offset and slope error of the HydroAHRS mk.I compared to the Leica.

One of the units was mounted on the same plate as a Simrad EK60  $10 \times 4^\circ$  transducer, as shown in the bottom of figure 2.15a, after it had been calibrated. This unit was used to report the tilt angle during the installation and use of the transducer. 15 meter long active USB-extension cables were used to connect the HydroAHRS mk.I to a computer.

The other HydroAHRS mk.I was later mounted on a sub-Atlantic pan (heading) tilt rotator, along with a transducer, as shown in figure 2.15b. The rotator had motors inside, making it possible to turn it around in two axes and adjusting its heading and tilt. The current heading and tilt was reported by the rotator using an integrated feedback potentiometer. A Rieker H5A1-90 one axis inclinometer was also mounted on the rotator. The rotator was set to different tilt angles, and the same Leica DISTO D5 was used to measure the angle. The results are shown in figure 2.16.

During the experiments using the HydroAHRS mk.I, two problems were encountered. A slight drift in the tilt and roll measurements was discovered. This is most likely caused by the system slowly converging against a value. More importantly, it was discovered that the HydroAHRS mk.I had low repeatability. Meaning that a power-cycle would change the measurements even though the unit was stationary. This made it practically impossible to calibrate the device, and we did not trust its data. After multiple days of attempting to get trustworthy readings, a fail-safe replacement was used instead (of any of the electronic measuring devices). The replacement was a simple protractor, consisting of a string with a

mass attached, an aluminium bar with degree markings and a webcam.

#### 2.4.2 HydroAHRS mk.II repeatability test

The repeatability of HydroAHRS mk.II was tested by mounting the device to a protractor. Multiple readings were then taken at various angles, with a power-cycle in between. The measurements showed that the same angle was found within  $0.05^\circ$  every time, after waiting for the device to settle. This was, however, only tested with one unit, so until a more thorough repeatability test has been conducted, we regard the tilt repeatability to be within  $\pm 0.1^\circ$ .

#### 2.4.3 HydroAHRS mk.II RTC drift test

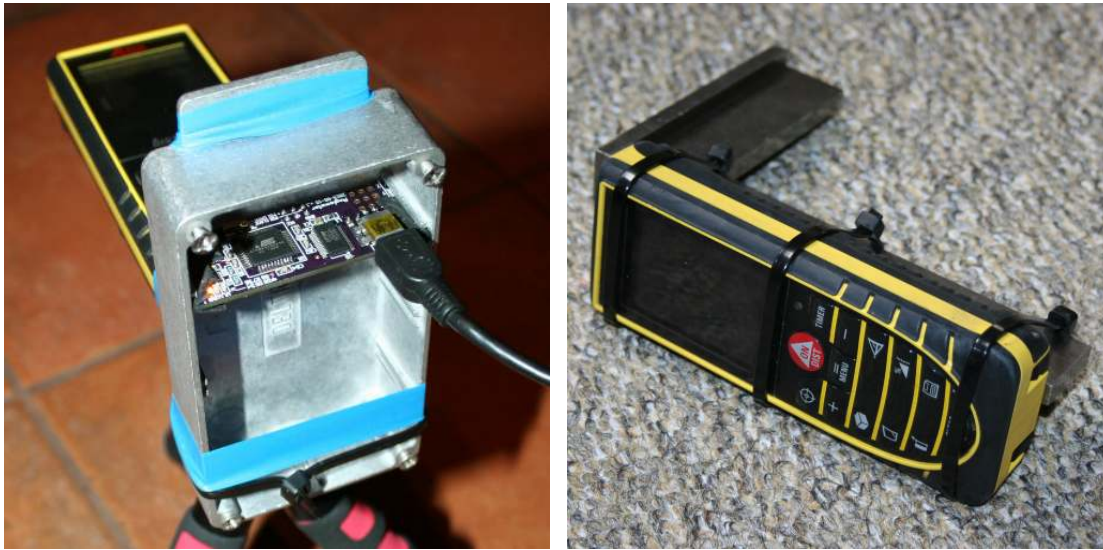
To find the level of drift on the timestamp calculated from the RTC crystal we let a unit run for multiple hours and compared the start and stop timestamp with a Network Time Protocol (NTP)-synchronized computer. We found approximately 13 seconds drift after 20 hours and 48 minutes. This corresponds to 174 ppm and is not adequate. A workaround is discussed in section 2.5.4.

#### 2.4.4 HydroAHRS mk.II indoor test and calibration

Two procedures were conducted to find the offset and slope error of each unit, and to see how linear their response were.

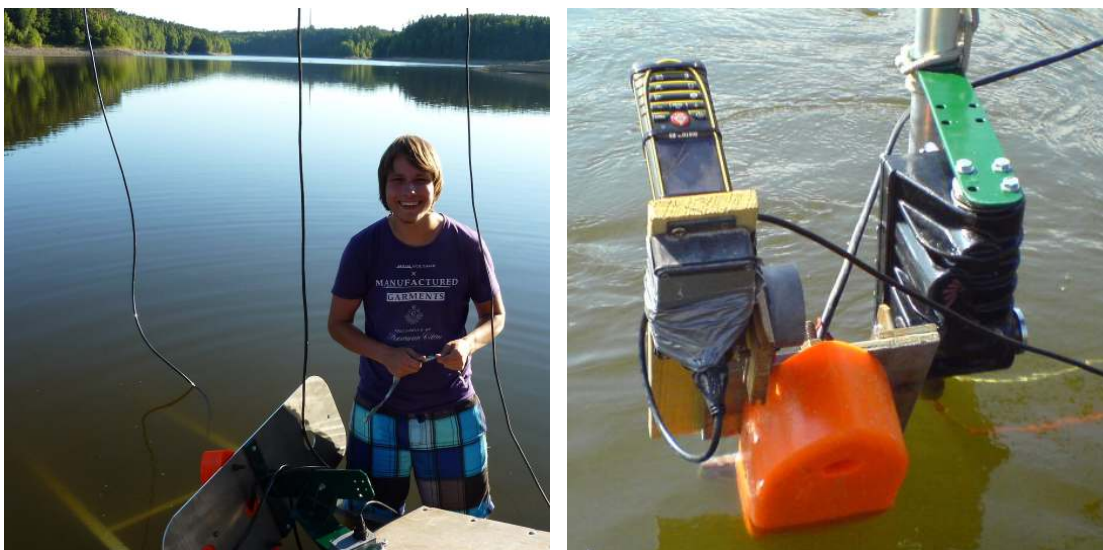
##### HydroAHRS mk.II offset calibration

First the offset error was found by placing the unit parallel to a vertical surface. The vertical surface was found by aligning an U-shaped aluminium bar to a long string with a mass attached at the end. The mass was suspended in a bucket of water to dampen its movements. The long length of the string and bar made it possible to align the bar accurately. The setup is shown in figure 2.17a.



(a) Leica mounted to a unit under test (UUT) (HydroAHRs mk.I). (b) Leica mounted to a try square for easy calibration.

Figure 2.14: Photographs of Leica DISTO D5 used for calibration.



(a) HydroAHRs mk.I during installation on transducer plate. (b) Rotator (black), along with the transducer (orange), the HydroAHRs mk.I (square grey box), the Rieker (round grey box) and the Leica (yellow).

Figure 2.15: HydroAHRs mk.I in use at lake Římov in 2013.

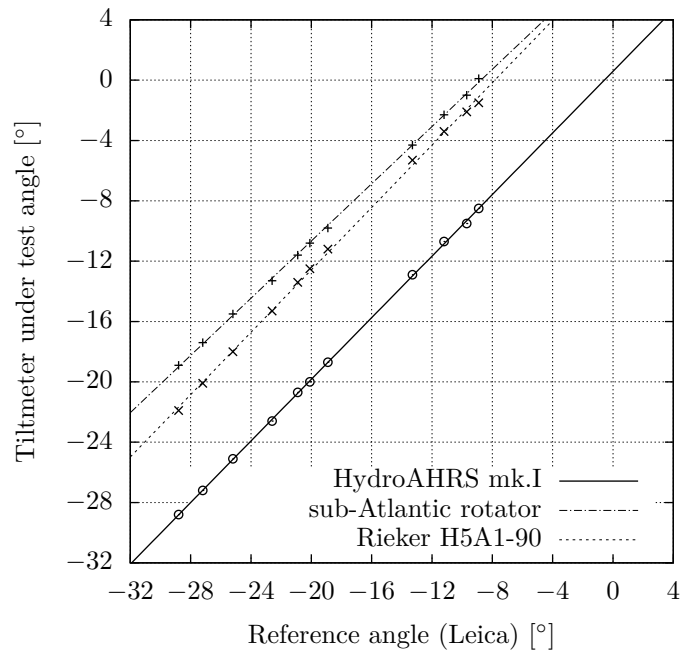


Figure 2.16: Calibration points and linear regression for tilt-meters used at lake Římov in 2013. The HydroAHRS mk.I, Rieker H5A1-90, and rotator-tiltmeter are shown.

### HydroAHRS mk.II slope calibration

We used Pythagoras theorem to find the slope error. The unit under test (UUT) was mounted to an aluminium bar (figure 2.17b) at a known distance away from a vertical ruler (figure 2.17c). The ruler was confirmed to be vertical by using the same method as described in the offset-calibration. A laser pointer was attached to the end of the bar. By tilting the bar, the laser dot was moved between different steps of the ruler. The angle reported by the UUT was recorded for each step. This was done with the UUT mounted both normal and reversed (flipped front-to-back), to expose the offset error. The measured data is shown in figure 2.18.

### 2.4.5 Fieldwork in Czech Republic 2014

HydroAHRS mk.II (both with the first and second casing) have been tested by FishEcU<sup>14</sup>

<sup>14</sup>Fish Ecology Unit of the Department of Fish and Zooplankton Ecology of the IH BC CAS. <http://www.fishecu.cz/>

during fieldwork they have conducted in the Czech Republic.

Multiple units of the first revision (plastic) casing stopped working (no response over USB). We suspect they died because of ESD-damage. None of the second revision casing units have died.

FishEcU reported that the heading calculation drifted substantially. This has been verified by us. We found approximately 130° drift over 8 hours.

### 2.4.6 Dynamic test

A dynamic test setup has been created (together with Jensen), but the test has not yet been performed with any of the HydroAHRS. This test would show the response time of the system. An AHRS can be placed on a wooden rod with one end connected to a wheel, while the other



(a) Vertical reference, using a string and a heavy mass. (b) Aluminium bar with UUT. (c) Vertical ruler.

Figure 2.17: Offset and slope-calibration setup.

end of the rod gliding along the table. The wheel is shown in figure 2.20.

When the wheel is turned to an angle  $\theta$ , it will move the rod to an angle  $\varphi$ , as shown in figure 2.21. The AHRS unit will measure the tilt angle  $\varphi$  directly and save it. Since we knew the geometry of the setup we can calculate  $\varphi$  from  $\theta$ , by using equation 2.1.

$$\varphi = \arcsin\left(\frac{r \sin(\theta) + b}{L}\right) \quad (2.1)$$

A PCB I created, containing a Atmel ATmega64 MCU (not ATxmega), is used to find  $\theta$ . Between every other pair of stokes on the wheel, a piece of semi-reflecting tape is attached. This reflects the light emitted from an infrared (IR) LED, and was received by a photodetector<sup>15</sup>. The photodetector module output a series of pulses as the wheel is turned. The higher the revolution, the higher the frequency of these pulses. The MCU received the pulses and counts the time between them.

<sup>15</sup>The photodetector modulated the IR LED, so it was quite resistant of disturbances from the surrounding light

The maximum and minimum angle of the rod can be determined by using a laser pointer, pointing at the opposing wall. By measuring the distance between the two points as well as from the wall to the setup, simple trigonometry can be used to give the angle, as done in the slope calibration setup.

#### 2.4.7 HydroAHRS mk.III indoor test and calibration

The HydroAHRS mk.III has not yet been tested or calibrated in a controlled environment. Although preliminary tests have not shown drift on the calculated heading. It has also shown that the unit is capable of handling high sample rates (up to 200 Hz). The tilt and roll calculations are based on the same firmware for the DMP as the HydroAHRS mk.II, and is thus expected to provide similar results.

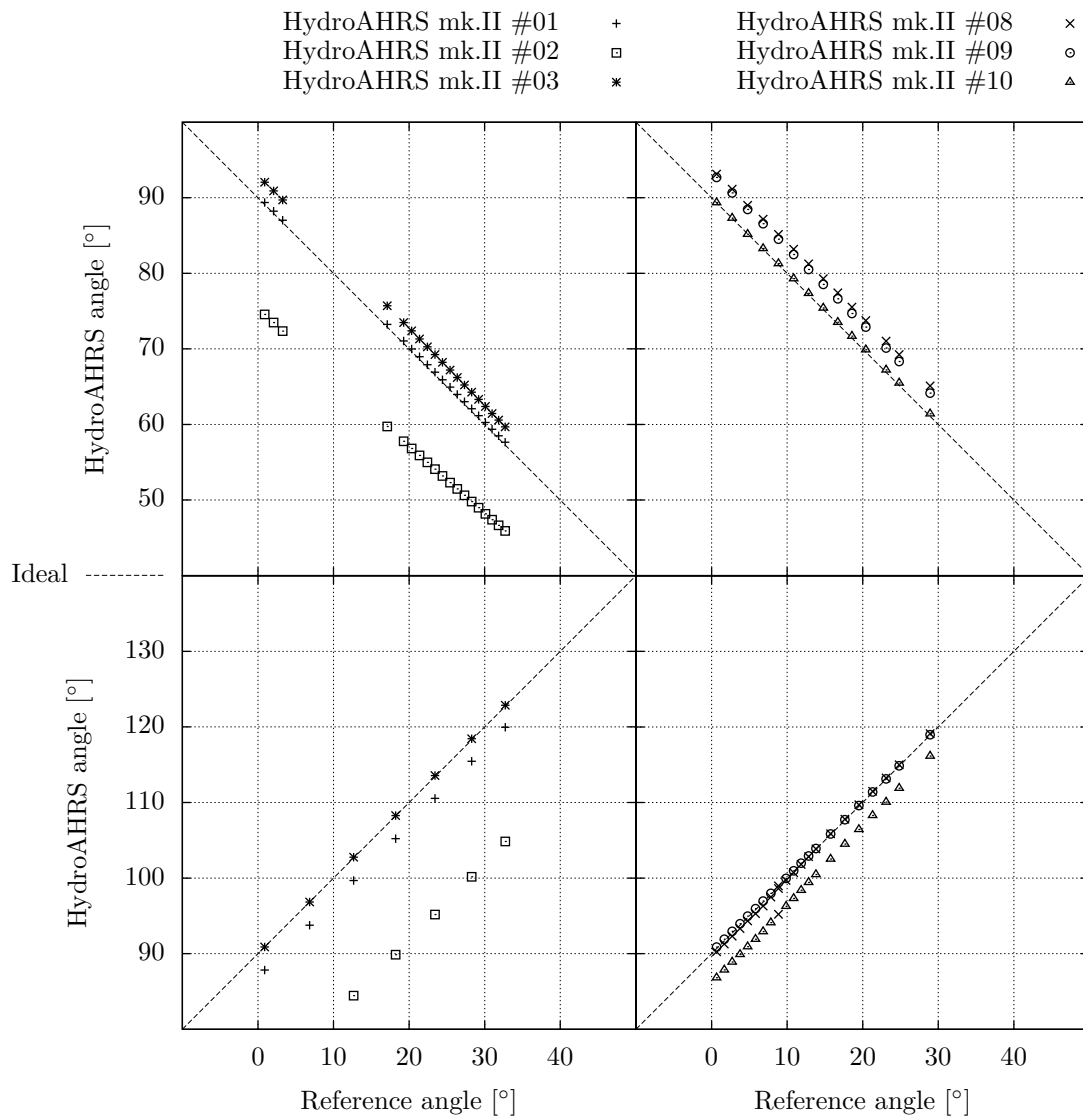


Figure 2.18: Slope calibration measurements taken with HydroAHRS mk.II units 1,2,3 in March 2014 and 8,9,10 in June 2014. Measurements were taken with the front side away from the ruler (top), and again with the front against (bottom). This shows the offset error, as can be seen with HydroAHRS mk.II unit 10. Its measurements follows the ideal line when facing away from the ruler, but misses the line after it has been flipped.



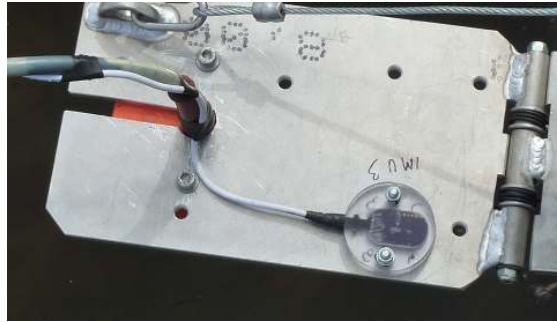


Figure 2.19: HydroAHRS mk.II (first casing revision), used in experiments conducted by FishEcU in the Czech Republic in 2014.



Figure 2.20: Photograph of the wheel and detector PCB for the dynamic test.

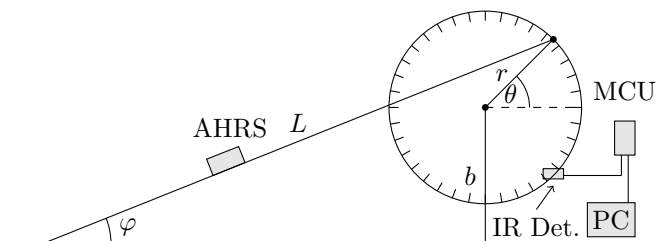


Figure 2.21: Diagram of the dynamic test.

## 2.5 Discussion

The following paragraphs discuss some thoughts regarding the development and use of the HydroAHRS.

### 2.5.1 HydroAHRS mk.II tilt test results

Figure 2.18 shows that the tilt-measurements from all the HydroAHRS mk.II units tested are linear over their working range. The repeatability test described in section 2.4.2 shows that power-cycles does not affect the measured tilt. The slope and offset error may thus be removed through calibration, which results in accurate tilt-measurements. The exact accuracy of the HydroAHRS mk.II (or mk.III) has not been determined, but an (absolute angle) accuracy near the repeatability of  $\pm 0.1^\circ$  should be achievable through calibration.

### 2.5.2 Comparison test

It should be emphasized that testing of the complimentary filter, which showed bad repeatability was done on the HydroAHRS mk.I. A test comparing Jensen's unit and HydroAHRS mk.I was planned, but got cancelled when a new solution for the sensor fusion emerged.

### 2.5.3 Mounting orientation

Care must be taken when calibrating and mounting the units, to make sure the units are mounted the same way as when they were calibrated.

### 2.5.4 Timestamp drift

To circumvent the high drift on the timestamp from HydroAHRS mk.II, we can use the client computer to reset the RTC frequently. Another solution is to let the client computer ask for single samples and timestamp the sample on the computer. If the computer is also used for receiving the data from an echosounder, both will be timestamped with the same clock and automatically be synchronized. There is a delay (with jitter) between the clients request and a sample has been delivered. This delay and jitter should be measured.

### 2.5.5 DMP and MPL documentation

The firmware downloaded into the DMP of the MPU-9150 is a pre-compiled binary blob. With the lacking documentation of the DMP, it makes it very difficult to inspect what the DMP is actually doing or change its behaviour. Nevertheless, testing has shown that the DMP is performing a satisfactory sensor fusion of the accelerometer and gyroscope data. The same applies for the pre-compiled MPL, used for the magnetometer fusion.

### 2.5.6 Future work

Future work involve testing the heading calculation of the final version, testing the dynamic response at the full sampling rate of 200 Hz, implementing Ethernet communication and saving all calibration data to EEPROM.

## 2.6 Summary

Three different attempts have been made on designing a fully functional AHRS, with both software and hardware undergoing these revisions. These are named HydroAHRS mk.I, mk.II and mk.III. The final version (mk.III) uses a 32-bit ARM Cortex-M MCU, a MPU-9150 (3-axis accelerometer, 3-axis gyroscope and 3-axis magnetometer) sensor, and code supplied from the manufacturer (InvenSense) to perform sensor fusion and angle calculations. Fieldwork has shown that the HydroAHRS mk.II, with the second casing revision, can be successfully used under water. A simple repeatability test with

power-cycles between each measurement was conducted with HydroAHRS mk.II. The test showed that the same angle was found within  $\pm 0.1^\circ$  every time, after waiting for the device to settle. Offset and slope calibrations have been performed on six HydroAHRS mk.II units, which all showed a linear response over their working range. Together with the repeatability test, an accuracy close to  $\pm 0.1^\circ$  should thus be achievable. We have managed to create a unit which fulfilled our requirements of *a)* low cost; *b)* sufficient accuracy; *c)* watertightness; *d)* repeatability; and *e)* with heading calculation, although the last version (mk.III) has not yet been tested.



## Paper 3

# Construction of a primitive autopilot for hydroacoustic work

### Abstract

This paper presents the construction and implementation of a primitive autopilot used to automatically steer a work platform for hydroacoustic work. Together with a vessel, this creates an Automatic Survey Vessel (ASV). A Global Navigation Satellite System (GNSS) and a compass is used to steer the vessel at constant speed through multiple waypoints. Realtime transmission of new waypoints enables dynamic route generation. Recorded or pre-planned routes can also be executed. The autopilot has been successfully tested on a land-based rover.

**Keywords:** Autopilot, GNSS

### 3.1 Introduction

A lot of equipment and manpower is required to conduct hydroacoustic surveys with high coverage. Earlier work described in paper 1 presents the construction of a remote controlled hydroacoustic work platform. This work platform can be controlled from a distance, thus making it possible to do hydroacoustic experiments or fieldwork without letting movement from the operator disturb the hydroacoustic recordings.

To further increase the efficiency of hydroacoustic fieldwork, an autopilot can be used to control the vessel during surveys. This has multiple applications, for instance automatic night surveying, automatic abundance estimation, environmental surveillance, and as a tool for manual surveying. By telling the autopilot to follow a larger vessel with a crew, higher survey coverage can be achieved and

complicated driving patterns can be created (as the small boat is more agile than the large manned vessel). This paper will describe the implementation of a primitive autopilot as a proof of concept for future work. A vessel with this autopilot is named an Automatic Survey Vessel (ASV).

The autopilot is able to follow a route consisting of multiple waypoints. These waypoints will be given to the autopilot while it is running. The waypoints can originate from a pre-planned route on a map, an earlier recorded route, or be dynamically calculated by a separate computer in realtime. This makes it possible to create driving patterns on-the-fly. For instance to make the ASV drive in patterns around a larger vessel with a crew. The autopilot will follow pre-programmed rules, without making any decisions while it is running. It is therefore not autonomous, but merely automatic. All activity will be under human supervision. A web-based control panel is used to observe and

control the vessel, and communicates with the autopilot using WebSockets.

A similar project was presented in 2013 [22], focusing on automatic estimation of fish density. This is one of the possible application with our ASV. Other related projects include [3, 23–25].

## 3.2 Theory

### 3.2.1 PID-regulator

A PID-regulator is a simple yet powerful control loop system. It continuously calculates the error between the current state of a system and the wanted state (setpoint). The error signal is modified by the PID-regulator to produce an optimal control signal for the system, using multiplicative, integral and derivative terms, as shown in figure 3.1.

More information about PID-regulators is available at [26–28].

### 3.2.2 WebSockets

WebSockets is protocol that allows full-duplex communication between a web-browser and a server. The server can send messages to the web-browser without being polled for the information. WebSockets makes it possible to create real-time interactive control systems in a web-browser.

It has been standardized by the Internet Engineering Task Force (IETF) as RFC 6455. More details can be found in the standard [29].

### 3.2.3 High accuracy positioning

Multiple solutions are possible to get high accuracy position data. Primarily the setup of a personal Differential GPS (DGPS) system, subscription to correction data distributed by satellite or land-based networks or the use of real-time kinematics.

A local DGPS system can be created by placing one or multiple Global Navigation Satellite System (GNSS)-receivers around an area to be examined. These will be fixed to the ground

and can average their position over time to accurately return their true position. A deviation from this position is an error, mainly created by atmospheric phenomena. This deviation (for each satellite) can be sent to a mobile vessel, enabling it to compensate for the error in its own measurements.

Multiple permanently installed base stations, often located along coastal shores for use in maritime navigation, make up already available DGPS systems. Access to the deviation data from these systems can be purchased, with a price depending on the level of accuracy needed (deci- or centimeter). The data is commonly relayed by radio from land stations. Kystverket [30] and Kartverket [31] provides such services in Norway. High accuracy is achieved when the distance to the base stations is short.

Deviation data can also be relayed using satellites (satellite-based augmentation system (SBAS)) and constitutes a wide area DGPS-system. The accuracy is not as high as a localized DGPS system, since the base station is usually far away, but the coverage is good. European Geostationary Navigation Overlay Service (EGNOS) is a SBAS deployed in Europe and Wide Area Augmentation System (WAAS) is a SBAS covering North America.

Real-time kinematics measures the phase of the carrier wave from a GNSS to achieve high accuracy, in addition to using the information encoded within the GNSS-messages [32]. RTKLIB is a free<sup>1</sup> program package for GNSS positioning, making all the calculations necessary to perform real-time kinematics. RTKLIB needs access to raw data from a GNSS-receiver, which is only available on selected devices (e.g. U-blox 6 PPP).

Further information about GNSS and SBAS can be found in [33].

### 3.2.4 Haversine

The haversine formula is an equation which gives the great-circle distance between two points on a sphere, with good numerical precision [34].

With  $\phi_1$ ,  $\phi_2$  as the latitude of point 1 and 2,  $\lambda_1$ ,  $\lambda_2$  is the longitude of point 1 and 2,

<sup>1</sup>as in free speech

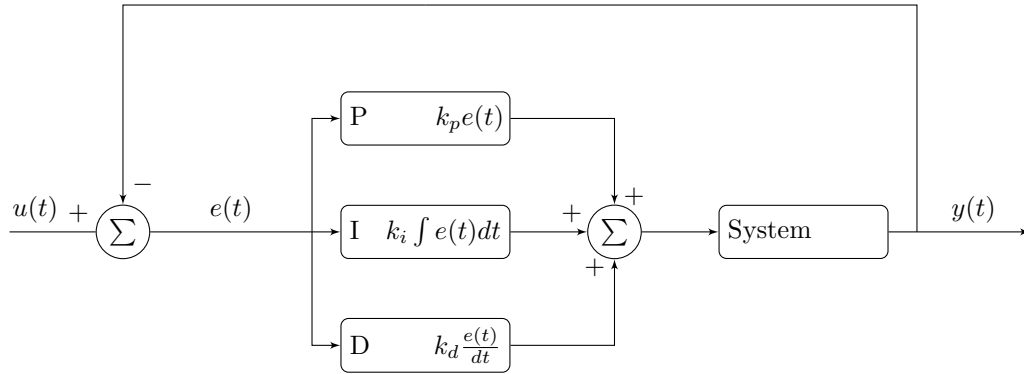


Figure 3.1: Overview over a PID-regulator.

$\Delta\phi = \phi_2 - \phi_1$  and  $\Delta\lambda = \lambda_2 - \lambda_1$ , the haversine is given as equation (3.1).

$$\begin{aligned} a &= \sin^2 \frac{\Delta\phi}{2} + \cos \phi_1 \cos \phi_2 \sin^2 \frac{\Delta\lambda}{2} \\ d &= 2r \arcsin(\sqrt{a}) \end{aligned} \quad (3.1)$$

where  $r$  is the radius of the sphere, and  $d$  is the distance.

### 3.2.5 Bearing

The initial bearing between two points on a sphere is given by equation (3.2) [35].

$$\begin{aligned} y &= \sin \Delta\lambda \cos \phi_2 \\ x &= \cos \phi_1 \sin \phi_2 - \sin \phi_1 \cos \phi_2 \cos \Delta\lambda \\ \theta &= \arctan \frac{y}{x} \end{aligned} \quad (3.2)$$

where  $\theta$  is the bearing,  $\phi_1$ ,  $\phi_2$  is the latitude of the initial and final point, and  $\Delta\lambda$  is the difference between the longitude of the initial and final point.

A normalized bearing (0–360°) is given by  $(\theta + 360) \% 360$ , where  $\%$  is the (floating) modulo operator.

## 3.3 Material and methods

The autopilot extends the remote work platform described in paper 1. It will be placed as

<sup>2</sup>CEP is a measure of precision. It sets the radius of a circle (with the mean as center), where 50% of all samples is expected to be included.

a fourth printed circuit board (PCB) on the control unit stack. A block schematic of the complete control unit stack is shown in figure 3.2, and a photograph in figure 3.3.

### 3.3.1 Selection of GNSS

We chose to use a single GNSS-receiver as a start, since this is the lowest entry point (simple and inexpensive) to a positioning system and is usable anywhere on Earth. The GNSS-receivers found have support for SBAS and this has been enabled most of the time. A single GNSS-receiver may be sufficient, as we do not have very strict requirements for absolute position. We will rely on the compass for short term navigation and by only using the autopilot in lakes (and away from the shore), we have few obstacles. Good coverage can also be achieved without having to follow an exact route. A more advanced positioning system can be implemented at a later stage, if needed.

We initially chose to use a Trimble Copernicus II, but preliminary testing showed disappointing accuracy and reception sensitivity (under difficult conditions). A U-blox 6 Precise Point Positioning (PPP) evaluation kit was acquired, since it promises sub-meter precision when used together with a SBAS. It can also output the raw data needed by RTKLIB.

Non-PPP variants within the same family have been used afterwards, namely U-blox NEO-

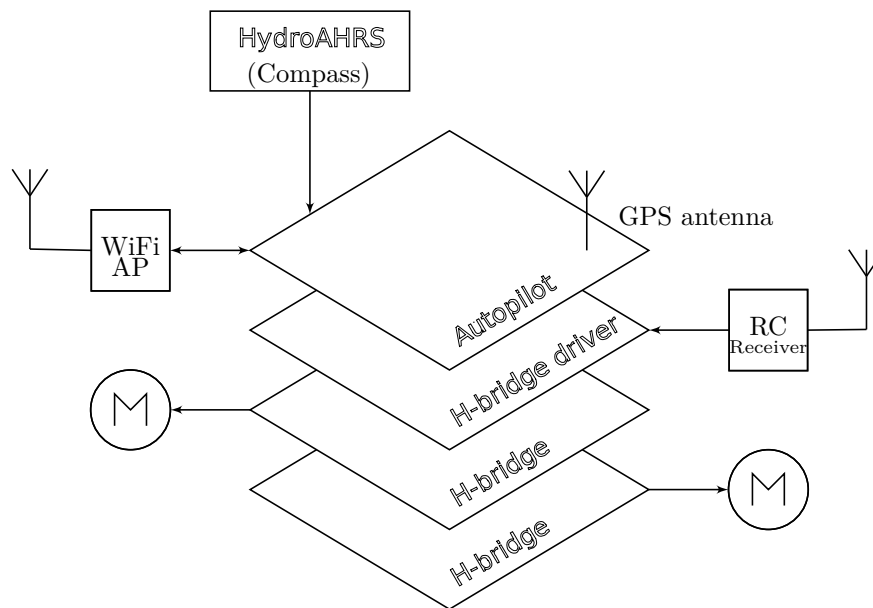


Figure 3.2: Block schematic of the complete control unit stack.

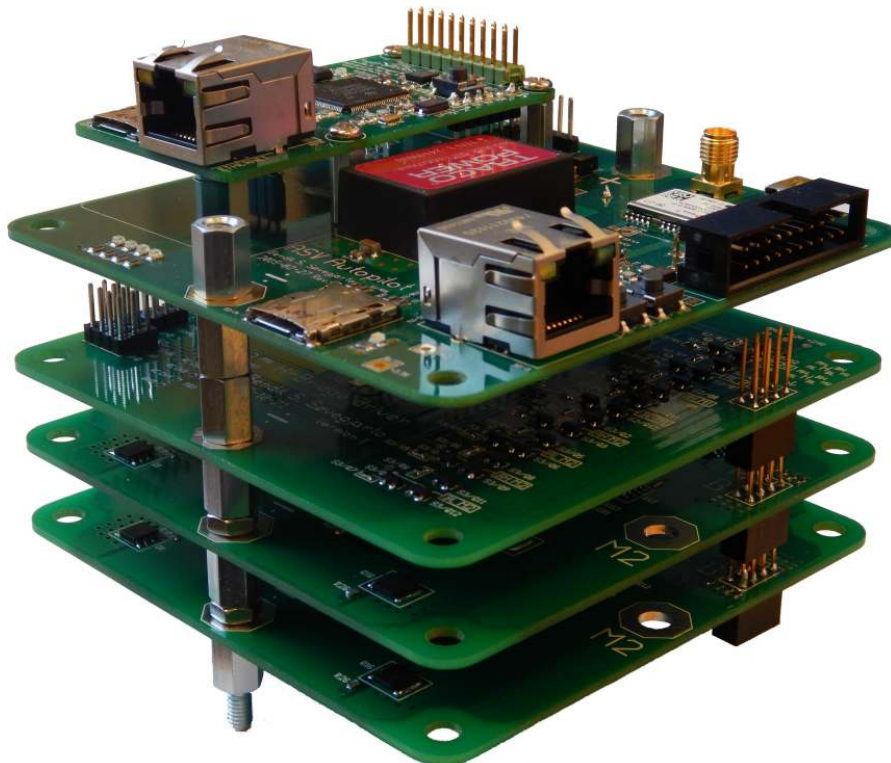


Figure 3.3: Photograph of the complete control unit stack, with motor control system, autopilot and compass.



6M, U-blox NEO-7M and U-blox NEO-M8N. U-blox NEO-M8N promises a position precision of 2.0m Circular Error Probable (CEP)<sup>2</sup> and has been chosen as the GNSS-receiver to use in the autopilot. This is the latest device in the U-blox NEO M-series and offers a good compromise between price (44EUR) and performance.

We have used active (GPS only) patch antennas supplied by U-blox during testing and development. As suggested by [36], changing to a geodetic-grade antenna is an effective way to increase performance.

Three tests investigating the performance of the chosen GNSS-receivers have been conducted, and are described in section 3.5.

### 3.3.2 Selection of compass

Along with a GNSS to get position data, the autopilot will use an Attitude and Heading Reference System (AHRS) to get the current heading. Our self-made HydroAHRS described in paper 2 will be used as the compass.

### 3.3.3 Selection of MCU

ATxmega32A4U from Atmel was initially chosen as the microcontroller unit (MCU) for the autopilot, but this was later changed to an ARM Cortex M4 MCU from Texas Instruments (TI) named TM4C129 (cost; ~15USD each at a quantity of 100). The change was done because TM4C129 has both Ethernet MAC and PHY layer embedded, making it possible for it to talk directly over Ethernet.

OpenOCD was used together with a USB JTAG debugger<sup>3</sup> from Olimex to flash (program) the ARM MCU. GNU Debugger (GDB) was used together with OpenOCD to debug the MCU.

### 3.3.4 Firmware and autopilot algorithm

A very simple algorithm is used in this autopilot. Based on the measured location of the vessel (from the GNSS), and where we want arrive (from the waypoint), the wanted

course is calculated using equation (3.2). The measured heading (from the compass) is then used to calculate the thrust needed to achieve the wanted heading corresponding to the calculated course. The heading is adjusted while maintaining (near) constant speed. A PID-regulator is used to calculate the thrust needed to achieve the wanted heading, as fast as possible with minimum overshoot. It is based on an application note and code from Atmel [28]. A waypoint is approved as passed when the GNSS-receiver reports a position within a perimeter around the waypoint's center. The next waypoint will then be used as the new destination, or the autopilot will stop if no new waypoints are available. New waypoints received by the autopilot will be acknowledged back to the sender upon arrival.

Firmware incorporating this algorithm was written for the TM4C129. A block schematic of the autopilot algorithm and program flow is shown in figure 3.4. The firmware for the autopilot is listed in appendix M.

### 3.3.5 Hardware and PCB

The hardware of the autopilot consist of the main MCU which calculates the thrust to each of the engines, a GNSS, a compass and a communication unit. All of these modules were separate PCBs during the development of the autopilot, as shown in figure 3.5.

These components were finally combined into one 4-layer PCB, with the compass externally connected as a snap-in board. To minimize the signal loss from the Global Positioning System (GPS) antenna to the GNSS-receiver special care was given during the routing of the RF-signal path. The trace width was adjusted together with the substrate thickness to give a matched impedance of 50Ω. A separate solid ground plane is placed under the GNSS-receiver circuit, connected to the digital ground plane in a single point. A guard ring is routed around the GNSS signal path to minimize noise reception.

The PCB is shown in figure 3.6 and an assembled autopilot is shown in figure 3.7.

<sup>3</sup>Prod. id: ARM-USB-OCD-H

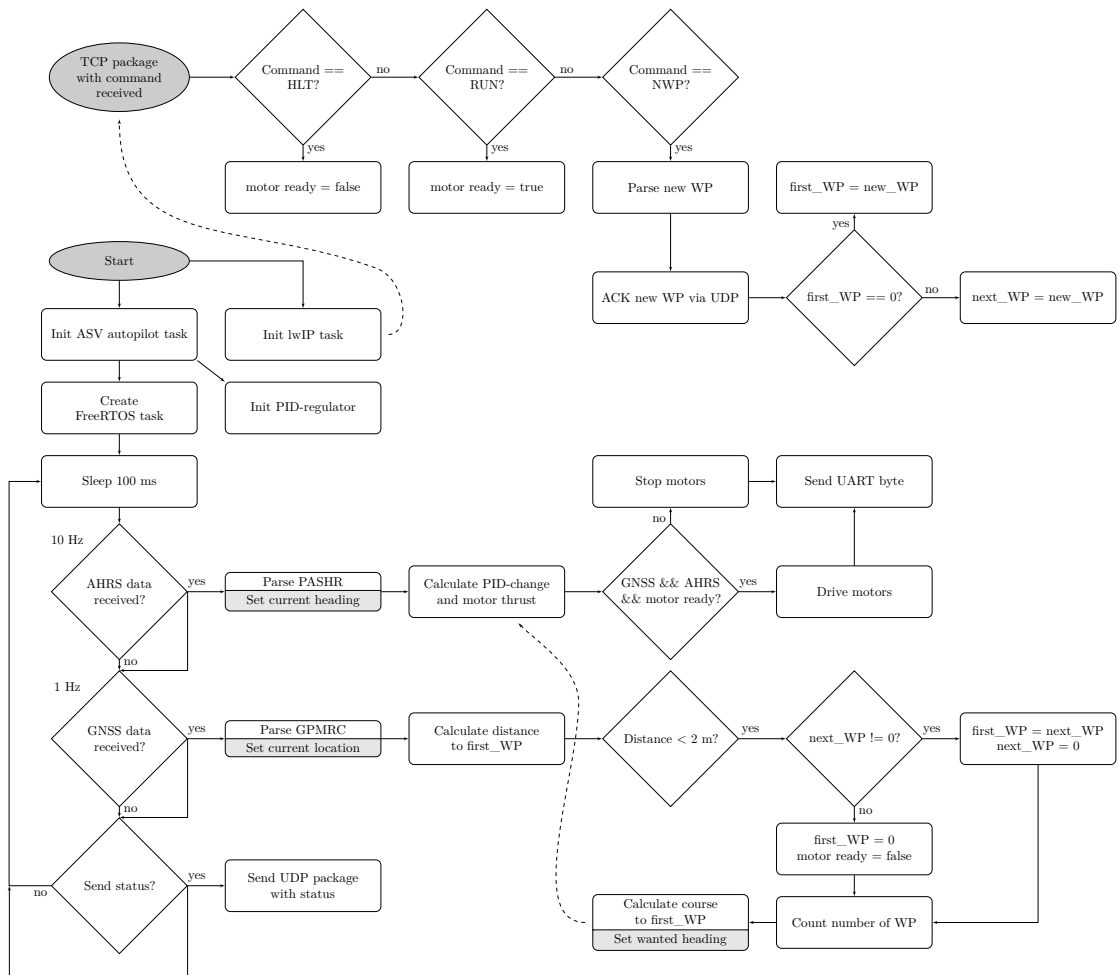


Figure 3.4: Block schematic of the autopilot algorithm and program flow. A callback function from lwIP starts the incoming TCP command parsing.

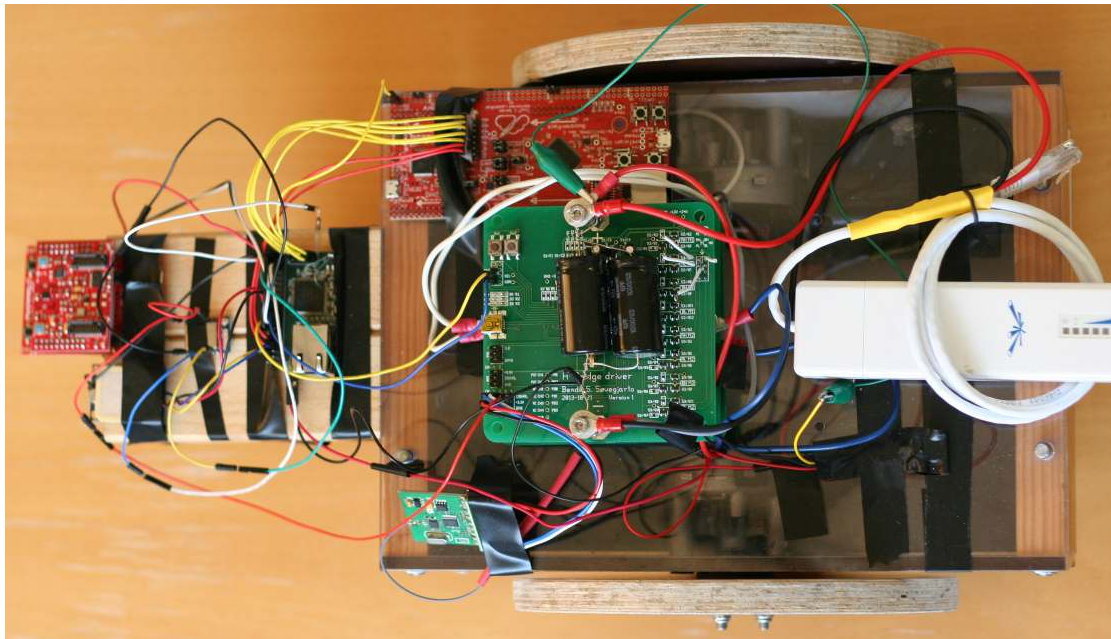


Figure 3.5: Prototype of the autopilot as multiple PCBs.

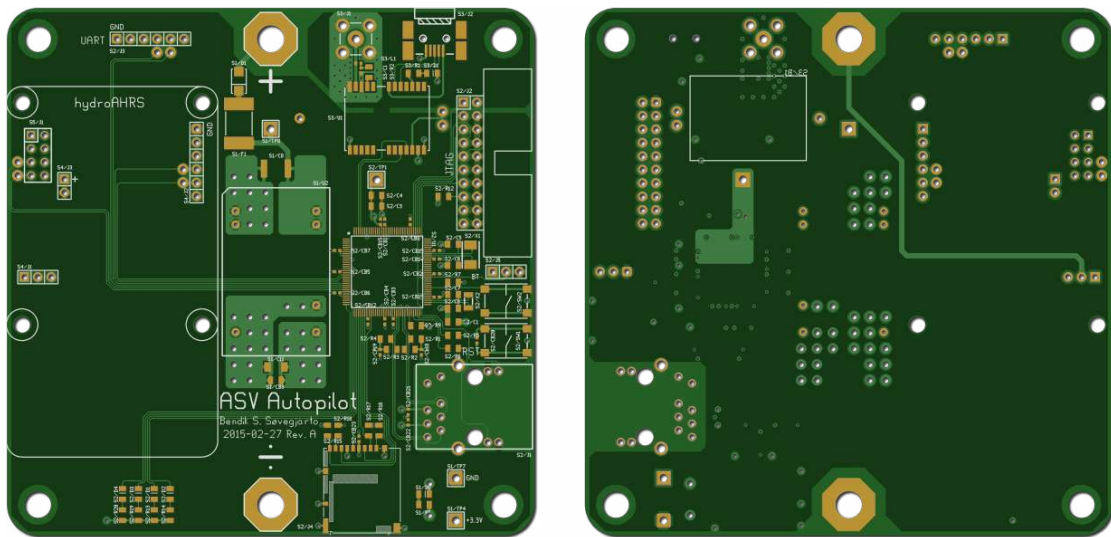


Figure 3.6: CGI of the autopilot PCB. Left image shows the top side, right image shows the bottom.

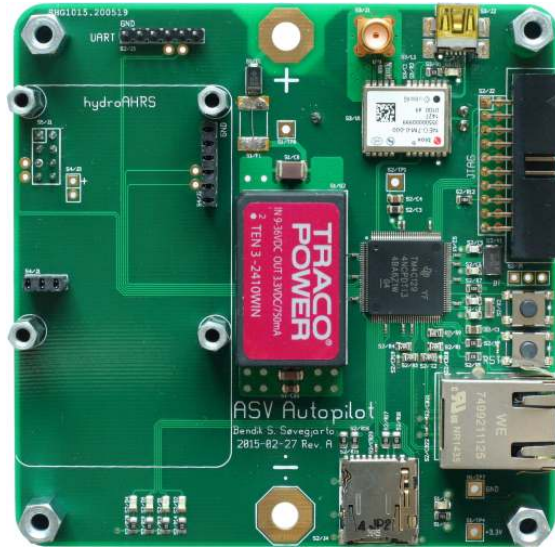


Figure 3.7: Photograph of assembled autopilot PCB.

### 3.3.6 Sub-system communication

All of the sub-systems communicate with the main MCU using UART. Inter-Integrated Circuit (I<sup>2</sup>C) was considered, but UART was found to be sufficient since the sub-systems do not need to share a bus or talk directly to each other. UART is simpler to work with since there is no need for a complex driver. Each sub-system talks to a separate UART instance in the MCU. When an interrupt signals the arrival of a new byte from each sub-system, this byte is put into a ring buffer with a common interrupt handler (shared by all sub-systems). A command counter for each sub-system is increased when a newline is received. The newline terminates one command.

### 3.3.7 Operator communication

We need a way for the operator to control the autopilot. The radio control (RC)-communication used for the remote work platform is too simple and does not have the required bandwidth. Ethernet is a widely used and robust network technology that is fitting for communication with the autopilot.

A radio link consisting of two Ubiquity PicoStation M2 delivers a stable, encrypted and long range wireless connection between the operator and the ASV. This was tested in paper 1. These units were chosen because they are *a)* omni-directional; *b)* physically small; *c)* inexpensive; and *d)* manufactured by a well-known company specializing in wireless bridges.

TM4C129 has Ethernet capabilities embedded, but still requires quite a lot of software to utilize (e.g. a real-time operating system (RTOS)<sup>4</sup> and an Internet Protocol (IP)-stack). In the terms of the OSI model, the physical-(PHY) and data link-layer (MAC) is embedded in hardware, while the network-(IP) and transport-layer (UDP/TCP) must be implemented in software. A different communication device was therefore initially used.

A very cheap (~5 USD) Chinese chip named ESP8266 incorporates a wireless transceiver and UART peripherals, all in one package. The transceiver was used as a serial to WiFi bridge, connecting the autopilot to one of the PicoStations. This was, however, found to be quite unreliable.

<sup>4</sup>A RTOS is not required, but convenient if multiple tasks needs to be performed at the same time.

The firmware was therefore adapted to run FreeRTOS and lwIP to utilize the hardware Ethernet capabilities of TM4C129 to send status messages. The ESP8266 was thus abandoned. User Datagram Protocol (UDP) was chosen as the transport-layer for the status messages as the loss of some status packets are tolerated. Incoming commands are transported using TCP, since their arrival is crucial. The Media Access Control (MAC)-address (02:41:53:56:00:00) was set to a local administered address [37].

The PicoStation functions as a wireless Access Point (AP), but not as a router since we want the MCU and the client to reside within the same network. We use static IP-addresses, as the PicoStation cannot function as a Dynamic Host Configuration Protocol (DHCP)-server without being a router. The MCU has the static address 192.168.0.21, the PicoStation 192.168.0.20, and the client a different address in the 192.168.0/24 range<sup>5</sup>.

Wireshark is a free<sup>6</sup> packet analyzer used to debug the Ethernet communication.

### 3.3.8 ASV Land

Many cycles of testing were needed while developing the autopilot. Since the autopilot relies on input from GNSS-satellites and needs room to move freely it is necessary to perform this testing outdoors. A land rover was therefore built to make the development of the autopilot easier. By using the land rover it is possible to test the autopilot frequently without having to travel to a lake. This saves time in planning, traveling and installation. It also makes it possible to mark the path (track) traveled on the ground. The land rover is shown in figure 3.8.

The land rover uses the same motor placement as a vessel on water. One motor on each side is connected to a wheel. A third wheel, which can rotate freely, is used for support. By controlling the rotational speed and direction of each of the side wheels, the land rover will steer and move in the same way as an ASV.

The land rover is built using plywood, wood, plastic laminated plates (Perstorp) and acrylic sheets. Two powerful motors were needed to drive the wheels. I visited a wrecking yard and bought two front wiper motors (8D1955113B by Bosch). Front wiper motors were chosen because of their high torque and easy availability.

The continues current draw of each of the motors without load was almost 5 A, while the peak inrush current was 8 A.

These motors have a RPM disk, making it possible to count their revolutions. Table 3.1 shows the revolutions per minute (RPM) for each of the motors. This was measured using an oscilloscope, with the disk connected through a pull-up resistor to a power source.

A maximum forward rotational speed of 110 RPM and a wheel diameter of 20 cm yields a maximum speed of about 4 km/h ( $20 \times \pi \times 110 \frac{\text{cm}}{\text{minute}}$ ).

Table 3.1: Rotational speed for the two front motor wipers used in the land rover.

Motor	Backward [rpm]	Forward [rpm]
Motor 1	92	112
Motor 2	83	128

The wheels are mounted to the motor by pressing a mounting bracket on the wheel to a rilled shaft on the motor. This type of mounting limits the size of the wheel, as it cannot withstand large forces perpendicular to the rotation of the wheel.

### 3.3.9 ASV control panel

A web-based control panel has been developed to control and monitor the autopilot. This control panel shows the location of the ASV on a map, its current and wanted heading, its distance to the first waypoint and other status information. The control panel automatically sends new waypoints to the autopilot when a waypoint has been passed (number of waypoints

<sup>5</sup>The slash notation /24 means that 24 bits is used for the address space, and the remaining 8 bit is used for the host address space. 8 bit gives us 256 host addresses (0–255).

<sup>6</sup>as in free speech

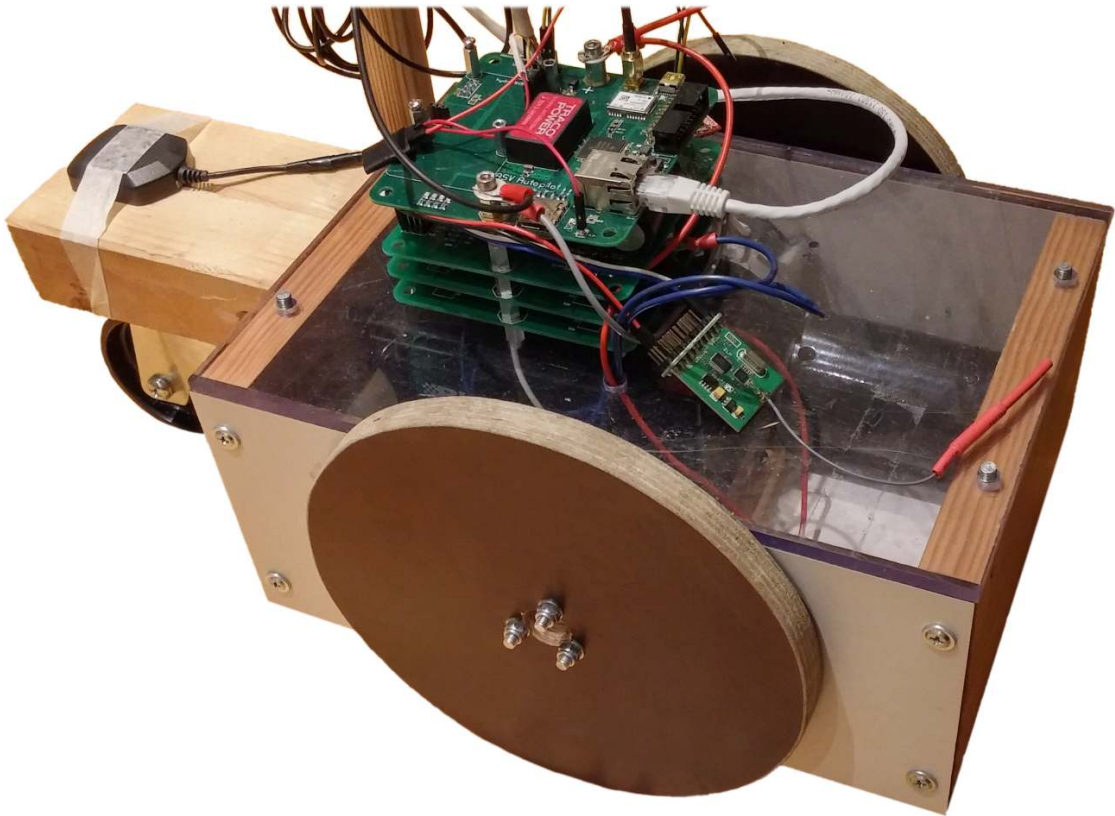


Figure 3.8: Photograph of land rover.

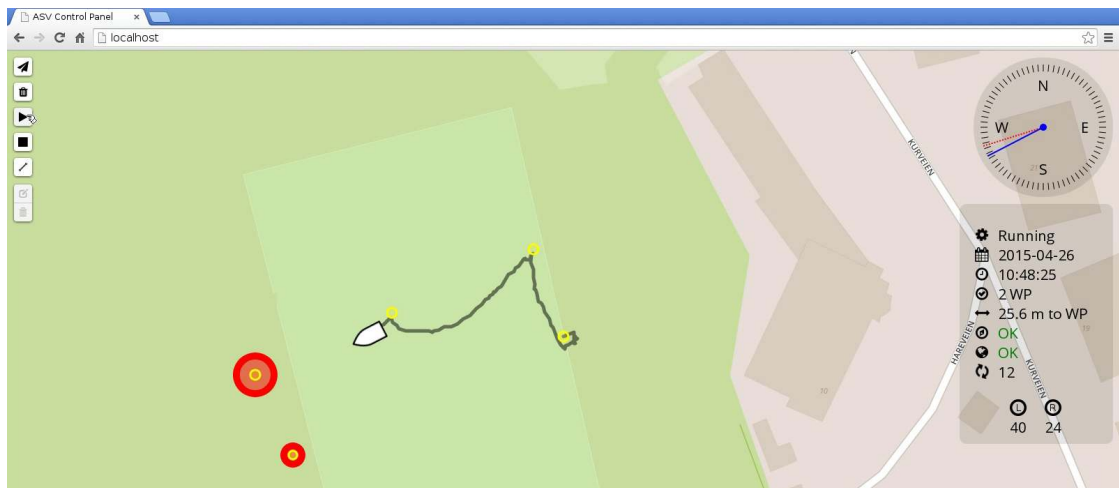


Figure 3.9: Web-based control panel for the ASV. The blue and red needle shows the current and wanted heading respectively. The red circles indicates the next waypoints.

reported by the autopilot is less than two). The control panel is shown in figure 3.9.

Node.js is used to relay the content of the UDP packages from the autopilot, over WebSockets (using Socket.IO) to the web-app. It also sends control messages over TCP back to the autopilot.

The code for the node.js server and control panel is listed in appendix N.

## 3.4 Testing and results

Three GNSS-receiver tests have been performed. One moving outdoor test, one long-term indoor stationary test and one short-term outdoor stationary test. The autopilot has been tested with the land rover.

### 3.4.1 Moving GNSS-receiver test

Keeping a GNSS-receiver stationary gives a good indication of its performance and this has been tested in the two consecutive test setups. We wanted, nevertheless, to make sure that there was no internal algorithm optimizing its position while being stationary. We would therefore like to test one of the GNSS-receivers (U-blox 6 PPP, with SBAS disabled) accuracy during movement. The best way to achieve this would be to have a good reference path for the GNSS-receiver to move along. A circular path created with a carousel was finally chosen because of the high accuracy and repeatability given by this setup. I built a large carousel using string, wood and aluminium bars as shown in figure 3.10. The antenna for the U-blox 6 PPP was placed at the end of the horizontal beam, 5.0 meter from the center.

The carousel was placed outside, and the data from the GNSS-receiver was recording to a Secure Digital (SD)-card using OpenLog<sup>7</sup>. We did four takes, moving the GNSS-receiver around multiple times at different speeds each time, as listed in table 3.2. The whole recording lasted 15 minutes and 50 seconds, and is showed

in figure 3.11. The two last takes had few data points because of their short period.

The recording of the first and second take, consisting of 10 cycles with an average period of 22.5 and 17.4 seconds respectively, is shown in figure 3.12. The center of the circle was found by averaging all the points along the circle (of the second take). It is therefore not the exact center, but the best measure I had available. By assuming the Earth is flat over the area around the circle, I found that 0.0027 arc minutes latitude and 0.0054 arc minutes longitude corresponds to 5 meters from the center. The samples are scaled accordingly, to show the distance from the center.

The deviation from the circle was found by a Python program. The program translated all the values to decimal degrees and calculated the haversine distance for each one to the center. This distance should be 5 meters. The sample size is too small to calculate standard deviation. The error for the second take is shown in figure 3.13, along with the latitudinal and longitudinal position referenced to the center. The error is less than  $\pm 1$  m for this take. The first (and other) take had worse latitudinal accuracy, as shown in figure 3.11 and 3.12.

### 3.4.2 Long-term indoor stationary GNSS test

Three of the GNSS-receivers considered for use with the autopilot were tested over a period of 40 days in a stationary setup. The receivers considered were U-blox 6 PPP, U-blox NEO-6M and Trimble Copernicus II. The U-blox 6 PPP had SBAS disabled. The Trimble stopped functioning during the setup. All of them were fixed to one location during the whole experiment. They were placed indoors in a window sill for weather-protection, as shown in figure 3.14. We thought the view of the sky was adequate, and the receivers were able to find a minimum of four satellites at most times. The receivers output NMEA-messages at an interval of 1 Hz which were logged by a computer over Universal Serial Bus (USB). Awk<sup>8</sup>

<sup>7</sup>SparkFun OpenLog is an open source data logger. Available at <https://www.sparkfun.com/products/9530>

<sup>8</sup>Awk is an interpreted programming language commonly used for data extraction and text processing



Figure 3.10: Carousel used in the moving GNSS-receiver test. The antenna of the GNSS-receiver is placed at the end of the horizontal beam. Joakim Myrland is standing next to it for scale.

Table 3.2: List of takes during carousel GNSS test.

Take #	From	To	# of per	Time [Sec]	Period [ $\frac{\text{Sec}}{\# \text{ of per}}$ ]
1	13:17:53	13:21:38	10	225	22.5
2	13:22:36	13:25:30	10	174	17.4
3	13:26:37	13:28:07	10	90	9.0
4	13:29:33	13:30:07	5	34	6.8

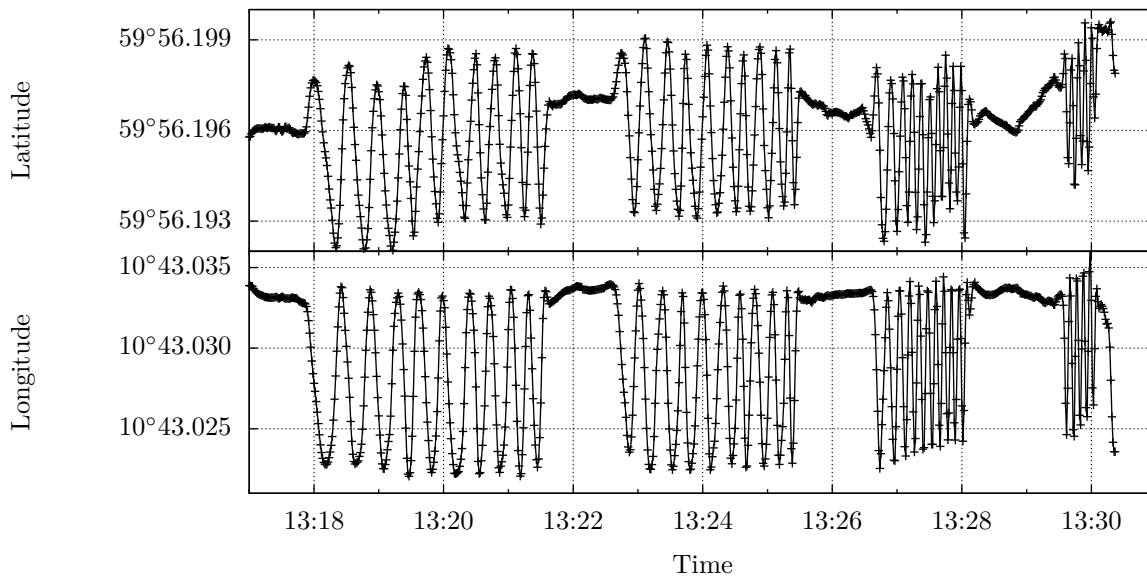


Figure 3.11: Raw latitude and longitude data of all takes in the GNSS carousel test.



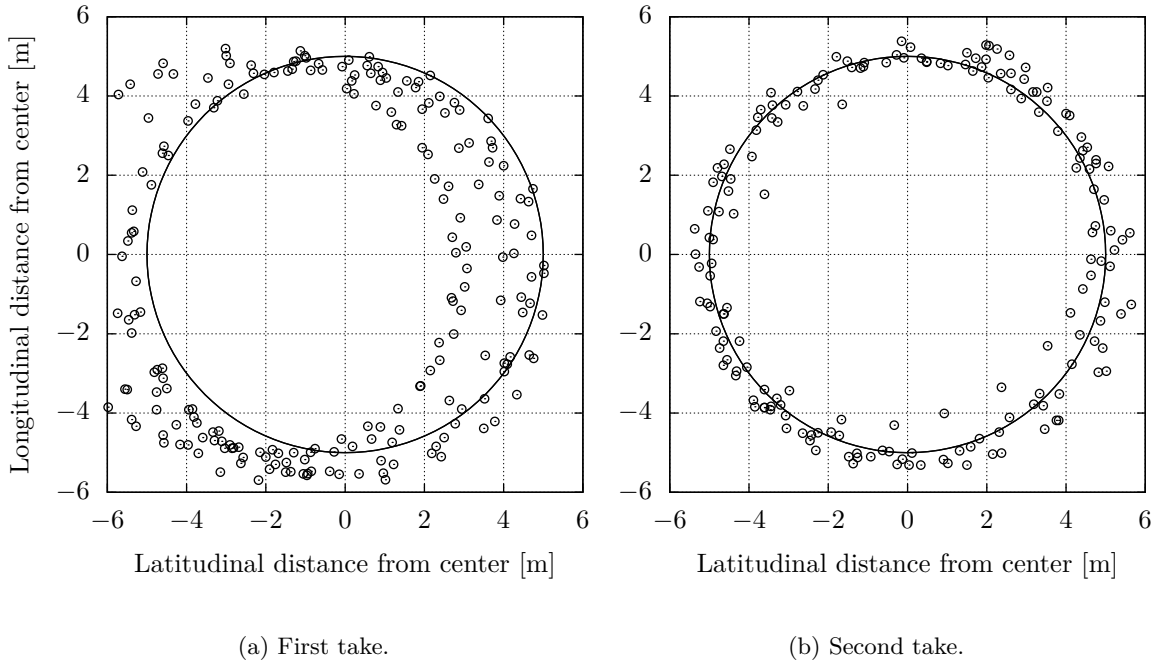


Figure 3.12: Plot of the distance from the center of the moving GNSS-circle to the mean, for the first and second take. The solid line marks the carousel.

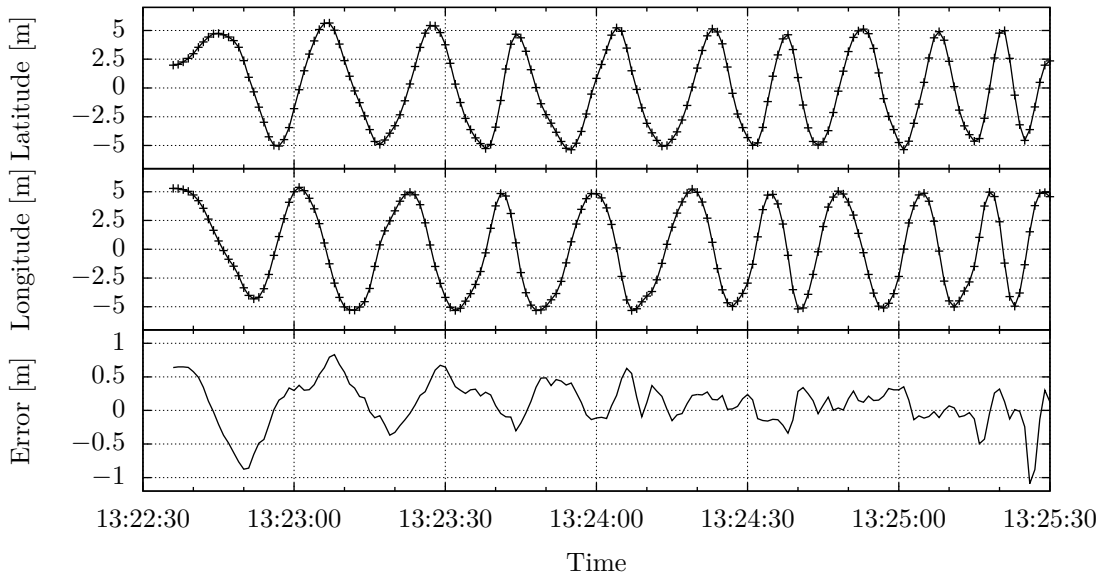


Figure 3.13: Data from the second GNSS carousel take. From the top: latitudinal position relative to center, longitudinal position relative to center and the error from the reference path.



Figure 3.14: The indoor stationary GNSS-receiver test setup. The gray box to the left is the U-blox 6 PPP evaluation kit with the black antenna. The red PCB is the Trimble Copernicus II with the beige antenna in the middle. The blue PCB is the U-blox NEO-6M with the rightmost beige antenna.

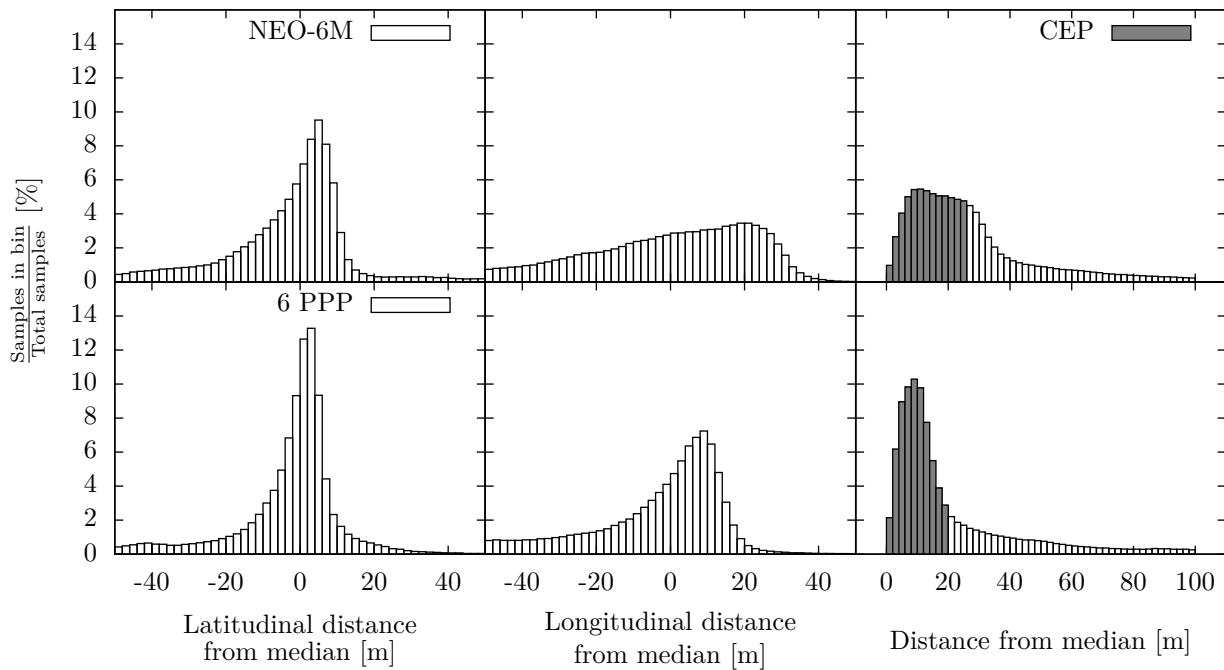


Figure 3.15: Histogram over the distance from the median coordinate, for the two GNSS-receivers used in the long time stationary indoor test. Bin-size is 2 m.

was used to extract the date, time, latitude and longitude position from the \$GPRMC message and number of satellites in view and horizontal dilution of precision (HDOP) from the \$GPGGA message, from 3.5 GB of NMEA 0183 (ASCII) data. The median location was calculated, and the distance from each sample was found using the haversine formula in Python. Figure 3.15 shows a histogram of the recorded data. U-blox 6 PPP had a CEP of 20.36 m, while U-blox NEO-6M had a CEP of 26.19 m in this test. Standard deviation was not calculated as the data did not have a normal distribution.

The results are discussed in section 3.5.1.

### 3.4.3 Outdoor stationary GNSS test

Due to the poor results from the indoor GNSS test, a new stationary test was set up outdoors. Five patch antennas were placed on the roof of a residential building, as shown in figure 3.16a. They were connected to five GNSS-receivers, as shown in figure 3.16b. Receiver 1 and 2 were U-blox NEO-M8N, receiver 3 and 4 were U-blox NEO-7M, and receiver 5 was the U-blox 6 PPP (with SBAS disabled) previously tested. Unit 1,2,3 and 4 were located on the autopilot PCB. Communication was initially done directly via USB to each GNSS-receiver, but USB communication for unit 1, 2, 3 and 4 stopped working after external power was applied to a USB-hub<sup>9</sup>. Data was therefore read from unit 1, 3 and 4 through the onboard MCU and sent to a computer via UART and FTDI (serial-to-USB) cables. Unit 2 was not used because not enough FTDI-cables were available.

The test was conducted over a period of 24 hours, in the same way as the indoor stationary test previously described.

The CEP, mean and standard deviation for each receivers latitudinal and longitudinal position was calculated, and is listed in table 3.3. A histogram of the data is shown in figure 3.17.

The results are discussed in section 3.5.2.

<sup>9</sup>The exact cause has not yet been investigated.

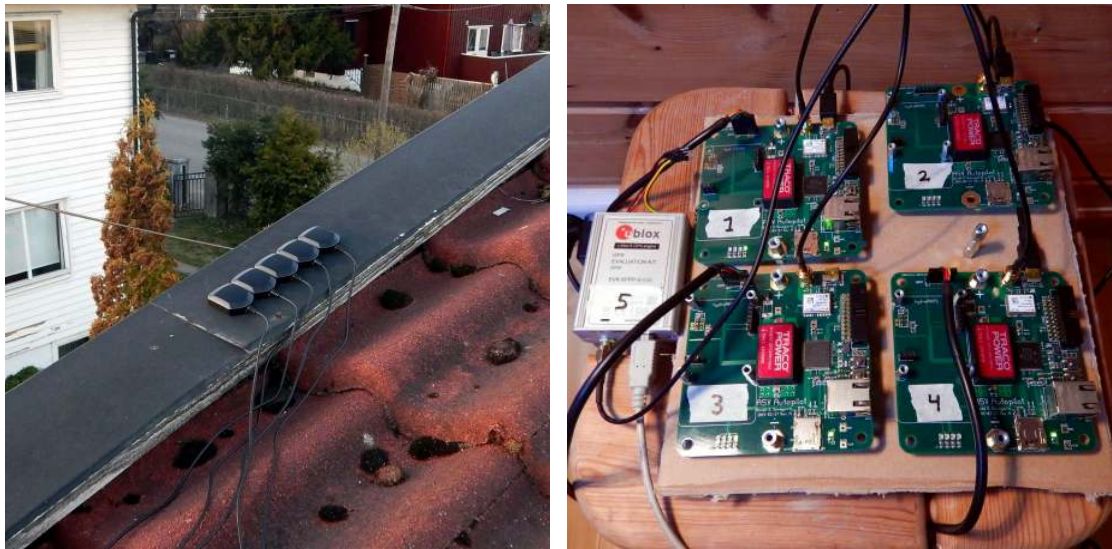
### 3.4.4 Autopilot test

The autopilot was tested on land using the land rover with a pre-defined route. I used the line-markings of a soccerfield with artificial turf to create a reference path for the autopilot to follow. I used a U-blox 7M GNSS-receiver to find the coordinates of five points on this path, by averaging the measured position for each point over a minimum of five minutes (shown in figure 3.20a). The coordinates were found six days before the test was conducted. These coordinates were used as waypoints for the autopilot.

Preliminary tests revealed that the compass behaved unreliable when mounted on top of the autopilot PCB. It suddenly changed its heading significantly within short time intervals. A LED indicating magnetic interference occasionally lit up as well. The compass was therefore mounted on top of a rod, to distance it from the magnetic fields from the motors and the GPS-antenna's magnet. The compass worked as expected and did no longer complain about interference after it was moved. The new setup is shown in figure 3.20b.

Another preliminary test showed oscillations when the land rover was trying to drive in a straight line. The autopilot tried to adjust its course, but overestimated the necessary thrust-difference needed to get the correct heading. The result was overshoot, and continues oscillations. The oscillations disappeared after the P-factor was decreased.

The land rover has different speeds on each motor, with the right wheel moving faster than the left wheel. This results in the land rover turning left, when the H-bridges is controlling them to move straight forward. This could have been fixed by implementing an adjustment factor for each individual motor in the steering controller of the H-bridge driver. We kept the asymmetrical behaviour, however, as it would give the autopilot a greater challenge and could be used as an simulation of the effect of external forces acting upon the ASV. For instance wind or a water current turning (but not drifting) the vessel.



(a) Five patch antennas mounted on a roof.

(b) Five GNSS-receivers. The four autopilot PCBs are shown to the right.

Figure 3.16: Outdoor stationary GNSS-receiver test setup.

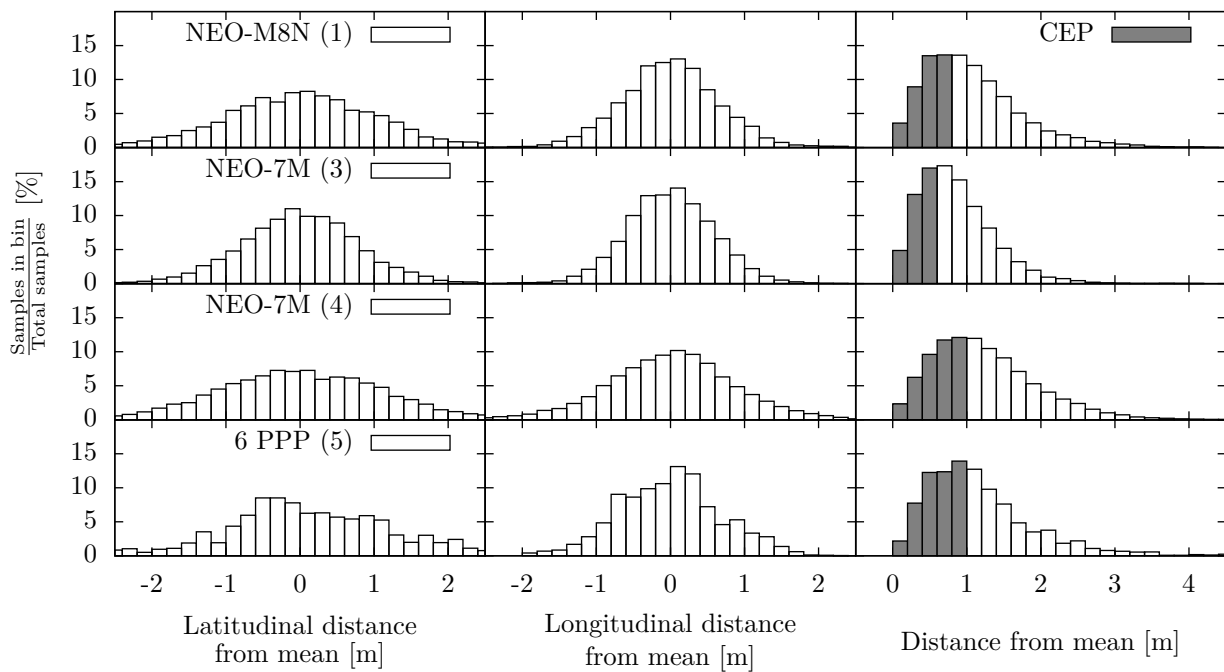


Figure 3.17: Histogram over the distance from the mean coordinate, for each of the four GNSS-receivers used in the outdoor stationary test. Bin-size is 20 cm.

Table 3.3: CEP, mean position and standard deviation ( $\sigma$ ) for the outdoor GNSS test.  
\* marks receivers with SBAS correction enabled.

Unit	U-blox model	# of samples	CEP [m]	Latitude		Longitude	
				Mean [°]	$\sigma$ [m]	Mean [°]	$\sigma$ [m]
1	NEO-M8N*	60613	0.95	59.9573828	1.06	10.7873568	0.65
2	NEO-M8N*	0	–	–	–	–	–
3	NEO-7M*	86400	0.77	59.9573831	0.80	10.7873701	0.58
4	NEO-7M*	86400	1.13	59.9573792	1.12	10.7873627	0.88
5	6 PPP	86400	1.02	59.9573799	1.26	10.7873459	0.68

The land rover was placed at the start coordinate, with its heading pointing away from the first waypoint. A laptop running the ASV control panel was connected to the wireless LAN (WLAN) AP on the land rover. After a connection was established, the land rover was started by sending the first waypoints and activating its motors. The ASV control panel automatically sent new waypoints after the land rover passed each one. Waypoints were approved as passed when the GNSS-receiver reported a position within a 2 m perimeter from the waypoint’s center. Status messages from the land rover was logged to file on the computer, as well as to an SD-card on the land rover (using the OpenLog).

To mark the path traveled, white stones were placed behind the support wheel of the land rover (located in the middle of the back). The distance from each stone to the reference line was later measured (together with its position along the line) as shown in figure 3.20c. The distance to the line was measured directly with a ruler and a tape measure. The position along the line was found by measuring the relative distance from the last stone. Since the relative measurements results in an accumulation of errors, each line position was finally scaled according to the total length of the line. A total of 243 stones were placed and measured.

The reference path and the path traveled (from the measured stones and GPS) are shown in figure 3.18. The wanted heading (calculated by the autopilot) and the current heading (mea-

sured by the compass) for the whole transect is shown in figure 3.19.

The results are discussed in section 3.5.3.

## 3.5 Discussion

The following paragraphs discusses test results, problems encountered and possible enhancements for further development of the autopilot.

### 3.5.1 Long-term indoor GNSS test

A median value was calculated instead of a mean because of outliers and a skewed distribution. The median gave a value closer to the mode. The latitudinal accuracy was better than the longitudinal. The window sill (and wall) the GNSS-receivers were placed in is almost parallel to the meridian, and this may explain the bad longitudinal accuracy.

It is clearly visible that the indoor test was inadequate, as the calculated CEP was greater than 20 meters. We found both very low accuracy and precision, much worse than the previous (moving) GNSS test. The long-term indoor GNSS test was thus incapable of giving an impression of the performance of the GNSS-receivers tested.

### 3.5.2 Outdoor GNSS test

The outdoor GNSS test was only conducted in 24 hours, but still provides an indication of the performance of the GNSS receivers. Unit

<sup>10</sup>gpsd is a free (as in free speech) GPS service daemon. <http://www.catb.org/gpsd/>

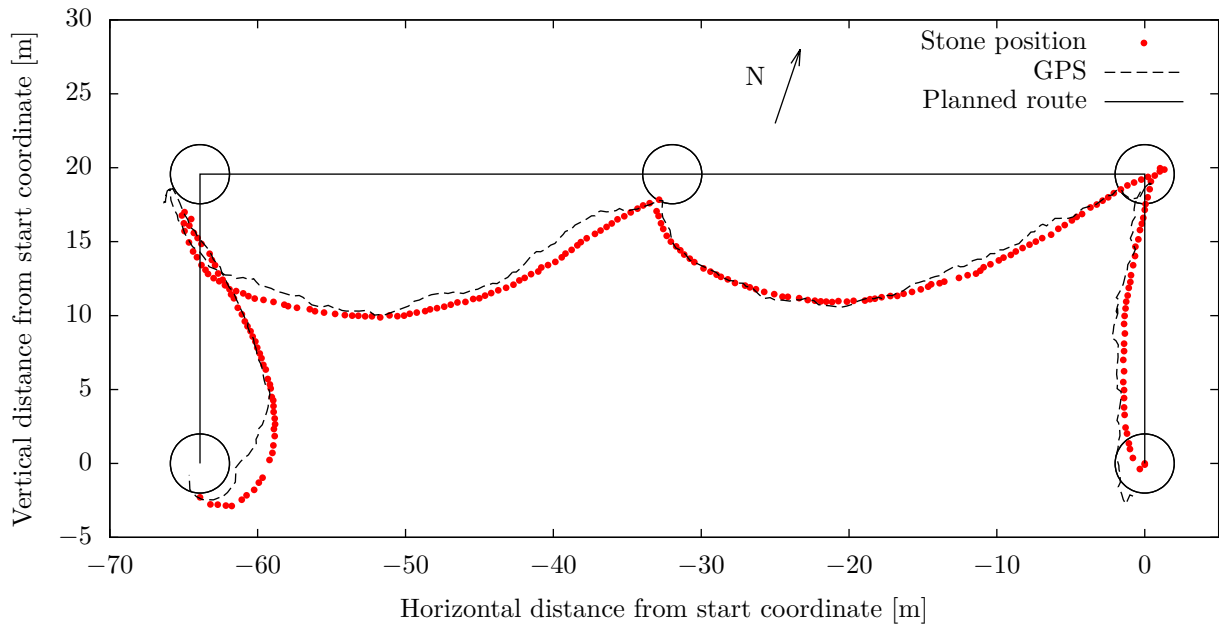


Figure 3.18: Reference path and path traveled (from measured stones and GPS) for the land rover autopilot test. The black circles have a radius of 2 meter, and shows the perimeter of the waypoints.

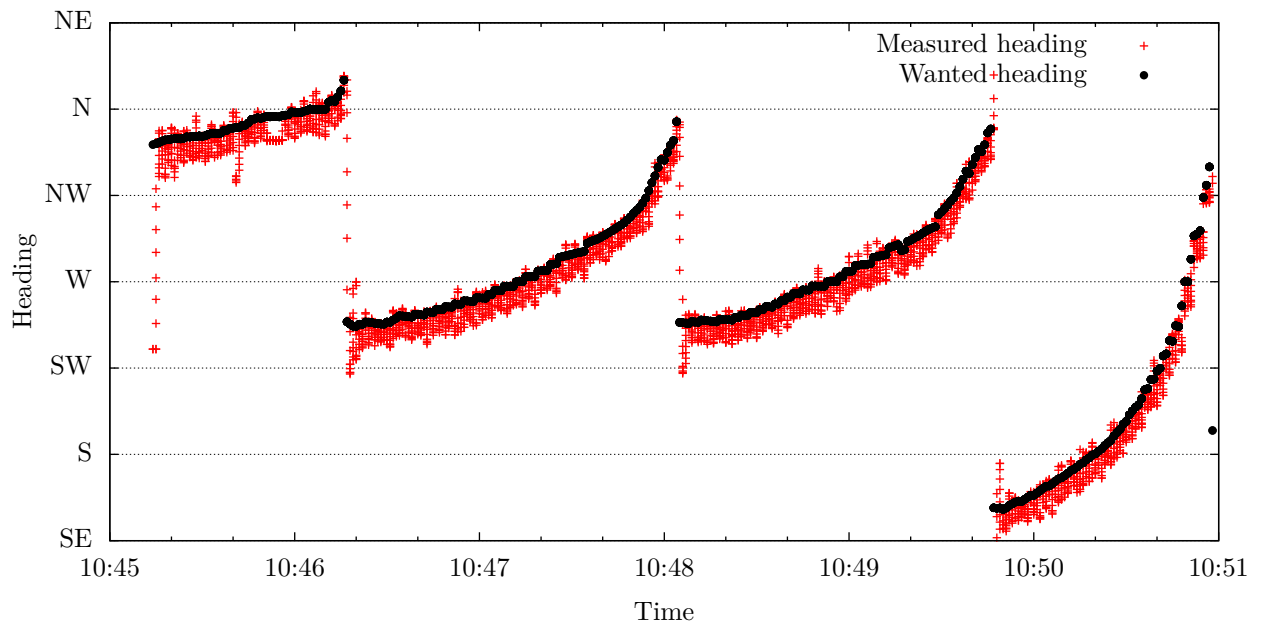


Figure 3.19: Wanted heading (calculated by the autopilot) and the measured heading (from the compass) for the land rover autopilot test.



(a) Setup used to find the coordinates for the waypoints. (b) Land rover with elevated compass. (c) Measurement setup to find stone positions.

Figure 3.20: Photographs of the autopilot test setup.

1 had almost 30% of its samples missing or invalid. This unit had earlier been automatically reconfigured by `gpsd`<sup>10</sup> and outputted different NMEA messages than the others. Some samples may have been corrupted when passing through the MCU. The longitudinal accuracy was consistently better than the latitudinal, and the overall accuracy was similar for all the receivers. SBAS was disabled (by error) on the U-blox 6 PPP, which makes a comparison between PPP and non-PPP units difficult. The accuracy found in the test was close to our expectations given by the manufacturer's specifications. A CEP of about 1 meter is sufficient for the autopilot.

### 3.5.3 Autopilot test

The autopilot managed to pass through all its waypoints (figure 3.18) and stopped when it arrived at the last. It did, however, drive between each waypoint in large arcs. We believe some of this behaviour is caused by the asymmetry in motor thrust, but the main source is the compass reporting wrong headings. This can be seen by the difference in the northbound (first and second waypoint) and the southbound (last and second to last waypoint) arc. The compass used should therefore be tested and investigated closer. The error may be solved

by using a lookup-table between the real and measured heading from the compass. This lookup table can be generated by driving in a full circle at a constant speed while the measured heading angles from the compass is recorded. Another way to create the look-up table would be to rotate the compass to certain known (relative) angles and interpolate the values in between.

The maximum deviation from the reference path was encountered halfway into the third arc, with a distance of  $\sim 9.69$  m to the reference line. The positions reported by the GPS corresponds to the measured stone positions, for the most part of the route.

The behaviour of the PID-regulator can be seen in figure 3.19. The measured heading is continuously adjusted to follow the wanted heading. The asymmetry of the motor thrust (that causes the rover to turn left) can also be seen, as the measured heading is almost always located below the wanted heading.

### 3.5.4 Communication enhancements

The status messages is currently sent to the broadcast address 255.255.255.255. This address should be changed to an address in the

IPv4 local multicast scope (239.255.0.0/16) [38, p. 2] to avoid spamming the whole network.

The new waypoint acknowledgement sent back from the autopilot to the control panel should be transferred using TCP instead of UDP, since the UDP package may never arrive.

### 3.5.5 Implement WebSocket in the MCU

The best way to communicate with the autopilot would be to use the Ethernet capabilities of the TM4C129 and implement a Hypertext Transfer Protocol (HTTP)- and WebSocket-server in the firmware. WebSockets enables full-duplex realtime communication directly between the front end software (webpage) and the MCU. The webpage could be served by the MCU, or preloaded on a device. By implementing everything in the MCU, very little is required by the operating device, just a modern web browser which supports WebSockets and other HyperText Markup Language (HTML) 5 components. Any laptop, tablet or even smart phone may thus be used to directly control the vessel, without the need of a separate computer.

### 3.5.6 PID-regulator

We are at the moment only using the P-factor in the PID-regulator, so it is currently only a P-regulator.

### 3.5.7 Approximations

The bearing changes while traveling between two points on a sphere (unless a rhumb line is followed), but we only use the initial bearing (forward azimuth) when calculating the course. We believe this is a valid approximation as the bearing is recalculated every second (after each GNSS-sample).

We assume a spherical Earth with a radius of 6373 km.

<sup>11</sup>as in free speech

### 3.5.8 Autopilot and motor driver communication bug

A bug between the autopilot and the motor driver results in the motor driver stopping the motors for a couple of seconds at seemingly random occasions. This happens if the motor drivers thinks the autopilot has stopped sending commands. The bug is visible 45 seconds into the heading-recordings of figure 3.19, as the heading-measurements were constant when the land-rover stopped temporarily. The cause of the bug has not yet been found.

### 3.5.9 ARM toolchain

The change to a new MCU platform was quite time-consuming, since a whole new toolchain was used. We faced challenges with the linker-script (mapping of the memory areas), dynamic memory allocation, and the floating-point unit (FPU). Stack-overflows and hardfaults were frequent. The final software solution has, however, proved to be stable and easy to expand. The use of a RTOS makes it very easy to add new tasks in seamless concurrency.

### 3.5.10 True north

The use of the magnetic north as reference instead of the true north causes an error when navigating. We believe this can be tolerated for the short distances (< 1 km) we operate with, and with the small magnetic declination of only  $\sim 2.8^\circ$  in Oslo [39, 40].

### 3.5.11 GNSS ready flag

The autopilot is currently halted if no GNSS fix is available. A more strict check could be used instead, e.g. by requiring a minimum number of visible satellites. Alternatively let the autopilot run, but warn the user that the accuracy is low.

### 3.5.12 Alternative software

OpenCPN is a free<sup>11</sup> chartplotter and navigation software project. It may be sensible to use



or integrate OpenCPN in the autopilot, as this is a mature and widely used software solution for maritime navigation.

Other free autopilot-systems exists, e.g. Parazzi [41]. Although this is made for aircrafts, some of the code base may be applicable for a maritime autopilot.

### 3.5.13 Future work

A more advanced autopilot should be developed before the system is usable for hydroacoustic work. It could for instance learn from its past history and use it to predict the future. A Kalman filter would be suitable for a new autopilot.

We had a requirement of constant speed. If minimum survey time is wanted, the thrust could be adjusted depending on the distance to the next waypoint.

Another improvement would be to use more than one waypoint to calculate the course. For instance by cutting corners and even out the track.

Additional sensors can be included in the control system to improve and extend the functionality of the ASV. For instance a RADAR for obstacle avoidance, an echosounder for depth measurements, water- and wind movement sen-

sors for improved navigation and a (IP) camera for supervision.

## 3.6 Summary

We have developed a primitive autopilot for hydroacoustic work using an inexpensive GNSS-receiver and our self-developed compass. The autopilot is running on a 32-bit ARM Cortex M4 MCU, and talks with its sub-systems (GNSS, compass, motor control system) over UART. A web-based control panel has been developed to observe and control the vessel. Status messages are transferred as UDP-packages over Ethernet to a nearby computer, which relays the data using WebSockets to the web-based control panel. Control commands are sent back to the autopilot using TCP.

Different positioning solutions have been considered, but the use of a single GNSS-receiver was chosen. Multiple GNSS-receivers have been tested, and a suitable low cost GNSS-receiver has been found.

A land rover has been built as a development platform for the autopilot. The autopilot has been tested on the land rover, and was able to follow waypoints, but with significant deviations from the planned route.



# Summary

This thesis has presented the development of an Automatic Survey Vessel (ASV). The goal was to create a low cost control unit which could be placed in an arbitrarily selected full-sized boat and be used to make hydroacoustic surveying on lakes more efficient. The work was presented through three papers.

Paper 1, «Construction of a remote controlled work platform for hydroacoustic work», covered the development of a motor control system. Two attempts on creating a (H-bridge based) motor control system for brushed DC-motors have been completed and is described in this paper. The final attempt has proved itself capable of controlling electric outboard engines powerful enough to propel a full-sized vessel. The motor control system has remote control functionality which allows an operator to steer the vessel from a distance. Tests conducted with a vessel carrying a scientific echosounder (with wireless transmission of data) has shown that the system can be used as a remote controlled work platform for hydroacoustic work. It was observed that switching-noise generated by the motor control system may corrupt hydroacoustic recordings, and this must be addressed in the future.

Paper 2, «Construction of an Attitude and Heading Reference System (AHRS)», covered the development of a compass needed for navigation. Three attempts on creating an AHRS have been completed and is described in this paper. The tilt-calculations from the second attempt has been tested in controlled- and fieldwork-environments and fulfilled our criteria of repeatability and accuracy. Preliminary testing of the heading-calculations (compass) from the third attempt showed promising results.

Paper 3, «Construction of a primitive autopilot for hydroacoustic work», used the motor control system from paper 1 and the compass from paper 2 to create a simple autopilot able to steer a full-sized boat. A primitive proof-of-concept autopilot capable of following dynamically generated waypoints has been developed and is described in this paper. The autopilot has been tested on a land rover with partial success. The land rover was able to pass through all waypoints, but with significant deviations from the planned route.

We have accomplished to lay the foundation for an Automatic Survey Vessel (ASV) platform, with a functional motor control system, a compass and a simple autopilot. A more advanced autopilot can reuse all the hardware developed, or just the motor control system and compass. Future work includes *a)* suppressing the motor-switching noise; *b)* testing and improving the reliability of the compass; *c)* incorporating a (local) DGPS solution; *d)* improving the autopilot, e.g. with the use of a Kalman filter; *e)* extending the ASV with multiple sensors (cameras, echosounders, RADAR, water- and wind movement); *f)* including obstacle avoidance and other safety requirements; and *g)* finally making the ASV autonomous with ongoing route planning.



# Closing

I have learned a lot, and had much fun while working on my master thesis. 13 printed circuit boards (PCBs) have been designed in association with this project, where 8 have been directly involved (and described in this thesis). This includes designs used to get familiar with some of the components, but excluding revisions with only minor adjustments. A total of 52 PCBs have been assembled.

I have designed hardware, written firmware in C, computer software in Python, JavaScript (node.js), Awk and Bash, as well as web-based front-ends in JavaScript/HTML/CSS.



# Appendix A

## Installation of ASV control panel

The following guide will install and setup the ASV control software on a fresh install of Debian Wheezy where sudo access is available. Installation of Debian is not covered in this guide, but can in short steps be done as described in step 0.

1. Add a new package repository (Debian Backports) to get access to newer software, e.g. nodejs.  

```
echo "deb http://http.debian.net/debian wheezy-backports main" | sudo tee -append /etc/apt/sources.list
```
2. Update available packages  

```
sudo apt-get update
```
3. Install nodejs (with a symlink for node)  

```
sudo apt-get -t wheezy-backports install nodejs-legacy
```
4. Install git and chromium, as well as necessary software to install npm  

```
sudo apt-get install git chromium curl build-essential
```
5. Install and update npm  

```
curl -L -insecure https://www.npmjs.org/install.sh | sudo sh  
sudo npm install -g npm
```
6. Install ASV control panel  

```
mkdir git && cd git  
git clone https://github.com/epsiro/ASV_control_panel  
cd ASV_control_panel/
```
7. Install node-static and socket.io  

```
npm install node-static  
npm install socket.io
```
8. Start the ASV control panel server  

```
nodejs server.js
```
9. Open the URL localhost with chromium. Alternatively the ASV control panel may be accessed from another computer/tablet/smartphone by opening the IP address of the machine were the ASV control panel was installed. E.g. 192.168.4.101 . The IP address may be found using ifconfig.

## 0. Install Debian Wheezy

- (a) Download the correct image (e.g. amd64 live) from [debian.org](http://debian.org)
- (b) Use `dd` on a linux machine to transfer the image to a usb stick. Be very careful not to overwrite the wrong device! e.g. `dd if=debian.img of=/dev/sdX bs=4M; sync` where `X` is your device.
- (c) Insert the USB stick into a computer and choose it as the preferred boot device by configuring the BIOS.
- (d) Follow the on-screen instructions for installation.
- (e) Add yourself to the `sudo` group to get SuperUser privileges by editing the file `/etc/group` as root and log off and on again.
  - `su`
  - then
  - `nano /etc/group`
- (f) Configure network



## Appendix B

# PCB production

One PCB is designed in Zuken CadStar. All other PCBs have been designed in gEDA pcb<sup>1</sup>. My self-developed footprint editor<sup>2</sup> was used to create many of the footprints. Very little proprietary software has been used in my master thesis.

The large PCBs (involved with the H-bridge) is mainly manufactured by Seeed Technology Inc. a company located in Shenzhen, China. The smaller PCBs is manufactured by OshPark, located in the United States of America (US). This is due to their different price models. The autopilot PCB was manufactured through the electronic laboratory at UiO's connection with Elprint.

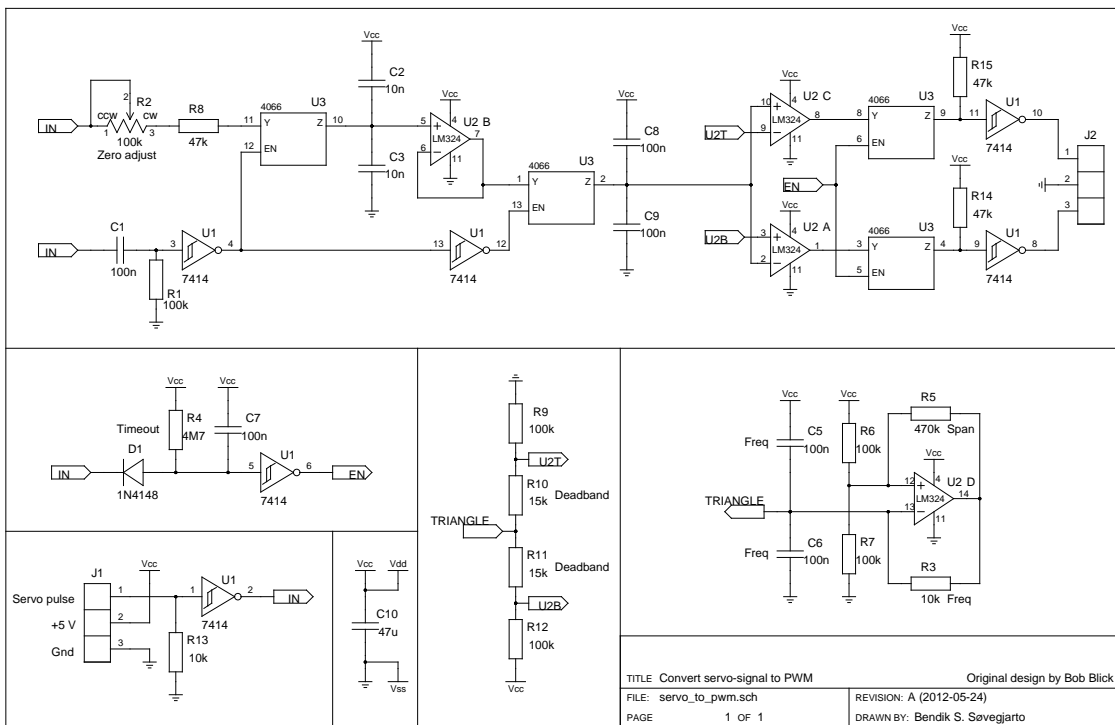
---

<sup>1</sup><http://pcb.geda-project.org/>

<sup>2</sup>Code available at <https://github.com/epsiro/pcb-footprint-editor>. Demo available at <http://pcb.zone> in 2015.

# Appendix C

## Analog steering controller schematics



## Appendix D

# MCU code for servo to PWM signal converter

Code for ATtiny85 proof of concept servo to PWM signal converter, as described in section 1.3.1. The code is licensed under the MIT license (listing D.1).

Listing D.1: MIT license

---

```
1 Copyright (c) 2015 Bendik S. Søvvegjarto
2
3 Permission is hereby granted, free of charge, to any person obtaining a copy
4 of this software and associated documentation files (the "Software"), to deal
5 in the Software without restriction, including without limitation the rights
6 to use, copy, modify, merge, publish, distribute, sublicense, and/or sell
7 copies of the Software, and to permit persons to whom the Software is
8 furnished to do so, subject to the following conditions:
9
10 The above copyright notice and this permission notice shall be included in
11 all copies or substantial portions of the Software.
12
13 THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR
14 IMPLIED, INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY,
15 FITNESS FOR A PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE
16 AUTHORS OR COPYRIGHT HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER
17 LIABILITY, WHETHER IN AN ACTION OF CONTRACT, TORT OR OTHERWISE, ARISING FROM,
18 OUT OF OR IN CONNECTION WITH THE SOFTWARE OR THE USE OR OTHER DEALINGS IN
19 THE SOFTWARE.
```

---

code/attiny\_servo\_to\_pwm.c

---

```
1 #define F_CPU 1000000
2
3 #include <avr/io.h>
4 #include <avr/interrupt.h>
5 #include <util/delay.h>
6
7 #define SPEED PB1
8 #define DIR PB3
9 #define DD_SPEED DDB1
10 #define DD_DIR DDB3
11 #define SERVO_IN PCINT4
12
13 void init() {
14     /* ***** For the out signal ***** */
15     /* SPEED and DIR as output */
16     DDRB = 1<<SPEED | 1<<DIR;
17
18     /* Mode: Fast PWM */
```

```

19  TCCR0A = 1<<WGM01 | 1<<WGM00;
20  TCCR0B = 0<<WGM02;
21
22  /* No prescaling of counter clock */
23  TCCR0B |= 0<<CS02 | 0<<CS01 | 1<<CS00;
24
25  /* OC0B on Compare Match, sets OC0B at BOTTOM */
26  TCCR0A |= 1<<COM0B1 | 0<<COM0B0;
27
28  /****** For the in signal *****/
29  /* Globally enable interrupts */
30  sei ();
31
32  /* Enable pin change interrupt */
33  GIMSK = 1<<PCIE;
34
35  /* Pin change interrupt is enabled on SERVO_IN */
36  PCMSK = 1<<SERVO_IN;
37
38  /* Start a normal counter1 with prescaler /4 */
39  TCCR1 = 1<<CS11 | 1<<CS10;
40 }
41
42 void main() {
43     init ();
44
45     for (;;) {
46     }
47 }
48
49 ISR(PCINT0_vect) {
50     /* We have a rising edge */
51     if (PINB & 1<<PB4) {
52         /* Reset counter1 */
53         TCNT1 = 0;
54
55         /* We have a falling edge */
56     } else {
57         if (TCNT1 >= 123 && TCNT1 <= 131) {
58             PORTB &= ~1<<DIR;
59             OCR0B = 0;
60         } else {
61             /* Motor forward */
62             if (TCNT1 & 0x80) {
63                 PORTB |= 1<<DIR;
64
65                 /* Set the PWM duty cycle */
66                 /* (TCNT1 - 0x80)*2 */
67                 OCR0B = (TCNT1 & ~0x80)<<1;
68
69                 /* Motor backward */
70             } else {
71                 PORTB &= ~1<<DIR;
72
73                 /* Set the PWM duty cycle */
74                 OCR0B = (0x80 - TCNT1)<<1;
75             }
76         }
77     }
78 }
79 }

```

---

## Appendix E

# MCU code for first digital steering controller

Code for ATxmega128A1 digital steering controller, as described in section 1.3.1. The code is licensed under the MIT license (listing D.1).

code/HIP4081\_controller/main.c

---

```
1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <stdio.h>
4 #include <stdint.h>
5
6 #define F_CPU 8000000UL
7
8 #define PWM_FREQ 1000L
9 #define PER (F_CPU/PWM_FREQ)
10
11 #define sec_to_tic(sec) sec*F_CPU
12 #define CENTER_LEN 1.5e-3
13 #define DEAD_BAND 0.02e-3
14
15 #define LEFT_STICK 0x00
16 #define RIGHT_STICK 0x01
17
18 static int16_t min_pulse_width[2];
19 static int16_t max_pulse_width[2];
20
21 static int8_t left_stick;
22 static int8_t right_stick;
23
24 static int16_t left_motor_fwd;
25 static int16_t left_motor_bck;
26 static int16_t right_motor_fwd;
27 static int16_t right_motor_bck;
28
29 void
30 clock_init() {
31
32     /* RC2M->PLL, 4x multiplier */
33     OSC.PLLCTRL = OSC_PLLSRC_RC2M_gc | 0x4;
34
35     /* Start PLL */
36     OSC.CTRL |= OSC_PLEN_bm;
37
38     /* Wait for PLL to become stable */
39     while (!(OSC.STATUS & OSC_PLLRDY_bm));
40
41     /* Disable ccp "Configuration Change Protection" */
```

```

42     CCP = CCP_IOREG_gc;
43
44     /* Use PLL output as the system clock */
45     CLK_CTRL = CLK_SCLKSEL_PLL_gc;
46 }
47
48 void
49 servo_init() {
50
51     /* Configure PC0 and PC1 for input */
52     PORTC.DIRCLR = PIN0_bm;
53     PORTC.DIRCLR = PIN1_bm;
54
55     /* Interrupt on both edges. */
56     PORTC.PIN0CTRL = PORT_ISC_BOTHEDGES_gc;
57     PORTC.PIN1CTRL = PORT_ISC_BOTHEDGES_gc;
58
59     /* Select PC0 as input to event channel 0,
60      * and PC1 as input to event channel 1. */
61     EVSYS.CHMUX = EVSYS_CHMUX_PORTC_PIN0_gc;
62     EVSYS.CHMUX = EVSYS_CHMUX_PORTC_PIN1_gc;
63
64     /* Pulse width capture, using event channel 0 and 1 */
65     TCC1.CTRLD = TC_EVSEL_CH0_gc | TC_EVACT_PW_gc;
66
67     /* Enable Input "Capture or Compare" channel A and B. */
68     TCC1.CTRLB |= TC0_CCAEN_bm | TC0_CCBEN_bm;
69
70     /* Set the period of the counter. Quote from datasheet:
71      *
72      * If the Period register value is set lower than 0x8000, the polarity of
73      * the I/O pin edge will be stored in the Most Significant Bit (MSB) of the
74      * Capture register after a Capture. If the MSB of the Capture register is
75      * zero, a falling edge generated the Capture. If the MSB is one, a rising
76      * edge generated the Capture.
77      */
78     TCC1.PER = 0x7fff;
79
80     /* Start timer by selecting a clock source. No prescaling. */
81     TCC1.CTRLA = TC_CLKSEL_DIV1_gc;
82
83     /* Enable CCA and CCB interrupt */
84     TCC1.INTCTRLB = TC_CCAINTLVL_LO_gc | TC_CCBINTLVL_LO_gc;
85 }
86
87 void
88 pwm_init() {
89
90     /* Enable the PWM outputs, PE0, PE1, PE2 and PE3 */
91     PORTE.DIR = 0b00001111;
92
93     /* Set the TC period */
94     TCE0.PER = PER;
95
96     /* Configure the TC for single slope mode. */
97     TCE0.CTRLB = TC_WGMODE_SS_gc;
98
99     /* Enable Compare channel A, B, C and D. */
100    TCE0.CTRLB |= TC0_CCAEN_bm | TC0_CCBEN_bm | TC0_CCCEN_bm | TC0_CCDEN_bm;
101
102    /* Start timer by selecting a clock source. No prescaler. */
103    TCE0.CTRLA = TC_CLKSEL_DIV1_gc;
104 }
105
106 void
107 int_init() {
108
109     /* Enable low level interrupt */
110     PMIC_CTRL |= PMIC_LOLVLEN_bm;
111
112     /* Enable global interrupt */
113     sei();
114 }

```

```

115
116 int
117 main() {
118
119     // TODO: Save this in eeprom and add calibration button
120     min_pulse_width[LEFT_STICK] = -3431L;
121     max_pulse_width[LEFT_STICK] = 3379L;
122
123     min_pulse_width[RIGHT_STICK] = -3423L;
124     max_pulse_width[RIGHT_STICK] = 3385L;
125
126     clock_init();
127     servo_init();
128     pwm_init();
129     int_init();
130
131     for(;;) {
132     }
133 }
134
135 void
136 calibrate_stick(int16_t * pulse_width, int8_t stick) {
137
138     if (*pulse_width < min_pulse_width[stick]) {
139         min_pulse_width[stick] = *pulse_width;
140     }
141
142     if (*pulse_width > max_pulse_width[stick]) {
143         max_pulse_width[stick] = *pulse_width;
144     }
145 }
146
147 void
148 drive_motors() {
149
150     /* Left and right stick is in middle position (within dead band) =>
151     * Stand still */
152     if (left_stick == 0 && right_stick == 0) {
153         left_motor_fwd = 0;
154         left_motor_bck = 0;
155         right_motor_fwd = 0;
156         right_motor_bck = 0;
157
158         /* Turn on the spot */
159     } else if (left_stick == 0) {
160         left_motor_fwd = right_stick;
161         left_motor_bck = -right_stick;
162         right_motor_fwd = -right_stick;
163         right_motor_bck = right_stick;
164
165         /* Drive forwards */
166     } else if (left_stick > 0) {
167         left_motor_fwd = left_stick + right_stick;
168         left_motor_bck = 0;
169         right_motor_fwd = left_stick - right_stick;
170         right_motor_bck = 0;
171
172         /* Drive backwards */
173     } else if (left_stick < 0) {
174         left_motor_fwd = 0;
175         left_motor_bck = -left_stick - right_stick;
176         right_motor_fwd = 0;
177         right_motor_bck = -left_stick + right_stick;
178     }
179
180     /* Remove overflows */
181     if (left_motor_fwd < 0) left_motor_fwd = 0;
182     if (left_motor_bck < 0) left_motor_bck = 0;
183     if (right_motor_fwd < 0) right_motor_fwd = 0;
184     if (right_motor_bck < 0) right_motor_bck = 0;
185
186     if (left_motor_fwd > 127) left_motor_fwd = 127;
187     if (left_motor_bck > 127) left_motor_bck = 127;

```

```

188     if (right_motor_fwd > 127) right_motor_fwd = 127;
189     if (right_motor_bck > 127) right_motor_bck = 127;
190
191     /* Set PWM registers */
192     TCE0_CCABUF = (left_motor_fwd*PER)/127L;
193     TCE0_CCBBUF = (left_motor_bck*PER)/127L;
194     TCE0_CCCBUF = (right_motor_fwd*PER)/127L;
195     TCE0_CCDBUF = (right_motor_bck*PER)/127L;
196 }
197
198 int8_t
199 normalize_stick_position(int16_t * pulse_width, uint8_t stick) {
200     int16_t stick_position;
201
202     /* Center around 1.5 ms */
203     *pulse_width -= sec_to_tic(CENTER_LEN);
204
205     /* Dead band */
206     if (abs(*pulse_width) <= sec_to_tic(DEAD_BAND)) {
207         stick_position = 0;
208     }
209
210     /* Scale to -127..127 */
211     } else if (*pulse_width >= 0) {
212         stick_position = (*pulse_width * 127L)/max_pulse_width[stick];
213     } else {
214         stick_position = -(*pulse_width * 127L)/min_pulse_width[stick];
215     }
216
217     /* 125..127 -> 127 */
218     if (stick_position >= 125) {
219         stick_position = 127;
220     }
221
222     return (int8_t) stick_position;
223 }
224
225 ISR(TCC1_CCA_vect) {
226     left_stick = normalize_stick_position(&TCC1_CCA, LEFT_STICK);
227     drive_motors();
228 }
229
230 ISR(TCC1_CCB_vect) {
231     right_stick = normalize_stick_position(&TCC1_CCB, RIGHT_STICK);
232 }

```

---

### code/HIP4081\_controller/Makefile

---

```

1 DEVICE = atxmega128a1
2 AVRDUDE = avrdude -p x128a1 -c jtagmkII -P usb
3 AVARICE = avarice --mkII -j usb --xmega :4242

5 CC = avr-gcc
6 CFLAGS = -g -W -Wall -O2 -std=c99 -mmcu=$(DEVICE)

8 OBJECTS = main.o

10 debug: flash
11     sleep 2
12     $(AVARICE) &

14 flash: all
15     $(AVRDUDE) -U flash:w:main.hex:i

17 all:    main.hex

19 clean:
20     rm -f *.hex *.lst *.o *.bin

22 $(OBJECTS): | depend

24 main.bin: $(OBJECTS)

```

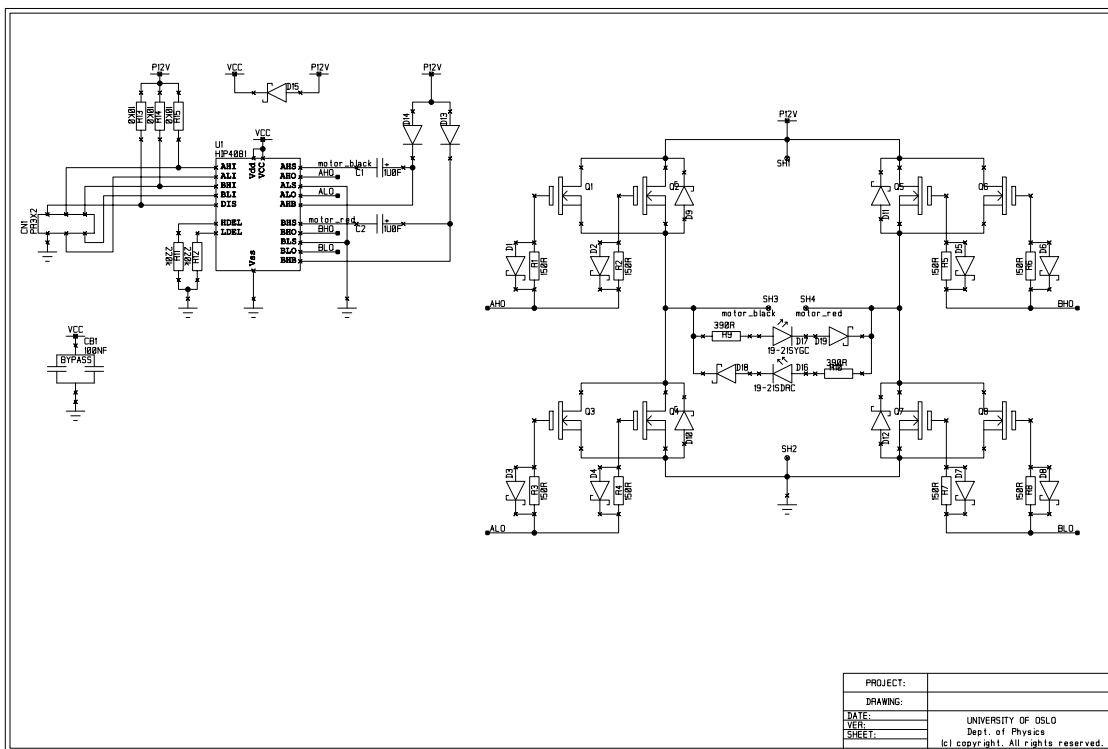


```
25     $(CC) $(CFLAGS) -o main.bin $(OBJECTS)
27 main.hex: main.bin
28     avr-objcopy -j .text -j .data -O ihex main.bin main.hex
29     avr-size --totals *.o
31 read:
32     $(AVRDUDE) -U eeprom:r:eeprom.dat:r
33     hd eeprom.dat
35 depend:
36     @$$(CC) -MM $(ALL_CFLAGS) *.c | sed 's/$$/ Makefile/'
```

---

# Appendix F

## First H-bridge schematics



## Appendix G

# MCU code for ASV H-bridge controller

Code for ATxmega32A4U digital steering and H-bridge controller, as described in section 1.3.2. A block schematic of the program flow is shown in figure 1.18. The code is also available at [https://github.com/epsiro/ASV\\_H-bridge\\_controller](https://github.com/epsiro/ASV_H-bridge_controller) and is licensed under GNU General Public License (GPL) version 2 or later.

code/ASV\_H-bridge\_controller/main.c

---

```
1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <stdio.h>
4 #include <stdint.h>
5 #include <stdlib.h>
6
7 #define BAUD_D 9600UL
8 #define BSEL_D(baud) ((F_CPU / (16 * baud)) - 1)
9
10 #define PWM_FREQ 1000L
11 #define PER (F_CPU/(2*PWM_FREQ))
12
13 #define STATE_FREQ 100L
14 #define STATE_PER ((F_CPU/8)/STATE_FREQ)
15
16 #define RC_FREQ 50L
17
18 #define NUMBER_OF_RC_CMD_TO_AVERAGE 8
19
20 #define sec_to_tic(sec) sec*F_CPU
21 #define CENTER_LEN 1.5e-3
22 #define DEAD_BAND 0.04e-3
23
24 #define ON PER
25 #define OFF 0
26 #define DELAY (4*PER)/100L
27
28 #define LEFT_STICK 0x00
29 #define RIGHT_STICK 0x01
30
31 #define LEFT_MOTOR 0x00
32 #define RIGHT_MOTOR 0x01
33
34 #define FORWARD 1
35 #define BACKWARD -1
36
37 #define AH 0x00
38 #define AL 0x01
```

```

39 #define BH 0x02
40 #define BL 0x03
41
42 #define TRUE 0x01
43 #define FALSE 0x00
44
45 static volatile uint16_t* drive[4][2] = {
46     {&TCC0_CCABUF, &TCC0_CCCBUF},
47     {0, 0},
48     {&TCC0_CCBBUF, &TCC0_CCDBUF},
49     {0, 0}};
50
51 static int16_t min_pulse_width[2];
52 static int16_t max_pulse_width[2];
53
54 static int8_t left_stick = 0;
55 static int8_t right_stick = 0;
56 static int8_t average_motor_thrust[2] = {0, 0};
57
58 static volatile int8_t uart_motor_thrust[2] = {0, 0};
59
60 static volatile uint32_t number_of_rc_commands = 0;
61 static volatile uint32_t number_of_runs_without_rc_command = 0;
62 static volatile uint8_t rc_receiver_ready = FALSE;
63
64 static volatile uint32_t number_of_uart_commands = 0;
65 static volatile uint32_t number_of_runs_without_uart_command = 0;
66 static volatile uint8_t uart_receiver_ready = FALSE;
67
68 void
69 clock_init() {
70
71     /* RC2M->PLL, 4x multiplier */
72     OSC.PLLCTRL = OSC_PLLSRC_RC2M_gc | 0x4;
73
74     /* Start PLL */
75     OSC.CTRL |= OSC_PLEN_bm;
76
77     /* Wait for PLL to become stable */
78     while (!(OSC.STATUS & OSC_PLLRDY_bm));
79
80     /* Disable ccp "Configuration Change Protection" */
81     CCP = CCP_IOREG_gc;
82
83     /* Use PLL output as the system clock */
84     CLK.CTRL = CLK_SCLKSEL_PLL_gc;
85 }
86
87 void
88 rc_init() {
89
90     /* Configure PB0 and PB1 for input */
91     PORTB.DIRCLR = PIN0_bm;
92     PORTB.DIRCLR = PIN1_bm;
93
94     /* Interrupt on both edges. */
95     PORTB.PIN0CTRL = PORT_ISC_BOTHEDGES_gc;
96     PORTB.PIN1CTRL = PORT_ISC_BOTHEDGES_gc;
97
98     /* Select PB0 as input to event channel 0,
99      * and PB1 as input to event channel 1. */
100     EVSYS.CH0MUX = EVSYS_CHMUX_PORTB_PIN0_gc;
101     EVSYS.CH1MUX = EVSYS_CHMUX_PORTB_PIN1_gc;
102
103     /* Pulse width capture, using event channel 0 and 1 */
104     TCC1.CTRLD = TC_EVSEL_CH0_gc | TC_EVACT_PW_gc;
105
106     /* Enable Input "Capture or Compare" channel A and B. */
107     TCC1.CTRLB |= TC0_CCAEN_bm | TC0_CCBEN_bm;
108
109     /* Set the period of the counter. Quote from datasheet:
110     *
111     * If the Period register value is set lower than 0x8000, the polarity of

```

```

112     * the I/O pin edge will be stored in the Most Significant Bit (MSB) of the
113     * Capture register after a Capture. If the MSB of the Capture register is
114     * zero, a falling edge generated the Capture. If the MSB is one, a rising
115     * edge generated the Capture.
116     */
117     TCC1.PER = 0x7fff;
118
119     /* Start timer by selecting a clock source. No prescaling. */
120     TCC1.CTRLA = TC_CLKSEL_DIV1_gc;
121
122     /* Enable CCA and CCB interrupt */
123     TCC1.INTCTRLB = TC_CCAINTLVL_LO_gc | TC_CCBINTLVL_LO_gc;
124 }
125
126 void
127 fsm_init() {
128
129     /* Set the period of the counter */
130     TCD0.PER = STATE_PER;
131
132     /* Start timer by selecting a clock source. /8 prescaling. */
133     TCD0.CTRLA = TC_CLKSEL_DIV8_gc;
134
135     /* Enable overflow interrupt */
136     TCD0.INTCTRLA = TC_OVFINTLVL_HI_gc;
137 }
138
139 void
140 pwm_init() {
141
142     /* Enable the PWM outputs for the left and right motor
143     * Left: PC0, PC1, PC2 and PC3
144     * Right: PC4, PC5, PC6 and PC7
145     */
146     PORTC.DIR = 0xFF;
147
148     /* Set the TC period */
149     TCC0.PER = PER;
150
151     /* Configure the TC for dual slope mode. */
152     TCC0.CTRLB = TC_WGMODE_DS_T_gc;
153
154     /* Enable Compare channel A, B, C and D. */
155     TCC0.CTRLB |= TC0_CCAEN_bm | TC0_CCBEN_bm | TC0_CCCEN_bm | TC0_CCDEN_bm;
156
157     /* Start timer by selecting a clock source. No prescaler. */
158     TCC0.CTRLA = TC_CLKSEL_DIV1_gc;
159
160     /* Enable AwEx Dead Time Insertion */
161     AWEXC.CTRL |= AWEX_DTICCAEN_bm | AWEX_DTICCBEN_bm
162                 | AWEX_DTICCCEN_bm | AWEX_DTICCDEN_bm;
163
164     /* Set the dead time (both high and low)
165     * in number of CPU cycles
166     * 240 ticks = 30us */
167     AWEXC.DTBOOTH = 240;
168
169     /* Override all pins */
170     AWEXC.OUTOVEN = 0xff;
171
172     /* Invert all pins */
173     PORTC.PIN0CTRL |= PORT_INVEN_bm;
174     PORTC.PIN1CTRL |= PORT_INVEN_bm;
175     PORTC.PIN2CTRL |= PORT_INVEN_bm;
176     PORTC.PIN3CTRL |= PORT_INVEN_bm;
177     PORTC.PIN4CTRL |= PORT_INVEN_bm;
178     PORTC.PIN5CTRL |= PORT_INVEN_bm;
179     PORTC.PIN6CTRL |= PORT_INVEN_bm;
180     PORTC.PIN7CTRL |= PORT_INVEN_bm;
181
182     /* Make sure we have a safe start */
183     *drive[AH][LEFT_MOTOR] = OFF;
184     *drive[BH][LEFT_MOTOR] = OFF;

```

```

185     *drive[AH][RIGHT_MOTOR] = OFF;
186     *drive[BH][RIGHT_MOTOR] = OFF;
187
188 }
189
190 void
191 int_init() {
192
193     /* Enable high and low level interrupt */
194     PMIC.CTRL |= PMIC_HILVLEN_bm;
195     PMIC.CTRL |= PMIC_LOLVLEN_bm;
196
197     /* Enable global interrupt */
198     sei();
199 }
200
201 void
202 button_init() {
203
204     /* Configure PE2 and PE3 for input */
205     PORTE.DIRCLR = PIN2_bm;
206     PORTE.DIRCLR = PIN3_bm;
207 }
208
209 void
210 led_init() {
211
212     /* Set Green LED-pin PD4 as output */
213     PORTD.DIRSET = PIN4_bm;
214
215     /* Set Red LED-pin PD5 as output */
216     PORTD.DIRSET = PIN5_bm;
217
218     /* Invert the LED pins, so the LEDs light when we set them high */
219     PORTD.PIN4CTRL |= PORT_INVEN_bm;
220     PORTD.PIN5CTRL |= PORT_INVEN_bm;
221 }
222
223 void
224 uart_init(void) {
225
226     /* Set PD3 (TX) as output */
227     PORTD.DIRSET = PIN3_bm;
228
229     /* Set the baud rate */
230     uint16_t bsel = BSEL_D(BAUD_D);
231     USARTD0.BAUDCTRLB = (bsel >> 8) & 0xFF;
232     USARTD0.BAUDCTRLA = (uint8_t) bsel;
233
234     /* Enable low level UART receive interrupt */
235     USARTD0.CTRLA = USART_RXCINTLVL_LO_gc;
236
237     /* Enable the UART transmitter and receiver */
238     USARTD0.CTRLB = USART_RXEN_bm | USART_TXEN_bm;
239
240     /* Asynchronous USART, no parity, 1 stop bit, 8 data bits */
241     USARTD0.CTRLC = USART_CHSIZE0_bm | USART_CHSIZE1_bm;
242 }
243
244 int
245 main() {
246
247     // FIXME: Save this in eeprom and add calibration button
248     min_pulse_width[LEFT_STICK] = -3431L;
249     max_pulse_width[LEFT_STICK] = 3379L;
250
251     min_pulse_width[RIGHT_STICK] = -3423L;
252     max_pulse_width[RIGHT_STICK] = 3385L;
253
254     clock_init();
255     rc_init();
256     fsm_init();
257     pwm_init();

```

```

258     int_init();
259     button_init();
260     led_init();
261     uart_init();
262
263     //PORTD.OUTSET = PIN4_bm;
264     //PORTD.OUTSET = PIN5_bm;
265
266     for (;;) {
267     }
268 }
269
270 void
271 calibrate_stick(int16_t * pulse_width, int8_t stick) {
272
273     if (*pulse_width < min_pulse_width[stick]) {
274         min_pulse_width[stick] = *pulse_width;
275     }
276
277     if (*pulse_width > max_pulse_width[stick]) {
278         max_pulse_width[stick] = *pulse_width;
279     }
280 }
281
282 void
283 single_sticks() {
284     drive_motor(LEFT_MOTOR, left_stick);
285     drive_motor(RIGHT_MOTOR, right_stick);
286 }
287
288 void
289 combo_sticks() {
290     int16_t motor_thrust_left;
291     int16_t motor_thrust_right;
292
293     if (left_stick == 0 && right_stick != 0) {
294
295         /* Turn on the spot */
296         motor_thrust_left = right_stick;
297         motor_thrust_right = -right_stick;
298     }
299     else {
300
301         /* Drive */
302         motor_thrust_left = left_stick + right_stick;
303         motor_thrust_right = left_stick - right_stick;
304     }
305
306     /* Remove overflows */
307     if (motor_thrust_left > 127) motor_thrust_left = 127;
308     if (motor_thrust_left < -127) motor_thrust_left = -127;
309     if (motor_thrust_right > 127) motor_thrust_right = 127;
310     if (motor_thrust_right < -127) motor_thrust_right = -127;
311
312     drive_motor_lp(LEFT_MOTOR, (int8_t) motor_thrust_left);
313     drive_motor_lp(RIGHT_MOTOR, (int8_t) motor_thrust_right);
314 }
315
316 void
317 drive_motor_lp(int8_t motor, int8_t motor_thrust) {
318
319     /* Calculate moving average */
320     average_motor_thrust[motor] = ((int32_t)
321         (NUMBER_OF_RC_CMD_TO_AVERAGE - 1)*average_motor_thrust[motor]
322         + motor_thrust)/NUMBER_OF_RC_CMD_TO_AVERAGE;
323
324     drive_motor(motor, average_motor_thrust[motor]);
325 }
326
327 void
328 drive_motor(int8_t motor, int8_t motor_thrust) {

```

```

331
332  /* Scale the pwm-value */
333  uint16_t pwm = (abs(motor_thrust)*PER)/127L;
334
335  if (motor_thrust > 0) {
336
337      /* Drive forward */
338      *drive[AH][motor] = OFF;
339      *drive[BH][motor] = pwm;
340
341  } else {
342
343      /* Drive backward */
344      *drive[AH][motor] = pwm;
345      *drive[BH][motor] = OFF;
346  }
347 }
348
349 int8_t
350 normalize_stick_position(int16_t * pulse_width, uint8_t stick) {
351
352     int16_t stick_position;
353
354     /* Center around 1.5 ms */
355     *pulse_width -= sec_to_tic(CENTER_LEN);
356
357     /* Dead band */
358     if (abs(*pulse_width) <= sec_to_tic(DEAD_BAND)) {
359         stick_position = 0;
360
361     /* Scale to -127..127 */
362     } else if (*pulse_width >= 0) {
363
364         stick_position = (*pulse_width * 127L)/max_pulse_width[stick];
365
366     } else {
367
368         stick_position = -(*pulse_width * 127L)/min_pulse_width[stick];
369     }
370
371     /* 125..127 -> 127 */
372     if (stick_position >= 125) {
373         stick_position = 127;
374     }
375
376     return (int8_t) stick_position;
377 }
378
379 ISR(TCC1_CCA_vect) {
380
381     number_of_runs_without_rc_command = 0;
382
383
384     if (rc_receiver_ready == TRUE) {
385         left_stick = normalize_stick_position(&TCC1_CCA, LEFT_STICK);
386     } else {
387
388         /* Skip the first interrupts since the receiver is not stable then */
389         if (++number_of_rc_commands >= 1000*RC_FREQ*2) {
390             rc_receiver_ready = TRUE;
391         }
392     }
393 }
394
395 ISR(TCC1_CCB_vect) {
396     if (rc_receiver_ready == TRUE) {
397         right_stick = normalize_stick_position(&TCC1_CCB, RIGHT_STICK);
398     }
399 }
400
401 ISR(TCD0_OVF_vect) {
402
403     /* Turn off both LEDs, so they will light only when they should */

```



```

404 PORTD.OUTCLR = PIN4_bm;
405 PORTD.OUTCLR = PIN5_bm;
406
407 /* Timeout if we do not get any rc commands.
408 * Triggered after waiting for two missing commands */
409 if (++number_of_runs_without_rc_command >= 2*(STATE_FREQ/RC_FREQ)) {
410     rc_receiver_ready = FALSE;
411     number_of_rc_commands = 0;
412 }
413
414 /* Timeout if we do not get any uart commands. */
415 if (++number_of_runs_without_uart_command >= 20) {
416     uart_receiver_ready = FALSE;
417     number_of_uart_commands = 0;
418 }
419
420 if (rc_receiver_ready == TRUE) {
421     //if (PORTE.IN & PIN2_bm)
422         //single_sticks();
423     //else
424
425         /* Set red LED to show that we are using commands from RC receiver */
426         PORTD.OUTSET = PIN5_bm;
427
428         combo_sticks();
429
430 } else if (uart_receiver_ready == TRUE) {
431
432     /* Set green LED to show that we are using commands from UART */
433     PORTD.OUTSET = PIN4_bm;
434
435     drive_motor(LEFT_MOTOR, uart_motor_thrust[LEFT_MOTOR]);
436     drive_motor(RIGHT_MOTOR, uart_motor_thrust[RIGHT_MOTOR]);
437
438 } else {
439
440     drive_motor(LEFT_MOTOR, 0);
441     drive_motor(RIGHT_MOTOR, 0);
442 }
443 }
444
445 ISR(USARTD0_RXC_vect) {
446
447     number_of_runs_without_uart_command = 0;
448
449     uint8_t motor;
450     int8_t direction;
451
452     uint8_t command = USARTD0.DATA;
453
454     /* Get the two MSB of command */
455     switch (command >> 6) {
456
457     case 0:
458         motor = LEFT_MOTOR;
459         direction = BACKWARD;
460         break;
461
462     case 1:
463         motor = LEFT_MOTOR;
464         direction = FORWARD;
465         break;
466
467     case 2:
468         motor = RIGHT_MOTOR;
469         direction = BACKWARD;
470         break;
471
472     case 3:
473         motor = RIGHT_MOTOR;
474         direction = FORWARD;
475         break;
476
477     }

```

```

477
478 /*
479  * Set the left or right motor thrust, with the correct direction and speed.
480  *
481  * 0x3f = 0b00111111 and masks out the speed from the command. This
482  * speed (0..63) is then scaled to (0..127) by multiplying by two and
483  * adding one. The direction variable is positiv for forward and
484  * negative for backward direction, and thus give the final speed as an
485  * int8_t of range -127..127
486  *
487 */
488 uart_motor_thrust[motor] = direction*(command & 0x3f)*2 + 1;
489
490 if (uart_receiver_ready == FALSE) {
491
492     /* Skip the first interrupts since the receiver is not stable then */
493     if (++number_of_uart_commands >= 100) {
494         uart_receiver_ready = TRUE;
495     }
496 }
497 }

```

---

### code/ASV\_H-bridge\_controller/Makefile

---

```

1 DEVICE = atxmega32a4
2 AVRDUDE = avrdude -p x32a4 -c jtag2pdi -P usb
3 F_CPU=800000UL

5 CC = avr-gcc
6 CFLAGS = -g -W -Wall -O2 -std=gnu99 -mmcu=$(DEVICE) -DF_CPU=$(F_CPU) -lm -pedantic

8 OBJECTS = main.o

10 all: main.hex

12 clean:
13     rm -f *.hex *.lst *.o *.bin

15 $(OBJECTS): | depend

17 main.bin: $(OBJECTS)
18     $(CC) $(CFLAGS) -o main.bin $(OBJECTS)

20 main.hex: main.bin
21     avr-objcopy -j .text -j .data -O ihex main.bin main.hex
22     avr-size --totals *.o
23     avr-size -C --mcu=$(DEVICE) *.o

25 read:
26     $(AVRDUDE) -U eeprom:r:eeprom.dat:r
27     hd eeprom.dat

29 depend:
30     @$(CC) -MM $(ALL_CFLAGS) *.c | sed 's/$$/ Makefile/'

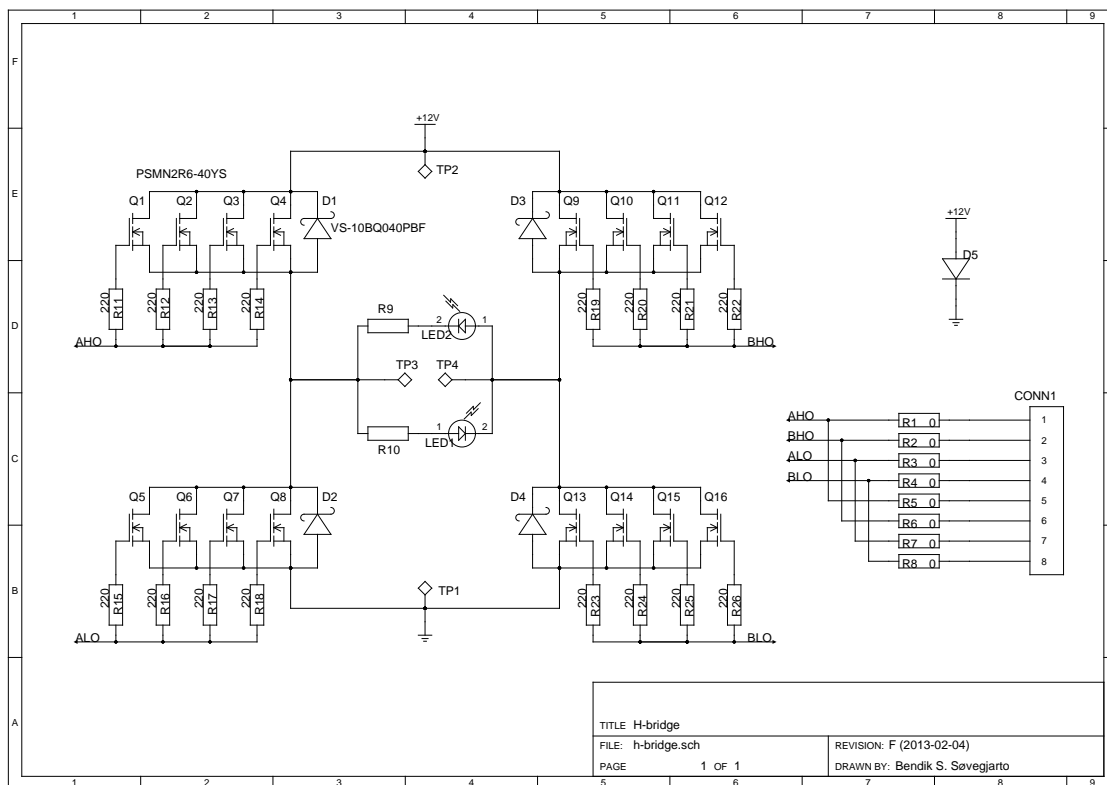
32 flash: all
33     $(AVRDUDE) -U flash:w:main.hex:i

```

---

# Appendix H

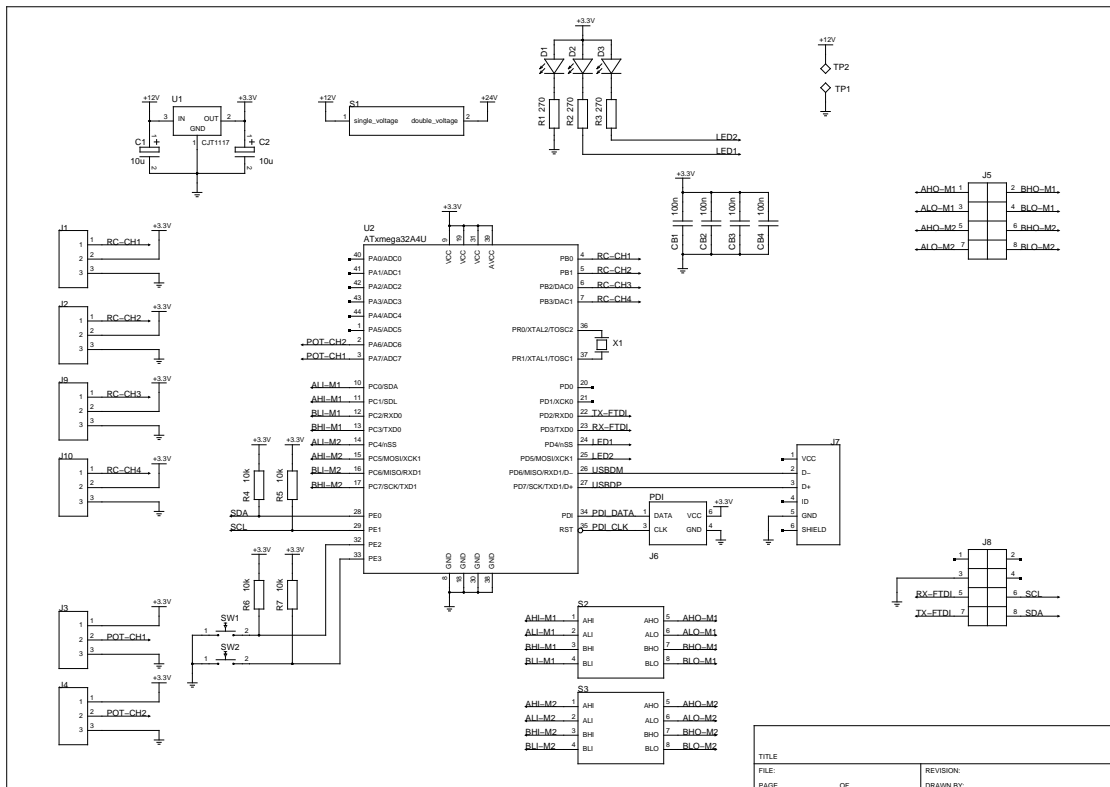
## Second H-bridge schematics



# Appendix I

## H-bridge driver schematics

The schematic for the voltage doubler (subcircuit S1) is shown in figure 1.22. The schematic for the single H-bridge driver (subcircuit S2 and S3) is shown in figure 1.21.



## Appendix J

# HydroAHRS mk.I MCU code

Code for ATxmega32A4U in HydroAHRS mk.I, as described in section 2.3.1. The code is licensed under GNU GPL version 2 or later.

code/hydroAHRS\_mkI/main.c

```
1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <util/delay.h>
4 #include <inttypes.h>
5 #include <stdio.h>
6
7 #include "main.h"
8 #include "twi.h"
9 #include "uart.h"
10 #include "matrix.h"
11 #include "MPU9150.h"
12 #include "imu_calc.h"
13
14 void
15 CCP_write( volatile uint8_t * address, uint8_t value ) {
16
17     /* Begin a critical task, so we must disable interrupt */
18     uint8_t volatile saved_sreg = SREG;
19     cli();
20
21     volatile uint8_t * tmpAddr = address;
22     asm volatile(
23         "movw_r30,0"           "\n\t"
24         "ldi_r16,%2"          "\n\t"
25         "out_3,r16"           "\n\t"
26         "st_0,Z,%1"           "\n\t"
27         :
28         : "r" (tmpAddr), "r" (value), "M" (CCP_IOREG_gc), "i" (&CCP)
29         : "r16", "r30", "r31"
30     );
31
32     /* End the critical task */
33     SREG = saved_sreg;
34 }
35
36 void
37 clock_init(void) {
38
39     /* Select 32 kHz as external clock */
40     OSC.XOSCCTRL = OSC_XOSCSEL_32KHz_gc;
41
42     /* Start the 32MHz and external clock, but keep 2MHz clock on */
43     OSC.CTRL |= OSC_XOSCEN_bm | OSC_RC32MEN_bm;
44
45     /* Wait until 32MHz clock is ready */
```

```

46     while( !(OSC.STATUS & OSC_RC32MRDY_bm) );
47
48     /* Wait for the external oscillator to stabilize. */
49     while ( !(OSC.STATUS & OSC_XOSCRDY_bm) );
50
51     /* Enable write access to protected register CLK_CTRL with CCP_write
52     * Set 32MHz clock as source */
53     CCP_write( &CLK_CTRL, CLK_SCLKSEL_RC32M_gc );
54
55     /* Wait for the system to switch clock */
56     _delay_us(2);
57
58     /* Disable all clocks except 32MHz and external clock */
59     OSC_CTRL = OSC_XOSCEN_bm | OSC_RC32MEN_bm;
60
61     /* Set 32 kHz TOSC as calibration reference */
62     OSC_DFLLCTRL = OSC_RC32MCREF_bm;
63
64     /* Enable automatic run-time calibration */
65     DFLLRC32M_CTRL = DFLL_ENABLE_bm;
66 }
67
68 void
69 rtc_init(void) {
70
71     /* Set 32 kHz from external 32kHz oscillator as clock source for RTC.
72     CLK_RTCCTRL = CLK_RTCSRC_TOSC32_gc | CLK_RTCEN_bm;
73
74     /* Wait until RTC is not busy.
75     while ( RTC.STATUS & RTC_SYNCBUSY_bm );
76
77     /* Period register value. Must subtract 1, because zero value counted.
78     * Gives an overflow every 1/1024 second */
79     RTC.PER = 32 - 1;
80
81     /* Make sure COMP and CNT is 0. */
82     RTC.COMP = 0;
83     RTC.CNT = 0x0000;
84
85     /* Divide by 1, so 32.768 kHz frequency */
86     RTC_CTRL = RTC_PRESCALER_DIV1_gc;
87
88     /* Enable overflow interrupt. */
89     RTC.INTCTRL = RTC_OVFINTLVL_HI_gc | RTC_COMPINTLVL_OFF_gc;
90 }
91
92 void
93 int_init() {
94
95     /* Configure PB2 as input */
96     PORTB.DIRCLR = PIN2_bm;
97
98     /* Enable INT0 as low level interrupt */
99     PORTB.INTCTRL = PORT_INT0LVL_LO_gc;
100
101     /* Assign PB2 as source for INT0 */
102     PORTB.INT0MASK = PIN2_bm;
103
104     /* Configure interrupt on falling edge */
105     PORTB.PIN2CTRL = PORT_ISC_FALLING_gc;
106
107     /* Enable high and low level interrupt */
108     PMIC_CTRL |= PMIC_HILVLEN_bm;
109     PMIC_CTRL |= PMIC_LOLVLEN_bm;
110
111     /* Enable global interrupt */
112     sei();
113 }
114
115 void
116 led_init() {
117
118     /* Set Red LED-pin PC2 as output */

```

```

119 //PORTC.DIRSET = PIN2_bm;
120
121 /* Set Red LED-pin PC2 as input, so it won't light up before we have
122 * received the 'start'-signal */
123 PORTC.DIRCLR = PIN2_bm;
124
125 /* Set Green LED-pin PC3 as output */
126 PORTC.DIRSET = PIN3_bm;
127
128 /* Invert the LED pins, so the LEDs light when we set them high */
129 PORTC.PIN2CTRL |= PORT_INVEN_bm;
130 PORTC.PIN3CTRL |= PORT_INVEN_bm;
131 }
132
133
134 int
135 main() {
136     clock_init();
137     uart_init();
138     rtc_init();
139     twi_init();
140     int_init();
141     led_init();
142
143     /* Clear screen */
144     uart_send_str("\x1b[H\x1b[2J");
145
146     _delay_ms(50);
147
148     MPU9150_init();
149
150     for(;;) {
151     }
152 }
153
154
155 ISR(PORTB_INT0_vect) {
156
157     /* Toggle the green LED when we get data from the sensor */
158     PORTC.OUTTGL = PIN3_bm;
159
160     /* Turn the red LED on when we begin to compute */
161     //PORTC.OUTSET = PIN2_bm;
162
163     read_calculate_send_angles();
164
165     /* Turn the red LED off when we are finished with the computation */
166     //PORTC.OUTCLR = PIN2_bm;
167 }
168
169 relative_time_t relative_time;
170 uint8_t running = FALSE;
171 uint8_t send_one_measurement = FALSE;
172
173 ISR(USARTD0_RXC_vect) {
174
175     uint8_t command;
176
177     command = USARTD0.DATA;
178
179     /* Start sending angles */
180     if (command == 's') {
181         running = TRUE;
182         RTC.CNT = 0x0000;
183         TCC0.CNT = 0;
184         relative_time.ticks = 0;
185         relative_time.secs = 0;
186         relative_time.minutes = 0;
187         relative_time.hours = 0;
188         relative_time.days = 0;
189         PORTC.DIRSET = PIN2_bm;
190     }
191

```

```

192  /* Stop sending angles */
193  if (command == 'S') {
194      running = FALSE;
195      PORTC.DIRCLR = PIN2_bm;
196  }
197
198  /* Set gyro bias */
199  if (command == 'g') {
200      set_gyro_bias();
201  }
202
203  /* Set angle reference */
204  if (command == 'a') {
205      set_angle_reference();
206  }
207
208  /* Send one measurment */
209  if (command == '1') {
210      RTC.CNT = 0x0000;
211      TCC0.CNT = 0;
212      relative_time.ticks = 0;
213      relative_time.secs = 0;
214      relative_time.minutes = 0;
215      relative_time.hours = 0;
216      relative_time.days = 0;
217      send_one_measurment = TRUE;
218  }
219 }
220
221 ISR(RTC_OVF_vect) {
222
223     /* Ticks is 1/1024 seconds */
224     relative_time.ticks++;
225
226     if (relative_time.ticks > 1023) {
227         relative_time.ticks = 0;
228         relative_time.secs++;
229
230         PORTC.OUTTGL = PIN2_bm;
231     }
232
233     if (relative_time.secs > 59) {
234         relative_time.secs = 0;
235         relative_time.minutes++;
236     }
237
238     if (relative_time.minutes > 59) {
239         relative_time.minutes = 0;
240         relative_time.hours++;
241     }
242
243     if (relative_time.hours > 23) {
244         relative_time.hours = 0;
245         relative_time.days++;
246     }
247 }

```

---

code/hydroAHRS\_mkI/main.h

```

1 # ifndef MAIN_H
2 # define MAIN_H
3
4 #define TRUE 1
5 #define FALSE 0
6
7 typedef struct {
8     uint16_t ticks;
9     uint8_t secs;
10    uint8_t minutes;
11    uint8_t hours;
12    uint16_t days;
13

```



```

14 } relative_time_t;
15
16 extern relative_time_t relative_time;
17 extern uint8_t running;
18 extern uint8_t send_one_measurement;
19
20 #endif

```

---

code/hydroAHRS\_mkI/imu\_calc.c

---

```

1 #include <math.h>
2 #include <stdlib.h>
3 #include <inttypes.h>
4
5 #include "main.h"
6 #include "imu_calc.h"
7 #include "matrix.h"
8 #include "twi.h"
9 #include "uart.h"
10 #include "MPU9150.h"
11
12 static void get_sensor_data(raw_sensor_t *raw_sensor_data);
13 static void remove_gyro_bias(int8_t *gyro_bias, raw_sensor_t *raw_sensor_data);
14 static void convert_sensor_data(raw_sensor_t *raw_sensor_data, sensor_t *sensor_data);
15 static void apply_drift_correction(float *omega_correction_P, float *omega_correction_I
    , sensor_t *sensor_data);
16 static void update_rotation_matrix(sensor_t *sensor_data, float R[][3]);
17 static void normalize_rotation_matrix(float R[][3]);
18 static void calculate_roll_pitch_error(sensor_t *sensor_data, float R[][3], float *
    roll_pitch_error);
19 static void calculate_omega_correction_P(float *roll_pitch_error, float *
    omega_correction_P);
20 static void calculate_omega_correction_I(float *roll_pitch_error, float *
    omega_correction_I);
21 static void retrieve_euler_angles(axes_t *angle, float R[][3]);
22 static void send_angles(raw_sensor_t *raw_sensor_data, sensor_t *sensor_data, axes_t *
    angle, relative_time_t *relative_time);
23 static void send_raw_data(raw_sensor_t *raw_sensor_data);
24 static int16_t comp2int(uint8_t msb, uint8_t lsb);
25
26 raw_sensor_t raw_sensor_data;
27 sensor_t sensor_data;
28 axes_t angle;
29
30 static float R[3][3] = {{1, 0, 0},
31                        {0, 1, 0},
32                        {0, 0, 1}};
33
34 static int8_t gyro_bias[3] = {12, 24, 5};
35 static float angle_reference[3] = {0.0, 0.0, 0.0};
36
37 static float omega_correction_P[3];
38 static float omega_correction_I[3];
39
40 float roll_pitch_error[3];
41
42 extern void
43 read_calculate_send_angles() {
44
45     get_sensor_data(&raw_sensor_data);
46
47     // send_raw_data(&raw_sensor_data);
48
49     remove_gyro_bias(gyro_bias, &raw_sensor_data);
50
51     convert_sensor_data(&raw_sensor_data, &sensor_data);
52
53     apply_drift_correction(omega_correction_P, omega_correction_I, &sensor_data);
54
55     update_rotation_matrix(&sensor_data, R);
56
57

```

```

58     normalize_rotation_matrix(R);
59
60     /* Calculate drift correction */
61     calculate_roll_pitch_error(&sensor_data, R, roll_pitch_error);
62
63     calculate_omega_correction_P(roll_pitch_error, omega_correction_P);
64
65     calculate_omega_correction_I(roll_pitch_error, omega_correction_I);
66
67     retrieve_euler_angles(&angle, R);
68
69     if (send_one_measurement == TRUE) {
70         send_angles(&raw_sensor_data, &sensor_data, &angle, &relative_time);
71         //uart_send_str("\x04");
72         send_one_measurement = FALSE;
73     }
74     else if (running == TRUE) {
75         send_angles(&raw_sensor_data, &sensor_data, &angle, &relative_time);
76     }
77 }
78
79 /*
80 * Get the data from the accelerometer and gyroscope
81 * -----
82 *
83 * Output: Accelerometer raw data
84 * Output: Gyroscope raw data
85 */
86 static void
87 get_sensor_data(raw_sensor_t *raw_sensor_data) {
88
89     i2c_slave_addr = MPU9150_ADDR;
90
91     raw_sensor_data->temperature = (read_reg(TEMP_OUT_H)<<8) + read_reg(TEMP_OUT_L);
92
93     raw_sensor_data->accl_x = comp2int(read_reg(ACCEL_XOUT_H), read_reg(ACCEL_XOUT_L));
94     raw_sensor_data->accl_y = comp2int(read_reg(ACCEL_YOUT_H), read_reg(ACCEL_YOUT_L));
95     raw_sensor_data->accl_z = comp2int(read_reg(ACCEL_ZOUT_H), read_reg(ACCEL_ZOUT_L));
96
97     raw_sensor_data->gyro_x = comp2int(read_reg(GYRO_XOUT_H), read_reg(GYRO_XOUT_L));
98     raw_sensor_data->gyro_y = comp2int(read_reg(GYRO_YOUT_H), read_reg(GYRO_YOUT_L));
99     raw_sensor_data->gyro_z = comp2int(read_reg(GYRO_ZOUT_H), read_reg(GYRO_ZOUT_L));
100
101     i2c_slave_addr = AK8975C_ADDR;
102     write_reg(AK_CNIL, 0x01);
103
104     raw_sensor_data->magn_x = comp2int(read_reg(AK_HXH), read_reg(AK_HXL));
105     raw_sensor_data->magn_y = comp2int(read_reg(AK_HYH), read_reg(AK_HYL));
106     raw_sensor_data->magn_z = comp2int(read_reg(AK_HZH), read_reg(AK_HZL));
107 }
108
109 inline void
110 set_gyro_bias() {
111
112     // TODO: Average over multiple measurements
113     gyro_bias[0] = raw_sensor_data.gyro_x;
114     gyro_bias[1] = raw_sensor_data.gyro_y;
115     gyro_bias[2] = raw_sensor_data.gyro_z;
116
117     char str_buf[30];
118
119     uart_send_str("gyro_x\bias:\n");
120     itoa(gyro_bias[0], str_buf, 10);
121     uart_send_str(str_buf);
122
123     uart_send_str("\n,\bgyro_y\bias:\n");
124     itoa(gyro_bias[1], str_buf, 10);
125     uart_send_str(str_buf);
126
127     uart_send_str("\n,\bgyro_z\bias:\n");
128     itoa(gyro_bias[2], str_buf, 10);
129     uart_send_str(str_buf);
130

```

```

131     uart_send_str("\n\r");
132 }
133
134 /*
135  * Remove the raw bias from the gyroscope data
136  * _____
137  *
138  * Input: Gyroscope raw data with bias
139  * Output: Gyroscope raw data without bias
140  */
141 static inline void
142 remove_gyro_bias(int8_t *gyro_bias, raw_sensor_t *raw_sensor_data) {
143
144     raw_sensor_data->gyro_x = raw_sensor_data->gyro_x - gyro_bias[0];
145     raw_sensor_data->gyro_y = raw_sensor_data->gyro_y - gyro_bias[1];
146     raw_sensor_data->gyro_z = raw_sensor_data->gyro_z - gyro_bias[2];
147 }
148
149 static void
150 convert_sensor_data(raw_sensor_t *raw_sensor_data, sensor_t *sensor_data) {
151
152     /* Convert accelerometer and gyroscope data to real units */
153
154     /* Get acceleration in mg */
155     sensor_data->accl_x = (float) raw_sensor_data->accl_x / ACC_LSB;
156     sensor_data->accl_y = (float) raw_sensor_data->accl_y / ACC_LSB;
157     sensor_data->accl_z = (float) raw_sensor_data->accl_z / ACC_LSB;
158
159     /* Get angular rotaion in deg/sec */
160     /* TODO: Make sure raw_sensor_data should be divided on GYRO_HZ */
161     sensor_data->gyro_x = (float) raw_sensor_data->gyro_x / (GYRO_LSB * GYRO_HZ);
162     sensor_data->gyro_y = (float) raw_sensor_data->gyro_y / (GYRO_LSB * GYRO_HZ);
163     sensor_data->gyro_z = (float) raw_sensor_data->gyro_z / (GYRO_LSB * GYRO_HZ);
164
165     /* Convert gyroscope data from degrees to radians */
166     sensor_data->gyro_x = sensor_data->gyro_x * DEG2RAD;
167     sensor_data->gyro_y = sensor_data->gyro_y * DEG2RAD;
168     sensor_data->gyro_z = sensor_data->gyro_z * DEG2RAD;
169
170     /* Keep gyro_z at 0 untill the magnetometer is implemented */
171     sensor_data->gyro_z = 0;
172
173     /* Get temperatur in degrees Celcius */
174     sensor_data->temperature = (raw_sensor_data->temperature + 11900) / 340;
175 }
176
177
178 /*
179  * Add the drift correction to the angular change from last measurment
180  * _____
181  *
182  * Input: Measured change in angle (in degrees) since last measurment
183  * Input: Omega_p
184  * Input: Omega_i
185  * Output: Corrected measured change in angle since last measurment
186  */
187 static inline void
188 apply_drift_correction(float *omega_correction_P, float *omega_correction_I, sensor_t *
189     sensor_data) {
190
191     sensor_data->gyro_x = sensor_data->gyro_x + omega_correction_P[0] +
192         omega_correction_I[0];
193     sensor_data->gyro_y = sensor_data->gyro_y + omega_correction_P[1] +
194         omega_correction_I[1];
195     sensor_data->gyro_z = sensor_data->gyro_z + omega_correction_P[2] +
196         omega_correction_I[2];
197 }
198
199 /*
200  * Make a matrix consisting of the accelerometer corrected change in degrees
201  * since last measurment (that we got from the accelerometer)
202  *
203  * Update the rotation matrix by multiplying it with the update matrix

```

```

200 * "Add the change in degrees from last measurement to the current state"
201 *
202 */
203 static void
204 update_rotation_matrix(sensor_t *sensor_data, float R[][3]) {
205     float R_buffer[3][3];
206
207     /* Construct update matrix */
208     float R_update[3][3] = {{
209         1, -sensor_data->gyro_z, sensor_data
210         ->gyro_y },
211         { sensor_data->gyro_z,
212           ->gyro_x },
213         { -sensor_data->gyro_y, sensor_data->gyro_x,
214           1 }}};
215
216     /* Multiply "rotation matrix" with "update matrix" */
217     /* R_buffer = R * R_update */
218     matrix_mult(R_buffer, R, R_update);
219     /* R = R_buffer */
220     matrix_copy(R, R_buffer);
221 }
222
223 static float
224 root_sum_square(float x, float y, float z) {
225     return sqrt( square(x) + square(y) + square(z) );
226 }
227
228 static void
229 normalize_vector(float *vector) {
230     float rss = root_sum_square(vector[0], vector[1], vector[2]);
231
232     vector[0] = vector[0] / rss;
233     vector[1] = vector[1] / rss;
234     vector[2] = vector[2] / rss;
235 }
236
237 /*
238 * Renormalize the rotation matrix
239 *
240 * In a perfect world then the rotation matrix would remain orthonormal,
241 * but since it is not, we have to correct for this.
242 * -----
243 *
244 * Input: Rotation matrix
245 * Output: Orthonormal rotation matrix
246 */
247 static void
248 normalize_rotation_matrix(float R[][3]) {
249     float R_buffer[3][3];
250
251     /* Renormalize rotation matrix (to keep orthogonal) */
252
253     /* When the dot product of two vectors is zero, then they are normal to
254     * each other. If not, then they are not normal. We use this to form an
255     * error estimate for non-normalized vectors.
256     */
257
258     /* Dot product */
259     /* TODO: Make this a function */
260     float error = -0.5 * (R[0][0] * R[1][0] +
261                          R[0][1] * R[1][1] +
262                          R[0][2] * R[1][2]);
263
264     /* We apply the error scalar to the two rows in opposite direction by cross
265     * coupling */
266     R_buffer[0][0] = (R[0][0] * error) + R[0][0];
267     R_buffer[0][1] = (R[0][1] * error) + R[0][1];
268     R_buffer[0][2] = (R[0][2] * error) + R[0][2];

```

```

270 R_buffer[1][0] = (R[1][0] * error) + R[1][0];
271 R_buffer[1][1] = (R[1][1] * error) + R[1][1];
272 R_buffer[1][2] = (R[1][2] * error) + R[1][2];
273
274 /* Cross product */
275 R_buffer[2][0] = ( (R_buffer[0][1] * R_buffer[1][2]) - (R_buffer[0][2] * R_buffer
    [1][1]) );
276 R_buffer[2][1] = ( (R_buffer[0][2] * R_buffer[1][0]) - (R_buffer[0][0] * R_buffer
    [1][2]) );
277 R_buffer[2][2] = ( (R_buffer[0][0] * R_buffer[1][1]) - (R_buffer[0][1] * R_buffer
    [1][0]) );
278
279 normalize_vector(R_buffer[0]);
280 normalize_vector(R_buffer[1]);
281 normalize_vector(R_buffer[2]);
282
283 /* R = R_buffer */
284 matrix_copy(R, R_buffer);
285
286 }
287
288 /* Error estimate
289 * -----
290 *
291 * Input = normalized rotation matrix
292 * Input = normalized acceleration vector
293 * Output = Error estimate for roll and pitch (rpe)
294 */
295 static void
296 calculate_roll_pitch_error(sensor_t *sensor_data, float R[][3], float *roll_pitch_error
    ) {
297
298     float g_magnitude;
299     float g_vector[3];
300
301     g_magnitude = root_sum_square(sensor_data->accl_x, sensor_data->accl_y, sensor_data
    ->accl_z);
302
303     g_vector[0] = sensor_data->accl_x / g_magnitude;
304     g_vector[1] = sensor_data->accl_y / g_magnitude;
305     g_vector[2] = sensor_data->accl_z / g_magnitude;
306
307     /* Cross product */
308     /* TODO: cross product function */
309     roll_pitch_error[0] = ((g_vector[1] * R[2][2]) - (g_vector[2] * R[2][1])) / 65536L;
310     roll_pitch_error[1] = ((g_vector[2] * R[2][0]) - (g_vector[0] * R[2][2])) / 65536L;
311     roll_pitch_error[2] = ((g_vector[0] * R[2][1]) - (g_vector[1] * R[2][0])) / 65536L;
312
313 }
314
315 static inline void
316 calculate_omega_correction_P(float *roll_pitch_error, float *omega_correction_P) {
317
318     /* Calculate omega correction P */
319     omega_correction_P[0] = roll_pitch_error[0] * K_P_PITCH_ROLL;
320     omega_correction_P[1] = roll_pitch_error[1] * K_P_PITCH_ROLL;
321     omega_correction_P[2] = roll_pitch_error[2] * K_P_PITCH_ROLL;
322 }
323
324 static inline void
325 calculate_omega_correction_I(float *roll_pitch_error, float *omega_correction_I) {
326
327     static float acceleration_integral[3] = {0,0,0};
328
329     /* Calculate omega correction I */
330
331     /* */
332     acceleration_integral[0] = acceleration_integral[0] + (roll_pitch_error[0] *
    K_I_PITCH_ROLL) / (1024L * GYRO_HZ);
333     acceleration_integral[1] = acceleration_integral[1] + (roll_pitch_error[1] *
    K_I_PITCH_ROLL) / (1024L * GYRO_HZ);
334     acceleration_integral[2] = acceleration_integral[2] + (roll_pitch_error[2] *
    K_I_PITCH_ROLL) / (1024L * GYRO_HZ);

```

```

335     omega_correction_I[0] = acceleration_integral[0];
336     omega_correction_I[1] = acceleration_integral[1];
337     omega_correction_I[2] = acceleration_integral[2];
338 }
339 }
340
341 static inline void
342 retrieve_euler_angles(axes_t *angle, float R[][3]) {
343     angle->roll = ( atan2(-R[2][0], R[2][2]) ) * RAD2DEG;
344     angle->pitch = ( asin(R[2][1]) ) * RAD2DEG;
345     angle->yaw = ( atan2(-R[0][1], R[1][1]) ) * RAD2DEG;
346 }
347 }
348
349 static void
350 send_angles(raw_sensor_t *raw_sensor_data, sensor_t *sensor_data, axes_t *angle,
351             relative_time_t *relative_time) {
352     float scaled_roll;
353     float scaled_pitch;
354     char str_buf[30];
355
356     sprintf(str_buf, "%0.2d", relative_time->hours);
357     uart_send_str(str_buf);
358     sprintf(str_buf, ":%0.2d", relative_time->minutes);
359     uart_send_str(str_buf);
360     sprintf(str_buf, ":%0.2d", relative_time->secs);
361     uart_send_str(str_buf);
362     sprintf(str_buf, ".%0.3d", (relative_time->ticks*1000UL)/1024UL);
363     uart_send_str(str_buf);
364     uart_send_str(",");
365
366     sprintf(str_buf, "%+0.5d,", raw_sensor_data->accl_x);
367     uart_send_str(str_buf);
368
369     sprintf(str_buf, "%+0.5d,", raw_sensor_data->accl_y);
370     uart_send_str(str_buf);
371
372     sprintf(str_buf, "%+0.5d,", raw_sensor_data->accl_z);
373     uart_send_str(str_buf);
374
375     sprintf(str_buf, "%+0.5d,", raw_sensor_data->gyro_x);
376     uart_send_str(str_buf);
377
378     sprintf(str_buf, "%+0.5d,", raw_sensor_data->gyro_y);
379     uart_send_str(str_buf);
380
381     sprintf(str_buf, "%+0.5d,", raw_sensor_data->gyro_z);
382     uart_send_str(str_buf);
383
384     sprintf(str_buf, "%+0.2d", sensor_data->temperature);
385     uart_send_str(str_buf);
386
387     scaled_roll = angle->roll/1.1 - angle_reference[0]/1.1;
388
389     dtostrf(scaled_roll, 3, 2, str_buf);
390     uart_send_str(str_buf);
391     uart_send_str(",");
392
393     scaled_pitch = angle->pitch/1.1 - angle_reference[1]/1.1;
394
395     dtostrf(scaled_pitch, 3, 2, str_buf);
396     uart_send_str(str_buf);
397
398     uart_send_str("\n\r");
399 }
400
401 inline void
402 set_angle_reference() {
403
404     angle_reference[0] = angle.roll;
405     angle_reference[1] = angle.pitch;
406     angle_reference[2] = angle.yaw;

```

```
407
408     char str_buf[30];
409
410     uart_send_str("roll reference:");
411     dtostrf(angle_reference[0], 3, 2, str_buf);
412     uart_send_str(str_buf);
413
414     uart_send_str("pitch reference:");
415     dtostrf(angle_reference[1], 3, 2, str_buf);
416     uart_send_str(str_buf);
417
418     uart_send_str("\n\r");
419 }
420
421
422 static void
423 send_raw_data(raw_sensor_t *raw_sensor_data) {
424
425     char str_buf[30];
426
427     uart_send_str("Temp:");
428     itoa(raw_sensor_data->temperature, str_buf, 10);
429     uart_send_str(str_buf);
430
431     uart_send_str("\tAccl_x:");
432     itoa(raw_sensor_data->accl_x, str_buf, 10);
433     uart_send_str(str_buf);
434
435     uart_send_str("\tAccl_y:");
436     itoa(raw_sensor_data->accl_y, str_buf, 10);
437     uart_send_str(str_buf);
438
439     uart_send_str("\tAccl_z:");
440     itoa(raw_sensor_data->accl_z, str_buf, 10);
441     uart_send_str(str_buf);
442
443     uart_send_str("\tGyro_x:");
444     itoa(raw_sensor_data->gyro_x, str_buf, 10);
445     uart_send_str(str_buf);
446
447     uart_send_str("\tGyro_y:");
448     itoa(raw_sensor_data->gyro_y, str_buf, 10);
449     uart_send_str(str_buf);
450
451     uart_send_str("\tGyro_z:");
452     itoa(raw_sensor_data->gyro_z, str_buf, 10);
453     uart_send_str(str_buf);
454
455     uart_send_str("\n\r");
456 }
457
458 static int16_t
459 comp2int(uint8_t msb, uint8_t lsb) {
460
461     int16_t x = 0;
462
463     if ((msb & 0x80) == 0x80) {
464
465         lsb ^= 0xff;
466         msb ^= 0xff;
467         msb &= 0x7f;
468
469         x = (int16_t) ((msb << 8) + lsb + 1);
470
471         return -x;
472
473     } else {
474
475         x = (int16_t) ((msb << 8) + lsb);
476
477         return x;
478     }
479 }
```

## code/hydroAHRS\_mkI/imu\_calc.h

---

```

1 #ifndef IMU_CALC_H
2 #define IMU_CALC_H
3
4 #define DEG2RAD (M_PI/180L)
5 #define RAD2DEG (180L/M_PI)
6
7 #define K_I_PITCH_ROLL 512
8 #define K_P_PITCH_ROLL 512
9
10 typedef struct {
11     int16_t accl_x;
12     int16_t accl_y;
13     int16_t accl_z;
14
15     int16_t gyro_x;
16     int16_t gyro_y;
17     int16_t gyro_z;
18
19     int16_t magn_x;
20     int16_t magn_y;
21     int16_t magn_z;
22
23     int16_t temperature;
24 } raw_sensor_t;
25
26 typedef struct {
27     float accl_x;
28     float accl_y;
29     float accl_z;
30
31     float gyro_x;
32     float gyro_y;
33     float gyro_z;
34
35     float magn_x;
36     float magn_y;
37     float magn_z;
38
39     int16_t temperature;
40 } sensor_t;
41
42 typedef struct {
43     float roll;
44     float pitch;
45     float yaw;
46 } axes_t;
47
48 extern void read_calculate_send_angles();
49 extern void set_gyro_bias();
50 extern void set_angle_reference();
51
52 #endif

```

---

## code/hydroAHRS\_mkI/uart.c

---

```

1 #include <avr/io.h>
2 #include <inttypes.h>
3
4 #include "uart.h"
5
6 void
7 uart_init(void) {
8
9     /* Set PD3 (TX) as output */
10    PORTD.DIRSET = PIN3_bm;
11
12    /* Set the baud rate */
13    // USART1.BAUDCTRLA = (uint8_t) BAUD;
14    // USART1.BAUDCTRLB = ((USART1.BAUDCTRLB) & 0xF0) | ((BAUD >> 8) & 0x0F);
15    USARTD0.BAUDCTRLA = 0x2E;
16    USARTD0.BAUDCTRLB = 0x98;

```



---

```

17
18  /* Enable low level UART receive interrupt */
19  USARTD0.CTRLA = USART_RXCINTLVL_LO_gc;
20
21  /* Enable the UART transmitter and receiver */
22  USARTD0.CTRLB = USART_RXEN_bm | USART_TXEN_bm;
23
24  /* Asynchronous USART, no parity, 1 stop bit, 8 data bits */
25  USARTD0.CTRLC = USART_CHSIZE0_bm | USART_CHSIZE1_bm;
26 }
27
28 void
29 uart_send_byte(uint8_t ch) {
30
31  /* Wait for TX buffer to be empty,
32   * which is indicated by the UDRE1 bit being cleared */
33  while (!(USARTD0.STATUS & USART_DREIF_bm)) {
34  }
35
36  USARTD0.DATA = ch;
37 }
38
39 void
40 uart_send_hex(uint8_t ch) {
41  uint8_t temp;
42
43  uart_send_str("0x");
44
45  /* Send high nibble */
46  temp = (uint8_t) ((ch & 0xF0) >> 4) + 0x30;
47
48  if (temp > 0x39)
49      temp += 7;
50
51  uart_send_byte(temp);
52
53  /* Send low nibble */
54  temp = (ch & 0x0F) + 0x30;
55
56  if (temp > 0x39)
57      temp += 7;
58
59  uart_send_byte(temp);
60 }
61
62 void
63 uart_send_str(char *ptr) {
64
65  /* Send the string, one byte at time */
66  while(*ptr) {
67      uart_send_byte(*ptr);
68      ptr++;
69  }
70 }
71
72 void
73 uart_send_newline(void) {
74
75  /* Send newline and return */
76  uart_send_byte('\n');
77  uart_send_byte('\r');
78 }

```

---

code/hydroAHRs\_mkI/uart.h

---

```

1 #ifndef UART_H
2 #define UART_H
3
4 #define BAUD ((uint16_t) ((F_CPU / (16.0 * (115200))) + 0.5) - 1)
5
6 void uart_init(void);
7 void uart_send_byte(uint8_t ch);

```

```

8 void uart_send_hex(uint8_t ch);
9 void uart_send_str(char *ptr);
10 void uart_send_newline(void);
11
12 #endif

```

---

code/hydroAHRs\_mkI/twi.c

---

```

1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <inttypes.h>
4
5 #include "main.h"
6 #include "uart.h"
7 #include "twi.h"
8
9 volatile int8_t TWIBusy; // This flag is set when a send or receive is started
10 volatile int8_t byte2Send; // Number of bytes to send to DS1337
11 volatile int8_t byte2Read; // Number of bytes to read from DS1337
12 volatile int8_t status; // Transaction status
13
14 volatile uint8_t wIndex; // index to txBuf
15 volatile uint8_t rIndex; // index to rxBuf
16
17 int8_t rxBuf[15]; // Buffer to hold time read from keyboard
18 int8_t txBuf[10]; // Buffer of time for transfer to RTC
19 int8_t dispBuf[22]; // Display buffer
20
21 uint8_t i2c_slave_addr; // = MPU9150_ADDR;
22
23 void
24 twi_init() {
25     /* Configure SDA as output */
26     PORTC.DIRSET = PIN0_bm;
27
28     /* Configure SCL as output */
29     PORTC.DIRSET = PIN1_bm;
30
31     /* Enable TWI Master and (high level) TWI read and write interrupt */
32     TWIC.MASTER.CTRLA = TWI_MASTER_ENABLE_bm | TWI_MASTER_RIEN_bm | TWI_MASTER_WIEN_bm;
33     TWIC.MASTER.CTRLA |= TWI_MASTER_INTLVLO_bm | TWI_MASTER_INTLVL1_bm;
34
35     /* Set the TWI frequency */
36     TWIC_MASTER_BAUD = TWI_FREQ;
37
38     /* Force TWI bus to idle state */
39     TWIC.MASTER.STATUS = TWI_MASTER_BUSSTATE0_bm;
40 }
41
42 uint8_t
43 read_reg(uint8_t addr) {
44
45     /* Wait until TWI bus is idle */
46     while (TWIBusy);
47
48     /* Occupy the TWI bus */
49     TWIBusy = 1;
50
51     status = NORMAL;
52     wIndex = 0;
53     rIndex = 0;
54
55     byte2Send = 1;
56     byte2Read = 1;
57
58     txBuf[0] = addr;
59
60     /* Generate START condition and send SLA + W */
61     TWIC.MASTER.ADDR = i2c_slave_addr + 0;
62
63     /* Wait until read is complete */
64     while (TWIBusy);

```

```

65     return rxBuf[0];
66 }
67 }
68
69 void
70 write_reg(uint8_t addr, uint8_t value) {
71     /* Wait until TWI bus is idle */
72     while (TWIBusy);
73
74     /* Occupy the TWI bus */
75     TWIBusy = 1;
76
77     status = NORMAL;
78     wIndex = 0;
79     rIndex = 0;
80
81     byte2Send = 2;
82     byte2Read = 0;
83
84     txBuf[0] = addr;
85     txBuf[1] = value;
86
87     /* Generate START condition and send SLA + W */
88     TWIC.MASTER.ADDR = i2c_slave_addr + 0;
89
90     /* Wait until read is complete */
91     while (TWIBusy);
92 }
93
94 ISR(TWIC_TWIM_vect) {
95     /* Arbitration lost condition */
96     if (TWIC.MASTER.STATUS & TWI_MASTER_ARBLOST_bm) {
97         status = ARBLOST;
98     }
99
100     /* Bus error condition */
101     } else if (TWIC.MASTER.STATUS & TWI_MASTER_BUSERR_bm) {
102         status = BUSERR;
103     }
104
105     /* Send data */
106     } else if (TWIC.MASTER.STATUS & TWI_MASTER_WIF_bm) {
107
108         if (TWIC.MASTER.STATUS & TWI_MASTER_RXACK_bm) {
109
110             /* Receive negative ACK from slave */
111             status = NEGACK;
112
113             /* Generate a STOP condition */
114             TWIC.MASTER.CTRLA = 0x03;
115
116             /* Release TWI bus */
117             TWIBusy = 0;
118
119             /* More data to send? */
120             } else if (byte2Send > 0) {
121
122                 /* Send out next byte */
123                 TWIC.MASTER.DATA = txBuf[wIndex++];
124                 byte2Send--;
125
126             /* All data bytes have been transmitted */
127             } else {
128
129                 /* Need to read data? */
130                 if (byte2Read > 0) {
131
132                     /* generate RESTART & send SLA + R */
133                     //TWIC.MASTER.ADDR = MPU9150_ADDR + 1;
134                     TWIC.MASTER.ADDR = i2c_slave_addr + 1;
135
136                     /* All transaction is finished */
137                 } else {

```

```

138
139          /* generate STOP condition */
140          TWIC.MASTER.CTRLC = 0x03;
141
142          /* normal result */
143          status = NORMAL;
144
145          /* release TWI bus */
146          TWIBusy = 0;
147      }
148  }
149
150  /* Read data */
151  } else if (TWIC.MASTER.STATUS & TWI_MASTER_RIF_bm) {
152
153      if (byte2Read > 1) {
154
155          /* Send ACK and receive the next byte */
156          TWIC.MASTER.CTRLC = 0x02;
157
158      } else {
159
160          /* Send NACK and generate STOP condition */
161          TWIC.MASTER.CTRLC = 0x07;
162
163          /* Release TWI bus */
164          TWIBusy = 0;
165      }
166
167      rxBuf[rIndex++] = TWIC.MASTER.DATA;
168      byte2Read--;
169
170  } else {
171
172      /* Send NACK and generate STOP condition */
173      TWIC.MASTER.CTRLC = 0x07;
174
175      /* Release TWI bus */
176      TWIBusy = 0;
177  }
178 }

```

---

code/hydroAHRS\_mkI/twi.h

---

```

1 #ifndef TWI_H
2 #define TWI_H
3
4 #define TWI_FREQ ((F_CPU / (2.0 * (400000))) - 5)
5
6 #define NORMAL 0 // Normal successful data transfer
7 #define ARBLOST 1 // Arbitration lost condition
8 #define BUSERR 2 // Bus error condition
9 #define NEGACK 3 // Receive NACK condition
10
11 void twi_init(void);
12 //void send_cmd(uint8_t addr, uint8_t value);
13 uint8_t read_reg(uint8_t addr);
14 void write_reg(uint8_t addr, uint8_t value);
15
16 extern int8_t rxBuf[];
17 extern uint8_t i2c_slave_addr;
18
19 #endif

```

---

code/hydroAHRS\_mkI/matrix.c

---

```

1 #include <inttypes.h>
2
3 #include "matrix.h"
4
5 void

```

---

```

6 matrix_mult(float x[][3], float y[][3], float z[][3]) {
7
8     for (uint8_t i = 0; i < 3; i++) {
9
10        for(uint8_t j = 0; j < 3; j++) {
11
12            x[i][j] = 0;
13
14            for(uint8_t k = 0; k < 3; k++)
15
16                x[i][j] += y[i][k] * z[k][j];
17        }
18    }
19 }
20
21 void
22 matrix_copy(float x[][3], float y[][3]) {
23
24     for (uint8_t i = 0; i < 3; i++) {
25
26         for(uint8_t j = 0; j < 3; j++) {
27
28             x[i][j] = y[i][j];
29         }
30     }
31 }

```

---

code/hydroAHRS\_mkI/matrix.h

---

```

1 #ifndef MATRIX_H
2 #define MATRIX_H
3
4 void matrix_mult(float x[][3], float y[][3], float z[][3]);
5 void matrix_copy(float x[][3], float y[][3]);
6
7 #endif

```

---

code/hydroAHRS\_mkI/MPU9150.c

---

```

1 #include <inttypes.h>
2
3 #include "twi.h"
4 #include "MPU9150.h"
5
6 void
7 MPU9150_init() {
8
9     i2c_slave_addr = MPU9150_ADDR;
10
11     /* Wake up */
12     write_reg(PWR_MGMT_1, 0x00);
13
14     /* Set samplerate to 50 Hz */
15     write_reg(SMPLRT_DIV, SAMPLERATEDIVIDER);
16
17     /* Set low pass filter */
18     write_reg(CONFIG, DLPF_CFG);
19
20     /* Interrupt when new sample is ready */
21     write_reg(INT_ENABLE, 0x01);
22
23     /* I2C Pass-by */
24     write_reg(INT_PIN_CFG, BYPASS_CFG);
25 }

```

---

code/hydroAHRS\_mkI/MPU9150.h

---

```

1 #ifndef MPU9150_H
2 #define MPU9150_H
3

```

```

4 #define MPU9150_ADDR    0xD0
5 #define AK8975C_ADDR    0x18
6
7 /* Definitions of register MPU9150 map */
8 #define AUX_VDDIO        1
9 #define SMPLRT_DIV       25
10 #define CONFIG           26
11 #define GYRO_CONFIG      27
12 #define ACCEL_CONFIG     28
13 #define FF_THR           29
14 #define FF_DUR           30
15 #define MOT_THR          31
16 #define MOT_DUR          32
17 #define ZRMOT_THR        33
18 #define ZRMOT_DUR        34
19 #define FIFO_EN          35
20 #define I2C_MST_CTRL     36
21 #define I2C_SLV0_ADDR    37
22 #define I2C_SLV0_REG     38
23 #define I2C_SLV0_CTRL    39
24 #define I2C_SLV1_ADDR    40
25 #define I2C_SLV1_REG     41
26 #define I2C_SLV1_CTRL    42
27 #define I2C_SLV2_ADDR    43
28 #define I2C_SLV2_REG     44
29 #define I2C_SLV2_CTRL    45
30 #define I2C_SLV3_ADDR    46
31 #define I2C_SLV3_REG     47
32 #define I2C_SLV3_CTRL    48
33 #define I2C_SLV4_ADDR    49
34 #define I2C_SLV4_REG     50
35 #define I2C_SLV4_DO      51
36 #define I2C_SLV4_CTRL    52
37 #define I2C_SLV4_DI      53
38 #define I2C_MST_STATUS   54
39 #define INT_PIN_CFG      55
40 #define INT_ENABLE       56
41 #define INT_STATUS       58
42 #define ACCEL_XOUT_H     59
43 #define ACCEL_XOUT_L     60
44 #define ACCEL_YOUT_H     61
45 #define ACCEL_YOUT_L     62
46 #define ACCEL_ZOUT_H     63
47 #define ACCEL_ZOUT_L     64
48 #define TEMP_OUT_H      65
49 #define TEMP_OUT_L      66
50 #define GYRO_XOUT_H      67
51 #define GYRO_XOUT_L      68
52 #define GYRO_YOUT_H      69
53 #define GYRO_YOUT_L      70
54 #define GYRO_ZOUT_H      71
55 #define GYRO_ZOUT_L      72
56
57 /* External data registers */
58 #define MOT_DETECT_STAT_US 97
59 #define I2C_SLV0_DO       99
60 #define I2C_SLV1_DO      100
61 #define I2C_SLV2_DO      101
62 #define I2C_SLV3_DO      102
63 #define I2C_MST_DELAY_CT_RL 103
64 #define SIGNAL_PATH_RES_ET 104
65 #define MOT_DETECT_CTRL  105
66 #define USER_CTRL        106
67 #define PWR_MGMT_1        107
68 #define PWR_MGMT_2        108
69 #define FIFO_COUNT_H      114
70 #define FIFO_COUNT_L      115
71 #define FIFO_R_W          116
72 #define WHO_AM_I          117
73
74 /* Register map for AK8975C Magnetometer */
75 #define AK_INFO           1
76 #define AK_ST1            2

```

---

```

77 #define AK_HXL 3
78 #define AK_HXH 4
79 #define AK_HYL 5
80 #define AK_HYH 6
81 #define AK_HZL 7
82 #define AK_HZH 8
83 #define AK_ST2 9
84 #define AK_CNTL 10
85 #define AK_ASAX 16
86 #define AK_ASAY 17
87 #define AK_ASAZ 18 // Z-Axis sensitivity
88
89 /* MPU9150 Device options */
90 #define SAMPLERATEDIVIDER 39 // 1kHz/40 = 25Hz sample rate
91 #define DLPF_CFG 1 // Gives 1kHz sampling and 188Hz BW
92 #define BYPASS_CFG 2 // Set up for i2c by-pass mode
93
94 /* AK8975C Device options */
95 #define AK_ADR 12 // Slave address of magnetometer
96 #define AK_CNTL_MODE 1 // Single measurement mode
97
98 /* General options */
99 #define DEBUG_MODE 1 // If 1, program will be verbose
100 #define ACC_LSB 16384L // No. of bits pr g
101 #define GYRO_LSB 131L // No. of bits pr deg/s
102 #define GYRO_HZ 25 // Data output rate
103 // #define GYRO_DT ((1/GYRO_HZ)*1000)
104 #define GYRO_DT 40
105
106 void MPU9150_init(void);
107
108 #endif

```

---

#### code/hydroAHRs\_mkI/Makefile

---

```

1 DEVICE = atxmega32a4
2 AVRDUDE = avrdude -p x32a4 -c jtag2pdi -P usb
3 F_CPU=32000000UL

5 CC = avr-gcc
6 CFLAGS = -g -W -Wall -O2 -std=gnu99 -mmcu=$(DEVICE) -DF_CPU=$(F_CPU) -lm -pedantic

8 OBJECTS = main.o uart.o twi.o matrix.o MPU9150.o imu_calc.o

10 flash: all
11     $(AVRDUDE) -U flash:w:main.hex:i

13 all:    main.hex

15 clean:
16     rm -f *.hex *.lst *.o *.bin

18 $(OBJECTS): | depend

20 main.bin: $(OBJECTS)
21     $(CC) $(CFLAGS) -o main.bin $(OBJECTS)

23 main.hex: main.bin
24     avr-objcopy -j .text -j .data -O ihex main.bin main.hex
25     avr-size --totals *.o
26     #avr-size -C --mcu=$(DEVICE) *.o

28 read:
29     $(AVRDUDE) -U eeprom:r:eeprom.dat:r
30     hd eeprom.dat

32 depend:
33     @$(CC) -MM $(ALL_CFLAGS) *.c | sed 's/$$/ Makefile/'

```

---

## Appendix K

# HydroAHRS mk.II MCU code

Code for ATxmega32A4U in HydroAHRS mk.II, as described in section 2.3.2.

This code depends on LUFA-130901 from Dean Camera, eMPL from InvenSense and Two Wire Interface (TWI)-drivers from Atmel. LUFA-130901 is available at <http://www.github.com/abcminiuser/lufa/archive/LUFA-130901.zip>. eMPL is available at <http://www.invensense.com/developers/>. TWI drivers is available at <http://www.atmel.com/tools/AVRSOFTWAREFRAMEWORK.aspx>. The code is licensed under the MIT license (listing D.1) unless otherwise specified.

code/hydroAHRS\_mkII/main.c

```
1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <avr/eeprom.h>
4 #include <util/delay.h>
5 #include <inttypes.h>
6 #include <stdio.h>
7
8 #include "main.h"
9 #include "twi.h"
10 #include "uart.h"
11 #include "mpu9150.h"
12 #include "usb_talk.h"
13 #include "nmea.h"
14
15 #define LED_PORT PORTC
16 #define LED_PIN PIN2_bm
17
18 relative_time_t relative_time;
19 uint8_t running = FALSE;
20 uint8_t send_one_measurement = FALSE;
21 uint8_t getting_angle_reference = FALSE;
22
23 //static float avarage_pan = 0.0;
24
25 static int calibrate_mag = 0;
26 static int calibrate_accel = 0;
27 //static int change = 0;
28
29 typedef struct {
30     uint8_t calibrated;
31     int16_t minVal[3];
32     int16_t maxVal[3];
33 } accel_cal_st;
34
35 typedef struct {
36     uint8_t calibrated;
37     int16_t minVal[3];
38     int16_t maxVal[3];
39 } mag_cal_st;
```



```

40
41 /* Create variable in EEPROM */
42 accel_cal_st EEMEM EEaccel_cal;
43 mag_cal_st EEMEM EEmag_cal;
44
45 char EEMEM EEsrial_number[5];
46
47 /* Create variable in RAM */
48 accel_cal_st accel_cal;
49 mag_cal_st mag_cal;
50
51 char serial_number[5] = "0011";
52
53 caldata_t accel_cal_or;
54 caldata_t mag_cal_or;
55
56 void(* start_bootloader)(void) = (void (*)(void))(BOOT_SECTION_START/2+0x1FC/2);
57
58 int get_ms(unsigned long *count) {
59     return 0;
60 }
61
62 void __no_operation(void) { }
63
64 void
65 CCP_write( volatile uint8_t * address, uint8_t value ) {
66
67     /* Begin a critical task, so we must disable interrupt */
68     uint8_t volatile saved_sreg = SREG;
69     cli();
70
71     volatile uint8_t * tmpAddr = address;
72     asm volatile(
73         "movw_r30,uu%0"           "\n\t"
74         "ldi_r16,uu%2"           "\n\t"
75         "out_uuu%3,r16"          "\n\t"
76         "st_uuuuZ,uu%1"         "\n\t"
77         :
78         : "r" (tmpAddr), "r" (value), "M" (CCP_IOREG_gc), "i" (&CCP)
79         : "r16", "r30", "r31"
80     );
81
82     /* End the critical task */
83     SREG = saved_sreg;
84 }
85
86 void clock_init() {
87
88     /* Start the PLL to multiply the 2MHz RC oscillator to 32MHz */
89     OSC.PLLCTRL = OSC_PLLSRC_RC2M_gc | (F_CPU / 2000000);
90     OSC.CTRL |= OSC_PLEN_bm;
91     while (!(OSC.STATUS & OSC_PLLRDY_bm));
92
93     /* and switch the CPU core to run from it */
94     CCP_write( &CLK.CTRL, CLK_SCLKSEL_PLL_gc );
95
96     /* Wait for the system to switch clock */
97     _delay_us(2);
98
99     /* Select 32 kHz as external clock */
100    OSC.XOSCCTRL = OSC_XOSCSEL_32KHz_gc;
101
102    /* Start the 32MHz internal RC oscillator and external clock, but keep 2MHz clock
on */
103    OSC.CTRL |= OSC_XOSCEN_bm | OSC_RC32MEN_bm;
104
105    /* Wait until 32MHz clock is ready */
106    while( !(OSC.STATUS & OSC_RC32MRDY_bm) );
107
108    /* Wait for the external oscillator to stabilize. */
109    while ( !(OSC.STATUS & OSC_XOSCRDY_bm) );
110
111    /* and start the DFLL to increase it to 48MHz using the USB SOF as a reference */

```

```

112 OSC.DFLLCTRL |= (2 << OSC_RC32MCREG_gp);
113 DFLLRC32M.COMP1 = ((F_USB / 1000) & 0xFF);
114 DFLLRC32M.COMP2 = ((F_USB / 1000) >> 8);
115
116 NVM.CMD = NVM_CMD_READ_CALIB_ROW_gc;
117 DFLLRC32M.CALA = pgm_read_byte(offsetof(NVM_PROD_SIGNATURES_t, USBRCOSCA));
118 DFLLRC32M.CALB = pgm_read_byte(offsetof(NVM_PROD_SIGNATURES_t, USBRCOSC));
119 NVM.CMD = 0;
120
121 /* Enable automatic run-time calibration */
122 DFLLRC32M.CTRL = DFLL_ENABLE_bm;
123 }
124
125 void
126 rtc_init(void) {
127
128 /* Set 32 kHz from external 32kHz oscillator as clock source for RTC. */
129 CLK.RTCCTRL = CLK_RTCSRC_TOSC32_gc | CLK_RTCEN_bm;
130
131 /* Wait until RTC is not busy */
132 while ( RTC.STATUS & RTC_SYNCBUSY_bm );
133
134 /* Period register value. Must subtract 1, because zero value is counted.
135 * Gives an overflow every 1/1024 second */
136 RTC.PER = 32 - 1;
137
138 /* Make sure COMP and CNT is 0. */
139 RTC.COMP = 0;
140 RTC.CNT = 0x0000;
141
142 /* Divide by 1, so 32.768 kHz frequency */
143 RTC.CTRL = RTC_PRESCALER_DIV1_gc;
144
145 /* Enable overflow interrupt. */
146 RTC.INTCTRL = RTC_OVFINTLVL_HI_gc | RTC_COMPINTLVL_OFF_gc;
147 }
148
149 void
150 int_init() {
151
152 /* Enable high, medium and low level interrupt */
153 PMIC.CTRL = PMIC_LOLVLEN_bm | PMIC_MEDLVLEN_bm | PMIC_HILVLEN_bm;
154
155 /* Enable global interrupt */
156 sei();
157 }
158
159 void
160 led_init() {
161
162 /* Set Green LED-pin PC2 as output */
163 //LED_PORT.DIRSET = LED_PIN;
164
165 /* Invert the LED-pin, so the LED light when we set it high */
166 LED_PORT.PIN2CTRL |= PORT_INVEN_bm;
167
168 /* Set it to light as default, so we can toggle it later on */
169 LED_PORT.OUTSET = LED_PIN;
170 }
171
172 void print_accel(mpu_data_t *mpu) {
173 /* Clear screen */
174 printf("\x1b[H\x1b[2J");
175
176 printf("\rCalibrate accelerometer");
177 printf("X_%4d|Y_%4d|Z_%4d",
178        accel_cal.minVal[0], mpu->rawAccel[0], accel_cal.maxVal[0],
179        accel_cal.minVal[1], mpu->rawAccel[1], accel_cal.maxVal[1],
180        accel_cal.minVal[2], mpu->rawAccel[2], accel_cal.maxVal[2]);
181
182 //fflush(stdout);
183 }
184

```

```

185 void print_mag(mpdata_t *mpu) {
186     /* Clear screen */
187     printf("\x1b[H\x1b[2J");
188
189     printf("\rCalibrate magnetometer");
190     printf("X%4d|Y%4d|Z%4d",
191           mag_cal.minVal[0], mpu->rawMag[0], mag_cal.maxVal[0],
192           mag_cal.minVal[1], mpu->rawMag[1], mag_cal.maxVal[1],
193           mag_cal.minVal[2], mpu->rawMag[2], mag_cal.maxVal[2]);
194
195     //flush(stdout);
196 }
197
198 void print_fused_euler_angles_as_nmea(mpdata_t *mpu) {
199
200     char nmea_sentence_time[80];
201     char nmea_sentence_data[80];
202     char nmea_checksum_buffer[3];
203     char nmea_sentence[80];
204
205     sprintf(nmea_sentence_time, "$PASHR,%0.2d%0.2d%0.2d.%0.3d",
206           relative_time.hours,
207           relative_time.minutes,
208           relative_time.secs,
209           (relative_time.ticks*1000UL)/1024UL);
210
211     sprintf(nmea_sentence_data, "%7.2f,M%7.2f,%7.2f,,0.1,0.1,1.0,0,1",
212           mpu->fusedEuler[VEC3_Z] * RAD_TO_DEGREE,
213           mpu->fusedEuler[VEC3_X] * RAD_TO_DEGREE,
214           mpu->fusedEuler[VEC3_Y] * RAD_TO_DEGREE);
215
216     sprintf(nmea_sentence, "%s%s", nmea_sentence_time, nmea_sentence_data);
217     sprintf(nmea_checksum_buffer, "%02X", nmea_checksum(nmea_sentence));
218     sprintf(nmea_sentence, "%s%s", nmea_sentence, nmea_checksum_buffer);
219     printf("%s\r\n", nmea_sentence);
220 }
221
222
223 void print_fused_euler_angles(mpdata_t *mpu) {
224     const number_of_samples = 50;
225
226     //avarage_pan = ((number_of_samples - 1)*avarage_pan + mpu->fusedEuler[VEC3_Z] *
227     RAD_TO_DEGREE)/number_of_samples;
228
229     printf("\r\n%0.2d:%0.2d:%0.2d.%0.3d,",
230           relative_time.hours,
231           relative_time.minutes,
232           relative_time.secs,
233           (relative_time.ticks*1000UL)/1024UL);
234     printf("%6.1f,%6.1f,%4.0f",
235           mpu->fusedEuler[VEC3_X] * RAD_TO_DEGREE,
236           mpu->fusedEuler[VEC3_Y] * RAD_TO_DEGREE,
237           mpu->fusedEuler[VEC3_Z] * RAD_TO_DEGREE);
238     //avarage_pan);
239 }
240
241 void print_debug_data(mpdata_t *mpu) {
242     printf(",%4i,%4i,%4i",
243           mpu->rawMag[VEC3_X],
244           mpu->rawMag[VEC3_Y],
245           mpu->rawMag[VEC3_Z]);
246     printf(",%4i,%4i,%4i",
247           mpu->calibratedMag[VEC3_X],
248           mpu->calibratedMag[VEC3_Y],
249           mpu->calibratedMag[VEC3_Z]);
250 }
251
252 void parse_command(char command) {
253
254     /* Start sending angles */
255     if (command == 's') {
256         running = TRUE;

```

```

257     RTC.CNT = 0x0000;
258     TCC0.CNT = 0;
259     relative_time.ticks = 0;
260     relative_time.secs = 0;
261     relative_time.minutes = 0;
262     relative_time.hours = 0;
263     relative_time.days = 0;
264     LED_PORT.DIRSET = LED_PIN;
265     calibrate_mag = 0;
266     calibrate_accel = 0;
267
268     /* Clear screen */
269     printf("\x1b[H\x1b[2J");
270 }
271
272 /* Stop sending angles */
273 if (command == 'S') {
274     running = FALSE;
275     calibrate_mag = FALSE;
276     calibrate_accel = FALSE;
277     LED_PORT.DIRCLR = LED_PIN;
278 }
279
280 /* Reset counter */
281 if (command == 'r') {
282     RTC.CNT = 0x0000;
283     TCC0.CNT = 0;
284     relative_time.ticks = 0;
285     relative_time.secs = 0;
286     relative_time.minutes = 0;
287     relative_time.hours = 0;
288     relative_time.days = 0;
289     calibrate_mag = FALSE;
290     calibrate_accel = FALSE;
291 }
292
293 /* Send one measurment */
294 if (command == '1') {
295     send_one_measurment = TRUE;
296     calibrate_mag = FALSE;
297     calibrate_accel = FALSE;
298 }
299
300 /* Calibrate mag */
301 if (command == 'm') {
302     calibrate_mag = TRUE;
303     calibrate_accel = FALSE;
304     //change = TRUE;
305 }
306
307 /* Calibrate accel */
308 if (command == 'a') {
309     calibrate_mag = FALSE;
310     calibrate_accel = TRUE;
311     //change = TRUE;
312 }
313
314 /* Reset mag */
315 if (command == 'M') {
316     mag_cal.calibrated = FALSE;
317
318     for (uint8_t i = 0; i < 3; i++) {
319         mag_cal.minVal[i] = INT16_MAX; //0x7fff; ??
320         mag_cal.maxVal[i] = INT16_MIN; //0x8000; ??
321     }
322     //eeprom_write_block(&accel_cal, &EEaccel_cal, sizeof(accel_cal_st));
323 }
324
325 /* Reset accel */
326 if (command == 'A') {
327     accel_cal.calibrated = FALSE;
328
329     for (uint8_t i = 0; i < 3; i++) {

```

```

330         accel_cal.minVal[i] = INT16_MAX; //0x7fff; ??
331         accel_cal.maxVal[i] = INT16_MIN; //0x8000; ??
332     }
333
334     //eeprom_write_block(&accel_cal, &EEaccel_cal, sizeof(accel_cal_st));
335 }
336
337 /* Run bootloader */
338 if (command == 'b') {
339     printf("Resetting and starting bootloader\r\n");
340     _delay_ms(1500);
341
342     /* Start a software reset */
343     CCP_write( &RST.CTRL, RST_SWRST_bm );
344 }
345
346
347 /* Get debug info */
348 if (command == 'd') {
349     printf("%d<->%d\n%d<->%d\n%d<->%d\n",
350         mag_cal.minVal[0], mag_cal.maxVal[0],
351         mag_cal.minVal[1], mag_cal.maxVal[1],
352         mag_cal.minVal[2], mag_cal.maxVal[2]);
353     printf("%d: %d\n%d: %d\n%d: %d\n",
354         mag_cal_or.range[0], mag_cal_or.offset[0],
355         mag_cal_or.range[1], mag_cal_or.offset[1],
356         mag_cal_or.range[2], mag_cal_or.offset[2]);
357 }
358
359 if (command == 'w') {
360
361     if (calibrate_accel) {
362         accel_cal.calibrated = TRUE;
363         eeprom_write_block(&accel_cal, &EEaccel_cal, sizeof(accel_cal_st));
364         /* Clear screen */
365         printf("\x1b[H\x1b[2J");
366         printf("accel_cal values written to EEPROM.");
367     }
368
369     if (calibrate_mag) {
370         mag_cal.calibrated = TRUE;
371         eeprom_write_block(&mag_cal, &EEmag_cal, sizeof(mag_cal_st));
372         /* Clear screen */
373         printf("\x1b[H\x1b[2J");
374         printf("mag_cal values written to EEPROM.");
375     }
376
377     calibrate_mag = FALSE;
378     calibrate_accel = FALSE;
379     running = FALSE;
380 }
381
382 /* Read EEPROM calibration data */
383 if (command == 'e') {
384     eeprom_read_block(&accel_cal, &EEaccel_cal, sizeof(accel_cal_st));
385     eeprom_read_block(&mag_cal, &EEmag_cal, sizeof(mag_cal_st));
386
387     /* Clear screen */
388     printf("\x1b[H\x1b[2J");
389     printf("accel_cal: X_%4d|Y_%4d|Z_%4d\r\n",
390         accel_cal.minVal[0], accel_cal.maxVal[0],
391         accel_cal.minVal[1], accel_cal.maxVal[1],
392         accel_cal.minVal[2], accel_cal.maxVal[2]);
393
394     printf("mag_cal: X_%4d|Y_%4d|Z_%4d",
395         mag_cal.minVal[0], mag_cal.maxVal[0],
396         mag_cal.minVal[1], mag_cal.maxVal[1],
397         mag_cal.minVal[2], mag_cal.maxVal[2]);
398
399     calibrate_mag = FALSE;
400     calibrate_accel = FALSE;
401     running = FALSE;
402 }

```

```

403
404     /* Read EEPROM serial number */
405     if (command == 'i') {
406         eeprom_read_block(&serial_number, &EEserial_number, sizeof(serial_number));
407         printf("serial_number: %s\r\n", serial_number);
408     }
409
410 #if 0
411     /* Write EEPROM serial number */
412     if (command == 'I') {
413         eeprom_write_block(&serial_number, &EEserial_number, sizeof(serial_number));
414     }
415 #endif
416 }
417
418 void run_bootloader_on_soft_reset() {
419     if (RST.STATUS & RST_SRF_bm) {
420
421         LED_PORT.DIRSET = LED_PIN;
422
423         for (uint8_t i; i < 7; i++) {
424             LED_PORT.OUTTGL = LED_PIN;
425             _delay_ms(200);
426         }
427
428         cli();
429         EIND = BOOT_SECTION_START >> 17;
430         start_bootloader();
431     }
432 }
433
434 void load_calibration_from_eeprom() {
435     eeprom_read_block(&accel_cal, &EEaccel_cal, sizeof(accel_cal_st));
436     eeprom_read_block(&mag_cal, &EEmag_cal, sizeof(mag_cal_st));
437
438
439     accel_cal_or.offset[0] = (short) ((accel_cal.minVal[0] + accel_cal.maxVal[0]) / 2);
440     accel_cal_or.offset[1] = (short) ((accel_cal.minVal[1] + accel_cal.maxVal[1]) / 2);
441     accel_cal_or.offset[2] = (short) ((accel_cal.minVal[2] + accel_cal.maxVal[2]) / 2);
442
443     accel_cal_or.range[0] = (short) (accel_cal.maxVal[0] - accel_cal_or.offset[0]);
444     accel_cal_or.range[1] = (short) (accel_cal.maxVal[1] - accel_cal_or.offset[1]);
445     accel_cal_or.range[2] = (short) (accel_cal.maxVal[2] - accel_cal_or.offset[2]);
446
447
448     mag_cal_or.offset[0] = (short) ((mag_cal.minVal[0] + mag_cal.maxVal[0]) / 2);
449     mag_cal_or.offset[1] = (short) ((mag_cal.minVal[1] + mag_cal.maxVal[1]) / 2);
450     mag_cal_or.offset[2] = (short) ((mag_cal.minVal[2] + mag_cal.maxVal[2]) / 2);
451
452     mag_cal_or.range[0] = (short) (mag_cal.maxVal[0] - mag_cal_or.offset[0]);
453     mag_cal_or.range[1] = (short) (mag_cal.maxVal[1] - mag_cal_or.offset[1]);
454     mag_cal_or.range[2] = (short) (mag_cal.maxVal[2] - mag_cal_or.offset[2]);
455
456     if (accel_cal.calibrated == TRUE) {
457         mpu9150_set_accel_cal(&accel_cal_or);
458     }
459
460     if (mag_cal.calibrated == TRUE) {
461         mpu9150_set_mag_cal(&mag_cal_or);
462     }
463 }
464
465
466 int
467 main() {
468     clock_init();
469     led_init();
470     run_bootloader_on_soft_reset();
471     //uart_init();
472     rtc_init();
473     twi_init();
474     int_init();
475     usb_talk_init();

```

```

476
477 /* Hack: set the serial_number */
478 eeprom_write_block(&serial_number , &Eserial_number , sizeof(serial_number));
479
480 _delay_ms(50);
481
482 mpu9150_init(SAMPLE_RATE, YAW_MIXING_FACTOR);
483
484 load_calibration_from_eeprom();
485
486 mpudata_t mpu;
487
488 memset(&mpu, 0, sizeof(mpudata_t));
489
490 for (uint8_t i = 0; i < 3; i++) {
491     accel_cal.minVal[i] = INT16_MAX; //0x7fff; ??
492     accel_cal.maxVal[i] = INT16_MIN; //0x8000; ??
493 }
494
495 for (uint8_t i = 0; i < 3; i++) {
496     mag_cal.minVal[i] = INT16_MAX; //0x7fff; ??
497     mag_cal.maxVal[i] = INT16_MIN; //0x8000; ??
498 }
499
500 for (;;) {
501
502     /* Receive bytes from the host */
503     char command = CDC_Device_ReceiveByte(&VirtualSerial_CDC_Interface);
504     parse_command(command);
505
506     CDC_Device_USBTask(&VirtualSerial_CDC_Interface);
507
508     USB_USBTask();
509
510     if (mpu9150_read(&mpu) == 0) {
511         if (calibrate_mag) {
512             for (uint8_t i = 0; i < 3; i++) {
513                 if (mpu.rawMag[i] < mag_cal.minVal[i]) {
514                     mag_cal.minVal[i] = mpu.rawMag[i];
515                     //change = TRUE;
516                 }
517
518                 if (mpu.rawMag[i] > mag_cal.maxVal[i]) {
519                     mag_cal.maxVal[i] = mpu.rawMag[i];
520                     //change = TRUE;
521                 }
522             }
523         } else if (calibrate_accel) {
524             for (uint8_t i = 0; i < 3; i++) {
525                 if (mpu.rawAccel[i] < accel_cal.minVal[i]) {
526                     accel_cal.minVal[i] = mpu.rawAccel[i];
527                     //change = TRUE;
528                 }
529
530                 if (mpu.rawAccel[i] > accel_cal.maxVal[i]) {
531                     accel_cal.maxVal[i] = mpu.rawAccel[i];
532                     //change = TRUE;
533                 }
534             }
535         } else {
536             if (send_one_measurement == TRUE) {
537                 print_fused_euler_angles_as_nmea(&mpu);
538                 //print_fused_euler_angles(&mpu);
539                 //print_debug_data(&mpu);
540                 send_one_measurement = FALSE;
541             } else if (running) {
542                 print_fused_euler_angles_as_nmea(&mpu);
543                 //print_debug_data(&mpu);
544             }
545         }
546     }
547 }
548

```

```

549     //if (change) {
550         if (calibrate_mag)
551             print_mag(&mpu);
552         else if (calibrate_accel)
553             print_accel(&mpu);
554     //}
555
556     //change = FALSE;
557
558     // 50 Hertz
559     _delay_ms(20);
560 }
561 }
562
563 ISR(RTC_OVF_vect) {
564
565     /* Ticks is 1/1024 seconds */
566     relative_time.ticks++;
567
568     if (relative_time.ticks > 1023) {
569         relative_time.ticks = 0;
570         relative_time.secs++;
571
572         LED_PORT.OUTTGL = LED_PIN;
573     }
574
575     if (relative_time.secs > 59) {
576         relative_time.secs = 0;
577         relative_time.minutes++;
578     }
579
580     if (relative_time.minutes > 59) {
581         relative_time.minutes = 0;
582         relative_time.hours++;
583     }
584
585     if (relative_time.hours > 23) {
586         relative_time.hours = 0;
587         relative_time.days++;
588     }
589 }

```

## code/hydroAHRS\_mkII/main.h

```

1 #ifndef MAIN_H
2 #define MAIN_H
3
4 #define TRUE 1
5 #define FALSE 0
6
7 #define SAMPLE_RATE 50
8 #define YAW_MIXING_FACTOR 4
9
10 #define LOOP_DELAY ((1000 / SAMPLE_RATE) - 2)
11
12 #define LENGTH(array) (sizeof(array) / sizeof(array[0]))
13
14 #define delay_ms _delay_ms
15 #define get_ms linux_get_ms
16 #define log_i printf
17 #define log_e printf
18 #define min(a, b) ((a < b) ? a : b)
19
20 static inline int reg_int_cb(struct int_param_s *int_param) {
21     return 0;
22 }
23
24 #define MAX_CMD_LENGTH 20
25
26 typedef struct {
27     uint16_t ticks;
28     uint8_t secs;

```



```

29     uint8_t minutes;
30     uint8_t hours;
31     uint16_t days;
32
33 } relative_time_t;
34
35 void __no_operation(void);
36
37 extern relative_time_t relative_time;
38 extern uint8_t running;
39 extern uint8_t send_one_measurement;
40
41 #endif

```

---

code/hydroAHRS\_mkII/mpu9150.c

---

```

1 ///////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////
2 //
3 //   This file is part of linux-mpu9150
4 //
5 //   Copyright (c) 2013 Pansenti, LLC
6 //
7 //   Permission is hereby granted, free of charge, to any person obtaining a copy of
8 //   this software and associated documentation files (the "Software"), to deal in
9 //   the Software without restriction, including without limitation the rights to use,
10 //   copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the
11 //   Software, and to permit persons to whom the Software is furnished to do so,
12 //   subject to the following conditions:
13 //
14 //   The above copyright notice and this permission notice shall be included in all
15 //   copies or substantial portions of the Software.
16 //
17 //   THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR IMPLIED,
18 //   INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A
19 //   PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE AUTHORS OR COPYRIGHT
20 //   HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION
21 //   OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 //   SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #include <inttypes.h>
25
26 #include "eMPL/inv_mpu.h"
27 #include "eMPL/inv_mpu_dmp_motion_driver.h"
28 #include "mpu9150.h"
29
30 static int data_ready();
31 static void calibrate_data(mpudata_t *mpu);
32 static void tilt_compensate(quaternion_t magQ, quaternion_t unfusedQ);
33 static int data_fusion(mpudata_t *mpu);
34 static unsigned short inv_row_2_scale(const signed char *row);
35 static unsigned short inv_orientation_matrix_to_scalar(const signed char *mtx);
36
37 int debug_on;
38 int yaw_mixing_factor;
39
40 #if 0
41 int use_accel_cal;
42 caldata_t accel_cal_data;
43
44 int use_mag_cal;
45 caldata_t mag_cal_data;
46 #endif
47
48 void mpu9150_set_debug(int on) {
49     debug_on = on;
50 }
51
52 uint8_t mpu9150_init(uint8_t sample_rate, uint8_t yaw_mixing_factor) {
53
54     signed char gyro_orientation[9] = { 1, 0, 0,
55                                         0, 1, 0,

```

```

57         0, 0, 1 };
58
59     mpu_init();
60     mpu_set_sensors(INV_XYZ_GYRO | INV_XYZ_ACCEL | INV_XYZ_COMPASS);
61     mpu_configure_fifo(INV_XYZ_GYRO | INV_XYZ_ACCEL);
62     mpu_set_sample_rate(sample_rate);
63     mpu_set_compass_sample_rate(sample_rate);
64     dmp_load_motion_driver_firmware();
65     dmp_set_orientation(inv_orientation_matrix_to_scalar(gyro_orientation));
66     dmp_enable_feature(DMP_FEATURE_6X_LP_QUAT | DMP_FEATURE_SEND_RAW_ACCEL |
67         DMP_FEATURE_SEND_CAL_GYRO | DMP_FEATURE_GYRO_CAL);
68     dmp_set_fifo_rate(sample_rate);
69     mpu_set_dmp_state(1);
70
71     return 0;
72 }
73
74 void mpu9150_exit() {
75     // Turn off the DMP on exit
76     mpu_set_dmp_state(0);
77
78     // TODO: Should turn off the sensors too
79 }
80
81 #if 0
82 void mpu9150_set_accel_cal(caldata_t *cal) {
83     int i;
84     long bias[3];
85
86     if (!cal) {
87         use_accel_cal = 0;
88         return;
89     }
90
91     memcpy(&accel_cal_data, cal, sizeof(caldata_t));
92
93     for (i = 0; i < 3; i++) {
94         if (accel_cal_data.range[i] < 1)
95             accel_cal_data.range[i] = 1;
96         else if (accel_cal_data.range[i] > ACCEL_SENSOR_RANGE)
97             accel_cal_data.range[i] = ACCEL_SENSOR_RANGE;
98
99         bias[i] = -accel_cal_data.offset[i];
100     }
101
102     if (debug_on) {
103         printf("\naccel_cal(range:offset)\n");
104
105         //for (i = 0; i < 3; i++)
106             //printf("%d : %d\n", accel_cal_data.range[i], accel_cal_data.offset[i]);
107     }
108
109     mpu_set_accel_bias(bias);
110
111     use_accel_cal = 1;
112 }
113
114 void mpu9150_set_mag_cal(caldata_t *cal) {
115     int i;
116
117     if (!cal) {
118         use_mag_cal = 0;
119         return;
120     }
121
122     memcpy(&mag_cal_data, cal, sizeof(caldata_t));
123
124     for (i = 0; i < 3; i++) {
125         if (mag_cal_data.range[i] < 1)
126             mag_cal_data.range[i] = 1;
127         else if (mag_cal_data.range[i] > MAG_SENSOR_RANGE)
128             mag_cal_data.range[i] = MAG_SENSOR_RANGE;
129

```

```

130     if (mag_cal_data.offset[i] < -MAG_SENSOR_RANGE)
131         mag_cal_data.offset[i] = -MAG_SENSOR_RANGE;
132     else if (mag_cal_data.offset[i] > MAG_SENSOR_RANGE)
133         mag_cal_data.offset[i] = MAG_SENSOR_RANGE;
134 }
135
136 if (1) {
137     printf("\nmag_cal(range:offset)\n");
138
139     for (i = 0; i < 3; i++)
140         printf("%d:%d\n", mag_cal_data.range[i], mag_cal_data.offset[i]);
141 }
142
143 use_mag_cal = 1;
144 }
145 #endif
146
147 int mpu9150_read_dmp(mpudata_t *mpu) {
148     short sensors;
149     unsigned char more;
150
151
152     if (!data_ready())
153         return -1;
154
155     if (dmp_read_fifo(mpu->rawGyro, mpu->rawAccel, mpu->rawQuat, &mpu->dmpTimestamp, &
156 sensors, &more) < 0) {
157         printf("dmp_read_fifo() failed\n");
158         return -1;
159     }
160
161     while (more) {
162         // Fell behind, reading again
163         if (dmp_read_fifo(mpu->rawGyro, mpu->rawAccel, mpu->rawQuat, &mpu->dmpTimestamp
164 , &sensors, &more) < 0) {
165             printf("dmp_read_fifo() failed\n");
166             return -1;
167         }
168     }
169     return 0;
170 }
171
172 int mpu9150_read_mag(mpudata_t *mpu) {
173     if (mpu_get_compass_reg(mpu->rawMag, 0) < 0) {
174         printf("mpu_get_compass_reg() failed\n");
175         return -1;
176     }
177
178     return 0;
179 }
180
181
182 int mpu9150_read(mpudata_t *mpu) {
183     if (mpu9150_read_dmp(mpu) != 0)
184         return -1;
185
186     if (mpu9150_read_mag(mpu) != 0)
187         return -1;
188
189     calibrate_data(mpu);
190
191     return data_fusion(mpu);
192 }
193
194
195 int data_ready() {
196     short status;
197
198     if (mpu_get_int_status(&status) < 0) {
199         printf("mpu_get_int_status() failed\n");
200     }

```

```

201     return 0;
202 }
203
204 // debug
205 //if (status != 0x0103)
206 //    fprintf(stderr, "%04X\n", status);
207
208 return (status == (MPU_INT_STATUS_DATA_READY | MPU_INT_STATUS_DMP |
209                 MPU_INT_STATUS_DMP_0));
210 }
211 void calibrate_data(mpdata_t *mpu) {
212
213     if (use_mag_cal) {
214         mpu->calibratedMag[VEC3_Y] = -(short)((((long)(mpu->rawMag[VEC3_X] - mag_cal_data.
215             offset[VEC3_X])
216             * (long)MAG_SENSOR_RANGE) / (long)mag_cal_data.range[VEC3_X]);
217
218         mpu->calibratedMag[VEC3_X] = (short)((((long)(mpu->rawMag[VEC3_Y] - mag_cal_data.
219             offset[VEC3_Y])
220             * (long)MAG_SENSOR_RANGE) / (long)mag_cal_data.range[VEC3_Y]);
221
222         mpu->calibratedMag[VEC3_Z] = (short)((((long)(mpu->rawMag[VEC3_Z] - mag_cal_data.
223             offset[VEC3_Z])
224             * (long)MAG_SENSOR_RANGE) / (long)mag_cal_data.range[VEC3_Z]);
225     } else {
226         mpu->calibratedMag[VEC3_X] = mpu->rawMag[VEC3_Y];
227         mpu->calibratedMag[VEC3_Y] = -mpu->rawMag[VEC3_X];
228         mpu->calibratedMag[VEC3_Z] = mpu->rawMag[VEC3_Z];
229     }
230
231     if (use_accel_cal) {
232         mpu->calibratedAccel[VEC3_X] = -(short)((((long)mpu->rawAccel[VEC3_X] * (long)
233             ACCEL_SENSOR_RANGE)
234             / (long)accel_cal_data.range[VEC3_X]);
235
236         mpu->calibratedAccel[VEC3_Y] = (short)((((long)mpu->rawAccel[VEC3_Y] * (long)
237             ACCEL_SENSOR_RANGE)
238             / (long)accel_cal_data.range[VEC3_Y]);
239
240         mpu->calibratedAccel[VEC3_Z] = (short)((((long)mpu->rawAccel[VEC3_Z] * (long)
241             ACCEL_SENSOR_RANGE)
242             / (long)accel_cal_data.range[VEC3_Z]);
243     } else {
244         mpu->calibratedAccel[VEC3_X] = -mpu->rawAccel[VEC3_X];
245         mpu->calibratedAccel[VEC3_Y] = mpu->rawAccel[VEC3_Y];
246         mpu->calibratedAccel[VEC3_Z] = mpu->rawAccel[VEC3_Z];
247     }
248 }
249
250 void tilt_compensate(Quaternion_t magQ, Quaternion_t unfusedQ) {
251     Quaternion_t unfusedConjugateQ;
252     Quaternion_t tempQ;
253
254     QuaternionConjugate(unfusedQ, unfusedConjugateQ);
255     QuaternionMultiply(magQ, unfusedConjugateQ, tempQ);
256     QuaternionMultiply(unfusedQ, tempQ, magQ);
257 }
258
259 int data_fusion(mpdata_t *mpu) {
260     Quaternion_t dmpQuat;
261     vector3d_t dmpEuler;
262     Quaternion_t magQuat;
263     Quaternion_t unfusedQuat;
264     float deltaDMPYaw;
265     float deltaMagYaw;
266     float newMagYaw;
267     float newYaw;
268
269     dmpQuat[QUAT_W] = (float)mpu->rawQuat[QUAT_W];
270     dmpQuat[QUAT_X] = (float)mpu->rawQuat[QUAT_X];
271     dmpQuat[QUAT_Y] = (float)mpu->rawQuat[QUAT_Y];
272     dmpQuat[QUAT_Z] = (float)mpu->rawQuat[QUAT_Z];

```

```

267     quaternionNormalize(dmpQuat);
268     quaternionToEuler(dmpQuat, dmpEuler);
269
270     mpu->dmpEuler[VEC3_X] = dmpEuler[VEC3_X];
271     mpu->dmpEuler[VEC3_Y] = dmpEuler[VEC3_Y];
272     mpu->dmpEuler[VEC3_Z] = dmpEuler[VEC3_Z];
273
274     mpu->fusedEuler[VEC3_X] = dmpEuler[VEC3_X];
275     mpu->fusedEuler[VEC3_Y] = -dmpEuler[VEC3_Y];
276     mpu->fusedEuler[VEC3_Z] = 0;
277
278     eulerToQuaternion(mpu->fusedEuler, unfusedQuat);
279
280     // Maybe the drift appeared here?
281     deltaDMPYaw = -dmpEuler[VEC3_Z] + mpu->lastDMPYaw;
282     //if ((deltaDMPYaw <= 0.0001) || (-deltaDMPYaw <= 0.0001)) {
283     if (fabs(deltaDMPYaw) < 0.0001) {
284         deltaDMPYaw = 0;
285     }
286     //printf("deltaDMPYaw(% 6.10f) = -dmpEuler[VEC3_Z](% 6.10f) + mpu->lastDMPYaw(%
287         6.10f)\n\r", deltaDMPYaw, dmpEuler[VEC3_Z], mpu->lastDMPYaw);
288     mpu->lastDMPYaw = dmpEuler[VEC3_Z];
289
290     magQuat[QUAT_W] = 0;
291     magQuat[QUAT_X] = mpu->calibratedMag[VEC3_X];
292     magQuat[QUAT_Y] = mpu->calibratedMag[VEC3_Y];
293     magQuat[QUAT_Z] = mpu->calibratedMag[VEC3_Z];
294
295     tilt_compensate(magQuat, unfusedQuat);
296
297     newMagYaw = -atan2f(magQuat[QUAT_Y], magQuat[QUAT_X]);
298
299     if (newMagYaw != newMagYaw) {
300         printf("newMagYaw_NAN\n");
301         return -1;
302     }
303
304     if (newMagYaw < 0.0f)
305         newMagYaw = TWO_PI + newMagYaw;
306
307     newYaw = mpu->lastYaw + deltaDMPYaw;
308
309     if (newYaw > TWO_PI)
310         newYaw -= TWO_PI;
311     else if (newYaw < 0.0f)
312         newYaw += TWO_PI;
313
314     deltaMagYaw = newMagYaw - newYaw;
315
316     if (deltaMagYaw >= (float)M_PI)
317         deltaMagYaw -= TWO_PI;
318     else if (deltaMagYaw < -(float)M_PI)
319         deltaMagYaw += TWO_PI;
320
321     //<yaw-mix-factor>    Effect of mag yaw on fused yaw data.
322     //                    0 = gyro only
323     //                    1 = mag only
324     //                    > 1 scaled mag adjustment of gyro data
325     //                    The default is 4.
326     if (yaw_mixing_factor > 0)
327         newYaw += deltaMagYaw / yaw_mixing_factor;
328
329     if (newYaw > TWO_PI)
330         newYaw -= TWO_PI;
331     else if (newYaw < 0.0f)
332         newYaw += TWO_PI;
333
334     mpu->lastYaw = newYaw;
335
336     if (newYaw > (float)M_PI)
337         newYaw -= TWO_PI;
338

```

```

339 //mpu->fusedEuler[VEC3_Z] = newYaw;
340 //mpu->fusedEuler[VEC3_Z] = newMagYaw;
341 mpu->fusedEuler[VEC3_Z] = dmpEuler[VEC3_Z];
342
343 eulerToQuaternion(mpu->fusedEuler, mpu->fusedQuat);
344
345 return 0;
346 }
347
348 /* These next two functions convert the orientation matrix (see
349 * gyro_orientation) to a scalar representation for use by the DMP.
350 * NOTE: These functions are borrowed from InvenSense's MPL.
351 */
352 unsigned short inv_row_2_scale(const signed char *row) {
353
354     unsigned short b;
355
356     if (row[0] > 0)
357         b = 0;
358     else if (row[0] < 0)
359         b = 4;
360     else if (row[1] > 0)
361         b = 1;
362     else if (row[1] < 0)
363         b = 5;
364     else if (row[2] > 0)
365         b = 2;
366     else if (row[2] < 0)
367         b = 6;
368     else
369         b = 7; // error
370     return b;
371 }
372
373 unsigned short inv_orientation_matrix_to_scalar(const signed char *mtx) {
374
375     unsigned short scalar;
376     /*
377     XYZ 010_001_000 Identity Matrix
378     XZY 001_010_000
379     YXZ 010_000_001
380     YZX 000_010_001
381     ZXY 001_000_010
382     ZYX 000_001_010
383     */
384     scalar = inv_row_2_scale(mtx);
385     scalar |= inv_row_2_scale(mtx + 3) << 3;
386     scalar |= inv_row_2_scale(mtx + 6) << 6;
387     return scalar;
388 }

```

---

code/hydroAHRS\_mkII/mpu9150.h

---

```

1 ///////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////
2 //
3 // This file is part of linux-mpu9150
4 //
5 // Copyright (c) 2013 Pansenti, LLC
6 //
7 // Permission is hereby granted, free of charge, to any person obtaining a copy of
8 // this software and associated documentation files (the "Software"), to deal in
9 // the Software without restriction, including without limitation the rights to use,
10 // copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the
11 // Software, and to permit persons to whom the Software is furnished to do so,
12 // subject to the following conditions:
13 //
14 // The above copyright notice and this permission notice shall be included in all
15 // copies or substantial portions of the Software.
16 //
17 // THE SOFTWARE IS PROVIDED "AS IS", WITHOUT WARRANTY OF ANY KIND, EXPRESS OR IMPLIED,
18 // INCLUDING BUT NOT LIMITED TO THE WARRANTIES OF MERCHANTABILITY, FITNESS FOR A
19 // PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE AUTHORS OR COPYRIGHT

```

---

```

20 // HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION
21 // OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 // SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #ifndef MPU9150_H
25 #define MPU9150_H
26
27 #include "quaternion.h"
28
29 #define MAG_SENSOR_RANGE 4096
30 #define ACCEL_SENSOR_RANGE 32000
31
32 // Somewhat arbitrary limits here. The values are samples per second.
33 // The MIN comes from the way we are timing our loop in imu and imucal.
34 // That's easily worked around, but no one probably cares.
35 // The MAX comes from the compass. This could be avoided with separate
36 // sample rates for the compass and the accel/gyros which can handle
37 // faster sampling rates. This is a TODO item to see if it's useful.
38 // There are some practical limits on the speed that come from a 'userland'
39 // implementation like this as opposed to a kernel or 'bare-metal' driver.
40 #define MIN_SAMPLE_RATE 2
41 #define MAX_SAMPLE_RATE 100
42
43 typedef struct {
44     short offset[3];
45     short range[3];
46 } caldata_t;
47
48 typedef struct {
49     short rawGyro[3];
50     short rawAccel[3];
51     long rawQuat[4];
52     unsigned long dmpTimestamp;
53
54     short rawMag[3];
55     unsigned long magTimestamp;
56
57     short calibratedAccel[3];
58     short calibratedMag[3];
59
60     quaternion_t fusedQuat;
61     vector3d_t fusedEuler;
62
63     vector3d_t dmpEuler;
64
65     float lastDMPYaw;
66     float lastYaw;
67 } mpudata_t;
68
69
70 void mpu9150_set_debug(int on);
71 uint8_t mpu9150_init(uint8_t sample_rate, uint8_t yaw_mixing_factor);
72 void mpu9150_exit();
73 int mpu9150_read(mpudata_t *mpu);
74 int mpu9150_read_dmp(mpudata_t *mpu);
75 int mpu9150_read_mag(mpudata_t *mpu);
76 void mpu9150_set_accel_cal(caldata_t *cal);
77 void mpu9150_set_mag_cal(caldata_t *cal);
78
79 #endif

```

---

code/hydroAHRs\_mkII/nmea.c

---

```

1 #include "nmea.h"
2
3
4 #if 0
5 void handle_gnss_data() {
6     get_uart_str(GNSS_DEVICE, message[GNSS_DEVICE]);
7
8     /* Get the wanted message type */
9     if(!strncmp(message[GNSS_DEVICE], "$GPRMC", 6)) {

```

```

10     printf("%s\r\n", message[GNSS_DEVICE]);
11     //nmea_checksum(&message[GNSS_DEVICE]);
12 }
13 }
14 #endif
15
16 uint8_t nmea_checksum(int8_t *nmea_sentence) {
17     //char checksum[3];
18
19     uint8_t temp = 0;
20
21     // 1 because we want to skip the $
22     uint8_t i = 1;
23
24     //while(nmea_sentence[i++] != ',');
25
26     /* Calculate checksum */
27     while(nmea_sentence[i] != '*')
28
29         // XOR each character
30         temp ^= nmea_sentence[i++];
31
32     return temp;
33
34     //sprintf(checksum, "%02X\n", temp);
35
36 #if 0
37     /* Compare the two last characters in nmea_sentence with the checksum in (ascii) */
38     if (nmea_sentence[+i] == checksum[0] &&
39         nmea_sentence[+i] == checksum[1]) {
40
41         return 0;
42     } else {
43
44         return 1;
45     }
46 #endif
47 }
48 #endif
49 }
50
51 uint8_t nmea_parse(int8_t *nmea_sentence) {
52     printf("nmea:_%s\r\n", nmea_sentence);
53
54     return 0;
55 }
56 }

```

## code/hydroAHRs\_mkII/nmea.h

```

1 #ifndef NMEA_H
2 #define NMEA_H
3
4 #include <inttypes.h>
5
6 uint8_t nmea_checksum(int8_t *nmea_sentence);
7 uint8_t nmea_parse(int8_t *nmea_sentence);
8
9 #endif

```

## code/hydroAHRs\_mkII/twi.c

```

1 #include <avr/io.h>
2 #include <avr/interrupt.h>
3 #include <stdio.h>
4 #include <inttypes.h>
5
6 #include "main.h"
7 #include "uart.h"
8 #include "twi.h"
9 #include "twim.h"

```



```

10
11 void
12 twi_init() {
13     /* Configure SDA as output */
14     PORTC.DIRSET = PIN0_bm;
15
16     /* Configure SCL as output */
17     PORTC.DIRSET = PIN1_bm;
18
19     /* Enable TWI Master and (high level) TWI read and write interrupt */
20     //TWI.C.MASTER.CTRLA = TWI_MASTER_ENABLE_bm | TWI_MASTER_RIEN_bm |
21     TWI_MASTER_WIEN_bm;
22     //TWI.C.MASTER.CTRLA |= TWI_MASTER_INTLVLO_bm | TWI_MASTER_INTLVL1_bm;
23
24     /* Set the TWI frequency */
25     //TWI.C.MASTER.BAUD = TWI_FREQ;
26
27     /* Force TWI bus to idle state */
28     //TWI.C.MASTER.STATUS = TWI_MASTER_BUSSTATE0_bm;
29
30     twi_options_t m_options = {
31         .speed = TWI_SPEED,
32         //.chip = TWI_MASTER_ADDR,
33         .chip = 0x00,
34         .speed_reg = TWI_FREQ
35     };
36
37     twi_master_init(&TWI_MASTER, &m_options);
38     twi_master_enable(&TWI_MASTER);
39 }
40 int
41 i2c_write(unsigned char slave_addr, unsigned char reg_addr,
42          unsigned char length, unsigned char const *data) {
43
44
45     uint8_t tmp[20];
46
47     status_code_t master_status;
48
49     tmp[0] = reg_addr;
50
51     for (uint8_t i = 0; i < length; i++) {
52         tmp[i+1] = data[i];
53     }
54
55     // Package to send
56     twi_package_t packet = {
57         .addr_length = 0,
58         .chip = slave_addr,
59         .buffer = (void *)tmp,
60         .length = length + 1,
61         .no_wait = false
62     };
63
64     #if DEBUG
65     printf("\ti2c_write(%02X, %02X, %u, ", slave_addr, reg_addr, length);
66
67     if (length == 0) {
68         printf("_NULL_\n");
69     }
70     else {
71         for (uint8_t i = 0; i < length; i++)
72             printf("%02X", data[i]);
73
74         printf("_]\n");
75     }
76     #endif
77
78     master_status = twi_master_write(&TWI_MASTER, &packet);
79 }
80
81 int

```

```

82 i2c_read(unsigned char slave_addr, unsigned char reg_addr,
83          unsigned char length, unsigned char *data) {
84
85     status_code_t master_status;
86
87     #if DEBUG
88     printf("\ti2c_read(%02X,□%02X,□%u,□...)\n", slave_addr, reg_addr, length);
89     #endif
90
91     uint8_t tmp[2];
92     tmp[0] = reg_addr;
93     twi_package_t packet = {
94         .addr_length = 0,
95         .chip         = slave_addr,
96         .buffer       = tmp,
97         .length       = 1,
98         .no_wait      = false
99     };
100
101     master_status = twi_master_write(&TWI_MASTER, &packet);
102
103     twi_package_t packet_rcv = {
104         .addr_length = 0,
105         .chip        = slave_addr,
106         .buffer      = data,
107         .length      = length,
108         .no_wait     = false
109     };
110
111     master_status = twi_master_read(&TWI_MASTER, &packet_rcv);
112
113     #if DEBUG
114     printf("\tLeaving i2c_read(), read□d□bytes:□", length);
115
116     for (uint8_t i = 0; i < length; i++)
117         printf("%02X□", data[i]);
118
119     printf("\n");
120     #endif
121
122     return 0;
123 }

```

## code/hydroAHRS\_mkII/twi.h

```

1 #ifndef TWI_H
2 #define TWI_H
3
4 #define TWI_MASTER          TWIC
5 #define TWI_MASTER_PORT    PORTC
6 #define TWI_SPEED          400000
7
8 // #define TWI_FREQ ((F_CPU / (2.0 * (400000))) - 5)
9 #define TWI_FREQ ((F_CPU / (2.0 * (TWI_SPEED))) - 5)
10
11 // #define NORMAL 0 // Normal successful data transfer
12 // #define ARBLOST 1 // Arbitration lost condition
13 // #define BUSERR 2 // Bus error condition
14 // #define NEGACK 3 // Receive NACK condition
15
16 void twi_init(void);
17 // void send_cmd(uint8_t addr, uint8_t value);
18 uint8_t read_reg(uint8_t addr);
19 void write_reg(uint8_t addr, uint8_t value);
20
21 extern int8_t rxBuf[];
22 extern uint8_t i2c_slave_addr;
23
24 #endif

```

## code/hydroAHRS\_mkII/uart.c

---

```
1 #include <stdio.h>
2 #include <avr/io.h>
3 #include <inttypes.h>
4
5 #include "uart.h"
6
7 //static FILE mystdout = FDEV_SETUP_STREAM(uart_putchar, NULL, _FDEV_SETUP_WRITE);
8
9 void
10 uart_init(void) {
11
12     /* Set PD3 (TX) as output */
13     PORTD.DIRSET = PIN3_bm;
14
15     /* Set the baud rate */
16     // USARTC1.BAUDCTRLA = (uint8_t) BAUD;
17     // USARTC1.BAUDCTRLB = ((USARTC1.BAUDCTRLB) & 0xF0) | ((BAUD >> 8) & 0x0F);
18     USARTD0.BAUDCTRLA = 0x2E;
19     USARTD0.BAUDCTRLB = 0x98;
20
21     /* Enable low level UART receive interrupt */
22     USARTD0.CTRLA = USART_RXCINTLVL_LO_gc;
23
24     /* Enable the UART transmitter and receiver */
25     USARTD0.CTRLB = USART_RXEN_bm | USART_TXEN_bm;
26
27     /* Asynchronous USART, no parity, 1 stop bit, 8 data bits */
28     USARTD0.CTRLC = USART_CHSIZE0_bm | USART_CHSIZE1_bm;
29
30     //stdout = &mystdout;
31 }
32
33 void
34 uart_send_byte(uint8_t ch) {
35
36     /* Wait for TX buffer to be empty,
37      * which is indicated by the UDRE1 bit being cleared */
38     while (!(USARTD0.STATUS & USART_DREIF_bm)) {
39     }
40
41     USARTD0.DATA = ch;
42 }
43
44 void
45 uart_send_hex(uint8_t ch) {
46     uint8_t temp;
47
48     uart_send_str("0x");
49
50     /* Send high nibble */
51     temp = (uint8_t) ((ch & 0xF0) >> 4) + 0x30;
52
53     if (temp > 0x39)
54         temp += 7;
55
56     uart_send_byte(temp);
57
58     /* Send low nibble */
59     temp = (ch & 0x0F) + 0x30;
60
61     if (temp > 0x39)
62         temp += 7;
63
64     uart_send_byte(temp);
65 }
66
67 void
68 uart_send_str(char *ptr) {
69
70     /* Send the string, one byte at time */
71     while(*ptr) {
72         uart_send_byte(*ptr);
```

```

73     ptr++;
74 }
75 }
76
77 void
78 uart_send_newline(void) {
79     /* Send newline and return */
80     uart_send_byte('\n');
81     uart_send_byte('\r');
82 }
83
84
85
86 int uart_putchar (char ch, FILE *stream) {
87     if (ch == '\n')
88         uart_putchar('\r', stream);
89
90     uart_send_byte(ch);
91
92     return 0;
93 }

```

---

code/hydroAHRS\_mkII/uart.h

---

```

1 #ifndef UART_H
2 #define UART_H
3
4 #define BAUD ((uint16_t) ((F_CPU / (16.0 * (115200))) + 0.5) - 1)
5
6 void uart_init(void);
7 void uart_send_byte(uint8_t ch);
8 void uart_send_hex(uint8_t ch);
9 void uart_send_str(char *ptr);
10 void uart_send_newline(void);
11 int uart_putchar(char c, FILE *stream);
12
13 #endif

```

---

code/hydroAHRS\_mkII/usb\_talk.c

---

```

1 #include "usb_talk.h"
2
3 /* LUSA CDC Class driver interface configuration and state information. This
4 * structure is passed to all CDC Class driver functions, so that multiple
5 * instances of the same class within a device can be differentiated from one
6 * another.
7 */
8 USB_ClassInfo_CDC_Device_t VirtualSerial_CDC_Interface = {
9     .Config = {
10         .ControlInterfaceNumber = 0,
11         .DataINEndpoint = {
12             .Address = CDC_TX_EPADDR,
13             .Size = CDC_TXRX_EPSIZE,
14             .Banks = 1,
15         },
16         .DataOUTEndpoint = {
17             .Address = CDC_RX_EPADDR,
18             .Size = CDC_TXRX_EPSIZE,
19             .Banks = 1,
20         },
21         .NotificationEndpoint = {
22             .Address = CDC_NOTIFICATION_EPADDR,
23             .Size = CDC_NOTIFICATION_EPSIZE,
24             .Banks = 1,
25         },
26     },
27 };
28
29 void usb_talk_init(void) {
30     USB_Init();
31 }

```

---

```

32  /* Create a regular character stream for the interface so that it can be
33  * used with the stdio.h functions */
34  CDC_Device_CreateStream(&VirtualSerial_CDC_Interface, stdout);
35
36  //LEDs_SetAllLEDs(LEDMASK_USB_NOTREADY);
37 }
38
39 #if 0
40 void usb_talk_task(void) {
41     /* Receive bytes from the host */
42     char c = CDC_Device_ReceiveByte(&VirtualSerial_CDC_Interface);
43
44     switch (c) {
45         case 'b':
46             printf("Starting bootloader ... \r\n");
47
48 #if 0
49             cli();
50             delay_ms(2000);
51             EIND = BOOT_SECTION_START >> 17;
52             start_bootloader();
53 #endif
54         break;
55     }
56
57     CDC_Device_USBTask(&VirtualSerial_CDC_Interface);
58     USB_USBTask();
59 }
60 #endif
61
62 void EVENT_USB_Device_Connect(void) {
63     //LEDs_SetAllLEDs(LEDMASK_USB_ENUMERATING);
64 }
65
66 void EVENT_USB_Device_Disconnect(void) {
67     //LEDs_SetAllLEDs(LEDMASK_USB_NOTREADY);
68 }
69
70 void EVENT_USB_Device_ConfigurationChanged(void) {
71     bool ConfigSuccess = true;
72
73     ConfigSuccess &= CDC_Device_ConfigureEndpoints(&VirtualSerial_CDC_Interface);
74
75     //LEDs_SetAllLEDs(ConfigSuccess ? LEDMASK_USB_READY : LEDMASK_USB_ERROR);
76 }
77
78 void EVENT_USB_Device_ControlRequest(void) {
79     CDC_Device_ProcessControlRequest(&VirtualSerial_CDC_Interface);
80 }
81
82 }

```

---

code/hydroAHRs\_mkII/usb\_talk.h

---

```

1 #ifndef _USB_TALK_H_
2 #define _USB_TALK_H_
3
4 #include "Descriptors.h"
5
6 #include <LUFA/Drivers/USB/USB.h>
7
8 void usb_talk_init(void);
9 void usb_talk_task(void);
10
11 void EVENT_USB_Device_Connect(void);
12 void EVENT_USB_Device_Disconnect(void);
13 void EVENT_USB_Device_ConfigurationChanged(void);
14 void EVENT_USB_Device_ControlRequest(void);
15
16 extern USB_ClassInfo_CDC_Device_t VirtualSerial_CDC_Interface;
17
18 #endif

```

---

## code/hydroAHRS\_mkII/vector3d.c

---

```

1 ///////////////////////////////////////////////////////////////////
2 //
3 //   This file is part of linux-mpu9150
4 //
5 //   Copyright (c) 2013 Pansenti, LLC
6 //
7 //   Permission is hereby granted, free of charge, to any person obtaining a copy of
8 //   this software and associated documentation files (the "Software"), to deal in
9 //   the Software without restriction, including without limitation the rights to use,
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11 //   Software, and to permit persons to whom the Software is furnished to do so,
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16 //
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21 //   OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 //   SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #include "vector3d.h"
25
26 void vector3DotProduct(vector3d_t a, vector3d_t b, float *d) {
27     *d = a[VEC3_X] * b[VEC3_X] + a[VEC3_Y] * b[VEC3_Y] + a[VEC3_Z] * b[VEC3_Z];
28 }
29
30 void vector3CrossProduct(vector3d_t a, vector3d_t b, vector3d_t d) {
31     d[VEC3_X] = a[VEC3_Y] * b[VEC3_Z] - a[VEC3_Z] * b[VEC3_Y];
32     d[VEC3_Y] = a[VEC3_Z] * b[VEC3_X] - a[VEC3_X] * b[VEC3_Z];
33     d[VEC3_Z] = a[VEC3_X] * b[VEC3_Y] - a[VEC3_Y] * b[VEC3_X];
34 }

```

---

## code/hydroAHRS\_mkII/vector3d.h

---

```

1 ///////////////////////////////////////////////////////////////////
2 //
3 //   This file is part of linux-mpu9150
4 //
5 //   Copyright (c) 2013 Pansenti, LLC
6 //
7 //   Permission is hereby granted, free of charge, to any person obtaining a copy of
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21 //   OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 //   SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #ifndef VECTOR3D_H
25 #define VECTOR3D_H
26
27 #include <math.h>
28
29 #define DEGREE_TO_RAD          ((float)M_PI / 180.0f)
30 #define RAD_TO_DEGREE          (180.0f / (float)M_PI)
31
32 #define TWO_PI                 (2.0f * (float)M_PI)
33
34 #define VEC3_X                 0

```

---

```

35 #define VEC3_Y      1
36 #define VEC3_Z      2
37
38 typedef float vector3d_t [3];
39
40 void vector3DotProduct(vector3d_t a, vector3d_t b, float *d);
41 void vector3CrossProduct(vector3d_t a, vector3d_t b, vector3d_t d);
42
43 #endif

```

---

code/hydroAHRs\_mkII/quaternion.c

---

```

1  //////////////////////////////////////
2  //
3  //   This file is part of linux-mpu9150
4  //
5  //   Copyright (c) 2013 Pansenti, LLC
6  //
7  //   Permission is hereby granted, free of charge, to any person obtaining a copy of
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10 //   copy, modify, merge, publish, distribute, sublicense, and/or sell copies of the
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20 //   HOLDERS BE LIABLE FOR ANY CLAIM, DAMAGES OR OTHER LIABILITY, WHETHER IN AN ACTION
21 //   OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 //   SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #include "quaternion.h"
25
26 void quaternionNorm(quaternion_t q, float *n)
27 {
28     *n = sqrtf(q[QUAT_W] * q[QUAT_W] + q[QUAT_X] * q[QUAT_X] +
29              q[QUAT_Y] * q[QUAT_Y] + q[QUAT_Z] * q[QUAT_Z]);
30 }
31
32 void quaternionNormalize(quaternion_t q)
33 {
34     float length;
35
36     quaternionNorm(q, &length);
37
38     if (length == 0)
39         return;
40
41     q[QUAT_W] /= length;
42     q[QUAT_X] /= length;
43     q[QUAT_Y] /= length;
44     q[QUAT_Z] /= length;
45 }
46
47 void quaternionToEuler(quaternion_t q, vector3d_t v)
48 {
49     // fix roll near poles with this tolerance
50     float pole = (float)M_PI / 2.0f - 0.05f;
51
52     v[VEC3_Y] = asinf(2.0f * (q[QUAT_W] * q[QUAT_Y] - q[QUAT_X] * q[QUAT_Z]));
53
54     if ((v[VEC3_Y] < pole) && (v[VEC3_Y] > -pole)) {
55         v[VEC3_X] = atan2f(2.0f * (q[QUAT_Y] * q[QUAT_Z] + q[QUAT_W] * q[QUAT_X]),
56                          1.0f - 2.0f * (q[QUAT_X] * q[QUAT_X] + q[QUAT_Y] * q[QUAT_Y]));
57     }
58
59     v[VEC3_Z] = atan2f(2.0f * (q[QUAT_X] * q[QUAT_Y] + q[QUAT_W] * q[QUAT_Z]),
60                      1.0f - 2.0f * (q[QUAT_Y] * q[QUAT_Y] + q[QUAT_Z] * q[QUAT_Z]));

```

```

61 }
62
63 void eulerToQuaternion(vector3d_t v, quaternion_t q)
64 {
65     float cosX2 = cosf(v[VEC3_X] / 2.0f);
66     float sinX2 = sinf(v[VEC3_X] / 2.0f);
67     float cosY2 = cosf(v[VEC3_Y] / 2.0f);
68     float sinY2 = sinf(v[VEC3_Y] / 2.0f);
69     float cosZ2 = cosf(v[VEC3_Z] / 2.0f);
70     float sinZ2 = sinf(v[VEC3_Z] / 2.0f);
71
72     q[QUAT_W] = cosX2 * cosY2 * cosZ2 + sinX2 * sinY2 * sinZ2;
73     q[QUAT_X] = sinX2 * cosY2 * cosZ2 - cosX2 * sinY2 * sinZ2;
74     q[QUAT_Y] = cosX2 * sinY2 * cosZ2 + sinX2 * cosY2 * sinZ2;
75     q[QUAT_Z] = cosX2 * cosY2 * sinZ2 - sinX2 * sinY2 * cosZ2;
76
77     quaternionNormalize(q);
78 }
79
80 void quaternionConjugate(quaternion_t s, quaternion_t d)
81 {
82     d[QUAT_W] = s[QUAT_W];
83     d[QUAT_X] = -s[QUAT_X];
84     d[QUAT_Y] = -s[QUAT_Y];
85     d[QUAT_Z] = -s[QUAT_Z];
86 }
87
88 void quaternionMultiply(quaternion_t qa, quaternion_t qb, quaternion_t qd)
89 {
90     vector3d_t va;
91     vector3d_t vb;
92     float dotAB;
93     vector3d_t crossAB;
94
95     va[VEC3_X] = qa[QUAT_X];
96     va[VEC3_Y] = qa[QUAT_Y];
97     va[VEC3_Z] = qa[QUAT_Z];
98
99     vb[VEC3_X] = qb[QUAT_X];
100    vb[VEC3_Y] = qb[QUAT_Y];
101    vb[VEC3_Z] = qb[QUAT_Z];
102
103    vector3DotProduct(va, vb, &dotAB);
104    vector3CrossProduct(va, vb, crossAB);
105
106    qd[QUAT_W] = qa[QUAT_W] * qb[QUAT_W] - dotAB;
107    qd[QUAT_X] = qa[QUAT_W] * vb[VEC3_X] + qb[QUAT_W] * va[VEC3_X] + crossAB[VEC3_X];
108    qd[QUAT_Y] = qa[QUAT_W] * vb[VEC3_Y] + qb[QUAT_W] * va[VEC3_Y] + crossAB[VEC3_Y];
109    qd[QUAT_Z] = qa[QUAT_W] * vb[VEC3_Z] + qb[QUAT_W] * va[VEC3_Z] + crossAB[VEC3_Z];
110 }

```

---

code/hydroAHRS\_mkII/quaternion.h

---

```

1 ////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////////
2 //
3 // This file is part of linux-mpu9150
4 //
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19 // PARTICULAR PURPOSE AND NONINFRINGEMENT. IN NO EVENT SHALL THE AUTHORS OR COPYRIGHT

```



---

```

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21 // OF CONTRACT, TORT OR OTHERWISE, ARISING FROM, OUT OF OR IN CONNECTION WITH THE
22 // SOFTWARE OR THE USE OR OTHER DEALINGS IN THE SOFTWARE.
23
24 #ifndef MPUQUATERNION_H
25 #define MPUQUATERNION_H
26
27 #include "vector3d.h"
28
29 #define QUAT_W      0
30 #define QUAT_X      1
31 #define QUAT_Y      2
32 #define QUAT_Z      3
33
34 typedef float quaternion_t[4];
35
36 void quaternionNormalize(quaternion_t q);
37 void quaternionToEuler(quaternion_t q, vector3d_t v);
38 void eulerToQuaternion(vector3d_t v, quaternion_t q);
39 void quaternionConjugate(quaternion_t s, quaternion_t d);
40 void quaternionMultiply(quaternion_t qa, quaternion_t qb, quaternion_t qd);
41
42
43 #endif /* MPUQUATERNION_H */

```

---

code/hydroAHRS\_mkII/Descriptors.c

---

```

1 #include "Descriptors.h"
2
3 /* Device descriptor structure. This descriptor, located in FLASH memory, describes
4  * the overall
5  * device characteristics, including the supported USB version, control endpoint size
6  * and the
7  * number of device configurations. The descriptor is read out by the USB host when
8  * the enumeration
9  * process begins.
10 */
11 const USB_Descriptor_Device_t PROGMEM DeviceDescriptor = {
12     .Header = {.Size = sizeof(USB_Descriptor_Device_t), .Type =
13         DTYPE_Device},
14     .USBSpecification = VERSION_BCD(01.10),
15     .Class = CDC_CSCP_CDCClass,
16     .SubClass = CDC_CSCP_NoSpecificSubclass,
17     .Protocol = CDC_CSCP_NoSpecificProtocol,
18     .Endpoint0Size = FIXED_CONTROL_ENDPOINT_SIZE,
19     .VendorID = 0x03EB,
20     .ProductID = 0x2044,
21     .ReleaseNumber = VERSION_BCD(00.01),
22     .ManufacturerStrIndex = STRING_ID_Manufacturer,
23     .ProductStrIndex = STRING_ID_Product,
24     .SerialNumStrIndex = USE_INTERNAL_SERIAL,
25     .NumberOfConfigurations = FIXED_NUM_CONFIGURATIONS
26 };
27
28
29 /** Configuration descriptor structure. This descriptor, located in FLASH memory,
30  * describes the usage
31  * of the device in one of its supported configurations, including information about
32  * any device interfaces
33  * and endpoints. The descriptor is read out by the USB host during the enumeration
34  * process when selecting
35  * a configuration so that the host may correctly communicate with the USB device.
36 */
37 const USB_Descriptor_Configuration_t PROGMEM ConfigurationDescriptor = {
38     .Config = {
39         .Header = {.Size = sizeof(
40             USB_Descriptor_Configuration_Header_t), .Type = DTYPE_Configuration},

```

```

38     .TotalConfigurationSize = sizeof(USB_Descriptor_Configuration_t),
39     .TotalInterfaces         = 2,
40
41     .ConfigurationNumber    = 1,
42     .ConfigurationStrIndex  = NO_DESCRIPTOR,
43
44     .ConfigAttributes       = (USB_CONFIG_ATTR_RESERVED |
45                               USB_CONFIG_ATTR_SELFPOWERED),
46
47     .MaxPowerConsumption   = USB_CONFIG_POWER_MA(100)
48 },
49 .CDC_CCI_Interface = {
50     .Header                 = {.Size = sizeof(USB_Descriptor_Interface_t), .
51                               Type = DTYPE_Interface},
52
53     .InterfaceNumber        = 0,
54     .AlternateSetting       = 0,
55
56     .TotalEndpoints         = 1,
57
58     .Class                  = CDC_CSCP_CDCClass,
59     .SubClass               = CDC_CSCP_ACMSubclass,
60     .Protocol               = CDC_CSCP_ATCommandProtocol,
61
62     .InterfaceStrIndex     = NO_DESCRIPTOR
63 },
64 .CDC_Functional_Header = {
65     .Header                 = {.Size = sizeof(
66                               USB_CDC_Descriptor_FunctionalHeader_t), .Type = DTYPE_CSInterface},
67     .Subtype               = CDC_DSUBTYPE_CSInterface_Header,
68
69     .CDCSpecification       = VERSION_BCD(01.10),
70 },
71 .CDC_Functional_ACM = {
72     .Header                 = {.Size = sizeof(
73                               USB_CDC_Descriptor_FunctionalACM_t), .Type = DTYPE_CSInterface},
74     .Subtype               = CDC_DSUBTYPE_CSInterface_ACM,
75
76     .Capabilities          = 0x06,
77 },
78 .CDC_Functional_Union = {
79     .Header                 = {.Size = sizeof(
80                               USB_CDC_Descriptor_FunctionalUnion_t), .Type = DTYPE_CSInterface},
81     .Subtype               = CDC_DSUBTYPE_CSInterface_Union,
82
83     .MasterInterfaceNumber = 0,
84     .SlaveInterfaceNumber  = 1,
85 },
86 .CDC_NotificationEndpoint = {
87     .Header                 = {.Size = sizeof(USB_Descriptor_Endpoint_t), .Type
88                               = DTYPE_Endpoint},
89
90     .EndpointAddress        = CDC_NOTIFICATION_EPADDR,
91     .Attributes            = (EP_TYPE_INTERRUPT | ENDPOINT_ATTR_NO_SYNC |
92                               ENDPOINT_USAGE_DATA),
93     .EndpointSize          = CDC_NOTIFICATION_EPSIZE,
94     .PollingIntervalMS     = 0xFF
95 },
96 .CDC_DCI_Interface = {
97     .Header                 = {.Size = sizeof(USB_Descriptor_Interface_t), .
98                               Type = DTYPE_Interface},
99
100     .InterfaceNumber        = 1,
101     .AlternateSetting       = 0,
102     .TotalEndpoints         = 2,

```

```

103         .Class                = CDC_CSCP_CDCDataClass,
104         .SubClass              = CDC_CSCP_NoDataSubclass,
105         .Protocol               = CDC_CSCP_NoDataProtocol,
106
107         .InterfaceStrIndex     = NO_DESCRIPTOR
108     },
109
110     .CDC_DataOutEndpoint = {
111         .Header                 = {.Size = sizeof(USB_Descriptor_Endpoint_t), .Type
112                                 = DTYPE_Endpoint},
113
114         .EndpointAddress        = CDC_RX_EPADDR,
115         .Attributes             = (EP_TYPE_BULK | ENDPOINT_ATTR_NO_SYNC |
116                                 ENDPOINT_USAGE_DATA),
117         .EndpointSize           = CDC_TXRX_EPSIZE,
118         .PollingIntervalMS      = 0x05
119     },
120
121     .CDC_DataInEndpoint = {
122         .Header                 = {.Size = sizeof(USB_Descriptor_Endpoint_t), .Type
123                                 = DTYPE_Endpoint},
124
125         .EndpointAddress        = CDC_TX_EPADDR,
126         .Attributes             = (EP_TYPE_BULK | ENDPOINT_ATTR_NO_SYNC |
127                                 ENDPOINT_USAGE_DATA),
128         .EndpointSize           = CDC_TXRX_EPSIZE,
129         .PollingIntervalMS      = 0x05
130     }
131 };
132
133 /** Language descriptor structure. This descriptor, located in FLASH memory, is
134     * returned when the host requests
135     * the string descriptor with index 0 (the first index). It is actually an array of
136     * 16-bit integers, which indicate
137     * via the language ID table available at USB.org what languages the device supports
138     * for its string descriptors.
139 */
140 const USB_Descriptor_String_t PROGMEM LanguageString = {
141     .Header                 = {.Size = USB_STRING_LEN(1), .Type = DTYPE_String},
142     .UnicodeString          = {LANGUAGE_ID_ENG}
143 };
144
145 /** Manufacturer descriptor string. This is a Unicode string containing the
146     * manufacturer's details in human readable
147     * form, and is read out upon request by the host when the appropriate string ID is
148     * requested, listed in the Device
149     * Descriptor.
150 */
151 const USB_Descriptor_String_t PROGMEM ManufacturerString = {
152     .Header                 = {.Size = USB_STRING_LEN(6), .Type = DTYPE_String},
153     .UnicodeString          = L"Epsiro"
154 };
155
156 /** Product descriptor string. This is a Unicode string containing the product's
157     * details in human readable form,
158     * and is read out upon request by the host when the appropriate string ID is
159     * requested, listed in the Device
160     * Descriptor.
161 */
162 const USB_Descriptor_String_t PROGMEM ProductString = {
163     .Header                 = {.Size = USB_STRING_LEN(25), .Type = DTYPE_String},
164     .UnicodeString          = L"Inertial□Measurement□Unit"
165 };
166
167 /** This function is called by the library when in device mode, and must be overridden
168     * (see library "USB Descriptors"
169     * documentation) by the application code so that the address and size of a requested
170     * descriptor can be given

```

```

162 * to the USB library. When the device receives a Get Descriptor request on the
163 * control endpoint, this function
164 * is called so that the descriptor details can be passed back and the appropriate
165 * descriptor sent back to the
166 * USB host.
167 */
168 uint16_t CALLBACK_USB_GetDescriptor(const uint16_t wValue,
169                                     const uint8_t wIndex,
170                                     const void** const DescriptorAddress) {
171     const uint8_t DescriptorType = (wValue >> 8);
172     const uint8_t DescriptorNumber = (wValue & 0xFF);
173     const void* Address = NULL;
174     uint16_t Size = NO_DESCRIPTOR;
175     switch (DescriptorType) {
176     case DTYPE_Device:
177         Address = &DeviceDescriptor;
178         Size = sizeof(USB_Descriptor_Device_t);
179         break;
180     case DTYPE_Configuration:
181         Address = &ConfigurationDescriptor;
182         Size = sizeof(USB_Descriptor_Configuration_t);
183         break;
184     case DTYPE_String:
185         switch (DescriptorNumber) {
186         case STRING_ID_Language:
187             Address = &LanguageString;
188             Size = pgm_read_byte(&LanguageString.Header.Size);
189             break;
190         case STRING_ID_Manufacturer:
191             Address = &ManufacturerString;
192             Size = pgm_read_byte(&ManufacturerString.Header.Size);
193             break;
194         case STRING_ID_Product:
195             Address = &ProductString;
196             Size = pgm_read_byte(&ProductString.Header.Size);
197             break;
198         }
199     }
200     break;
201 }
202 }
203
204 *DescriptorAddress = Address;
205 return Size;
206 }

```

## code/hydroAHRS\_mkII/Descriptors.h

```

1 #ifndef _DESCRIPTORS_H_
2 #define _DESCRIPTORS_H_
3
4 /* Includes: */
5 #include <avr/pgmspace.h>
6
7 #include <LUFA/Drivers/USB/USB.h>
8
9 /* Macros: */
10 /** Endpoint address of the CDC device-to-host notification IN endpoint. */
11 #define CDC_NOTIFICATION_EPADDR (ENDPOINT_DIR_IN | 2)
12
13 /** Endpoint address of the CDC device-to-host data IN endpoint. */
14 #define CDC_TX_EPADDR (ENDPOINT_DIR_IN | 3)
15
16 /** Endpoint address of the CDC host-to-device data OUT endpoint. */
17 #define CDC_RX_EPADDR (ENDPOINT_DIR_OUT | 4)
18
19 /** Size in bytes of the CDC device-to-host notification IN endpoint. */
20 #define CDC_NOTIFICATION_EPSIZE 8
21
22 /** Size in bytes of the CDC data IN and OUT endpoints. */

```

```

23 #define CDC_TXRX_EPSIZE          16
24
25 /* Type Defines: */
26 /** Type define for the device configuration descriptor structure. This must be defined
27     * in the
28     * application code, as the configuration descriptor contains several sub-descriptors
29     * which
30     * vary between devices, and which describe the device's usage to the host.
31     */
32 typedef struct {
33     USB_Descriptor_Configuration_Header_t    Config;
34     // CDC Control Interface
35     USB_Descriptor_Interface_t              CDC_CCI_Interface;
36     USB_CDC_Descriptor_FunctionalHeader_t    CDC_Functional_Header;
37     USB_CDC_Descriptor_FunctionalACM_t       CDC_Functional_ACM;
38     USB_CDC_Descriptor_FunctionalUnion_t     CDC_Functional_Union;
39     USB_Descriptor_Endpoint_t                CDC_NotificationEndpoint;
40     // CDC Data Interface
41     USB_Descriptor_Interface_t              CDC_DCI_Interface;
42     USB_Descriptor_Endpoint_t                CDC_DataOutEndpoint;
43     USB_Descriptor_Endpoint_t                CDC_DataInEndpoint;
44 } USB_Descriptor_Configuration_t;
45
46 /** Enum for the device string descriptor IDs within the device. Each string descriptor
47     * should
48     * have a unique ID index associated with it, which can be used to refer to the string
49     * from
50     * other descriptors.
51     */
52 enum StringDescriptors_t {
53     STRING_ID_Language      = 0, /**< Supported Languages string descriptor ID (must be
54         * zero) */
55     STRING_ID_Manufacturer  = 1, /**< Manufacturer string ID */
56     STRING_ID_Product       = 2, /**< Product string ID */
57 };
58
59 /* Function Prototypes: */
60 uint16_t CALLBACK_USB_GetDescriptor(const uint16_t wValue,
61     const uint8_t wIndex,
62     const void** const DescriptorAddress)
63     ATTR_WARN_UNUSED_RESULT ATTR_NON_NULL_PTR_ARG(3);
64
65 #endif

```

---

#### code/hydroAHRS\_mkII/LUFAConfig.h

---

```

1 #ifndef _LUFACONFIG_H_
2 #define _LUFACONFIG_H_
3
4 /* General USB Driver Related Tokens: */
5 #define USE_STATIC_OPTIONS      (USB_DEVICE_OPT_FULLSPEED |
6     USB_OPT_RC32MCLKSRC | USB_OPT_BUSEVENT_PRIHIGH)
7
8 /* USB Device Mode Driver Related Tokens: */
9 #define USE_FLASH_DESCRIPTOR
10 #define FIXED_CONTROL_ENDPOINT_SIZE      8
11 #define FIXED_NUM_CONFIGURATIONS        1
12 #define MAX_ENDPOINT_INDEX              4
13 #endif

```

---

#### code/hydroAHRS\_mkII/Makefile

---

```

1 MCU          = atxmega128a4u
2 F_CPU        = 32000000
3 F_USB        = 48000000
4
5 AVRDUDE = avrdude -p x128a4u -c jtag2pdi -P usb

```

---

```

7 CC = avr-gcc
8 CC_FLAGS = -DUSE_LUFA_CONFIG_HEADER
9 CC_FLAGS += -g -W -O2 -std=gnu99 -mmcu=$(MCU) -DF_CPU=$(F_CPU) -pedantic -mrelax
10 CC_FLAGS += -ffunction-sections
11 LD_FLAGS = -Wl,-gc-sections -Wl,-u,vfprintf -lprintf_flt -lm
12 DEFS = -DEMPL_TARGET_LINUX -DMPU9150 -DAK8975_SECONDARY -DTWIC

14 EMPLDIR = eMPL

16 SRC = $(TARGET).c usb_talk.c Descriptors.c uart.c twi.c twim.c mpu9150.c
      quaternion.c vector3d.c inv_mpu.c inv_mpu_dmp_motion_driver.c nmea.c $(LUFA_SRC_USB
      ) $(LUFA_SRC_USBCLASS)
17 TARGET = main
18 ARCH = XMEGA
19 OPTIMIZATION = s
20 LUFA_PATH = ../LUFA-130901/LUFA

22 include $(LUFA_PATH)/Build/lufa_sources.mk
23 include $(LUFA_PATH)/Build/lufa_build.mk
24 include $(LUFA_PATH)/Build/lufa_hid.mk

26 flash: all
27     #$(AVRDUDE) -U flash:w:main.hex:i
28     -echo "b" > /dev/ttyACM0
29     sleep 3
30     dfu-programmer atxmega128a4u erase
31     dfu-programmer atxmega128a4u flash main.hex --debug 5
32     ! dfu-programmer atxmega128a4u start

34 inv_mpu_dmp_motion_driver.o : $(EMPLDIR)/inv_mpu_dmp_motion_driver.c
35     $(CC) $(CC_FLAGS) $(DEFS) -I $(EMPLDIR) -c $(EMPLDIR)/inv_mpu_dmp_motion_driver.c

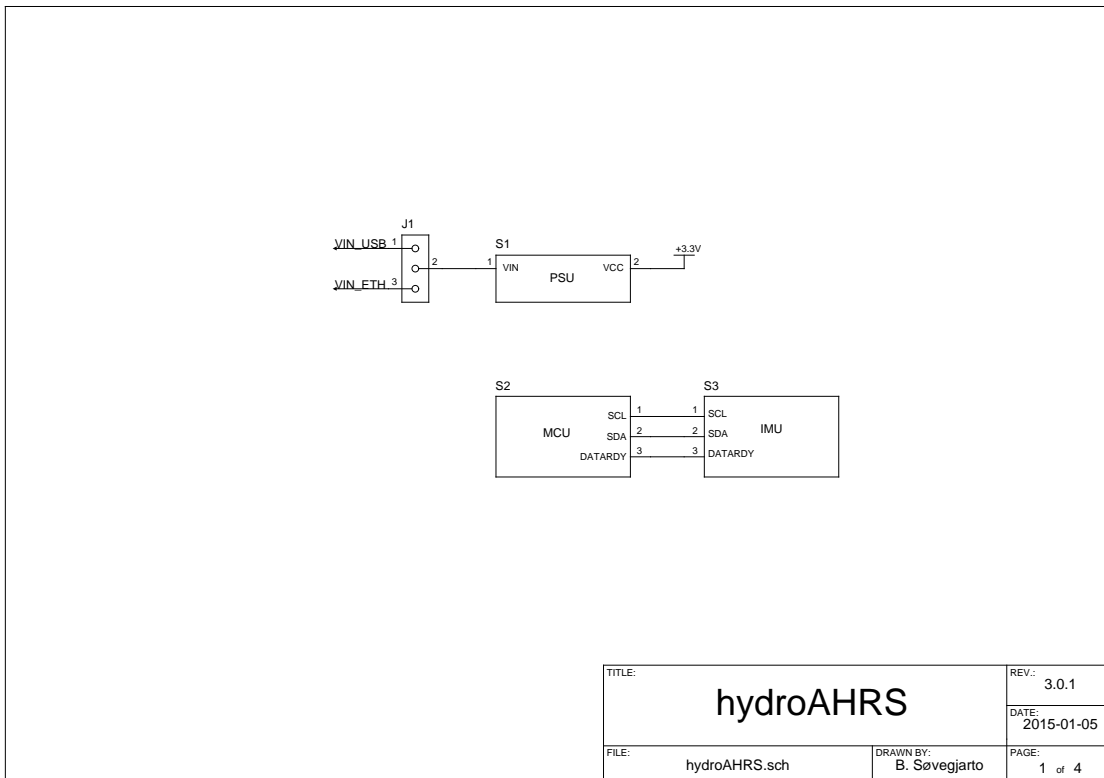
37 inv_mpu.o : $(EMPLDIR)/inv_mpu.c
38     $(CC) $(CC_FLAGS) $(DEFS) -I $(EMPLDIR) -c $(EMPLDIR)/inv_mpu.c

```

---

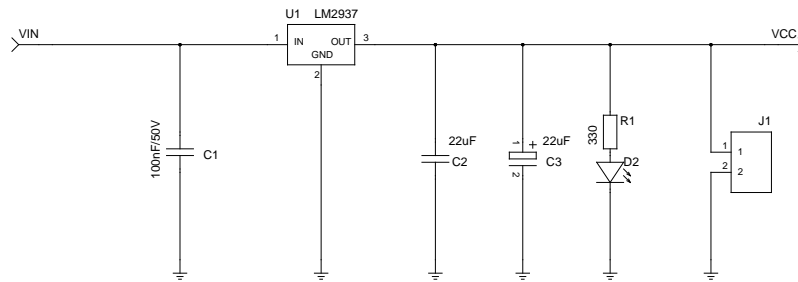
# Appendix L

## HydroAHRs mk.III schematics

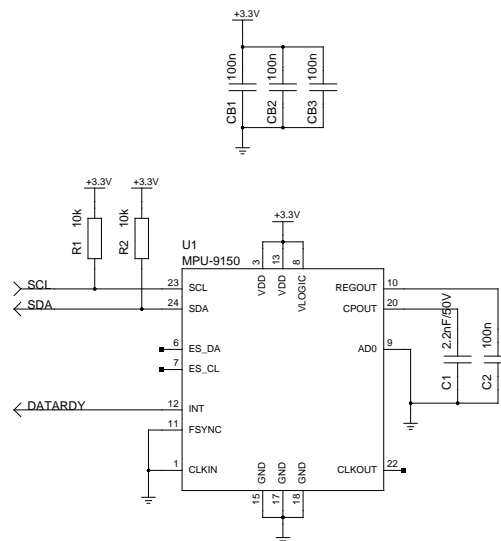








TITLE: <b>hydroAHRs</b>		REV.:
		DATE:
FILE: power_supply_unit.sch	DRAWN BY: B. Søvegjarto	PAGE: 2 of 4



TITLE: <b>hydroAHRs</b>		REV.:
		DATE:
FILE: inertial_measurement_unit.sch	DRAWN BY: B. Søvegjarto	PAGE: 4 of 4

## Appendix M

# MCU code for ASV autopilot

Code for TM4C129 ASV autopilot, as described in section 3.3.4. A block schematic of the algorithm running in the program is shown in figure 3.4.

This code depends on `iot_mcu_lib` and `iot_tiva_template` from Lindem Data Acquisition AS. `iot_mcu_lib` is available at [https://github.com/Lindem-Data-Acquisition-AS/iot\\_mcu\\_lib](https://github.com/Lindem-Data-Acquisition-AS/iot_mcu_lib). `iot_tiva_template` is available at [https://github.com/Lindem-Data-Acquisition-AS/iot\\_tiva\\_template](https://github.com/Lindem-Data-Acquisition-AS/iot_tiva_template). The code is licensed under the MIT license (listing D.1) unless otherwise specified.

code/asv\_ap\_mcu/src/main.c

```
1 #include <stdbool.h>
2 #include <stdint.h>
3 #include <stddef.h>
4 #include <stdio.h>
5 #include "inc/hw_gpio.h"
6 #include "inc/hw_memmap.h"
7 #include "inc/hw_nvic.h"
8 #include "inc/hw_types.h"
9 #include "inc/tm4c129xnczad.h"
10
11 #include "driverlib/gpio.h"
12 #include "driverlib/pin_map.h"
13 #include "driverlib/rom.h"
14 #include "driverlib/rom_map.h"
15 #include "driverlib/sysctl.h"
16 #include "led_task.h"
17 #include "lwip_task.h"
18 #include "hello_world_task.h"
19 #include "asv_ap/uart.h"
20 #include "asv_ap/course_calc.h"
21
22 #include "FreeRTOS.h"
23 #include "task.h"
24 #include "queue.h"
25 #include "semphr.h"
26
27 uint32_t g_system_clock;
28
29 #ifdef DEBUG
30 void
31 __error__(char *pcFilename, uint32_t ui32Line) {
32     send_debug_assert(pcFilename, ui32Line);
33 }
34 #endif
35
36 void
37 vApplicationStackOverflowHook(xTaskHandle *pxTask, signed char *pcTaskName) {
38
```

```

39     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_0, 0); // Red
40     ROM_GPIOPinWrite(GPIO_PORTH_BASE, GPIO_PIN_2, 0); // Yellow
41
42     while(1) {
43     }
44 }
45 }
46
47 void
48 pin_init(void) {
49
50     // Enable all the GPIO peripherals
51     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOA);
52     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOB);
53     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOC);
54     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
55     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOE);
56     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOF);
57     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOG);
58     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOH);
59     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOJ);
60     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOK);
61     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOL);
62     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOM);
63     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPION);
64     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOP);
65     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOQ);
66     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOR);
67     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOS);
68
69     // PF1/PK4/PK6 are used for Ethernet LEDs
70     ROM_GPIOPinConfigure(GPIO_PK4_EN0LED0);
71     ROM_GPIOPinConfigure(GPIO_PK6_EN0LED1);
72     GPIOPinTypeEthernetLED(GPIO_PORTK_BASE, GPIO_PIN_4);
73     GPIOPinTypeEthernetLED(GPIO_PORTK_BASE, GPIO_PIN_6);
74
75 #ifdef DEVKIT
76     // PN5 is used for the user LED
77     ROM_GPIOPinTypeGPIOOutput(GPIO_PORTN_BASE, GPIO_PIN_5);
78     ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, 0);
79 #else
80     // PA0-1 is used for the user LED
81     ROM_GPIOPinTypeGPIOOutput(GPIO_PORTA_BASE, GPIO_PIN_1 | GPIO_PIN_0);
82     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1 | GPIO_PIN_0, GPIO_PIN_0);
83     // PH2-3 is used for the user LED
84     ROM_GPIOPinTypeGPIOOutput(GPIO_PORTH_BASE, GPIO_PIN_3 | GPIO_PIN_2);
85     ROM_GPIOPinWrite(GPIO_PORTH_BASE, GPIO_PIN_3 | GPIO_PIN_2, GPIO_PIN_3 | GPIO_PIN_2)
86     ;
87 #endif
88 }
89
90 int
91 main(void) {
92
93     // Run from the PLL at 120 MHz.
94     g_system_clock = MAP_SysCtlClockFreqSet((SYSCTL_XTAL_25MHZ |
95         SYSCTL_OSC_MAIN |
96         SYSCTL_USE_PLL |
97         SYSCTL_CFG_VCO_480),
98         configCPU_CLOCK_HZ);
99
100     MAP_FPULazyStackingEnable();
101     MAP_FPUEnable();
102
103     // UART init
104     UART_init();
105
106     // Enable processor interrupts.
107     ROM_IntMasterEnable();
108
109     // Initialize the device pinout appropriately for this board.
110     pin_init();

```

```

111
112 // Make sure the main oscillator is enabled because this is required by
113 // the PHY. The system must have a 25MHz crystal attached to the OSC
114 // pins. The SYSCCTL_MOSC_HIGHFREQ parameter is used when the crystal
115 // frequency is 10MHz or higher.
116 SysCtlMOSCConfigSet(SYSCCTL_MOSC_HIGHFREQ);
117
118 printf("ASV_ autopilot_ started.\r\n");
119
120 // Create the LED task.
121 if (LEDTaskInit() != 0) {
122     while (1) {
123     }
124 }
125
126 }
127
128 // Create the lwIP tasks.
129 if (lwIPTaskInit() != 0) {
130     while (1) {
131     }
132 }
133
134 }
135
136 // Create the hello world task.
137 if (hello_world_init() != 0) {
138     while (1) {
139     }
140 }
141
142 }
143
144 // Create the ASV autopilot task.
145 if (ASV_ap_init() != 0) {
146     while (1) {
147     }
148 }
149
150 }
151
152 // Start the scheduler. This should not return.
153 vTaskStartScheduler();
154
155 // In case the scheduler returns for some reason, loop forever.
156 while (1) {
157 }
158 }

```

---

code/asv\_ap\_mcu/src/main.h

---

```
1 extern uint32_t g_system_clock;
```

---

code/asv\_ap\_mcu/src/asv\_ap/course\_calc.c

---

```

1 #include <stdint.h>
2 #include <stdbool.h>
3 #include <inttypes.h>
4 #include <string.h>
5 #include <math.h>
6 #include <stdio.h>
7 #include <stdlib.h>
8
9 #include "lwip/tcp.h"
10 #include "course_calc.h"
11 #include "driverlib/rom.h"
12 #include "uart.h"
13 #include "nmea.h"
14 #include "priorities.h"
15 #include "FreeRTOS.h"
16 #include "task.h"

```

```

17 #include "queue.h"
18 #include "semphr.h"
19 #include "udp_send.h"
20
21 #define STACKSIZE_ASV_AP_TASK      600
22
23 #define K_P      0.30
24 #define K_I      0.00
25 #define K_D      0.00
26
27 coord_t g_current_location;
28 coord_t g_first_waypoint = {0, 0};
29 coord_t g_next_waypoint = {0, 0};
30 float g_current_heading;
31 float g_wanted_heading;
32 uint32_t g_current_time;
33 uint32_t g_current_date;
34 uint8_t g_magnetometer_accuracy;
35 uint8_t g_run_motors = false;
36 uint8_t g_nr_of_wp = 0;
37 float g_distance_to_wp;
38 int8_t g_port_motor_thrust = 0;
39 int8_t g_stbd_motor_thrust = 0;
40 uint8_t g_send_status_msg = false;
41 uint8_t g_gps_ready = false;
42 uint8_t g_compass_ready = false;
43
44 static char message[4][UART_BUFFER_SIZE];
45
46
47 float
48 get_heading(coord_t initial_coordinate, coord_t final_coordinate) {
49
50     float lat1 = initial_coordinate.latitude * DEG2RAD;
51     float lat2 = final_coordinate.latitude * DEG2RAD;
52
53     float delta_lon = (final_coordinate.longitude - initial_coordinate.longitude) *
54         DEG2RAD;
55
56     float y = sin(delta_lon) * cos(lat2);
57     float x = cos(lat1)*sin(lat2) - sin(lat1)*cos(lat2)*cos(delta_lon);
58
59     float heading = atan2(y, x);
60
61     //          (heading*RAD2DEG + 360.0)% 360.0;
62     return fmod((heading*RAD2DEG + 360.0), 360.0);
63 }
64
65 float
66 get_distance(coord_t initial_coordinate, coord_t final_coordinate) {
67
68     uint32_t R = 6373;
69
70     float lat1 = initial_coordinate.latitude * DEG2RAD;
71     float lat2 = final_coordinate.latitude * DEG2RAD;
72
73     float delta_lon = (final_coordinate.longitude - initial_coordinate.longitude) *
74         DEG2RAD;
75     float delta_lat = (final_coordinate.latitude - initial_coordinate.latitude) *
76         DEG2RAD;
77
78     float a = pow(sin(delta_lat/2), 2) + cos(lat1)*cos(lat2)*pow(sin(delta_lon/2), 2);
79     float c = 2 * atan2(sqrt(a), sqrt(1 - a));
80     float distance = R * c;
81
82     return 1000*distance;
83 }
84
85 void
86 drive_motors(int8_t port_motor_int, int8_t stbd_motor_int) {
87
88     /* Get the direction */
89     uint8_t port_motor_direction = (port_motor_int > 0) ? 1 : 0;

```

```

87     uint8_t stbd_motor_direction = (stbd_motor_int > 0) ? 1 : 0;
88
89     /* Get the speed */
90     uint8_t port_motor_speed = abs(port_motor_int);
91     uint8_t stbd_motor_speed = abs(stbd_motor_int);
92
93     /* Build the steering commands */
94     uint8_t port_motor_cmd = (PORT_MOTOR << 7) | (port_motor_direction << 6) | (
95         port_motor_speed & 0x3f);
96     uint8_t stbd_motor_cmd = (STBD_MOTOR << 7) | (stbd_motor_direction << 6) | (
97         stbd_motor_speed & 0x3f);
98
99     /* Send the steering commands to the MC */
100    ROM_UARTCharPut(MC_MODULE, port_motor_cmd);
101    ROM_UARTCharPut(MC_MODULE, stbd_motor_cmd);
102 }
103
104 void
105 pid_init(int16_t p_factor, int16_t i_factor, int16_t d_factor, pid_data_t *pid) {
106     // Start values for PID controller
107     pid->sum_error = 0;
108     pid->last_process_value = 0;
109
110     // Tuning constants for PID loop
111     pid->P_factor = p_factor;
112     pid->I_factor = i_factor;
113     pid->D_factor = d_factor;
114
115     // Limits to avoid overflow
116     pid->max_error = MAX_INT / (pid->P_factor + 1);
117     pid->max_sum_error = MAX_I_TERM / (pid->I_factor + 1);
118 }
119
120 int16_t
121 modulo(int16_t a, int16_t n) {
122     return a - floor(a/n) * n;
123 }
124
125 int16_t
126 angle_difference(int16_t a1, int16_t a2) {
127     return modulo(((a1 - a2) + 180), 360) - 180;
128 }
129
130 int16_t
131 pid_controller(int16_t set_point, int16_t process_value, pid_data_t *pid) {
132     int16_t error, p_term, d_term;
133     int32_t i_term, ret, temp;
134
135     error = angle_difference(set_point, process_value);
136
137     // Calculate p_term and limit error overflow
138     if (error > pid->max_error) {
139         p_term = MAX_INT;
140     } else if (error < -pid->max_error) {
141         p_term = -MAX_INT;
142     } else {
143         p_term = pid->P_factor * error;
144     }
145
146     // Calculate i_term and limit integral runaway
147     temp = pid->sum_error + error;
148
149     if (temp > pid->max_sum_error) {
150         i_term = MAX_I_TERM;
151     } else if (temp < -pid->max_sum_error) {
152         i_term = -MAX_I_TERM;
153     } else {
154         i_term = pid->I_factor * temp;
155     }
156
157     d_term = pid->D_factor * (temp - pid->last_process_value);
158
159     ret = p_term + i_term + d_term;
160     pid->sum_error = temp;
161     pid->last_process_value = process_value;
162
163     return ret;
164 }

```

```

158     } else {
159         pid->sum_error = temp;
160         i_term = pid->I_factor * pid->sum_error;
161     }
162 }
163
164 // Calculate d_term
165 d_term = pid->D_factor * (pid->last_process_value - process_value);
166
167 pid->last_process_value = process_value;
168
169 ret = (p_term + i_term + d_term) / SCALING_FACTOR;
170 if (ret > MAX_INT) {
171     ret = MAX_INT;
172 } else if (ret < -MAX_INT) {
173     ret = -MAX_INT;
174 }
175
176 return ( (int16_t) ret);
177 }
178
179 void
180 calculate_motor_thrust(pid_data_t *pid) {
181     //int8_t constant_thrust = 0;
182     int8_t constant_thrust = 32;
183
184     int8_t port_motor_thrust;
185     int8_t stbd_motor_thrust;
186     int8_t diff_thrust;
187
188     int8_t stbd_motor_sign;
189     int8_t port_motor_sign;
190
191     /* PID regulator */
192     int16_t change = pid_controller(g_wanted_heading, g_current_heading, pid);
193
194     /* Use different driving direction for each of the motors */
195     stbd_motor_sign = (change > 0) ? -1 : 1;
196     port_motor_sign = (change > 0) ? 1 : -1;
197
198     /* Run both motors at the same speed if change is below threshold */
199     if (abs(change) < 2) {
200         diff_thrust = 0;
201     }
202
203     /* Set lower bounds */
204     } else if (abs(change) < 8) {
205         diff_thrust = 8;
206     }
207
208     /* Set upper bounds */
209     } else if (abs(change) > 24) {
210         diff_thrust = 24;
211     }
212     } else {
213         diff_thrust = abs(change);
214     }
215
216     stbd_motor_thrust = constant_thrust + diff_thrust*stbd_motor_sign;
217     port_motor_thrust = constant_thrust + diff_thrust*port_motor_sign;
218
219 #if VERBOSE
220     printf("Motor thrust (change, port, stbd): %i %i %i\n\r", change, port_motor_thrust
221         , stbd_motor_thrust);
222     fflush(stdout);
223 #endif
224
225     g_compass_ready = true;
226     if (g_run_motors && g_gps_ready && g_compass_ready) {
227         g_stbd_motor_thrust = stbd_motor_thrust;
228         g_port_motor_thrust = port_motor_thrust;
229
230         drive_motors(port_motor_thrust, stbd_motor_thrust);

```

```

230     } else {
231
232         g_stbd_motor_thrust = 0;
233         g_port_motor_thrust = 0;
234
235         drive_motors(0, 0);
236     }
237 }
238
239 void handle_mc_data() {
240     get_uart_str(MC_DEVICE, message[MC_DEVICE]);
241
242     #ifndef DEBUG
243     printf("MC: %s\r\n", message[MC_DEVICE]);
244     #endif
245 }
246
247
248 void handle_imu_data(pid_data_t *pid) {
249     get_uart_str(IMU_DEVICE, message[IMU_DEVICE]);
250
251     #ifndef DEBUG
252     printf("IMU: %s\r\n", message[IMU_DEVICE]);
253     #endif
254
255     /* Check that the sentence is not corrupt */
256     //if(nmea_checksum(message[GNSS_DEVICE]) == 0) {
257     nmea_parse_PASHR(message[IMU_DEVICE]);
258
259     if (g_magnetometer_accuracy == 3) {
260         g_compass_ready = true;
261     } else {
262         g_compass_ready = false;
263     }
264 }
265
266 #if VERBOSE
267     printf("Course (wanted, current, error): %+07.2f %+07.2f %+07.2f\r\n",
268         g_wanted_heading, g_current_heading, (g_wanted_heading -
269         g_current_heading));
270 #endif
271
272     calculate_motor_thrust(pid);
273     g_send_status_msg = true;
274 } //}
275 }
276
277 void handle_gnss_data() {
278     get_uart_str(GNSS_DEVICE, message[GNSS_DEVICE]);
279
280     #ifndef DEBUG
281     printf("GNSS: %s\r\n", message[GNSS_DEVICE]);
282     #endif
283
284     /* Get the wanted message type */
285     if (!strncmp(message[GNSS_DEVICE], "$GPRMC", 6)) {
286
287         /* Check that the sentence is not corrupt */
288         if(nmea_checksum(message[GNSS_DEVICE]) == 0) {
289
290             /* And if it good, parse it and get the current location */
291             nmea_parse_GPRMC(message[GNSS_DEVICE]);
292
293             if (g_current_location.latitude == 0 && g_current_location.longitude == 0)
294             {
295                 g_gps_ready = false;
296             } else {
297
298                 g_gps_ready = true;
299
300                 g_distance_to_wp = get_distance(g_current_location, g_first_waypoint);

```



```

301
302     if (g_distance_to_wp < 2) {
303
304         if (g_next_waypoint.latitude != 0 && g_next_waypoint.longitude !=
305             0) {
306             g_first_waypoint.latitude = g_next_waypoint.latitude;
307             g_first_waypoint.longitude = g_next_waypoint.longitude;
308             g_next_waypoint.latitude = 0;
309             g_next_waypoint.longitude = 0;
310         } else {
311             g_first_waypoint.latitude = 0;
312             g_first_waypoint.longitude = 0;
313
314             /* Stop motors if we do not have a waypoint to go to */
315             g_run_motors = false;
316         }
317     }
318
319     g_nr_of_wp = 0;
320     if (g_first_waypoint.latitude != 0 && g_first_waypoint.longitude != 0)
321         g_nr_of_wp++;
322     if (g_next_waypoint.latitude != 0 && g_next_waypoint.longitude != 0)
323         g_nr_of_wp++;
324
325     /* We can now calculate our new course based on our current
326        * location and the first waypoint */
327     g_wanted_heading = get_heading(g_current_location, g_first_waypoint);
328 }
329 }
330
331 void
332 parse_command(struct tcp_pcb *tpcb, struct pbuf *p) {
333
334     char incoming_command[512];
335     char response[128];
336     uint32_t index = 0;
337     double latitude;
338     double longitude;
339
340     send_udp_bin((uint8_t *) p->payload, p->len);
341
342     memcpy(&incoming_command, p->payload, p->len);
343     printf("%s\r\n", incoming_command);
344
345     /* Check message type*/
346     if (!strcmp(&incoming_command[index], "HLT", 3)) {
347         g_run_motors = false;
348     } else if (!strcmp(&incoming_command[index], "RUN", 3)) {
349         g_run_motors = true;
350     } else if (!strcmp(&incoming_command[index], "DEL", 3)) {
351         g_first_waypoint.latitude = 0;
352         g_first_waypoint.longitude = 0;
353         g_next_waypoint.latitude = 0;
354         g_next_waypoint.longitude = 0;
355         g_nr_of_wp = 0;
356     } else if (!strcmp(&incoming_command[index], "NWP", 3)) {
357
358         /* Skip to message */
359         while (incoming_command[index++] != ',');
360
361         // Get latitude [+-----]
362         latitude = atof(&incoming_command[index]);
363
364         // Skip the latitude (already parsed)
365         while (incoming_command[index++] != ',');
366
367         // Get longitude [+-----]

```

```

371     longitude = atof(&incomming_command[index]);
372
373     // Skip the longitude (already parsed)
374     while (incomming_command[index++] != ',');
375
376     // Acknowledge new WP
377     sprintf(response, "ACKRWP,%.6f,%.6f", latitude, longitude);
378     send_udp(response);
379     // FIXME use tcp for ack. tcp_write(tpcb, &response, strlen(response), 1);
380
381     if (g_first_waypoint.latitude == 0 && g_first_waypoint.longitude == 0) {
382         g_first_waypoint.latitude = latitude;
383         g_first_waypoint.longitude = longitude;
384
385     } else {
386         g_next_waypoint.latitude = latitude;
387         g_next_waypoint.longitude = longitude;
388     }
389
390     g_nr_of_wp = 0;
391     if (g_first_waypoint.latitude != 0 && g_first_waypoint.longitude != 0)
392         g_nr_of_wp++;
393     if (g_next_waypoint.latitude != 0 && g_next_waypoint.longitude != 0)
394         g_nr_of_wp++;
395
396     g_distance_to_wp = get_distance(g_current_location, g_first_waypoint);
397 }
398 static void
399 ASV_ap_task(void *pvParameters) {
400     char status[512];
401     uint8_t gps_ok = 0;
402
403     // Loop forever
404     while (1) {
405         vTaskDelay(100 / portTICK_RATE_MS);
406
407         if (cmd_counter[MC_DEVICE] > 0) {
408             handle_mc_data();
409         }
410
411         if (cmd_counter[IMU_DEVICE] > 0) {
412             handle_imu_data(pvParameters);
413         }
414
415         if (cmd_counter[GNSS_DEVICE] > 0) {
416             handle_gnss_data();
417         }
418
419         if (g_current_location.latitude != 0 && g_current_location.longitude != 0) {
420             gps_ok = 1;
421         } else {
422             gps_ok = 0;
423         }
424
425         if (g_send_status_msg) {
426             sprintf(status, "STATUS,%06lu,%06lu,%.6f,%.6f,%i,%.2f,%.2f,%.1f,%i,%d,%d
427                 ,%.6f,%.6f,%.6f,%.6f,%i,%i",
428                 (unsigned long) g_current_date,
429                 (unsigned long) g_current_time,
430                 g_current_location.latitude,
431                 g_current_location.longitude,
432                 gps_ok,
433                 g_current_heading,
434                 g_wanted_heading,
435                 g_distance_to_wp,
436                 g_nr_of_wp,
437                 g_magnetometer_accuracy,
438                 g_run_motors,

```

```

441         g_first_waypoint.latitude ,
442         g_first_waypoint.longitude ,
443         g_next_waypoint.latitude ,
444         g_next_waypoint.longitude ,
445         g_port_motor_thrust ,
446         g_stbd_motor_thrust);
447
448         //UART_send(LOG_MODULE, status);
449         printf("%s\r\n", status);
450         send_udp( status );
451
452         g_send_status_msg = false;
453     }
454 }
455 }
456
457 uint32_t
458 ASV_ap_init(void) {
459     // Parameters for regulator
460     static pid_data_t pid_data;
461
462     // PID init
463     pid_init(K_P * SCALING_FACTOR, K_I * SCALING_FACTOR , K_D * SCALING_FACTOR , &
464             pid_data);
465
466     // Start AHRS
467     //UART_send(IMU_MODULE, "s");
468
469     // Create the ASV autopilot task.
470     if (xTaskCreate(ASV_ap_task, (const portCHAR * const)"ASV_ap",
471                   STACKSIZE_ASV_AP_TASK, &pid_data, tskIDLE_PRIORITY + PRIORITY_ASV_AP_TASK, NULL
472                   ) != pdTRUE) {
473         return(1);
474     }
475     // Success.
476     return(0);
477 }

```

---

code/asv\_ap\_mcu/src/asv\_ap/course\_calc.h

---

```

1 #ifndef COURSE_CALC_H
2 #define COURSE_CALC_H
3
4 /** 239.255.66.83 */
5 // #define IPADDR_ASV_MULTICAST ((u32_t)0xefff4253UL)
6 /** 192.168.0.19 */
7 // #define IPADDR_ASV_MULTICAST ((u32_t)0xC0A80013UL)
8 #define IPADDR_ASV_MULTICAST ((u32_t)0xffffffffUL)
9
10 #include <stdint.h>
11
12 #define SCALING_FACTOR 128
13
14 #define PORT_MOTOR 0x00
15 #define STBD_MOTOR 0x01
16
17 #define DEG2RAD (2*M_PI)/360.0
18 #define RAD2DEG 360.0/(2*M_PI)
19
20 typedef struct {
21     double latitude;
22     double longitude;
23 } coord_t;
24
25 extern coord_t g_current_location;
26 extern coord_t g_first_waypoint;
27 extern coord_t g_next_waypoint;
28
29 extern float g_current_heading;
30 extern float g_wanted_heading;

```

```

31
32 extern uint32_t g_current_time;
33 extern uint32_t g_current_date;
34
35 extern uint8_t g_magnetometer_accuracy;
36 extern uint8_t g_run_motors;
37
38 extern uint32_t hello_world_init(void);
39
40 /* PID Status
41 * Setpoints and data used by the PID control algorithm
42 */
43 typedef struct {
44
45     // Last process value, used to find derivative of process value.
46     int16_t last_process_value;
47
48     // Summation of errors, used for integrate calculations
49     int32_t sum_error;
50
51     // The Proportional tuning constant, multiplied with SCALING_FACTOR
52     int16_t P_factor;
53
54     // The Integral tuning constant, multiplied with SCALING_FACTOR
55     int16_t I_factor;
56
57     // The Derivative tuning constant, multiplied with SCALING_FACTOR
58     int16_t D_factor;
59
60     // Maximum allowed error, avoid overflow
61     int16_t max_error;
62
63     // Maximum allowed sumerror, avoid overflow
64     int32_t max_sum_error;
65
66 } pid_data_t;
67
68 /* Maximum values
69 * Needed to avoid sign/overflow problems
70 */
71 #define MAX_INT           INT16_MAX
72 #define MAX_LONG         INT32_MAX
73 #define MAX_I_TERM       (MAX_LONG / 2)
74
75 void pid_init(int16_t p_factor, int16_t i_factor, int16_t d_factor, pid_data_t *pid);
76 void handle_mc_data();
77 void handle_imu_data(pid_data_t *pid);
78 void handle_gnss_data();
79 void parse_command(struct tcp_pcb *tpcb, struct pbuf *p);
80 uint32_t ASV_ap_init(void);
81
82 #endif

```

---

code/asv\_ap\_mcu/src/asv\_ap/nmea.c

---

```

1 #include "nmea.h"
2
3 #include <inttypes.h>
4 #include <ctype.h>
5 #include <stdlib.h>
6 #include <stdint.h>
7 #include <stdio.h>
8 #include <string.h>
9 #include <math.h>
10 #include "course_calc.h"
11 #include "uart.h"
12
13 uint8_t
14 nmea_checksum(char nmea_sentence[]) {
15     char checksum[3];
16
17

```

```

18     uint8_t temp = 0;
19
20     // 1 because we want to skip the $
21     uint8_t i = 1;
22
23     /* Calculate checksum */
24     while(nmea_sentence[i] != '*')
25
26         // XOR each character
27         temp ^= nmea_sentence[i++];
28
29     sprintf(checksum, "%02X\n", temp);
30
31     /* Compare the two last characters in nmea_sentence with the checksum in (ascii) */
32     if (nmea_sentence[++i] == checksum[0] &&
33         nmea_sentence[++i] == checksum[1]) {
34         return 0;
35     } else {
36         return 1;
37     }
38 }
39
40 double
41 convert_from_degree_minutes_to_pure_degrees(double angle) {
42
43     double degrees;
44     double minutes_fraction;
45
46     minutes_fraction = modf(angle/100, &degrees);
47     angle = degrees + (minutes_fraction*100)/60;
48
49     return angle;
50 }
51
52 void
53 nmea_parse_GPRMC(char nmea_sentence[]) {
54
55     uint32_t index;
56     float time;
57     uint32_t date;
58     double latitude;
59     double longitude;
60
61     index = 0;
62
63     // Skip $GPRMC
64     while (nmea_sentence[index++] != ',');
65
66     // Get time
67     time = atoi(&nmea_sentence[index]);
68
69     // Skip time (already parsed)
70     while (nmea_sentence[index++] != ',');
71
72     // Skip A
73     while (nmea_sentence[index++] != ',');
74
75     // Get latitude [ddmm.mmmmm]
76     latitude = atof(&nmea_sentence[index]);
77     //printf("current_location n: %s\n", nmea_sentence[index]);
78     //printf("current_location n: %d\n", (int) latitude);
79
80     // Convert to pure degrees [dd.ddd] format
81     latitude = convert_from_degree_minutes_to_pure_degrees(latitude);
82
83     // Skip the latitude (already parsed)
84     while (nmea_sentence[index++] != ',');
85
86     // Correct latitude for N/S
87     if (nmea_sentence[index] == 'S') {
88         latitude = -latitude;
89     }
90 }

```

```

91 // Skip "N/S" (already parsed)
92 while (nmea_sentence[index++] != ',');
93
94 // Get longitude [ddmm.nnnmm]
95 longitude = atof(&nmea_sentence[index]);
96
97 // Convert to pure degrees [dd.ddd] format
98 longitude = convert_from_degree_minutes_to_pure_degrees(longitude);
99
100 // Skip the longitude (already parsed)
101 while (nmea_sentence[index++] != ',');
102
103 // Correct latitude for E/W
104 if (nmea_sentence[index] == 'W') {
105     longitude = -longitude;
106 }
107
108 // Skip "E/W" (already parsed)
109 while (nmea_sentence[index++] != ',');
110
111 // Skip speed
112 while (nmea_sentence[index++] != ',');
113
114 // Skip course
115 while (nmea_sentence[index++] != ',');
116
117 // Get date
118 date = atoi(&nmea_sentence[index]);
119
120 // Skip date (already parsed)
121 while (nmea_sentence[index++] != ',');
122
123 // Update the global variables
124 g_current_location.latitude = latitude;
125 g_current_location.longitude = longitude;
126 g_current_time = (uint32_t) time;
127 g_current_date = date;
128 }
129
130 void
131 nmea_parse_PASHR(char nmea_sentence[]) {
132     //printf("current_location: %.6f,%.6f", latitude, longitude);
133
134     float tilt;
135     float roll;
136
137     uint32_t index = 0;
138
139     // Skip $PASHR
140     while (nmea_sentence[index++] != ',');
141
142     // Skip time
143     while (nmea_sentence[index++] != ',');
144
145     // Get heading
146     g_current_heading = atof(&nmea_sentence[index]);
147     while (nmea_sentence[index++] != ',');
148
149     // Skip M
150     while (nmea_sentence[index++] != ',');
151
152     // Get tilt
153     tilt = atof(&nmea_sentence[index]);
154     while (nmea_sentence[index++] != ',');
155
156     // Get roll
157     tilt = atof(&nmea_sentence[index]);
158     while (nmea_sentence[index++] != ',');
159
160     // Skip empty
161     while (nmea_sentence[index++] != ',');
162     while (nmea_sentence[index++] != ',');
163     while (nmea_sentence[index++] != ',');

```

```

164     while (nmea_sentence[index++] != ',');
165     while (nmea_sentence[index++] != ',');
166
167     // Get magnetometer accuracy
168     g_magnetometer_accuracy = atoi(&nmea_sentence[index]);
169 }

```

---

code/asv\_ap\_mcu/src/asv\_ap/nmea.h

---

```

1 #ifndef NMEA_H
2 #define NMEA_H
3
4 #include <stdint.h>
5
6 uint8_t nmea_checksum(char nmea_sentence[]);
7 void nmea_parse_GPRMC(char nmea_sentence[]);
8 void nmea_parse_PASHR(char nmea_sentence[]);
9
10 #endif

```

---

code/asv\_ap\_mcu/src/asv\_ap/uart.c

---

```

1 #include "uart.h"
2
3 #include <stdint.h>
4 #include <stdio.h>
5 #include <stdbool.h>
6 #include <string.h>
7
8 #include "main.h"
9 #include "inc/hw_ints.h"
10 #include "inc/hw_memmap.h"
11 #include "driverlib/pin_map.h"
12 #include "driverlib/rom.h"
13 #include "driverlib/uart.h"
14 #include "driverlib/sysctl.h"
15 #include "driverlib/gpio.h"
16
17 /*
18  * GNSS Tx: PD4 U2Rx 9600
19  * GNSS Rx: PD5 U2Tx
20  * AHRs Tx: PK0 U4Rx 115200
21  * AHRs Rx: PK1 U4Tx
22  * DBG Tx: PC6 U5Rx 115200
23  * DBG Rx: PC7 U5Tx
24  * MC Tx: PC4 U7Rx 9600
25  * MC Rx: PC5 U7Tx
26  *
27  */
28
29 volatile char uart_buffer[4][UART_BUFFER_SIZE];
30 volatile uint8_t cmd_counter[4] = {0, 0, 0, 0};
31 volatile uint8_t write_index[4] = {0, 0, 0, 0};
32 volatile uint8_t read_index[4] = {0, 0, 0, 0};
33 volatile uint8_t byte_counter[4] = {0, 0, 0, 0};
34
35 void
36 UART_init(void) {
37
38     // Enable UART2, UART4, UART5, UART7 and GPIO peripherals
39     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_UART2);
40     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_UART4);
41     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_UART5);
42     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_UART7);
43     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOC);
44     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOK);
45     ROM_SysCtlPeripheralEnable(SYSCTL_PERIPH_GPIOD);
46
47     // Set pin D4 and D5 as UART2
48     ROM_GPIOPinConfigure(GPIO_PD4_U2RX);
49     ROM_GPIOPinConfigure(GPIO_PD5_U2TX);

```

```

50 ROM_GPIOPinTypeUART(GPIO_PORTD_BASE, GPIO_PIN_4 | GPIO_PIN_5);
51
52 // Set pin K0 and K1 as UART4
53 ROM_GPIOPinConfigure(GPIO_PK0_U4RX);
54 ROM_GPIOPinConfigure(GPIO_PK1_U4TX);
55 ROM_GPIOPinTypeUART(GPIO_PORTK_BASE, GPIO_PIN_0 | GPIO_PIN_1);
56
57 // Set pin C6 and C7 as UART5
58 ROM_GPIOPinConfigure(GPIO_PC6_U5RX);
59 ROM_GPIOPinConfigure(GPIO_PC7_U5TX);
60 ROM_GPIOPinTypeUART(GPIO_PORTC_BASE, GPIO_PIN_6 | GPIO_PIN_7);
61
62 // Set pin C4 and C5 as UART7
63 ROM_GPIOPinConfigure(GPIO_PC4_U7RX);
64 ROM_GPIOPinConfigure(GPIO_PC5_U7TX);
65 ROM_GPIOPinTypeUART(GPIO_PORTC_BASE, GPIO_PIN_4 | GPIO_PIN_5);
66
67 // Configure UART2 as 9600 baud 8N1
68 ROM_UARTConfigSetExpClk(UART2_BASE, g_system_clock, 9600,
69                          (UART_CONFIG_WLEN_8 | UART_CONFIG_STOP_ONE |
70                           UART_CONFIG_PAR_NONE));
71
72 // Configure UART4 as 115200 baud 8N1
73 ROM_UARTConfigSetExpClk(UART4_BASE, g_system_clock, 115200,
74                          (UART_CONFIG_WLEN_8 | UART_CONFIG_STOP_ONE |
75                           UART_CONFIG_PAR_NONE));
76
77 // Configure UART5 as 115200 baud 8N1
78 ROM_UARTConfigSetExpClk(UART5_BASE, g_system_clock, 115200,
79                          (UART_CONFIG_WLEN_8 | UART_CONFIG_STOP_ONE |
80                           UART_CONFIG_PAR_NONE));
81
82 // Configure UART7 as 9600 baud 8N1
83 ROM_UARTConfigSetExpClk(UART7_BASE, g_system_clock, 9600,
84                          (UART_CONFIG_WLEN_8 | UART_CONFIG_STOP_ONE |
85                           UART_CONFIG_PAR_NONE));
86
87
88 // Enable UART interrupts
89 ROM_IntEnable(INT_UART2);
90 ROM_IntEnable(INT_UART4);
91 ROM_IntEnable(INT_UART5);
92 ROM_IntEnable(INT_UART7);
93 ROM_UARTIntEnable(UART2_BASE, UART_INT_RX | UART_INT_RT);
94 ROM_UARTIntEnable(UART4_BASE, UART_INT_RX | UART_INT_RT);
95 ROM_UARTIntEnable(UART5_BASE, UART_INT_RX | UART_INT_RT);
96 ROM_UARTIntEnable(UART7_BASE, UART_INT_RX | UART_INT_RT);
97 }
98
99 void
100 UART_send_nn(uint32_t module, char *string) {
101     // Loop until string is over
102     while (*string != '\0') {
103         // Write single character
104         ROM_UARTCharPut(module, *string++);
105     }
106 }
107
108 void
109 UART_send(uint32_t module, char *string) {
110     UART_send_nn(module, string);
111     ROM_UARTCharPut(module, '\r');
112     ROM_UARTCharPut(module, '\n');
113 }
114
115 char get_uart_char(uint8_t device) {
116     char data;
117 }

```



```

123  /* Wait for data from device */
124  while (byte_counter[device] == 0);
125
126  /* Disable interrupt because we want to access the uart buffer */
127  ROM_IntMasterDisable();
128
129  /* Copy a byte from the uart buffer to a local variable */
130  data = uart_buffer[device][read_index[device]++];
131
132  /* Reset the read counter if we have reached the end of the buffer,
133  * the message continues from the start of the memory */
134  if (read_index[device] == UART_BUFFER_SIZE) {
135      read_index[device] = 0;
136  }
137
138  /* We have finished getting one byte, so there is one less to do */
139  byte_counter[device]--;
140
141  /* We can enable interrupt again */
142  ROM_IntMasterEnable();
143
144  /* And return the data */
145  return data;
146 }
147
148 void get_uart_str(uint8_t device, char *message_buffer) {
149     uint8_t i = 0;
150
151     for (i = 0; ( (message_buffer[i] = get_uart_char(device)) != '\n'); i++);
152
153     /* Null terminate string */
154     message_buffer[i] = 0;
155
156     /* Decrement command counter (without being interrupted) */
157     ROM_IntMasterDisable();
158     cmd_counter[device]--;
159     ROM_IntMasterEnable();
160 }
161
162 void
163 uart_rx_interrupt_handler(uint8_t device, uint32_t module) {
164
165     uint32_t ui32Status;
166
167     // Get the interrupt status.
168     ui32Status = ROM_UARTIntStatus(module, true);
169
170     // Clear the asserted interrupts.
171     ROM_UARTIntClear(module, ui32Status);
172
173     // Loop while there are characters in the receive FIFO.
174     while (ROM_UARTCharsAvail(module)) {
175
176         char data = ROM_UARTCharGet(module);
177
178         if (data != '\r') {
179
180             uart_buffer[device][write_index[device]++] = data;
181
182             if (data == '\n') {
183                 cmd_counter[device]++;
184             }
185
186             if (write_index[device] == UART_BUFFER_SIZE) {
187                 write_index[device] = 0;
188             }
189
190             byte_counter[device]++;
191
192             /* Error handling */
193             if (byte_counter[device] == UART_BUFFER_SIZE) {
194                 byte_counter[device] = 0;
195                 cmd_counter[device] = 0;

```

```

196         }
197     }
198 }
199 }
200
201 void
202 UART5_int_handler(void) {
203     uint32_t status;
204
205     // Get the interrupt status.
206     status = ROM_UARTIntStatus(UART5_BASE, true);
207
208     // Clear the asserted interrupts.
209     ROM_UARTIntClear(UART5_BASE, status);
210
211     // Loop while there are characters in the receive FIFO.
212     while (ROM_UARTCharsAvail(UART5_BASE)) {
213         ROM_UARTCharPut(UART5_BASE, ROM_UARTCharGet(UART5_BASE));
214     }
215 }
216 }
217
218 void
219 UART2_int_handler(void) {
220     uart_rx_interrupt_handler(GNSS_DEVICE, GNSS_MODULE);
221 }
222
223 void
224 UART4_int_handler(void) {
225     uart_rx_interrupt_handler(IMU_DEVICE, IMU_MODULE);
226 }
227
228 void
229 UART7_int_handler(void) {
230     uart_rx_interrupt_handler(MC_DEVICE, MC_MODULE);
231 }

```

---

code/asv\_ap\_mcu/src/asv\_ap/uart.h

---

```

1 #include <stdint.h>
2 #include "inc/hw_memmap.h"
3
4 #define UART_BUFFER_SIZE 255
5
6 #define LOG_MODULE UART5_BASE
7 #define MC_MODULE UART7_BASE
8 #define IMU_MODULE UART4_BASE
9 #define GNSS_MODULE UART2_BASE
10 #define WLAN_MODULE UART5_BASE
11
12 // #define LOG_DEVICE 0x00
13 #define MC_DEVICE 0x00
14 #define IMU_DEVICE 0x01
15 #define GNSS_DEVICE 0x02
16 #define WLAN_DEVICE 0x03
17
18 void UART_init(void);
19 void UART_send(uint32_t module, char *string);
20 void UART_send_nn(uint32_t module, char *string);
21 void WLAN_send(char *string);
22 void get_uart_str(uint8_t device, char *message_buffer);
23 void UART2_int_handler(void);
24 void UART4_int_handler(void);
25 void UART5_int_handler(void);
26 void UART7_int_handler(void);
27
28 extern volatile uint8_t cmd_counter[];

```

---

code/asv\_ap\_mcu/src/tasks/led\_task.c

---

```
1 /*
```

```

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3 *
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8 * http://www.apache.org/licenses/LICENSE-2.0
9 *
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11 * under the License is distributed on an "AS IS" BASIS, WITHOUT WARRANTIES OR
12 * CONDITIONS OF ANY KIND, either express or implied. See the License for the
13 * specific language governing permissions and limitations under the License.
14 *
15 * Author:          Joakim Myrland
16 * website:         www.LDA.as
17 * email:           joakim.myrland@LDA.as
18 * project:         https://github.com/Lindem-Data-Acquisition-AS/iot_tiva_template/
19 *
20 */
21
22 #include <stdint.h>
23 #include <stdbool.h>
24 #include "inc/hw_memmap.h"
25 #include "inc/hw_types.h"
26 #include "driverlib/gpio.h"
27 #include "driverlib/rom.h"
28 #include "config/lwiplib.h"
29 #include "led_task.h"
30 #include "priorities.h"
31 #include "FreeRTOS.h"
32 #include "task.h"
33 #include "queue.h"
34 #include "semphr.h"
35
36 // The stack size for the LED toggle task.
37 #define STACKSIZE_LEDTASK      128
38
39 // The amount of time to delay between toggles of the LED.
40 #define LED_DELAY_ON           750
41 #define LED_DELAY_OFF          250
42
43 // This task simply toggles the user LED at a 1 Hz rate.
44 static void
45 LEDTask(void *pvParameters) {
46
47     portTickType ui32LastTime;
48     uint32_t ui32Temp;
49
50     // Get the current tick count.
51     ui32LastTime = xTaskGetTickCount();
52
53     // Loop forever.
54     while (1) {
55
56         ui32Temp = lwIPLocalIPAddrGet();
57
58         /* No IP acquired */
59         if (ui32Temp == IPADDR_NONE || ui32Temp == IPADDR_ANY) {
60
61             // Turn off the user LED.
62             #ifdef DEVKIT
63                 ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, GPIO_PIN_5);
64             #else
65                 ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, GPIO_PIN_1);
66             #endif
67
68             // Wait for the required amount of time.
69             vTaskDelayUntil(&ui32LastTime, LED_DELAY_OFF / portTICK_RATE_MS);
70
71             // Turn on the user LED.
72             #ifdef DEVKIT
73                 ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, 0);
74             #else

```

```

75     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, 0);
76     #endif
77
78     // Wait for the required amount of time.
79     vTaskDelayUntil(&ui32LastTime, LED_DELAY_OFF / portTICK_RATE_MS);
80
81     /* Auto IP acquired */
82     } else if ( (ui32Temp & 0xFFFF) == 0xFE9 ) {
83
84         // Turn off the user LED.
85         #ifdef DEVKIT
86             ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, GPIO_PIN_5);
87         #else
88             ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, GPIO_PIN_1);
89         #endif
90
91         // Wait for the required amount of time.
92         vTaskDelayUntil(&ui32LastTime, LED_DELAY_ON / portTICK_RATE_MS);
93
94         // Turn on the user LED.
95         #ifdef DEVKIT
96             ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, 0);
97         #else
98             ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, 0);
99         #endif
100
101         // Wait for the required amount of time.
102         vTaskDelayUntil(&ui32LastTime, LED_DELAY_OFF / portTICK_RATE_MS);
103
104     /* DHCP IP acquired */
105     } else if ( (ui32Temp & 0xFFFF) == 0xA8C0 ) {
106
107         // Turn off the user LED.
108         #ifdef DEVKIT
109             ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, GPIO_PIN_5);
110         #else
111             ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, GPIO_PIN_1);
112         #endif
113
114         // Wait for the required amount of time.
115         vTaskDelayUntil(&ui32LastTime, LED_DELAY_OFF / portTICK_RATE_MS);
116
117         // Turn on the user LED.
118         #ifdef DEVKIT
119             ROM_GPIOPinWrite(GPIO_PORTN_BASE, GPIO_PIN_5, 0);
120         #else
121             ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_1, 0);
122         #endif
123
124         // Wait for the required amount of time.
125         vTaskDelayUntil(&ui32LastTime, LED_DELAY_ON / portTICK_RATE_MS);
126
127     } else {
128         vTaskDelayUntil(&ui32LastTime, 2000 / portTICK_RATE_MS);
129     }
130 }
131 }
132 }
133 }
134
135 // Initializes the LED task.
136 uint32_t
137 LEDTaskInit(void) {
138
139     // Create the LED task.
140     if (xTaskCreate(LEDTask, (const portCHAR * const)"LED", STACKSIZE_LEDTASK, NULL,
141         tskIDLE_PRIORITY + PRIORITY_LED_TASK, NULL) != pdTRUE) {
142         return(1);
143     }
144
145     // Success.
146     return(0);
147 }

```

---

## code/asv\_ap\_mcu/src/tasks/led\_task.h

---

```

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3  *
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13 * specific language governing permissions and limitations under the License.
14 *
15 * Author:          Joakim Myrland
16 * website:         www.LDA.as
17 * email:           joakim.myrland@LDA.as
18 * project:         https://github.com/Lindem-Data-Acquisition-AS/iot_tiva_template/
19 *
20 */
21
22 #ifndef LED_TASK_H
23 #define LED_TASK_H
24
25 //*****
26 //
27 // Prototypes for the LED task.
28 //
29 //*****
30 extern uint32_t g_ui32LEDDelay;
31 extern uint32_t LEDTaskInit(void);
32
33 #endif // LED_TASK_H

```

---

## code/asv\_ap\_mcu/src/tasks/lwip\_task.c

---

```

1 //*****
2 //
3 // lwip_task.c - Tasks to serve web pages over Ethernet using lwIP.
4 //
5 // Copyright (c) 2009-2014 Texas Instruments Incorporated. All rights reserved.
6 // Software License Agreement
7 //
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18 // CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL
19 // DAMAGES, FOR ANY REASON WHATSOEVER.
20 //
21 // This is part of revision 2.1.0.12573 of the DK-TM4C129X Firmware Package.
22 //
23 //*****
24
25 #include <stdint.h>
26 #include <stdbool.h>
27 #include "inc/hw_ints.h"
28 #include "inc/hw_types.h"
29 #include "driverlib/rom.h"
30 #include "lwiplib.h"
31 #include "httpserver_raw/httpd.h"
32 #include "httpserver_raw/fsdata.h"
33 #include "led_task.h"
34 #include "lwip_task.h"
35

```

---

```

36 extern uint32_t g_system_clock;
37
38 //*****
39 //
40 // Sets up the additional lwIP raw API services provided by the application.
41 //
42 //*****
43 void
44 SetupServices(void *pvArg)
45 {
46     //
47     // Initialize the sample httpd server.
48     //
49     httpd_init();
50
51     /* Initialize the tcp config server */
52     config_server_init();
53
54     //http_server_netconn_init();
55
56 }
57
58 //*****
59 //
60 // Initializes the lwIP tasks.
61 //
62 //*****
63 uint32_t
64 lwIPTaskInit(void)
65 {
66     uint8_t pui8MAC[6];
67     uint8_t hex_array[16] = {
68         '0', '1', '2', '3', '4', '5', '6', '7', '8', '9', 'A', 'B', 'C', 'D', 'E', 'F'
69     };
70
71     uint32_t ui32User0, ui32User1;
72     ROM_FlashUserGet(&ui32User0, &ui32User1);
73
74     if((ui32User0 == 0xffffffff) || (ui32User1 == 0xffffffff)) {
75
76         // MAC address has not been programmed, use default.
77         // ASV = 0x41 0x53 0x56
78         pui8MAC[0] = 0x02;
79         pui8MAC[1] = 0x41;
80         pui8MAC[2] = 0x53;
81         pui8MAC[3] = 0x56;
82         pui8MAC[4] = 0x00;
83         pui8MAC[5] = 0x00;
84
85     } else {
86
87         pui8MAC[0] = ((ui32User0 >> 0) & 0xff);
88         pui8MAC[1] = ((ui32User0 >> 8) & 0xff);
89         pui8MAC[2] = ((ui32User0 >> 16) & 0xff);
90         pui8MAC[3] = ((ui32User1 >> 0) & 0xff);
91         pui8MAC[4] = ((ui32User1 >> 8) & 0xff);
92         pui8MAC[5] = ((ui32User1 >> 16) & 0xff);
93
94     }
95
96     //
97     // Lower the priority of the Ethernet interrupt handler. This is required
98     // so that the interrupt handler can safely call the interrupt-safe
99     // FreeRTOS functions (specifically to send messages to the queue).
100    //
101    ROM_IntPrioritySet(INT_EMAC0, 0xC0);
102
103    //
104    // Initialize lwIP.
105    //
106    //lwIPInit(g_system_clock, pui8MAC, aton("192.168.1.21"), aton("255.255.255.0"),
107            aton("192.168.1.1"), IPADDR_USE_STATIC);

```

```

107  lwIPInit(g_system_clock, pui8MAC, 0xC0A80115, 0xFFFFFFFF00, 0xC0A80101,
108  IPADDR_USE_STATIC);
109  //lwIPInit(g_system_clock, pui8MAC, 0xC0A80015, 0xFFFFFFFF00, 0xC0A80001,
110  IPADDR_USE_STATIC);
111  //
112  // Setup the remaining services inside the TCP/IP thread's context.
113  //
114  tcpip_callback(SetupServices, 0);
115  //
116  // Success.
117  //
118  //
119  return(0);
120 }

```

---

code/asv\_ap\_mcu/src/tasks/lwip\_task.h

---

```

1  //*****
2  //
3  // lwip_task.h - Prototypes for the lwIP tasks.
4  //
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18 // CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL
19 // DAMAGES, FOR ANY REASON WHATSOEVER.
20 //
21 // This is part of revision 2.1.0.12573 of the DK-TM4C129X Firmware Package.
22 //
23 //*****
24
25 #ifndef __LWIP_TASK_H__
26 #define __LWIP_TASK_H__
27
28 //*****
29 //
30 // Prototypes.
31 //
32 //*****
33 extern uint32_t lwIPTaskInit(void);
34
35 #endif // __LWIP_TASK_H__

```

---

code/asv\_ap\_mcu/src/tasks/lwiplib.c

---

```

1  //*****
2  //
3  //
4  //
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13 //

```

```

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16 // NOT LIMITED TO, IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR
17 // A PARTICULAR PURPOSE APPLY TO THIS SOFTWARE. TI SHALL NOT, UNDER ANY
18 // CIRCUMSTANCES, BE LIABLE FOR SPECIAL, INCIDENTAL, OR CONSEQUENTIAL
19 // DAMAGES, FOR ANY REASON WHATSOEVER.
20 //
21 // This is part of revision 2.1.0.12573 of the DK-TM4C129X Firmware Package.
22 //
23 //*****
24
25 #include <stdint.h>
26 #include <stdbool.h>
27
28 #include "inc/hw_ints.h"
29 #include "inc/hw_memmap.h"
30 #include "inc/hw_nvic.h"
31 #include "inc/hw_emac.h"
32 #include "driverlib/debug.h"
33 #include "driverlib/emac.h"
34 #include "driverlib/rom.h"
35 #include "driverlib/rom_map.h"
36 #include "driverlib/sysctl.h"
37
38 #if RTOS_FREERTOS
39 #include "FreeRTOS.h"
40 #include "task.h"
41 #include "queue.h"
42 #include "semphr.h"
43 #endif
44
45 #include "lwiplib.h"
46 #include "lwip/tcpip.h"
47 #include "netif/tivaif.h"
48
49
50 //*****
51 //
52 // Ensure that ICMP checksum offloading is enabled; otherwise the TM4C129
53 // driver will not operate correctly.
54 //
55 //*****
56 #ifndef LWIP_OFFLOAD_ICMP_CHKSUM
57 #define LWIP_OFFLOAD_ICMP_CHKSUM 1
58 #endif
59
60
61 //*****
62 //
63 // The lwIP Library abstraction layer provides for a host callback function to
64 // be called periodically in the lwIP context. This is the timer interval, in
65 // ms, for this periodic callback. If the timer interval is defined to 0 (the
66 // default value), then no periodic host callback is performed.
67 //
68 //*****
69 #ifndef HOST_TMR_INTERVAL
70 #define HOST_TMR_INTERVAL 0
71 #else
72 extern void lwIPHostTimerHandler(void);
73 #endif
74
75 //*****
76 //
77 // The link detect polling interval.
78 //
79 //*****
80 #define LINK_TMR_INTERVAL 10
81
82 //*****
83 //
84 // Set the PHY configuration to the default (internal) option if necessary.
85 //
86 //*****

```



```

87 #ifndef EMAC_PHY_CONFIG
88 #define EMAC_PHY_CONFIG          (EMAC_PHY_TYPE_INTERNAL |           \
89                                 EMAC_PHY_INT_MDIX_EN |              \
90                                 EMAC_PHY_AN_100B_T_FULL_DUPLEX)
91 #endif
92
93
94 // The lwIP network interface structure for the Tiva Ethernet MAC.
95 static struct netif g_sNetIF;
96 // The application's interrupt handler for hardware timer events from the MAC.
97 tHardwareTimerHandler g_pfnTimerHandler;
98 // The default IP address acquisition mode.
99 static uint32_t g_ui32IPMode = IPADDR_USE_STATIC;
100
101 // The most recently detected link state.
102 #if LWIP_AUTOIP || LWIP_DHCP
103 static bool g_bLinkActive = false;
104 #endif
105
106 // The IP address to be used. This is used during the initialization of the
107 // stack and when the interface configuration is changed.
108 static uint32_t g_ui32IPAddr;
109 // The netmask to be used. This is used during the initialization of the stack
110 // and when the interface configuration is changed.
111 static uint32_t g_ui32NetMask;
112 // The gateway address to be used. This is used during the initialization of
113 // the stack and when the interface configuration is changed.
114 static uint32_t g_ui32GWAddr;
115
116 // The stack size for the interrupt task.
117 #if !NO_SYS
118 #define STACKSIZE_LWIPINTTASK    128
119
120 // The handle for the "queue" (semaphore) used to signal the interrupt task
121 // from the interrupt handler.
122 static xQueueHandle g_pInterrupt;
123
124 // This task handles reading packets from the Ethernet controller and supplying
125 // them to the TCP/IP thread.
126 static void
127 lwIPInterruptTask(void *pvArg)
128 {
129     //
130     // Loop forever.
131     //
132     while(1)
133     {
134         //
135         // Wait until the semaphore has been signaled.
136         //
137         while(xQueueReceive(g_pInterrupt, &pvArg, portMAX_DELAY) != pdPASS)
138         {
139         }
140
141         //
142         // Processes any packets waiting to be sent or received.
143         //
144         tivaif_interrupt(&g_sNetIF, (uint32_t)pvArg);
145
146         //
147         // Re-enable the Ethernet interrupts.
148         //
149         MAP_EMACIntEnable(EMAC0_BASE, (EMAC_INT_RECEIVE | EMAC_INT_TRANSMIT |
150                                         EMAC_INT_TX_STOPPED |
151                                         EMAC_INT_RX_NO_BUFFER |
152                                         EMAC_INT_RX_STOPPED | EMAC_INT_PHY));
153     }
154 }
155 #endif
156
157 // This function performs a periodic check of the link status and responds
158 // appropriately if it has changed.
159 #if LWIP_AUTOIP || LWIP_DHCP

```

```

160 static void
161 lwIPLinkDetect(void)
162 {
163     bool bHaveLink;
164     struct ip_addr ip_addr;
165     struct ip_addr net_mask;
166     struct ip_addr gw_addr;
167
168     //
169     // See if there is an active link.
170     //
171     bHaveLink = MAP_EMACPHYRead(EMACO_BASE, 0, EPHY_BMSR) & EPHY_BMSR_LINKSTAT;
172
173     //
174     // Return without doing anything else if the link state hasn't changed.
175     //
176     if(bHaveLink == g_bLinkActive)
177     {
178         return;
179     }
180
181     //
182     // Save the new link state.
183     //
184     g_bLinkActive = bHaveLink;
185
186     //
187     // Clear any address information from the network interface.
188     //
189     ip_addr.addr = 0;
190     net_mask.addr = 0;
191     gw_addr.addr = 0;
192     netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
193
194     //
195     // See if there is a link now.
196     //
197     if(bHaveLink)
198     {
199         //
200         // Start DHCP, if enabled.
201         //
202         #if LWIP_DHCP
203             if(g_ui32IPMode == IPADDR_USE_DHCP)
204             {
205                 dhcp_start(&g_sNetIF);
206             }
207         #endif
208
209         //
210         // Start AutoIP, if enabled and DHCP is not.
211         //
212         #if LWIP_AUTOIP
213             if(g_ui32IPMode == IPADDR_USE_AUTOIP)
214             {
215                 autoip_start(&g_sNetIF);
216             }
217         #endif
218     }
219     else
220     {
221         //
222         // Stop DHCP, if enabled.
223         //
224         #if LWIP_DHCP
225             if(g_ui32IPMode == IPADDR_USE_DHCP)
226             {
227                 dhcp_stop(&g_sNetIF);
228             }
229         #endif
230
231         //
232         // Stop AutoIP, if enabled and DHCP is not.

```

```

233 //
234 #if LWIP_AUTOIP
235     if(g_ui32IPMode == IPADDR_USE_AUTOIP)
236     {
237         autoip_stop(&g_sNetIF);
238     }
239 #endif
240 }
241 }
242 #endif
243
244 // Handles the timeout for the host callback function timer when using a RTOS.
245 #if !NO_SYS && HOST_TMR_INTERVAL
246 static void
247 lwIPPrivateHostTimer(void *pvArg)
248 {
249     //
250     // Call the application-supplied host timer callback function.
251     //
252     lwIPHostTimerHandler();
253
254     //
255     // Re-schedule the host timer callback function timeout.
256     //
257     sys_timeout(HOST_TMR_INTERVAL, lwIPPrivateHostTimer, NULL);
258 }
259 #endif
260
261 // Handles the timeout for the link detect timer when using a RTOS.
262 #if !NO_SYS && (LWIP_AUTOIP || LWIP_DHCP)
263 static void
264 lwIPPrivateLinkTimer(void *pvArg)
265 {
266     //
267     // Perform the link detection.
268     //
269     lwIPLinkDetect();
270
271     //
272     // Re-schedule the link detect timer timeout.
273     //
274     sys_timeout(LINK_TMR_INTERVAL, lwIPPrivateLinkTimer, NULL);
275 }
276 #endif
277
278 // Completes the initialization of lwIP. This is directly called when not
279 // using a RTOS and provided as a callback to the TCP/IP thread when using a
280 // RTOS.
281 static void
282 lwIPPrivateInit(void *pvArg)
283 {
284     struct ip_addr ip_addr;
285     struct ip_addr net_mask;
286     struct ip_addr gw_addr;
287
288 #if !NO_SYS
289 #if RTOS_FREERTOS
290     // If using a RTOS, create a queue (to be used as a semaphore) to signal
291     // the Ethernet interrupt task from the Ethernet interrupt handler.
292     g_pInterrupt = xQueueCreate(1, sizeof(void *));
293     // If using a RTOS, create the Ethernet interrupt task.
294     xTaskCreate(lwIPInterruptTask, (const portCHAR * const)"eth_isr",
295               STACKSIZE_LWIPINTTASK, 0, tskIDLE_PRIORITY + 1,
296               0);
297 #endif
298 #endif
299
300     //
301     // Setup the network address values.
302     //
303     if(g_ui32IPMode == IPADDR_USE_STATIC)
304     {
305         ip_addr.addr = htonl(g_ui32IPAddr);

```

```

306     net_mask.addr = htonl(g_ui32NetMask);
307     gw_addr.addr = htonl(g_ui32GWAddr);
308 }
309 else
310 {
311     ip_addr.addr = 0;
312     net_mask.addr = 0;
313     gw_addr.addr = 0;
314 }
315
316 //
317 // Create, configure and add the Ethernet controller interface with
318 // default settings. ip_input should be used to send packets directly to
319 // the stack when not using a RTOS and tcpip_input should be used to send
320 // packets to the TCP/IP thread's queue when using a RTOS.
321 //
322 netif_add(&g_sNetIF, &ip_addr, &net_mask, &gw_addr, NULL, tivaif_init,
323          tcpip_input);
324 netif_set_default(&g_sNetIF);
325
326 //
327 // Bring the interface up.
328 //
329 netif_set_up(&g_sNetIF);
330
331 //
332 // Setup a timeout for the host timer callback function if using a RTOS.
333 //
334 #if !NO_SYS && HOST_TMR_INTERVAL
335     sys_timeout(HOST_TMR_INTERVAL, lwIPPrivateHostTimer, NULL);
336 #endif
337
338 //
339 // Setup a timeout for the link detect callback function if using a RTOS.
340 //
341 #if !NO_SYS && (LWIP_AUTOIP || LWIP_DHCP)
342     sys_timeout(LINK_TMR_INTERVAL, lwIPPrivateLinkTimer, NULL);
343 #endif
344 }
345
346
347 /*****
348 /*  IPIsNetIfUp
349 /*
350 /*  Check if the interface and link is Up.
351 /*
352 /*  In      : none
353 /*  Out     : none
354 /*  Return: 0 / 1
355 /*****
356 int IPIsNetIfUp (void)
357 {
358     int NetIfUp = 0;
359
360     if ((netif_is_up(&g_sNetIF)) && (netif_is_link_up(&g_sNetIF)))
361     {
362         NetIfUp = 1;
363     }
364
365     return(NetIfUp);
366 } /* IPIsNetIfUp */
367
368 /*****
369 //
370 //!  Initializes the lwIP TCP/IP stack.
371 //!
372 //!  \param ui32SysClkHz is the current system clock rate in Hz.
373 //!  \param pui8MAC is a pointer to a six byte array containing the MAC
374 //!  address to be used for the interface.
375 //!  \param ui32IPAddr is the IP address to be used (static).
376 //!  \param ui32NetMask is the network mask to be used (static).
377 //!  \param ui32GWAddr is the Gateway address to be used (static).
378 //!  \param ui32IPMode is the IP Address Mode.  \b IPADDR_USE_STATIC will force

```

```

379 /// static IP addressing to be used, \b IPADDR_USE_DHCP will force DHCP with
380 /// fallback to Link Local (Auto IP), while \b IPADDR_USE_AUTOIP will force
381 /// Link Local only.
382 ///
383 /// This function performs initialization of the lwIP TCP/IP stack for the
384 /// Ethernet MAC, including DHCP and/or AutoIP, as configured.
385 ///
386 /// \return None.
387 ///
388 *****
389 void
390 lwIPInit(uint32_t ui32SysClkHz, const uint8_t *pui8MAC, uint32_t ui32IPAddr,
391          uint32_t ui32NetMask, uint32_t ui32GWAddr, uint32_t ui32IPMode)
392 {
393     ///
394     /// Check the parameters.
395     ///
396     #if LWIP_DHCP && LWIP_AUTOIP
397         ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||
398              (ui32IPMode == IPADDR_USE_DHCP) ||
399              (ui32IPMode == IPADDR_USE_AUTOIP));
400     #elif LWIP_DHCP
401         ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||
402              (ui32IPMode == IPADDR_USE_DHCP));
403     #elif LWIP_AUTOIP
404         ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||
405              (ui32IPMode == IPADDR_USE_AUTOIP));
406     #else
407         ASSERT(ui32IPMode == IPADDR_USE_STATIC);
408     #endif
409
410     ///
411     /// Enable the ethernet peripheral.
412     ///
413     MAP_SysCtlPeripheralEnable(SYSCTL_PERIPH_EMAC0);
414     MAP_SysCtlPeripheralReset(SYSCTL_PERIPH_EMAC0);
415
416     ///
417     /// Enable the internal PHY if it's present and we're being
418     /// asked to use it.
419     ///
420     if((EMAC_PHY_CONFIG & EMAC_PHY_TYPE_MASK) == EMAC_PHY_TYPE_INTERNAL)
421     {
422         ///
423         /// We've been asked to configure for use with the internal
424         /// PHY. Is it present?
425         ///
426         if(MAP_SysCtlPeripheralPresent(SYSCTL_PERIPH_EPHY0))
427         {
428             ///
429             /// Yes - enable and reset it.
430             ///
431             MAP_SysCtlPeripheralEnable(SYSCTL_PERIPH_EPHY0);
432             MAP_SysCtlPeripheralReset(SYSCTL_PERIPH_EPHY0);
433         }
434         else
435         {
436             ///
437             /// Internal PHY is not present on this part so hang here.
438             ///
439             while(1)
440             {
441             }
442         }
443     }
444
445     ///
446     /// Wait for the MAC to come out of reset.
447     ///
448     while(!MAP_SysCtlPeripheralReady(SYSCTL_PERIPH_EMAC0))
449     {
450     }
451

```

```

452 //
453 // Configure for use with whichever PHY the user requires.
454 //
455 MAP_EMACPHYConfigSet(EMAC0_BASE, EMAC_PHY_CONFIG);
456
457 //
458 // Initialize the MAC and set the DMA mode.
459 //
460 MAP_EMACInit(EMAC0_BASE, ui32SysC1kHz,
461             EMAC_BCONFIG_MIXED_BURST | EMAC_BCONFIG_PRIORITY_FIXED,
462             4, 4, 0);
463
464 //
465 // Set MAC configuration options.
466 //
467 MAP_EMACConfigSet(EMAC0_BASE, (EMAC_CONFIG_FULL_DUPLEX |
468                               EMAC_CONFIG_CHECKSUM_OFFLOAD |
469                               EMAC_CONFIG_7BYTE_PREAMBLE |
470                               EMAC_CONFIG_IF_GAP_96BITS |
471                               EMAC_CONFIG_USE_MACADDR0 |
472                               EMAC_CONFIG_SA_FROM_DESCRIPTOR |
473                               EMAC_CONFIG_BO_LIMIT_1024),
474                 (EMAC_MODE_RX_STORE_FORWARD |
475                 EMAC_MODE_TX_STORE_FORWARD |
476                 EMAC_MODE_TX_THRESHOLD_64_BYTES |
477                 EMAC_MODE_RX_THRESHOLD_64_BYTES), 0);
478
479 //
480 // Program the hardware with its MAC address (for filtering).
481 //
482 MAP_EMACAddrSet(EMAC0_BASE, 0, (uint8_t *)pui8MAC);
483
484 //
485 // Save the network configuration for later use by the private
486 // initialization.
487 //
488 g_ui32IPMode = ui32IPMode;
489 g_ui32IPAddr = ui32IPAddr;
490 g_ui32NetMask = ui32NetMask;
491 g_ui32GWAddr = ui32GWAddr;
492
493 //
494 // Initialize lwIP. The remainder of initialization is done immediately if
495 // not using a RTOS and it is deferred to the TCP/IP thread's context if
496 // using a RTOS.
497 //
498 tcpip_init(lwIPPrivateInit, 0);
499 }
500
501 //*****
502 //
503 //! Registers an interrupt callback function to handle the IEEE-1588 timer.
504 //!
505 //! \param pfnTimerFunc points to a function which is called whenever the
506 //! Ethernet MAC reports an interrupt relating to the IEEE-1588 hardware timer.
507 //!
508 //! This function allows an application to register a handler for all
509 //! interrupts generated by the IEEE-1588 hardware timer in the Ethernet MAC.
510 //! To allow minimal latency timer handling, the callback function provided
511 //! will be called in interrupt context, regardless of whether or not lwIP is
512 //! configured to operate with an RTOS. In an RTOS environment, the callback
513 //! function is responsible for ensuring that all processing it performs is
514 //! compatible with the low level interrupt context it is called in.
515 //!
516 //! The callback function takes two parameters. The first is the base address
517 //! of the MAC reporting the timer interrupt and the second is the timer
518 //! interrupt status as reported by EMACTimestampIntStatus(). Note that
519 //! EMACTimestampIntStatus() causes the timer interrupt sources to be cleared
520 //! so the application should not call EMACTimestampIntStatus() within the
521 //! handler.
522 //!
523 //! \return None.
524 //

```

```

525 //*****
526 void
527 lwIPTimerCallbackRegister(tHardwareTimerHandler pfnTimerFunc)
528 {
529     //
530     // Remember the callback function address passed.
531     //
532     g_pfnTimerHandler = pfnTimerFunc;
533 }
534
535
536 //*****
537 //
538 //! Handles Ethernet interrupts for the lwIP TCP/IP stack.
539 //!
540 //! This function handles Ethernet interrupts for the lwIP TCP/IP stack. At
541 //! the lowest level, all receive packets are placed into a packet queue for
542 //! processing at a higher level. Also, the transmit packet queue is checked
543 //! and packets are drained and transmitted through the Ethernet MAC as needed.
544 //! If the system is configured without an RTOS, additional processing is
545 //! performed at the interrupt level. The packet queues are processed by the
546 //! lwIP TCP/IP code, and lwIP periodic timers are serviced (as needed).
547 //!
548 //! \return None.
549 //
550 //*****
551 void
552 lwIPEthernetIntHandler(void)
553 {
554     uint32_t ui32Status;
555     uint32_t ui32TimerStatus;
556 #if !NO_SYS
557     portBASE_TYPE xWake;
558 #endif
559
560     //
561     // Read and Clear the interrupt.
562     //
563     ui32Status = MAP_EMACIntStatus(EMACO_BASE, true);
564
565     //
566     // If the interrupt really came from the Ethernet and not our
567     // timer, clear it.
568     //
569     if(ui32Status)
570     {
571         MAP_EMACIntClear(EMACO_BASE, ui32Status);
572     }
573
574     //
575     // Check to see whether a hardware timer interrupt has been reported.
576     //
577     if(ui32Status & EMAC_INT_TIMESTAMP)
578     {
579         //
580         // Yes - read and clear the timestamp interrupt status.
581         //
582         ui32TimerStatus = EMACTimestampIntStatus(EMACO_BASE);
583
584         //
585         // If a timer interrupt handler has been registered, call it.
586         //
587         if(g_pfnTimerHandler)
588         {
589             g_pfnTimerHandler(EMACO_BASE, ui32TimerStatus);
590         }
591     }
592
593     //
594     // The handling of the interrupt is different based on the use of a RTOS.
595     // A RTOS is being used. Signal the Ethernet interrupt task.
596     //
597     xQueueSendFromISR(g_pInterrupt, (void *)&ui32Status, &xWake);

```

```

598
599 //
600 // Disable the Ethernet interrupts. Since the interrupts have not been
601 // handled, they are not asserted. Once they are handled by the Ethernet
602 // interrupt task, it will re-enable the interrupts.
603 //
604 MAP_EMACIntDisable(EMAC0_BASE, (EMAC_INT_RECEIVE | EMAC_INT_TRANSMIT |
605                                EMAC_INT_TX_STOPPED |
606                                EMAC_INT_RX_NO_BUFFER |
607                                EMAC_INT_RX_STOPPED | EMAC_INT_PHY));
608
609 //
610 // Potentially task switch as a result of the above queue write.
611 //
612 #if RTOS_FREERTOS
613     if(xWake == pdTRUE)
614     {
615         portYIELD_FROM_ISR(true);
616     }
617 #endif
618 }
619
620 //*****
621 //
622 //! Returns the IP address for this interface.
623 //!
624 //! This function will read and return the currently assigned IP address for
625 //! the Stellaris Ethernet interface.
626 //!
627 //! \return Returns the assigned IP address for this interface.
628 //
629 //*****
630 uint32_t
631 lwIPLocalIPAddrGet(void)
632 {
633     #if LWIP_AUTOIP || LWIP_DHCP
634         if(g_bLinkActive)
635         {
636             return((uint32_t)g_sNetIF.ip_addr.addr);
637         }
638         else
639         {
640             return(0xffffffff);
641         }
642     #else
643         return((uint32_t)g_sNetIF.ip_addr.addr);
644     #endif
645 }
646
647 //*****
648 //
649 //! Returns the network mask for this interface.
650 //!
651 //! This function will read and return the currently assigned network mask for
652 //! the Stellaris Ethernet interface.
653 //!
654 //! \return the assigned network mask for this interface.
655 //
656 //*****
657 uint32_t
658 lwIPLocalNetMaskGet(void)
659 {
660     return((uint32_t)g_sNetIF.netmask.addr);
661 }
662
663 //*****
664 //
665 //! Returns the gateway address for this interface.
666 //!
667 //! This function will read and return the currently assigned gateway address
668 //! for the Stellaris Ethernet interface.
669 //!
670 //! \return the assigned gateway address for this interface.

```



```

671 //
672 //*****
673 uint32_t
674 lwIPLocalGWAddrGet(void)
675 {
676     return((uint32_t)g_sNetIF.gw.addr);
677 }
678
679 //*****
680 //
681 /*! Returns the local MAC/HW address for this interface.
682 /*!
683 /*! \param pui8MAC is a pointer to an array of bytes used to store the MAC
684 /*! address.
685 /*!
686 /*! This function will read the currently assigned MAC address into the array
687 /*! passed in \e pui8MAC.
688 /*!
689 /*! \return None.
690 /*!
691 //*****
692 void
693 lwIPLocalMACGet(uint8_t *pui8MAC)
694 {
695     MAP_EMACAddrGet(EMAC0_BASE, 0, pui8MAC);
696 }
697
698 //*****
699 //
700 /*! Completes the network configuration change. This is directly called when
701 /*! not using a RTOS and provided as a callback to the TCP/IP thread when using
702 /*! a RTOS.
703 /*!
704 //*****
705 static void
706 lwIPPrivateNetworkConfigChange(void *pvArg)
707 {
708     uint32_t ui32IPMode;
709     struct ip_addr ip_addr;
710     struct ip_addr net_mask;
711     struct ip_addr gw_addr;
712
713     //
714     /*! Get the new address mode.
715     //
716     ui32IPMode = (uint32_t)pvArg;
717
718     //
719     /*! Setup the network address values.
720     //
721     if(ui32IPMode == IPADDR_USE_STATIC)
722     {
723         ip_addr.addr = htonl(g_ui32IPAddr);
724         net_mask.addr = htonl(g_ui32NetMask);
725         gw_addr.addr = htonl(g_ui32GWAddr);
726     }
727 #if LWIP_DHCP || LWIP_AUTOIP
728     else
729     {
730         ip_addr.addr = 0;
731         net_mask.addr = 0;
732         gw_addr.addr = 0;
733     }
734 #endif
735
736     //
737     /*! Switch on the current IP Address Aquisition mode.
738     //
739     switch(g_ui32IPMode)
740     {
741         //
742         /*! Static IP
743         //

```

```

744     case IPADDR_USE_STATIC:
745     {
746         //
747         // Set the new address parameters. This will change the address
748         // configuration in lwIP, and if necessary, will reset any links
749         // that are active. This is valid for all three modes.
750         //
751         netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
752
753         //
754         // If we are going to DHCP mode, then start the DHCP server now.
755         //
756     #if LWIP_DHCP
757         if((ui32IPMode == IPADDR_USE_DHCP) && g_bLinkActive)
758         {
759             dhcp_start(&g_sNetIF);
760         }
761     #endif
762
763         //
764         // If we are going to AutoIP mode, then start the AutoIP process
765         // now.
766         //
767     #if LWIP_AUTOIP
768         if((ui32IPMode == IPADDR_USE_AUTOIP) && g_bLinkActive)
769         {
770             autoip_start(&g_sNetIF);
771         }
772     #endif
773
774         //
775         // And we're done.
776         //
777         break;
778     }
779
780     //
781     // DHCP (with AutoIP fallback).
782     //
783     #if LWIP_DHCP
784     case IPADDR_USE_DHCP:
785     {
786         //
787         // If we are going to static IP addressing, then disable DHCP and
788         // force the new static IP address.
789         //
790         if(ui32IPMode == IPADDR_USE_STATIC)
791         {
792             dhcp_stop(&g_sNetIF);
793             netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
794         }
795
796         //
797         // If we are going to AUTO IP addressing, then disable DHCP, set
798         // the default addresses, and start AutoIP.
799         //
800     #if LWIP_AUTOIP
801         else if(ui32IPMode == IPADDR_USE_AUTOIP)
802         {
803             dhcp_stop(&g_sNetIF);
804             netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
805             if(g_bLinkActive)
806             {
807                 autoip_start(&g_sNetIF);
808             }
809         }
810     #endif
811         break;
812     }
813 #endif
814
815     //
816     // AUTOIP

```

```

817 //
818 #if LWIP_AUTOIP
819     case IPADDR_USE_AUTOIP:
820     {
821         //
822         // If we are going to static IP addressing, then disable AutoIP and
823         // force the new static IP address.
824         //
825         if(ui32IPMode == IPADDR_USE_STATIC)
826         {
827             autoip_stop(&g_sNetIF);
828             netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
829         }
830
831         //
832         // If we are going to DHCP addressing, then disable AutoIP, set the
833         // default addresses, and start dhcp.
834         //
835 #if LWIP_DHCP
836         else if(ui32IPMode == IPADDR_USE_DHCP)
837         {
838             autoip_stop(&g_sNetIF);
839             netif_set_addr(&g_sNetIF, &ip_addr, &net_mask, &gw_addr);
840             if(g_bLinkActive)
841             {
842                 dhcp_start(&g_sNetIF);
843             }
844         }
845 #endif
846         break;
847     }
848 #endif
849 }
850
851 //
852 // Bring the interface up.
853 //
854 netif_set_up(&g_sNetIF);
855
856 //
857 // Save the new mode.
858 //
859 g_ui32IPMode = ui32IPMode;
860 }
861
862 //*****
863 //
864 //! Change the configuration of the lwIP network interface.
865 //!
866 //! \param ui32IPAddr is the new IP address to be used (static).
867 //! \param ui32NetMask is the new network mask to be used (static).
868 //! \param ui32GWAddr is the new Gateway address to be used (static).
869 //! \param ui32IPMode is the IP Address Mode. \b IPADDR_USE_STATIC 0 will
870 //! force static IP addressing to be used, \b IPADDR_USE_DHCP will force DHCP
871 //! with fallback to Link Local (Auto IP), while \b IPADDR_USE_AUTOIP will
872 //! force Link Local only.
873 //!
874 //! This function will evaluate the new configuration data. If necessary, the
875 //! interface will be brought down, reconfigured, and then brought back up
876 //! with the new configuration.
877 //!
878 //! \return None.
879 //
880 //*****
881 void
882 lwIPNetworkConfigChange(uint32_t ui32IPAddr, uint32_t ui32NetMask,
883                         uint32_t ui32GWAddr, uint32_t ui32IPMode)
884 {
885     //
886     // Check the parameters.
887     //
888 #if LWIP_DHCP && LWIP_AUTOIP
889     ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||

```

```

890         (ui32IPMode == IPADDR_USE_DHCP) ||
891         (ui32IPMode == IPADDR_USE_AUTOIP));
892 #elif LWIP_DHCP
893     ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||
894            (ui32IPMode == IPADDR_USE_DHCP));
895 #elif LWIP_AUTOIP
896     ASSERT((ui32IPMode == IPADDR_USE_STATIC) ||
897            (ui32IPMode == IPADDR_USE_AUTOIP));
898 #else
899     ASSERT(ui32IPMode == IPADDR_USE_STATIC);
900 #endif
901
902 //
903 // Save the network configuration for later use by the private network
904 // configuration change.
905 //
906 g_ui32IPAddr = ui32IPAddr;
907 g_ui32NetMask = ui32NetMask;
908 g_ui32GWAddr = ui32GWAddr;
909
910 //
911 // Complete the network configuration change. The remainder is done
912 // immediately if not using a RTOS and it is deferred to the TCP/IP
913 // thread's context if using a RTOS.
914 //
915 tcpip_callback(lwIPPrivateNetworkConfigChange, (void *)ui32IPMode);
916 }
917
918 //*****
919 // Close the Doxygen group.
920 //! @}
921 //
922 //
923 //*****

```

---

code/asv\_ap\_mcu/src/tm4c129.ld

---

```

1  _stack_size = 4K;
2
3  MEMORY
4  {
5      FLASH (rx) : ORIGIN = 0x00000000, LENGTH = 1024K
6      SRAM (rwx) : ORIGIN = 0x20000000, LENGTH = 256K
7  }
8
9  SECTIONS
10 {
11     .text :
12     {
13         _start_ext = .;
14         KEEP(*(.isr_vector))
15         *(.text*)
16         *(.rodata*)
17         _end_text = .;
18     } > FLASH
19
20     .data :
21     {
22         _start_data = .;
23         *(vtable)
24         *(.data*)
25         _end_data = .;
26     } > SRAM AT > FLASH
27
28     .ARM.extab : { *(.ARM.extab* .gnu.linkonce.armextab.*) } > FLASH
29     __exidx_start = .;
30     .ARM.exidx : { *(.ARM.exidx* .gnu.linkonce.armexidx.*) } > FLASH
31     __exidx_end = .;
32
33
34
35     .bss :

```

```

36  {
37      _start_bss = .;
38      *(.bss*)
39      *(COMMON)
40      _end_bss = .;
41  } > SRAM
42
43  _heap_bottom = .;
44  _heap_top = ORIGIN(SRAM) + LENGTH(SRAM) - _stack_size;
45
46  _stack_bottom = _heap_top;
47  _stack_top = ORIGIN(SRAM) + LENGTH(SRAM);
48
49  }

```

---

code/asv\_ap\_mcu/src/startup.c

---

```

1 /*
2 * Copyright (c) 2012, Mauro Scomparin
3 * All rights reserved.
4 *
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6 * modification, are permitted provided that the following conditions are met:
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15 *
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20 * DIRECT, INDIRECT, INCIDENTAL, SPECIAL, EXEMPLARY, OR CONSEQUENTIAL DAMAGES
21 * (INCLUDING, BUT NOT LIMITED TO, PROCUREMENT OF SUBSTITUTE GOODS OR SERVICES;
22 * LOSS OF USE, DATA, OR PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND
23 * ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
24 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS
25 * SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
26 *
27 * File:           LM4F_startup.c.
28 * Author:         Mauro Scomparin <http://scompoprojects.wordpress.com>.
29 * Version:        1.0.0.
30 * Description:    LM4F120H5QR startup code.
31 */
32
33 #include <stdint.h>
34 #include <stdbool.h>
35 #include "inc/hw_nvic.h"
36 #include "inc/hw_types.h"
37 #include "inc/hw_memmap.h"
38 #include "driverlib/rom.h"
39 #include "driverlib/gpio.h"
40
41 //-----
42 //                               Functions declarations
43 //-----
44
45 // Main should be defined on your main file so it's extern.
46 extern int main(void);
47 // rst_handler contains the code to run on reset.
48 void rst_handler(void);
49 // nmi_handler it's the code for an non maskable interrupt.
50 void nmi_handler(void);
51 // this is just the default handler.
52 void empty_def_handler(void);
53 // this is the code for an hard fault.
54 void hardfault_handler(void);
55

```

```

56 // External declaration for the interrupt handler used by the application.
57 extern void lwIPEthernetIntHandler(void);
58 extern void xPortPendSVHandler(void);
59 extern void vPortSVCHandler(void);
60 extern void xPortSysTickHandler(void);
61 extern void UART2_int_handler(void);
62 extern void UART4_int_handler(void);
63 extern void UART5_int_handler(void);
64 extern void UART7_int_handler(void);
65
66 //-----
67 //                                     Variables declarations
68 //-----
69
70 // defined by the linker it's the stack top variable (End of ram)
71 extern unsigned long _stack_top;
72 //extern uint32_t _stack_top; // Defined in the linker script
73 // defined by the linker, this are just start and end marker for each section.
74 // .text (code)
75 extern unsigned long _start_text;
76 extern unsigned long _end_text;
77 // .data (data to be copied on ram)
78 extern unsigned long _start_data;
79 extern unsigned long _end_data;
80 // .bss (uninitialized data to set to 0);
81 extern unsigned long _start_bss;
82 extern unsigned long _end_bss;
83
84 // NVIC ISR table
85 // the funny looking void(* myvectors[])(void) basically it's a way to make cc accept
86 // an array of function pointers.
87 // The vector table. Note that the proper constructs must be placed on this to
88 // ensure that it ends up at physical address 0x0000.0000.
89 __attribute__((section(".isr_vector")))
90 void(* myvectors[])(void) = {
91     // This are the fixed priority interrupts and the stack pointer loaded at startup
92     // at R13 (SP).
93     //-----
94     //                                     VECTOR N (Check Datasheet)
95     // here the compiler it's boring.. have to figure that out
96     //((void (*)(void))((uint32_t) &_stack_top)),
97     (void (*)(void))((unsigned long) &_stack_top),
98     //((void (*)(void)) &_stack_top),
99     // stack pointer should be
100     // placed here at startup.
101     0
102     rst_handler, // The reset handler
103     nmi_handler, // The NMI handler
104     hardfault_handler, // The hard fault handler
105     empty_def_handler, // The MPU fault handler
106     empty_def_handler, // The bus fault handler
107     empty_def_handler, // The usage fault handler
108     0, // Reserved
109     0, // Reserved
110     0, // Reserved
111     0, // Reserved
112     vPortSVCHandler, // SVCcall handler
113     empty_def_handler, // Debug monitor handler
114     0, // Reserved
115     xPortPendSVHandler, // The PendSV handler
116     xPortSysTickHandler, // The SysTick handler
117     empty_def_handler, // GPIO Port A
118     empty_def_handler, // GPIO Port B
119     empty_def_handler, // GPIO Port C
120     empty_def_handler, // GPIO Port D
121     empty_def_handler, // GPIO Port E
122     empty_def_handler, // UART0 Rx and Tx
123     empty_def_handler, // UART1 Rx and Tx
124     empty_def_handler, // SSIO Rx and Tx
125     empty_def_handler, // I2C0 Master and Slave
126     empty_def_handler, // PWM Fault
127     empty_def_handler, // PWM Generator 0
128     empty_def_handler, // PWM Generator 1
129     empty_def_handler, // PWM Generator 2
130     empty_def_handler, // Quadrature Encoder 0

```

```

127     empty_def_handler , // ADC Sequence 0
128     empty_def_handler , // ADC Sequence 1
129     empty_def_handler , // ADC Sequence 2
130     empty_def_handler , // ADC Sequence 3
131     empty_def_handler , // Watchdog timer
132     empty_def_handler , // Timer 0 subtimer A
133     empty_def_handler , // Timer 0 subtimer B
134     empty_def_handler , // Timer 1 subtimer A
135     empty_def_handler , // Timer 1 subtimer B
136     empty_def_handler , // Timer 2 subtimer A
137     empty_def_handler , // Timer 2 subtimer B
138     empty_def_handler , // Analog Comparator 0
139     empty_def_handler , // Analog Comparator 1
140     empty_def_handler , // Analog Comparator 2
141     empty_def_handler , // System Control (PLL, OSC, BO)
142     empty_def_handler , // FLASH Control
143     empty_def_handler , // GPIO Port F
144     empty_def_handler , // GPIO Port G
145     empty_def_handler , // GPIO Port H
146     UART2_int_handler , // UART2 Rx and Tx
147     empty_def_handler , // SSI1 Rx and Tx
148     empty_def_handler , // Timer 3 subtimer A
149     empty_def_handler , // Timer 3 subtimer B
150     empty_def_handler , // I2C1 Master and Slave
151     empty_def_handler , // CAN0
152     empty_def_handler , // CAN1
153     lwIPEthernetIntHandler , // Ethernet
154     empty_def_handler , // Hibernate
155     empty_def_handler , // USB0
156     empty_def_handler , // PWM Generator 3
157     empty_def_handler , // uDMA Software Transfer
158     empty_def_handler , // uDMA Error
159     empty_def_handler , // ADC1 Sequence 0
160     empty_def_handler , // ADC1 Sequence 1
161     empty_def_handler , // ADC1 Sequence 2
162     empty_def_handler , // ADC1 Sequence 3
163     empty_def_handler , // External Bus Interface 0
164     empty_def_handler , // GPIO Port J
165     empty_def_handler , // GPIO Port K
166     empty_def_handler , // GPIO Port L
167     empty_def_handler , // SSI2 Rx and Tx
168     empty_def_handler , // SSI3 Rx and Tx
169     empty_def_handler , // UART3 Rx and Tx
170     UART4_int_handler , // UART4 Rx and Tx
171     UART5_int_handler , // UART5 Rx and Tx
172     empty_def_handler , // UART6 Rx and Tx
173     UART7_int_handler , // UART7 Rx and Tx
174     empty_def_handler , // I2C2 Master and Slave
175     empty_def_handler , // I2C3 Master and Slave
176     empty_def_handler , // Timer 4 subtimer A
177     empty_def_handler , // Timer 4 subtimer B
178     empty_def_handler , // Timer 5 subtimer A
179     empty_def_handler , // Timer 5 subtimer B
180     empty_def_handler , // FPU
181     0 , // Reserved
182     0 , // Reserved
183     empty_def_handler , // I2C4 Master and Slave
184     empty_def_handler , // I2C5 Master and Slave
185     empty_def_handler , // GPIO Port M
186     empty_def_handler , // GPIO Port N
187     0 , // Reserved
188     empty_def_handler , // Tamper
189     empty_def_handler , // GPIO Port P (Summary or P0)
190     empty_def_handler , // GPIO Port P1
191     empty_def_handler , // GPIO Port P2
192     empty_def_handler , // GPIO Port P3
193     empty_def_handler , // GPIO Port P4
194     empty_def_handler , // GPIO Port P5
195     empty_def_handler , // GPIO Port P6
196     empty_def_handler , // GPIO Port P7
197     empty_def_handler , // GPIO Port Q (Summary or Q0)
198     empty_def_handler , // GPIO Port Q1
199     empty_def_handler , // GPIO Port Q2

```

```

200     empty_def_handler ,           // GPIO Port Q3
201     empty_def_handler ,           // GPIO Port Q4
202     empty_def_handler ,           // GPIO Port Q5
203     empty_def_handler ,           // GPIO Port Q6
204     empty_def_handler ,           // GPIO Port Q7
205     empty_def_handler ,           // GPIO Port R
206     empty_def_handler ,           // GPIO Port S
207     empty_def_handler ,           // SHA/MD5 0
208     empty_def_handler ,           // AES 0
209     empty_def_handler ,           // DES3DES 0
210     empty_def_handler ,           // LCD Controller 0
211     empty_def_handler ,           // Timer 6 subtimer A
212     empty_def_handler ,           // Timer 6 subtimer B
213     empty_def_handler ,           // Timer 7 subtimer A
214     empty_def_handler ,           // Timer 7 subtimer B
215     empty_def_handler ,           // I2C6 Master and Slave
216     empty_def_handler ,           // I2C7 Master and Slave
217     empty_def_handler ,           // HIM Scan Matrix Keyboard 0
218     empty_def_handler ,           // One Wire 0
219     empty_def_handler ,           // HIM PS/2 0
220     empty_def_handler ,           // HIM LED Sequencer 0
221     empty_def_handler ,           // HIM Consumer IR 0
222     empty_def_handler ,           // I2C8 Master and Slave
223     empty_def_handler ,           // I2C9 Master and Slave
224     empty_def_handler ,           // GPIO Port T
225 };
226
227 //-----
228 //                               Function implementations.
229 //-----
230
231 /*
232 * System on reset code. NVIC 1
233 * Here I prepare the memory for the c compiler.
234 * The stack pointer should be set at the beginning with the NVIC table already.
235 * Copy the .data segment from flash into ram.
236 * 0 to the .bss segment
237 */
238
239 void rst_handler(void){
240     // Copy the .data section pointers to ram from flash.
241     // Look at LD manual (Optional Section Attributes).
242
243     // source and destination pointers
244     unsigned long *src;
245     unsigned long *dest;
246
247     //this should be good!
248     src = &_end_text;
249     dest = &_start_data;
250
251     //this too
252     while(dest < &_end_data)
253     {
254         *dest++ = *src++;
255     }
256
257     // now set the .bss segment to 0!
258     dest = &_start_bss;
259     while(dest < &_end_bss){
260         *dest++ = 0;
261     }
262
263     // after setting copying .data to ram and "zero-ing" .bss we are good
264     // to start the main() method!
265     // There you go!
266     main();
267 }
268
269 // NMI Exception handler code NVIC 2
270 void nmi_handler(void){
271     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_0, 0); // Red
272     // Just loop forever, so if you want to debug the processor it's running.

```



```

273     while(1){
274     }
275 }
276
277 // Hard fault handler code NVIC 3
278 void hardfault_handler(void){
279     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_0, 0); // Red
280     // Just loop forever, so if you want to debug the processor it's running.
281     while(1){
282     }
283 }
284
285 // Empty handler used as default.
286 void empty_def_handler(void){
287     ROM_GPIOPinWrite(GPIO_PORTA_BASE, GPIO_PIN_0, 0); // Red
288     //ROM_GPIOPinWrite(GPIO_PORTH_BASE, GPIO_PIN_2, 0);
289     // Just loop forever, so if you want to debug the processor it's running.
290     while(1){
291     }
292 }

```

---

code/asv\_ap\_mcu/src/syscalls.c

---

```

1 #include <sys/types.h>           //Needed for caddr_t
2 #include <stdint.h>
3
4 #include "inc/hw_memmap.h"      //Needed for GPIO Pins/UART base
5 #include "inc/hw_types.h"      //Needed for SysTick Types
6 #include "driverlib/rom.h"
7 #include "driverlib/rom_map.h"
8
9 char *heap_end = 0;
10
11 caddr_t _sbrk(unsigned int incr) {
12
13     extern unsigned long _heap_bottom;
14     extern unsigned long _heap_top;
15     static char *prev_heap_end;
16
17     if (heap_end == 0) {
18         heap_end = (caddr_t)&_heap_bottom;
19     }
20
21     prev_heap_end = heap_end;
22
23     if (heap_end + incr > (caddr_t)&_heap_top) {
24         return (caddr_t)0;
25     }
26
27     heap_end += incr;
28
29     return (caddr_t) prev_heap_end;
30 }
31
32 int _close(int file) {
33     return -1;
34 }
35
36 int _fstat(int file) {
37     return -1;
38 }
39
40 int _isatty(int file) {
41     return -1;
42 }
43
44 int _lseek(int file, int ptr, int dir) {
45     return -1;
46 }
47
48 int _open(const char *name, int flags, int mode) {
49     return -1;

```

```

50 }
51
52 int _read(int file , char *ptr, int len) {
53     unsigned int i;
54     for( i = 0; i < len; i++){
55         ptr[i] = (char)ROM_UARTCharGet(UART5_BASE);
56     }
57     return len;
58 }
59 }
60
61 int _write(int file , char *ptr, unsigned int len) {
62     unsigned int i;
63     for(i = 0; i < len; i++){
64         ROM_UARTCharPut(UART5_BASE, ptr[i]);
65     }
66     return i;
67 }
68 }

```

---

code/asv\_ap\_mcu/src/Makefile

---

```

1 # Copyright (c) 2012, Mauro Scomparin
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3 #
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22 # ON ANY THEORY OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
23 # (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE OF THIS
24 # SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
25 #
26 # File:           Makefile.
27 # Author:        Mauro Scomparin <http://scompoprojects.wordpress.com>.
28 # Version:       1.0.1.
29 # Description:    Sample makefile, modified version inspired by Wollw @ github
30 #
31 # Modified by:   Joakim Myrland
32 # website:       www.LDA.as
33 # email:         joakim.myrland@LDA.as
34 # project:       https://github.com/Lindem-Data-Acquisition-AS/iot_tiva_template/
35 #
36
37 #global names
38 LIB_PORT_NAME   = tm4c129
39 PROJECT_NAME    = iot_template_$(LIB_PORT_NAME)
40
41 #path names relative to makefile:
42 ROOT            = ..
43 #path names relative to ROOT:
44 SRC_PATH        = src
45 BIN_PATH        = bin
46 TIVAWARE_PATH   = iot_mcu_lib/TivaWare_C_Series-2.1.0.12573
47 FreeRTOS_PATH   = iot_mcu_lib/FreeRTOSV8.2.0
48 lwIP_PATH        = iot_mcu_lib/lwip-1.4.1
49 jsmn_PATH        = iot_mcu_lib/jsmn

```

```

51 #compiler setup
52 COMPILER = gcc
53 TOOLCHAIN = arm-none-eabi
54 PART = TM4C1294NCPDT
55 #TM4C1294KCPDT
56 #TM4C1294NCPDT
57 #TM4C1294ENCPDT
58 CPU = cortex-m4
59 FPU = fpv4-sp-d16
60 FABI = softfp

62 LINKER_FILE = ${LIB_PORT_NAME}.ld
63 STARTUP_FILE = main

65 CC = ${TOOLCHAIN}-gcc
66 LD = ${TOOLCHAIN}-ld
67 CP = ${TOOLCHAIN}-objcopy
68 OD = ${TOOLCHAIN}-objdump

70 CFLAGS = -mthumb -mcpu=${CPU} -mfpu=${FPU} -mfloat-abi=${FABI}
71 CFLAGS += -Os -ffunction-sections -fdata-sections
72 CFLAGS += -MD -std=gnu99 -Wall
73 CFLAGS += -DPART_${PART} -c -DTARGET_IS_TM4C129_RA0
74 CFLAGS += -c -g
75 #CFLAGS += -DDEBUG
76 CFLAGS += -DRTOS_FREERTOS

79 #include header directories
80 CFLAGS += -I ${ROOT}
81 CFLAGS += -I ${ROOT}/${SRC_PATH}
82 CFLAGS += -I ${ROOT}/${SRC_PATH}/config
83 CFLAGS += -I ${ROOT}/${SRC_PATH}/tasks
84 CFLAGS += -I ${ROOT}/${TIVAWARE_PATH}
85 #include FreeRTOS
86 CFLAGS += -I ${ROOT}/${FreeRTOS_PATH}/FreeRTOS/Source/portable/GCC/ARM_CM4F
87 CFLAGS += -I ${ROOT}/${FreeRTOS_PATH}/FreeRTOS/Source/include
88 #include lwIP
89 CFLAGS += -I ${ROOT}/${lwIP_PATH}/ports/${LIB_PORT_NAME}/include
90 CFLAGS += -I ${ROOT}/${lwIP_PATH}/src/include
91 CFLAGS += -I ${ROOT}/${lwIP_PATH}/src/include/ipv4
92 CFLAGS += -I ${ROOT}/${lwIP_PATH}/src/include/netif
93 #include lwIP apps
94 CFLAGS += -I ${ROOT}/${lwIP_PATH}/apps
95 #include jsmn
96 CFLAGS += -I ${ROOT}/${jsmn_PATH}

98 #include source directories/files
99 SRC += $(wildcard ${ROOT}/${SRC_PATH}/*.c)
100 SRC += $(wildcard ${ROOT}/${SRC_PATH}/asv_ap/*.c)
101 SRC += $(wildcard ${ROOT}/${SRC_PATH}/tasks/*.c)
102 #include FreeRTOS
103 SRC += $(wildcard ${ROOT}/${FreeRTOS_PATH}/FreeRTOS/Source/portable/GCC/ARM_CM4F/*.c)
104 SRC += $(wildcard ${ROOT}/${FreeRTOS_PATH}/FreeRTOS/Source/portable/MemMang/heap_2.c)
105 SRC += $(wildcard ${ROOT}/${FreeRTOS_PATH}/FreeRTOS/Source/*.c)
106 #include lwIP
107 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/ports/${LIB_PORT_NAME}/*.c)
108 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/ports/${LIB_PORT_NAME}/netif/*.c)
109 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/core/*.c)
110 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/core/ipv4/*.c)
111 #SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/core/ipv6/*.c)
112 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/core/snmp/*.c)
113 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/netif/*.c)
114 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/netif/ppp/*.c)
115 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/src/api/*.c)
116 #include lwIP apps
117 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/apps/httpserver_raw/httpd.c)
118 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/apps/httpserver_raw/httpd_post.c)
119 SRC += $(wildcard ${ROOT}/${lwIP_PATH}/apps/httpserver_raw/fs.c)

121 #include jsmn
122 SRC += $(wildcard ${ROOT}/${jsmn_PATH}/jsmn.c)

```

```

124 LIB_GCC_PATH    = ${shell ${CC} ${CFLAGS} -print-libgcc-file-name}
125 LIBC_PATH      = ${shell ${CC} ${CFLAGS} -print-file-name=libc.a}
126 LIBM_PATH      = ${shell ${CC} ${CFLAGS} -print-file-name=libm.a}
127 LFLAGS         = --gc-sections
128 CPFLAGS        = -Obinary
129 ODFLAGS        = -S

131 OBJS = $(SRC:.c=.o)
132 D_FILES = $(SRC:.c=.d)

134 #this ECHO functions prints compile progress [nn%] to stdout
135 ifndef ECHO
136 T := $(shell $(MAKE) $(MAKECMDGOALS) --no-print-directory \
137     -nrRf $(firstword $(MAKEFILE_LIST)) \
138     ECHO="COUNTTHIS" | grep -c "COUNTTHIS")

140 N := x
141 C = $(words $N)$(eval N := x $N)
142 ECHO = echo " 'expr' \ 'expr $C '*' 100 / $T\" : : '.*\ (...)\ $$' : % "
143 endif

145 #rules
146 all: $(OBJS) ${PROJECT_NAME}.axf ${PROJECT_NAME}
147     size ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf
148     @echo make complete

150 %.o: %.c
151 ##echo .
152     @mkdir -p ${ROOT}/${BIN_PATH}
153     @$(CC) -c $(CFLAGS) $< -o $@
154     @$(ECHO)

156 ${PROJECT_NAME}.axf: $(OBJS)
157     @$(LD) -T $(LINKER_FILE) $(LFLAGS) -o ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf $(
158     OBJS) $(LIBM_PATH) $(LIBC_PATH) $(LIB_GCC_PATH) ${ROOT}/${TIVAWARE_PATH}/
159     driverlib/${COMPILER}/libdriver.a

159 ${PROJECT_NAME}: ${PROJECT_NAME}.axf
160     @$(CP) $(CPFLAGS) ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf ${ROOT}/${BIN_PATH}/${
161     PROJECT_NAME}.bin
162     @$(OD) $(ODFLAGS) ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf > ${ROOT}/${BIN_PATH}/${
163     PROJECT_NAME}.lst

163 # make clean rule
164 clean:
165     @rm -fr ${ROOT}/${BIN_PATH}
166     @rm -f ${OBJS}
167     @rm -f ${D_FILES}
168     @echo clean complete

170 # Rule to flash the project to the board
171 flash: all
172     openocd \
173     -c "source [find interface/ftdi/olimex-arm-usb-ocd-h.cfg]" \
174     -c "set WORKAREASIZE 0x40000" \
175     -c "set CHIPNAME TM4C1294NCPDT" \
176     -c "source [find target/stellaris.cfg]" \
177     -c "init" \
178     -c "targets" \
179     -c "reset halt" \
180     -c "flash info 0" \
181     -c "flash banks" \
182     -c "flash write_image erase ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf" \
183     -c "verify_image ${ROOT}/${BIN_PATH}/${PROJECT_NAME}.axf" \
184     -c "reset" \
185     -c "shutdown"

187 debug: flash
188     arm-none-eabi-gdb -ex \
189     'target extended-remote | openocd \
190     -c "source [find interface/ftdi/olimex-arm-usb-ocd-h.cfg]" \
191     -c "set WORKAREASIZE 0x40000" \
192     -c "set CHIPNAME TM4C1294NCPDT" \

```

---

```
193 -c "transport_␣select_␣jtag" \<
194 -c "source_␣[find_␣target/stellaris.cfg]" \<
195 "gdb_␣port_␣pipe;_␣log_␣output_␣openocd.log"; monitor reset halt '\
196 $(ROOT)/$(BIN_PATH)/$(PROJECT_NAME).axf
```

---

## Appendix N

# Code for ASV control panel

Server and client-side JavaScript code for the ASV control panel, as described in section 3.3.9. The code is licensed under the MIT license (listing D.1).

code/ASV\_control\_panel/server.js

---

```
1 /* Setup logging */
2 var winston = require('winston');
3 winston.add(winston.transports.File, { filename: 'nodejs.log' });
4
5 winston.info('Starting server. ');
6
7 /* Setting up webserver */
8 var node_static = require('node-static');
9
10 var files = new node_static.Server('./public');
11
12 function http_handler(request, response) {
13     request.on('end', function() {
14         files.serve(request, response);
15     }).resume();
16 }
17
18 var http = require('http');
19
20 var app = http.createServer(http_handler);
21 app.listen(80, '0.0.0.0');
22
23 /* Setting up socket.io */
24 var io = require('socket.io').listen(app);
25
26 io.sockets.on('connection', function(socket) {
27     socket.on('message', function(data) {
28         //console.log("sending:" + data);
29         winston.info("sending:" + data);
30
31         var client = new net.Socket();
32         var message = new Buffer(data);
33
34         client.connect(30470, "192.168.1.21", function() {
35             client.write(message);
36             client.destroy();
37         });
38     });
39 });
40
41 });
42 });
43
44 /* Setting up TCP client */
45 var net = require('net');
```

---

```

46
47 /* Setting up UDP server */
48 var dgram = require('dgram');
49
50 var udp_server = dgram.createSocket("udp4");
51
52 udp_server.on("listening", function() {
53     var address = udp_server.address();
54     //console.log("Server listening on " + address.address + ":" + address.port);
55     winston.info("Server listening on " + address.address + ":" + address.port);
56 });
57
58 udp_server.on("message", function(msg, rinfo) {
59     //console.log("Server got: " + msg + " from " + rinfo.address + ":" + rinfo.port);
60     winston.info("Server got: " + msg + " from " + rinfo.address + ":" + rinfo.port);
61     io.sockets.emit('message', "" + msg);
62 });
63
64 udp_server.on('error', function(err) {
65     console.error(err);
66     process.exit(0);
67 });
68
69 udp_server.bind(8000);

```

---

code/ASV\_control\_panel/public/js/application.js

---

```

1 $( document ).ready( function() {
2
3     var ready_for_new_wp = false;
4     var draw_wanted = false;
5     var got_message = false;
6
7     var asv_planned_route = [
8         [59.95990811, 10.79299452],
9         [59.95978586, 10.79244960],
10        [59.95966608, 10.79192318],
11        [59.95951144, 10.79206993]
12    ];
13
14    var heading_canvas = $('div#heading_canvas')[0];
15
16    if (heading_canvas.getContext('2d')) {
17        heading_ctx = heading_canvas.getContext('2d');
18    } else {
19        alert("Canvas not supported!");
20    }
21
22    var checkpoint_radius = 4;
23
24    var asv_nr_of_msg = 0;
25
26    /* Connect to socket */
27    var socket = io.connect("/");
28
29    socket.on('message', function(data) {
30
31        //console.log(data);
32
33        if (data.substring(0,6) == "STATUS") {
34
35            $('div#status_bar').text(data);
36
37            var data_array = data.split(",");
38
39            asv_date = data_array[1];
40            asv_time = data_array[2];
41            asv_latitude = data_array[3];
42            asv_longitude = data_array[4];
43            asv_gps_ok = data_array[5];
44            asv_current_heading = data_array[6];
45            asv_wanted_heading = data_array[7];

```

```

46     asv_distance_from_wp = data_array[8];
47     asv_nr_of_wp = data_array[9];
48     asv_mag_accuracy = data_array[10];
49     asv_run_motors = data_array[11];
50     asv_first_waypoint_latitude = data_array[12];
51     asv_first_waypoint_longitude = data_array[13];
52     asv_next_waypoint_latitude = data_array[14];
53     asv_next_waypoint_longitude = data_array[15];
54     asv_port_motor_thrust = data_array[16];
55     asv_stbd_motor_thrust = data_array[17];
56
57     $("span#asv_nr_of_wp").text(asv_nr_of_wp + " WP");
58     $("span#asv_port_motor_thrust").text(asv_port_motor_thrust);
59     $("span#asv_stbd_motor_thrust").text(asv_stbd_motor_thrust);
60
61     $("span#asv_motor_running").siblings('i').removeClass("fa-spin");
62     if (asv_run_motors == "1") {
63         $("span#asv_motor_running").siblings('i').addClass("fa-spin");
64         $("span#asv_motor_running").text("Running");
65     } else {
66         $("span#asv_motor_running").text("Not running");
67     }
68
69     if (asv_gps_ok == "1") {
70         $("span#asv_gps_ok").text("OK").css('color', 'green');
71         $("span#asv_date").text("20" + asv_date.substring(4,6) + "-" + asv_date
72             .substring(2,4) + "-" + asv_date.substring(0,2));
73         $("span#asv_time").text(parseInt(asv_time.substring(0,2))+2 + ":" +
74             asv_time.substring(2,4) + ":" + asv_time.substring(4,6));
75     } else {
76         $("span#asv_gps_ok").text("Not OK").css('color', 'red');
77         $("span#asv_date").text("N/A");
78         $("span#asv_time").text("N/A");
79     }
80
81     if (parseInt(asv_mag_accuracy) >= 3) {
82         $("span#asv_compass_ok").text("OK").css('color', 'green');
83     } else {
84         $("span#asv_compass_ok").text("Not OK").css('color', 'red');
85     }
86
87     if (asv_gps_ok = "1" && parseInt(asv_first_waypoint_latitude) != 0 &&
88         parseInt(asv_first_waypoint_longitude) != 0) {
89         $("span#asv_distance_from_wp").text(asv_distance_from_wp + " m to WP");
90         draw_wanted = true;
91     } else {
92         $("span#asv_distance_from_wp").text("N/A");
93         draw_wanted = false;
94     }
95
96     $("span#asv_nr_of_msg").text(++asv_nr_of_msg);
97     $("span#asv_nr_of_msg").siblings('i').addClass("fa-spin");
98
99     if (asv_nr_of_msg >= 99) {
100         asv_nr_of_msg = 0
101     }
102     got_message = true;
103
104     draw_indicator(heading_ctx, asv_wanted_heading, asv_current_heading,
105         draw_wanted);
106
107     asv_marker.options.angle = asv_current_heading; //direction * (180 / Math.
108         PI);
109     asv_marker.update();
110
111     if (asv_latitude != 0 && asv_longitude != 0) {
112         var asv_position = new L.LatLng(asv_latitude, asv_longitude);
113         asv_marker.setLatLng(asv_position);
114         asv_history.addLatLng(asv_position);
115     }
116
117     first_waypoint.setLatLng(new L.LatLng(asv_first_waypoint_latitude,
118         asv_first_waypoint_longitude));

```



```

113     next_waypoint.setLatLng(new L.LatLng(asv_next_waypoint_latitude ,
114         asv_next_waypoint_longitude));
115
116     if (parseInt(asv_nr_of_wp) < 2 && ready_for_new_wp == true) {
117         //planned_checkpoints.getLayers()[0].spliceLatLngs(0,1)[0];
118         //var last_cleared_checkpoint = planned_checkpoints.getLayers()[0].
119             getLatLngs()[0];
120         //console.log("checkpoint at latitude " + last_cleared_checkpoint.lat +
121             " and longitude " + last_cleared_checkpoint.lng + " cleared");
122         //cleared_checkpoints.addLatLng(last_cleared_checkpoint);
123         //next_checkpoint.setLatLng(last_cleared_checkpoint);
124         //socket.emit('message', 'NWP,'+ last_cleared_checkpoint.lat + "," +
125             last_cleared_checkpoint.lon);
126         var next = asv_planned_route.shift();
127         socket.emit('message', 'NWP,'+ next[0] + "," + next[1]);
128         ready_for_new_wp = false;
129     }
130 } else if (data.substring(0,6) == "ACKRWP") {
131     ready_for_new_wp = true;
132 }
133 });
134
135 socket.emit('message', 'ASV_Control_Panel_started');
136
137 /* Init map */
138 var map = L.map('map', {zoomControl: false}).setView([59.95974031, 10.79310980],
139     19);
140 L.tileLayer('img/tiles/{z}/{x}/{y}.png', {
141     zoom: 21,
142     maxZoom: 21,
143     detectRetina: true
144 }).addTo(map);
145
146 map.dragging.disable();
147 map.touchZoom.disable();
148 map.doubleClickZoom.disable();
149 map.scrollWheelZoom.disable();
150 map.boxZoom.disable();
151 map.keyboard.disable();
152
153 var asv_marker = L.rotatedMarker(new L.LatLng(59.959707, 10.792272), {
154     icon: L.icon({
155         iconUrl: 'img/asv_marker.png',
156         iconRetinaUrl: 'img/asv_marker@2x.png',
157         iconSize: [26, 50],
158         iconAnchor: [13, 25],
159     })
160 });
161
162 asv_marker.addTo(map);
163
164 /* Init buttons */
165
166 L.easyButton('fa-send', function() {
167     ready_for_new_wp = true;
168 }, "Send_WP", map);
169
170 L.easyButton('fa-trash', function() {
171     socket.emit('message', 'DEL');
172 }, "Delete_WP", map);
173
174 L.easyButton('fa-play', function() {
175     socket.emit('message', 'RUN');
176 }, "Start_the_ASV", map);
177
178 L.easyButton('fa-stop', function() {
179     socket.emit('message', 'HLT');
180 }, "Stop_the_ASV", map);
181
182 /* Init planned checkpoints */
183 var planned_checkpoints = new L.FeatureGroup();
184 map.addLayer(planned_checkpoints);

```

```

181     var draw_control = new L.Control.Draw({
182         draw: {
183             circle: false,
184             rectangle: false,
185             polygon: false,
186             marker: false
187         },
188         edit: {
189             edit: true,
190             featureGroup: planned_checkpoints
191         }
192     });
193     map.addControl(draw_control);
194
195     map.on('draw:created', function(e) {
196         console.log(e.layer.getLatLngs());
197         planned_checkpoints.addLayer(e.layer);
198     });
199
200     map.on('draw:edited', function(e) {
201         e.layers.eachLayer(function (layer) {
202             console.log(layer.getLatLngs());
203         });
204     });
205
206     /* Init asv history */
207     var asv_history_points = [ [59.95974031, 10.79310980] ];
208
209     var asv_history_options = {
210         smoothFactor: 1,
211         color: '#000'
212     };
213
214     var asv_history = L.polyline(asv_history_points, asv_history_options).addTo(map);
215
216     /* Init cleared checkpoints */
217     var cleared_checkpoints_points = [];
218
219     var cleared_checkpoints_option = {
220         smoothFactor: 0,
221         color: '#00f'
222     };
223
224     var cleared_checkpoints = L.polyline(cleared_checkpoints_points,
225         cleared_checkpoints_option).addTo(map);
226
227     /* Init future waypoint markers */
228     var waypoint_options = {
229         color: '#f00',
230         opacity: 1,
231         weight: 10,
232         fillColor: '#f00',
233         fillOpacity: 0.5
234     };
235     var first_waypoint = L.circle([0, 0], checkpoint_radius, waypoint_options).addTo(
236         map);
237     var next_waypoint = L.circle([0, 0], checkpoint_radius/2, waypoint_options).addTo(
238         map);
239
240     var circle_options = {
241         color: '#ff0',
242         opacity: 1,
243         weight: 3
244     };
245     L.circle([59.95974031, 10.79310980], 1, circle_options).addTo(map);
246     L.circle([59.95990811, 10.79299452], 1, circle_options).addTo(map);
247     L.circle([59.95978586, 10.79244960], 1, circle_options).addTo(map);
248     L.circle([59.95966608, 10.79192318], 1, circle_options).addTo(map);
249     L.circle([59.95951144, 10.79206993], 1, circle_options).addTo(map);
250
251     window.setInterval(function () {

```

```

251     if (!map.getBounds().contains(asv_marker.getLatLng())) {
252         //map.panTo(asv_marker.getLatLng());
253     }
254
255     if (got_message == false) {
256         $("span#asv_nr_of_msg").siblings('i').removeClass("fa-spin");
257     }
258     got_message = false;
259 }, 1000);
260
261 function clear_canvas(ctx) {
262     ctx.clearRect(0, 0, 200, 200);
263 }
264
265 function draw(degrees) {
266     clear_canvas();
267
268     // Draw the compass onto the canvas
269     ctx.drawImage(compass, 0, 0);
270
271     // Save the current drawing state
272     ctx.save();
273
274     // Now move across and down half the
275     ctx.translate(100, 100);
276
277     // Rotate around this point
278     ctx.rotate(degrees * (Math.PI / 180));
279
280     // Draw the image back and up
281     ctx.drawImage(needle, -100, -100);
282
283     // Restore the previous drawing state
284     ctx.restore();
285 }
286
287 function draw_indicator(ctx, degrees_wanted, degrees_current, draw_wanted) {
288
289     ctx.clearRect(0, 0, 200, 200);
290
291     var center_x = 100;
292     var center_y = 100;
293
294     // Draw outer circle
295     ctx.beginPath();
296     ctx.arc(center_x, center_y, 100, 0, 2*Math.PI, false);
297     ctx.lineWidth = 1;
298     ctx.strokeStyle = '#000';
299     ctx.fillStyle = "rgba(0,0,0,0.1)";
300     ctx.fill();
301
302     // Draw N, S, E, W
303     ctx.save();
304     ctx.translate(center_x, center_y);
305     ctx.fillStyle = "black";
306     ctx.font = "1.3em open_sansregular";
307     ctx.textBaseline = "middle";
308     ctx.textAlign = "center";
309     ctx.save();
310     ctx.translate(0, -60);
311     ctx.fillText("N", 0, 0);
312     ctx.restore();
313     ctx.save();
314     ctx.translate(60, 0);
315     ctx.fillText("E", 0, 0);
316     ctx.restore();
317     ctx.save();
318     ctx.translate(0, 60);
319     ctx.fillText("S", 0, 0);
320     ctx.restore();
321     ctx.save();
322
323

```

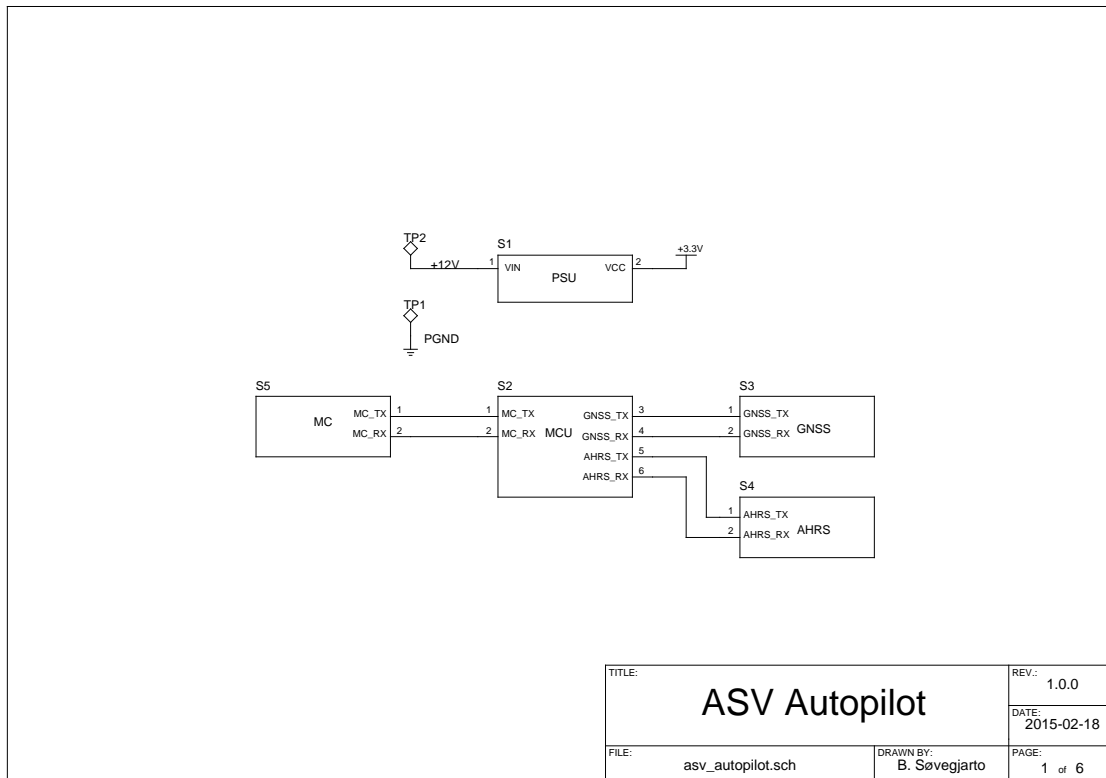
```

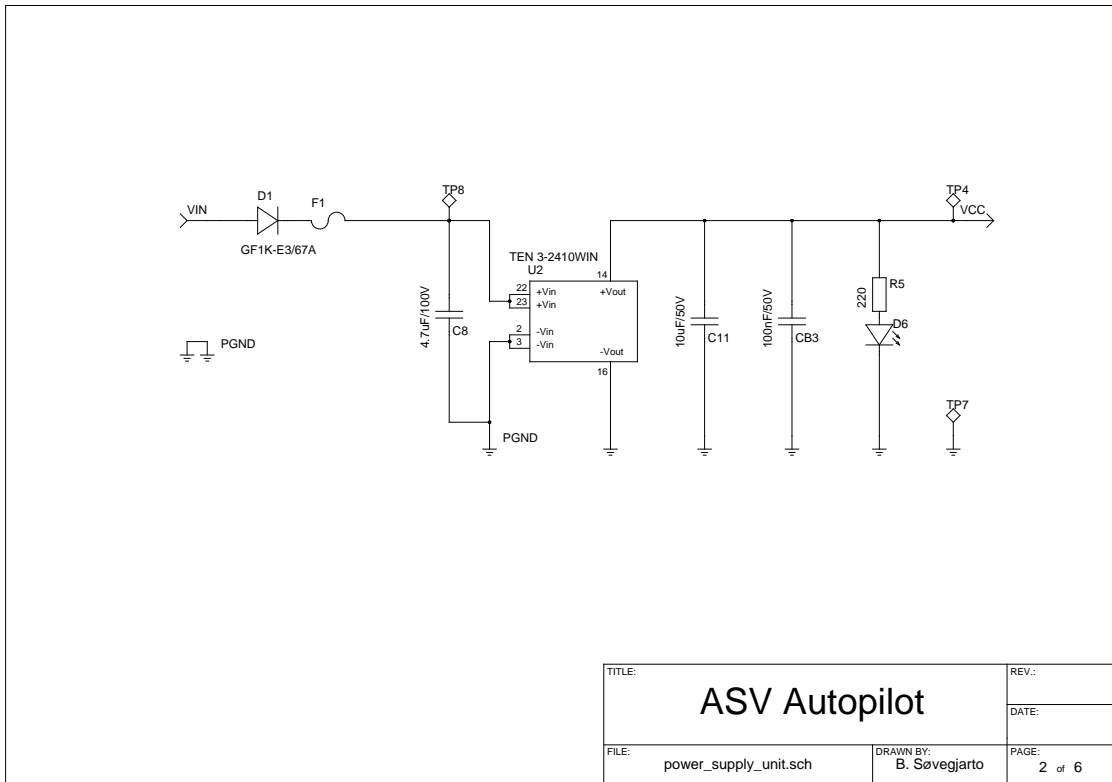
324     ctx.translate(-60, 0);
325     ctx.fillText("W", 0, 0);
326     ctx.restore();
327     ctx.restore();
328
329     if (draw_wanted == true) {
330         // Draw wanted heading
331         ctx.lineWidth = 2;
332         ctx.setLineDash([2]);
333         ctx.strokeStyle = '#f00';
334         draw_needle(90, degrees_wanted);
335         ctx.setLineDash([]);
336     }
337
338     // Draw needle mounting
339     ctx.beginPath();
340     ctx.arc(center_x, center_y, 5, 0, 2 * Math.PI, false);
341     ctx.fillStyle = '#00F';
342     ctx.fill();
343     ctx.closePath();
344
345     draw_ticks();
346
347     // Draw current heading
348     ctx.lineWidth = 2;
349     ctx.strokeStyle = '#00F';
350     draw_needle(90, degrees_current);
351
352     function draw_ticks() {
353         ctx.strokeStyle = "black";
354         ctx.lineWidth = 1;
355
356         // 360/72 = 5 degrees
357         var nr_of_ticks = 72;
358
359         for (var i = 0; i < nr_of_ticks; i++) {
360             ctx.save();
361             ctx.beginPath();
362             ctx.translate(center_x, center_y);
363             var angle = (i * (360/nr_of_ticks)) * Math.PI/180;
364             ctx.rotate(angle);
365             ctx.translate(0, -180/2);
366
367             ctx.moveTo(0, 0);
368             ctx.lineTo(0, 10);
369             ctx.stroke();
370             ctx.closePath();
371             ctx.restore();
372         }
373     }
374
375     function draw_needle(length, angle) {
376         ctx.save();
377         ctx.beginPath();
378         ctx.translate(center_x, center_y);
379
380         // Correct for top left origin
381         ctx.rotate(-180 * Math.PI/180);
382
383         ctx.rotate(angle * Math.PI/180);
384         ctx.moveTo(0, 0);
385         ctx.lineTo(0, length);
386         ctx.stroke();
387         ctx.closePath();
388         ctx.restore();
389     }
390 }
391 }
392 });

```

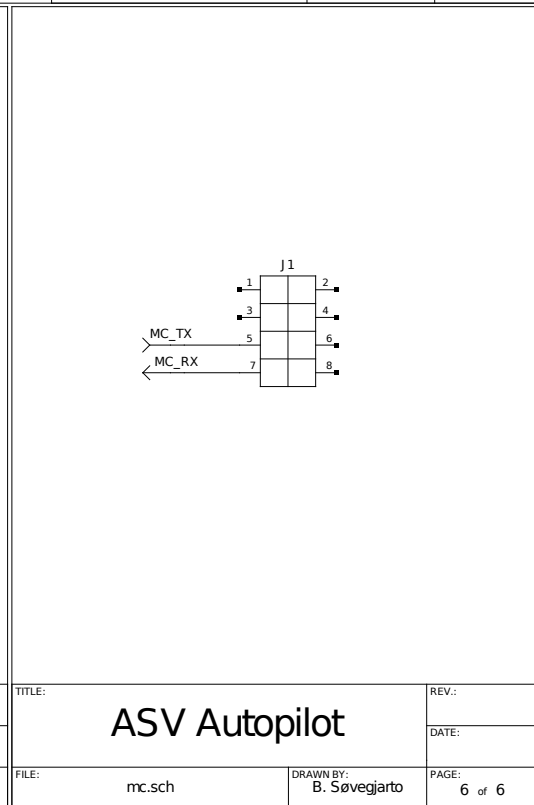
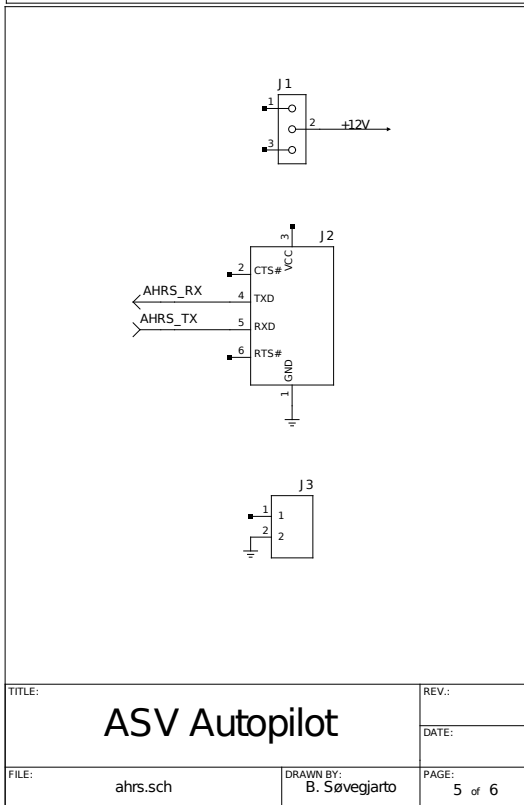
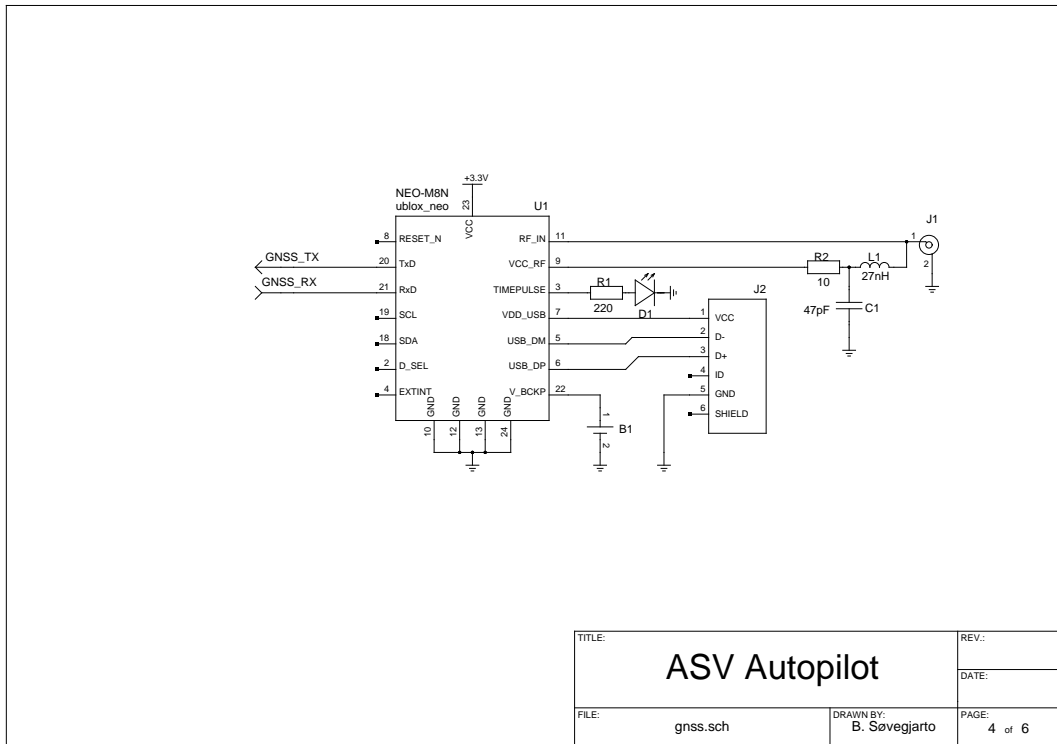
# Appendix O

## Autopilot schematics











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