

Message from ScalCom 2016 Program Chairs

Welcome to the 16th International Conference on Scalable Computing and Communications (ScalCom 2016). Parallel and distributed computing systems have undergone impressive change over recent years. The whole hardware industry shifted to both multicore and mobile platforms expanding the arena of communicating devices to an unprecedented scale. Scalability is becoming one of the central aspects of several disciplines. It is of paramount importance to assess all new technology developments in comparison with recent research achievements in the well-established areas of scalable computing and communications, from industry and the scientific community.

The 16th IEEE International Conference on Scalable Computing and Communications (ScalCom 2016) provides high-profile, leading-edge forum for researchers, engineers, and practitioners to present state-of-art advances and innovations in theoretical foundations, systems, algorithms, infrastructure, tools, testbeds, and applications for scalable computing and communications, as well as to identify emerging research topics and define the future.

ScalCom 2016 is organised by LAAS-CNRS, University of Toulouse and held on July 18-21, 2016. It is the next edition of the successful series, previously held in Beijing China (2015); Bali Indonesia (2014), Chengdu China (2013), Paphos Cyprus (2011), Bradford UK (2010), Dalian, China (2009), etc.

The 2016 edition revolves around five topics, which cover the currently areas of computer science and telecommunications for which scalability might represent a showstopper. They are 1) cloud computing, 2) GPU and accelerator computing, 3) modelling and simulation of complex systems, 4) extreme scale and exascale systems and 5) mobile computing and Internet of Things.

The growing number of interesting and significant research papers submitted to ScalCom demonstrates that the conference is becoming an ever more important international event in the field of parallel and distributed computing research. In particular, the Program Committee of this edition received 44 submissions from 23 countries.

On average each paper received 3.2 reviews, with no paper receiving fewer than two reviews. The result was the selection of 15 regular papers and 4 short papers for publication in these proceedings: the overall acceptance rate of full papers in the ScalCom 2016 is 34%. Among all ScalCom tracks, the “GPU, accelerators and novel architectures for Scalability-Rethinking” track was the most attractive one.

We wish to thank all who contributed to the success of the event: the authors, the members of the Programme Committee, the additional reviewers, all the Chairs and the Local Organisation at LAAS-CNRS. In particular we thank the Track Chairs: Patrizio Dazzi, Ewa Niewiadomska-Szynkiewicz, Jose Luis Vazquez-Poletti (Cloud computing and engineering); José Daniel García Sánchez (GPU, accelerators and novel architectures for Scalability-Rethinking); William Spataro, Marco Beccuti (Modelling and Simulation of Large Complex Systems); Francisco Javier Garcia Blas (Extreme scalable systems and applications); Pascal Berthou, Olivier Brun (Extreme scalable systems and applications)

We thank the Smart World Conference Organiser, Didier El Baz, for his efficient administration; Lisa O'Conner and the team at Conference Publishing Services for producing these proceedings; and all the Steering Committee members and other Chairs, for their guidance and support.

The PROGRAM CHAIRS OF ScalCom 2016

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