# Type-Based Analysis of Linear Communications 

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#### Abstract

This chapter presents a tool called Hypha for the type-based analysis of processes that communicate on linear channels. We describe the specification language used to model the systems under analysis (Section 9.1) followed by the typing rules on which the tool is based in order to verify two properties of systems, deadlock freedom and lock freedom (Section 9.2). In the final part of the chaper we illustrate the expressiveness and the limitations of the tool discussing a number of examples inspired by representative communication patterns using in parallel computing (Section 9.3) and then discuss closely related work (Section 9.4). The tool can be downloaded from the author's home page, the type system has been described by Padovani [18] and the corresponding reconstruction algorithms by Padovani et al. [19, 20].


### 9.1 Language

The Hypha specification language is a mildly sugared variant of the linear $\pi$ calculus [16] whose grammar is shown in Table 9.1. It makes use of booleans, integers, an infinite set $\mathfrak{K}$ of names, and comprises expressions and processes. The syntax shown here is somewhat simplified and tailored to the modeling of the examples discussed in this chapter. Hypha supports other forms that may be useful in the description of protocols with branching points and provide convenient syntactic sugar on top of those given in Table 9.1. The Hypha specification language is appropriate for modeling concurrent processes that exchange messages on private (or session) channels [10].

Table 9.1 Syntax of Hypha input language (partial)

| Notation | $\begin{array}{r} b \\ h, k, m, n \\ x, y, z, u, v \end{array}$ | $\begin{aligned} & \epsilon \\ & \epsilon \\ & \epsilon \end{aligned}$ | ```{true,false} Z x``` | Booleans Integers Names |
| :---: | :---: | :---: | :---: | :---: |
| Expression | $e$ | $\begin{gathered} ::= \\ \mid \\ \mid \\ \mid \end{gathered}$ | $\begin{aligned} & b \\ & n \\ & u \end{aligned}$ | Boolean constant <br> Integer constant Name |
| Process | P, Q | $\begin{aligned} & ::= \\ & \mid \\ & \mid \\ & \mid \\ & \mid \\ & \mid \\ & \mid \\ & \mid \end{aligned}$ | $\begin{aligned} & \} \\ & u!\left(e_{1}, \ldots, e_{n}\right) \\ & u ?\left(x_{1}, \ldots, x_{n}\right) \cdot P \\ & * u ?\left(x_{1}, \ldots, x_{n}\right) \cdot P \\ & \text { new } u_{1}, \ldots, u_{n} \text { in } P \\ & \text { if } e \text { then } P \text { else } Q \\ & P \mid Q \\ & \{P\} \end{aligned}$ | Idle process <br> Output <br> Input <br> Replicated input <br> Channel creation <br> Conditional execution <br> Parallel composition <br> Grouping |

For simplicity, in the provided syntax expressions are limited to values and comprise booleans, integers, and names. In the examples we will also make use of a few binary operators (such as + ) and relations (such as <). Processes comprise the usual terms of the $\pi$-calculus. The term \{ \} models the idle process that performs no actions. The term $u!\left(e_{1}, \ldots, e_{n}\right)$ models a process that outputs the tuple $\left(e_{1}, \ldots, e_{n}\right)$ on the channel $u$. We omit the parentheses when $n$ is 1 and write, for example, $u!n$ in place of $u!(n)$. We consider two forms of input processes. The term $u$ ? $\left(x_{1}, \ldots, x_{n}\right)$. P models an ephemeral input process that waits for one message from $u$, which is supposed to be an $n$-tuple, and then executes $P$ where $x_{i}$ is replaced by the $i$-th component of the tuple. The term * $u$ ? $\left(x_{1}, \ldots, x_{n}\right) . P$ models a persistent input process (also called service) that waits for an arbitrary number of messages. Each time a message is received, a new copy of $P$ (with the variables $x_{i}$ suitably instantiated) is spawned and the service makes itself available again for further receptions. The term new $u_{1}, \ldots, u_{n}$ in $P$ models a process creating new channels $u_{1}, \ldots, u_{n}$ with scope $P$. As usual in the $\pi$ calculus, the scope of a channel may broaden as a result of communications (scope extrusion). The terms if $e$ then $P$ else $Q$ and $P \mid Q$ respectively model conditional and parallel processes $P$ and $Q$. Finally, $\{P\}$ represents the same process as $P$ and is useful to disambiguate the way processes are

```
fibo?(n,c).
if n\leq0 then c!1
else new }a,b\mathrm{ in { fibo!(n-1,a) | fibo!(n-2,b) | a?(x).b?(y).c!(x+y) }
```

Listing 9.1 Modeling of the recursive Fibonacci function.
grouped. The notions of free and bound names of a process $P$, respectively denoted by $\mathrm{fn}(P)$ and $\mathrm{bn}(P)$, are as expected.

Example 1 (recursive Fibonacci function). Listing 9.1 shows the modeling of a service that computes the $n$-th number in the Fibonacci sequence. The service waits for invocations on channel fibo, each invocation consisting of the number $n$ and a channel $c$ on which the $n$-th Fibonacci number will be sent. The body of the service closely follows the familiar structure of the recursive definition of the Fibonacci sequence. When $n \leq 0$ the answer is immediately sent over $c$. When $n>0$, two new channels $a$ and $b$ are created, the service invokes itself twice to compute the $(n-1)$-th and ( $n-2$ )-th numbers in the sequence, and then the sum of the two partial results is sent over $c$.

As usual, the operational semantics of the language is defined in terms of a structural congruence relation, which identifies terms that are meant to have the same semantics, and a reduction relation that defines the proper computation steps. We omit the formal definition of structural congruence, which is essentially the same as in the $\pi$-calculus and includes commutativity and associativity laws for parallel composition and the usual laws for shrinking and extending the scope of channels. The second one is the least relation defined by the rules in Table 9.2 and closed by structural congruence and under the following reduction contexts:

$$
\text { Reduction context } \quad C \quad::=[]|C| P \mid \text { new } u_{1}, \ldots, u_{n} \text { in } C
$$

The fully-fledged formalization of the language also includes an evaluation relation for compound expressions [18].

To formulate the properties enforced by our typing discipline we introduce a few predicates that describe the pending communications of a process

Table 9.2 Operational semantics of processes

$$
\begin{aligned}
\hline u!\left(e_{1}, \ldots, e_{n}\right) \mid u ?\left(x_{1}, \ldots, x_{n}\right) \cdot P & \longrightarrow P\left\{e_{1} / x_{1}\right\} \cdots\left\{e_{n} / x_{n}\right\} \\
u!\left(e_{1}, \ldots, e_{n}\right) \mid * u ?\left(x_{1}, \ldots, x_{n}\right) \cdot P & \longrightarrow \\
\text { if true then } P \text { else } Q & \longrightarrow \\
\text { if false then } P \text { else } Q & \longrightarrow
\end{aligned}
$$

$P$ with respect to some channel $a$. We use the obvious extension of bound names we have introduced for processes to reduction contexts:

$$
\begin{aligned}
\operatorname{in}(a, P) & \stackrel{\text { def }}{\rightleftharpoons} P=C\left[a ?\left(x_{1}, \ldots, x_{n}\right) \cdot Q\right] \wedge a \notin \operatorname{bn}(C) \\
* \operatorname{in}(a, P) & \stackrel{\text { det }}{\Longleftrightarrow} P=C\left[* a ?\left(x_{1}, \ldots, x_{n}\right) \cdot Q\right] \wedge a \notin \operatorname{bn}(C) \\
\operatorname{out}(a, P) & \stackrel{\text { det }}{\rightleftharpoons} P=C\left[a!\left(e_{1}, \ldots, e_{n}\right)\right] \wedge a \notin \operatorname{bn}(C) \\
\operatorname{sync}(a, P) & \stackrel{\text { det }}{\Longleftrightarrow}(\operatorname{in}(a, P) \vee * \operatorname{in}(a, P)) \wedge \operatorname{out}(a, P) \\
\text { wait }(a, P) & \stackrel{\text { dif }}{\Longleftrightarrow}(\operatorname{in}(a, P) \vee \operatorname{out}(a, P)) \wedge \neg \operatorname{sync}(a, P)
\end{aligned}
$$

In words, in $(a, P)$ holds if there is a sub-process $Q$ within $P$ that is waiting for a message on channel $a$. Note that, by definition of reduction context, the input cannot be guarded by other actions. The condition $a \notin \mathrm{bn}(C)$ implies that $a$ is not captured by a binder in $\mathcal{C}$, i.e. it occurs free in $P$. The predicates out $(a, P)$ and in $(a, P)$ are similar, but they regard outputs and persistent inputs, respectively. Therefore, when in $(a, P)$ holds it means that there is a pending ephemeral input on $a$ and when out $(a, P)$ holds it means that there is a pending output on $a$. Then, $\operatorname{sync}(a, P)$ means that there are pending input/output operations on $a$, but a synchronization on $a$ is immediately possible. On the contrary, wait $(a, P)$ means that there is a pending output or a pending ephemeral input on $a$, but no immediate synchronization on $a$ is possible. Note the asymmetry in the way pending inputs and outputs trigger the wait predicate. We do not interpret $\%$ in $(a, P)$ as a pending input operation, meaning that we do not require a persistent input process to run infinitely often. At the same time, any pending output triggers the wait predicate, even when the output represents a service invocation.

We say that a process $P$ is deadlock free if every residual of $P$ that cannot reduce further contains no pending communications. Formally:

Definition 9.1. $P$ is deadlock free if whenever $P \longrightarrow{ }^{*}$ new $c_{1}, \ldots, c_{n}$ in $Q \rightarrow$ we have $\neg$ wait $(a, Q)$ for every $a$.

We say that a process $P$ is lock free if every residual $Q$ of $P$ in which there are pending communications can reduce further to a state in which such operations complete. Formally:
Definition 9.2. $P$ is lock free if whenever $P \longrightarrow{ }^{*}$ new $c_{1}, \ldots, c_{n}$ in $Q$ and wait $(a, Q)$ there is $R$ such that $Q \longrightarrow{ }^{*} R$ and $\operatorname{sync}(a, R)$.

In Definitions 9.1 and 9.2 , it is important to universally quantifiy over the topmost channel restrictions in a residual of $P$ so that the notion of (dead)lock freedom for $P$ concerns both free and bound channels of $P$.

It is easy to prove that lock freedom implies deadlock freedom [19]. On the other hand, there exist deadlock-free processes that are not lock free, as shown in the example below.

## Example 2 (deadlock-free, locked process). The process

```
new c in { *forever?(x).forever!x | forever!c | c!42 }
```

is deadlock free but not lock free. Indeed, the process reduces forever, but no input operation is ever performed on the $c$ channel. As a result, the pending output on $c$ cannot be completed.

### 9.2 Type System

In this section we describe a type system that enforces the properties introduced in Section 9.1: well-typed processes are guaranteed to be (dead)lock free. The tool Hypha then implements a type reconstruction algorithm for this type system and finds a typing derivation for a given process, provided there is one. Note that, while the type reconstruction algorithm is complete with respect to the type system, the type system itself is not complete with respect to (dead)lock freedom: there exist (dead)lock free processes that are ill typed according to the type system. In fact, it is undecidable in general to establish whether a $\pi$-calculus process is (dead)lock free, hence the type system is necessarily conservative.

Polarities, qualifiers, and types are defined by the following grammar:


Types comprise the base types bool and int of boolean and integer values, channel types $\kappa[\bar{t}]^{q}$, and the usual forms $\alpha$ and $\mu \alpha . t$ for representing recursive types. A channel type $\kappa[\bar{t}]^{\mathfrak{q}}$ consists of:

- A polarity $p$ specifying the operations allowed on the channel: $\emptyset$ means none, $\{?\}$ means input, $\{$ !\} means output, and $\{?,!\}$ means both. We will abbreviate $\{?,!\}$ with \# and $\{?\}$ and $\{!\}$ with ? and !, respectively.
- A sequence $t_{1}, \ldots, t_{n}$ of types, abbreviated as $\bar{t}$, specifying that each message exchanged over the channel is an $n$-tuple of values where the $i$-th value has type $t_{i}$.
- A qualifier q specifying how many times the channel can or must be used according to its polarity. The qualifier * means that the channel can
be used any number of times. A qualifier of the form ${ }_{k}^{h}$ means that the channel can only be used once. In this case, $h$ and $k$ are respectively the level and the tickets associated with the channel: channels with smaller levels must be used before channels with greater levels; a channel with $k$ tickets can be sent as a message on another channel at most $k$ times.
We require that, in a recursive type $\mu \alpha$.t, the type variable $\alpha$ can only occur guarded by a channel type constructor. For example, $\mu \alpha . \alpha$ is illegal while $\mu \alpha . ?[\alpha]^{*}$ is allowed. We identify two types modulo renaming of bound type variables and if they have the same infinite unfolding, that is if they denote the same (regular) tree [4]. In particular, we have $\mu \alpha . t=$ $t\{\mu \alpha . t / \alpha\}$.

Qualifiers distinguish service channels (with qualifier *) from linear channels (with qualifiers of the form $h, k$ ). Service channels are used for modeling persistent services, such as fibo in Listing 9.1 and forever in Example 2. Linear channels are used for modeling private communications between pairs of processes. Examples of linear channels are $a$ and $b$ in Listing 9.1 and $c$ in Example 2. The fact that a linear channel can be used for one communication only is not a limitation in practice. Structured private conversations made of arbitrarily many communications can be encoded using a continuation-passing style [5,12]. We will see several examples of this technique at work in the rest of the chapter. On the other hand, knowing that a channel is linear provides some guarantees on the fact that the channel will not be discarded without being used. This is a necessary (although not sufficient) condition for guaranteeing that communications on linear channels eventually occur.

The level of a linear channel measures the urgency with which the channel must be used: the lower the level is, the sooner the channel must be used. We extend this notion from linear channels to arbitrary types. To compute the level of a type, we define an auxiliary function $|\cdot|$ such that $|t|$ is an element of $\mathbb{Z} \cup\{\perp, T\}$ where $\perp<n<T$ for every $n \in \mathbb{Z}$ :

$$
|t| \stackrel{\text { def }}{ } \stackrel{\perp}{\perp} \begin{array}{ll}
\perp & \text { if } t=p[s]^{*} \text { and } ? \in p  \tag{9.1}\\
n & \text { if } t=p[s]_{m}^{n} \text { and } p \neq \emptyset \\
\top & \text { otherwise }
\end{array}
$$

According to this definition, service channels with input capability have the lowest level $\perp$ (first equation). This way we guarantee input receptiveness of services, for the use of a service channel with input capability cannot be postponed by any means. Base values, service channels with output
capability, and linear channels with no capabilities have the highest level T (last equation) because they do not affect (dead)lock freedom in any way. Linear channels with non-empty polarity must be used according to their level (second equation). We say that a (value with) type $t$ is unlimited if $|t|=\mathrm{T}$, that it is linear if $|t| \in \mathbb{Z}$, that it is relevant if $|t|=\perp$.

We define another auxiliary function $\$_{k}^{h}$ to shift levels and tickets: $\$_{k}^{h} t$ is the same as $t$ except when $t$ is a linear channel. In this case, the level/tickets in $t$ are transposed by $h$ and $k$ respectively. Formally:

$$
\$_{k}^{h} t \stackrel{\text { def }}{=} \begin{cases}p[s]_{m+k}^{n+h} & \text { if } t=p[s]_{m}^{n} \text { and } p \neq 0  \tag{9.2}\\ t & \text { otherwise }\end{cases}
$$

Note that positive/negative shifting of levels corresponds to decreasing/ increasing the urgency with which a value of a given type must be used.

The type system makes use of type environments $\Gamma$ to keep track of the type of names occurring free in processes. A type environment is a finite map from names to types written $u_{1}: t_{1}, \ldots, u_{n}: t_{n}$. We write dom( $\Gamma$ ) for the domain of $\Gamma$, namely the set of names for which there is an association in $\Gamma$, and $\Gamma, \Gamma^{\prime}$ for the union of $\Gamma$ and $\Gamma^{\prime}$ when $\operatorname{dom}(\Gamma) \cap \operatorname{dom}\left(\Gamma^{\prime}\right)=\emptyset$. We also need a more general way of composing type environments that takes into account the level and tickets of linear channel types and the fact that we can split channel types by distributing different capabilities to different processes. Following [16], we define a partial operator + between types, thus:

$$
\begin{align*}
t+t & =t & & \text { if } t \text { is unlimited } \\
p[t]^{*}+q[t]^{*} & =(p \cup q)[t]^{*} & &  \tag{9.3}\\
p[t]_{h}^{n}+q[t]_{k}^{n} & =(p \cup q)[t]_{h+k}^{n} & & \text { if } p \cap q=\emptyset
\end{align*}
$$

Informally, unlimited types combine with themselves without restrictions. The combination of two unlimited/relevant channel types has the union of their polarities. Two linear channel types can be combined only if they have the same level and disjoint polarities, and their combination has the union of their polarities and the sum of their tickets. We extend the partial operator + to type environments:

$$
\begin{gather*}
\Gamma+\Gamma^{\prime} \stackrel{\text { def }}{=} \Gamma_{,} \Gamma^{\prime} \quad \text { if } \operatorname{dom}(\Gamma) \cap \operatorname{dom}\left(\Gamma^{\prime}\right)=\emptyset \\
(\Gamma, u: t)+\left(\Gamma^{\prime}, u: s\right) \stackrel{\text { def }}{=}\left(\Gamma+\Gamma^{\prime}\right), u: t+s \tag{9.4}
\end{gather*}
$$

Note that $\Gamma+\Gamma^{\prime}$ is undefined if there is $u \in \operatorname{dom}(\Gamma) \cap \operatorname{dom}\left(\Gamma^{\prime}\right)$ such that $\Gamma(u)+\Gamma^{\prime}(u)$ is undefined and that $\operatorname{dom}\left(\Gamma+\Gamma^{\prime}\right)=\operatorname{dom}(\Gamma) \cup \operatorname{dom}\left(\Gamma^{\prime}\right)$. We let $|\Gamma|$ denote the lowest level of the types in the range of $\Gamma$, that is

$$
\begin{equation*}
|\Gamma| \stackrel{\text { def }}{=} \min \{|\Gamma(u)| \mid u \in \operatorname{dom}(\Gamma)\} \tag{9.5}
\end{equation*}
$$

We say that $\Gamma$ is unlimited if $|\Gamma|=T$.
We now turn our attention to the typing rules, which are meant to enforce the following properties of channels:

1. a service channel with input capability must be used by a replicated input process (we refer to this condition as input receptiveness);
2. a linear channel cannot be discarded until both its input and output capabilities have been used (we refer to this condition as linearity);
3. an operation on a linear channel cannot block channels with lower or equal level (with linearity, this condition guarantees deadlock freedom);
4. the use of a linear channel cannot be postponed forever (with linearity and deadlock freedom, this condition guarantees lock freedom).
The typing rules allow us to derive judgments of the form $\Gamma \vdash e: t$, stating that $e$ is well typed in the environment $\Gamma$ and has type $t$, and of the form $\Gamma \vdash_{k} P$, stating that $P$ is well typed in the environment $\Gamma$. The parameter $k \in\{0,1\}$ intuitively represents the "cost" for sending a channel over another channel: each output operation consumes $k$ tickets from the channels being sent. The type system is designed in such a way that a well typed, closed process $P$ is guaranteed to be deadlock free if $\emptyset \vdash_{0} P$ and lock free if $\emptyset \vdash_{1} P$.

Expressions. Because the model has an extremely simple expression language, the corresponding typing rules, shown below, are fairly obvious and extend easily to more complex expressions:

$$
\begin{array}{lll}
\text { [T-BOOL] } & {[\text { T-INT }]} & {[\text { T-NAME }]} \\
\emptyset \vdash b: \text { bool } & \emptyset \vdash n: \text { int } & u: t \vdash u: t
\end{array}
$$

The important remark concerning these rules is that the type environment used for typing an expression $e$ only contains associations for the free names occurring in $e$. This makes sure that no linear or relevant resource (namely, channels that must be used at least once) is left unused. Later on we will discuss a structural rule that allows us to discard resources whose use is not necessary in order to enforce (dead)lock freedom.

Idle and grouped processes. Rule [T-IDLE] states that the idle process is well typed in the empty environment only:

$$
[\text { T-IDLE }] \quad \emptyset \vdash_{k}\{ \}
$$

For example, the judgment $a:$ ! [int $]_{m}^{n} \vdash_{k}\{ \}$ is not derivable, because the linear channel $a$ is supposed to be used for one output operation, whereas the process $\}$ does nothing. This typing rule illustrates a key trait of the type system, making sure that linear channels with pending capabilities are not discarded. If this were not the case, one could write processes like

```
new }a\mathrm{ in }a\mathrm{ ?(x)
```

which are stuck waiting for messages that will never arrive.
The typing rule for a grouped process is simple and does not enforce any constraint other than typability of the process itself:

$$
\text { [T-GROUP] } \frac{\Gamma \vdash_{k} P}{\Gamma \vdash_{k}\{P\}}
$$

Outputs. Rule [т-оut] is used for typing output operations on linear channels:

$$
[\mathrm{T} \text {-out }] \quad u:![t]_{n}^{m}+v: \$_{k}^{n} t \vdash_{k} u!v \quad 0<|t|
$$

First of all, the channel $u$ being used for the output must indeed have capability !. The type of the message $v$ must be $t$ (as specified in the type of the channel $u$ ) except that its level is shifted by $m$ and its tickets are shifted by $k$. The shifting of the level means that the level of $t$ in! $[t]_{n}^{m}$ is relative to $m$. This, together with the side condition $0<|t|$, makes sure that the level of $v$ (the channel being sent as a message) is strictly greater than the level of $u$. The shifting of the tickets in $t$ accounts for the fact that, by sending $v$ as a message, one ticket from $v$ is consumed. Note that this is necessary only in the judgments for lock freedom $(k=1)$. Below are a few examples:

- The judgment $a$ : ! [? [int] $\left.0_{0}^{1}\right]_{0}^{2}, b:$ ? [int] $1_{1}^{3} \vdash_{1} a!b$ is derivable because ? [int] ${ }_{1}^{3}=\$_{1}^{2}$ ? [int] ${ }_{0}^{1}$. Note in particular that the channel to be sent on $a$ must have no tickets, which is in fact what happens to $b$ after 1 ticket is consumed from its type before it travels on $a$.
- The judgment $a:!\left[![\mathrm{int}]_{0}^{1}\right]_{0}^{0}, b:![\mathrm{int}]_{0}^{1} \vdash_{1} a!b$ is not derivable because $b$ has no tickets and so it cannot travel on $a$.
- Let $t=?[t]_{0}^{0}$ and observe that $\#[t]_{0}^{1}=![t]_{0}^{1}+?[t]_{0}^{1}$. The judgment $a: \#[t]_{0}^{1} \vdash_{0} a!a$ is not derivable, despite the message $a$ has the "right" type ? $[t]_{0}^{1}=\$_{0}^{1} t$, because the condition $0<|t|=0$ is not satisfied. A process like $a!a$ is deadlocked because the occurrence of $a$ that is meant to be used for matching this output operation is the very message sent on $a$ itself.
- The judgment $a:!\left[?[\mathrm{int}]^{*}\right]_{0}^{0}, b:$ ?[int]* $\vdash_{0} a!b$ is not derivable because $0 \nless \mid$ ? [int] ${ }^{*} \mid=\perp$. A service channel with input capability such as $b$ cannot be sent as a message to guarantee input receptiveness.
Rule [t-out*] is used for typing outputs on unlimited channels.

$$
\left[\mathrm{T} \text {-ouT*] } \quad u:![t]^{*}+v: \$_{k}^{n} t \vdash_{k} u!v \quad \perp<|t|\right.
$$

There are two key differences between [t-out] and [t-out*]. First, the condition $\perp<|t|$, where $t$ is the type of $v$ means that only unlimited channels with input capability cannot be communicated. Second, the type of $v$ does not need to match exactly the type $t$ in the channel type of $u$, but its level can be shifted by an arbitrary amount $n$. This is the technical realization of polymorphism. In particular, each distinct output on $u$ can shift the type of the message by a different amount, therefore allowing polymorphic recursion. We will often use this feature in the extended examples in the second half of this chapter.

Both [r-out] and [r-out*] generalize easily to an arbitrary number of message arguments. As an example, the former rule can be generalized as follows:
$u: ?\left[t_{1}, \ldots, t_{h}\right]_{n}^{m}+v_{1}: \$_{k}^{m} t_{1}+\cdots+v_{h}: \$_{k}^{m} t_{h} \vdash_{k} u!\left(v_{1}, \ldots, v_{h}\right) \quad 0<\left|t_{i}\right|$
Inputs. Rule [T-IN] is used for typing linear input operations:

$$
[\mathrm{T}-\mathrm{IN}] \quad \frac{\Gamma, x: \$_{0}^{n} t \vdash_{k} P}{\Gamma+u: ?[t]_{m}^{n} \vdash_{k} u ?(x) . P} \quad n<|\Gamma|
$$

The channel $u$ must have type $?[t]_{m}^{n}$ and the continuation $P$ is typed in an environment where the input polarity of $u$ has been removed and the received message $x$ has been added. The level of the type of $x$ is shifted by $n$, consistently with the relative interpretation of levels of message types that we have already discussed for [т-оит]. The tickets of $u$ are irrelevant since $u$ is used for an input operation, not as the content of a message. The condition $n<|\Gamma|$ ensures that the input on $u$ does not block operations on other channels with equal or lower level. In particular, $\Gamma$ cannot contain service channels with input capability. Below are some typical examples of ill-typed processes that violate this condition:

- $a:$ ? [int $]_{0}^{1}, b:![\mathrm{int}]_{0}^{0} \vdash_{k} a ?(x) . b!x$ is not derivable because $1 \nless 0$ : the input on $a$ blocks the output on $b$, but $b$ has lower level than $a$;
- $a:$ \# [int] ${ }_{0}^{h} \vdash_{k} a ?(x) . a!x$ is a degenerate case of the previous example, where the input on $a$ blocks the very output that should synchronize with it. Note that this process is well-typed in the traditional linear $\pi$-calculus [16].
- $a$ : ? [int] ${ }_{0}^{h}, c:$ ? [int] ${ }^{*} \vdash_{k} a$ ? $(x) . * c ?(y)$ is not derivable because $\mid$ ?[int]* $\mid=\perp$. To guarantee input receptiveness, we require that replicated inputs cannot be guarded by other operations.
Rule [ $\mathrm{T}-\mathrm{IN}{ }^{*}$ ] is used for typing replicated input operations corresponding to persistent services:

$$
[\mathrm{T}-\mathrm{IN} *] \quad \frac{\Gamma, x: t \vdash_{k} P}{\Gamma+u: ?[t]^{*} \vdash_{k} * u ?(x) \cdot P} \quad \top \leq|\Gamma|
$$

This rule differs from [T-IN] in three important ways. First of all, $u$ must be a service channel with input capability. Second, the side condition $T \leq|\Gamma|$ makes sure that the environment $\Gamma$ used for typing the body of the service is unlimited. This is because the service can be invoked an arbitrary number of times, hence its body cannot contain linear resources. Third, it may be the case that $u \in \operatorname{dom}(\Gamma)$, because $?[t]^{*}+![t]^{*}=\#[t]^{*}$ according to (9.3) and ! $[t]^{*}$ is unlimited. This means that services may recursively invoke themselves. We use this feature in several examples, including Example 1.

As for [T-out] and [T-out*], both [T-IN] and [T-IN*] can be easily generalized to handle arbitrary tuples of message arguments.

Conditional and parallel processes. The typing rule for conditional processes is shown below:

$$
[\mathrm{T}-\mathrm{IF}] \quad \frac{\Gamma_{1} \vdash e: \text { bool } \quad \Gamma_{2} \vdash_{k} P \quad \Gamma_{2} \vdash_{k} Q}{\Gamma_{1}+\Gamma_{2} \vdash_{k} \text { if } e \text { then } P \text { else } Q}
$$

As usual, the condition must have type bool and + is used for combining the type environments used in different parts of the process. Note that both branches must be typable using the same type environment, meaning that the linear channels occurring in $P$ and $Q$ must be used in the same order. For example, the judgment

$$
a: ?[\text { int }]_{0}^{1}, b: ?[\text { int }]_{0}^{2} \vdash_{k} \text { if } e \text { then } a ?(x) \cdot b ?(y) \text { else } b ?(y) \cdot a ?(x)
$$

is not derivable because the else branch uses the two linear channels $a$ and $b$ in an order not allowed by their levels.

The typing rule for parallel compositions is shown below:

$$
\left[\text { [T-PAR] } \frac{\Gamma_{1} \vdash_{k} P \quad \Gamma_{2} \vdash_{k} Q}{\Gamma_{1}+\Gamma_{2} \vdash_{k} P \mid Q}\right.
$$

Because of the definition of the + operator, which combines the types of linear channels only provided that such channels have the same level, the order in which linear channels are used in the branches of the parallel composition must be consistent. For example, the judgment

$$
a: \#[\operatorname{int}]_{0}^{1}, b: \#[\mathrm{int}]_{0}^{2} \vdash_{k} a ?(x) \cdot b!x \mid b ?(y) \cdot a!y
$$

cannot be derived because the second branch violates the side condition of $[\mathrm{T}-\mathrm{N}]$ requiring $b$ to have a strictly smaller level than $a$. Indeed, the whole process is deadlocked.

Channel creation. Restrictions can be used to introduce both linear and service channels. In the former case, the typing rule is

$$
[\mathrm{T}-\mathrm{NEW}] \frac{\Gamma, a: p[t]_{m}^{n} \vdash_{k} P}{\Gamma \vdash_{k} \operatorname{new} a \operatorname{in} P} \quad p \in\{0, \#\}
$$

Note that the rule "guesses" the right level and number of tickets that are necessary for typing $P$. The polarity is either \#, meaning that $a$ must be used for both one input and one output operation in $P$, or $\mathbb{0}$, meaning that $a$ is a depleted channel that is not supposed to be used at all in $P$. The reason why the typing rule accounts for this possibility is purely technical and is necessary to prove that process reductions preserve typing [18].

The typing rule for introducing a service channel is essentially the same:

$$
\left[\begin{array}{l}
\text { [-NEw* }]
\end{array} \frac{\Gamma, a: \#[t]^{*} \vdash_{k} P}{\Gamma \vdash_{k} \text { new } a \text { in } P}\right.
$$

Unlike linear channels, the capability of restricted unlimited channels is always \#. Since an unlimited channel $a$ with input capability must be used ( $c f .[T-\mathrm{IN} *])$, this guarantees that there is always a service waiting for invocations on $a$. On the other hand, a service channel with output capability does not have to be used (cf. (9.1)), therefore imposing that the capability of $a$ is \# does not mandate invocations on $a$.

Unused resources. The following structural rule provides a limited form of weakening whereby it is possible to add unused resources in a type environment, provided that these resources have an unlimited type:

$$
\text { [T-WEAK] } \quad \frac{\Gamma \vdash_{k} P}{\Gamma+\Gamma^{\prime} \vdash_{k} P} \quad \mathrm{~T} \leq\left|\Gamma^{\prime}\right|
$$

For example, both $a: \otimes[\text { int }]_{n}^{m} \vdash_{k}\{ \}$ and $x:$ int $\vdash_{k}\{ \}$ are derivable, because the type environments only contain resources that impose no usage and therefore can be discarded using [T-weak]. On the contrary, neither $a$ : ! [int] ${ }_{n}^{m} \vdash_{k}\{ \}$ nor $a:$ ? [int]* $\vdash_{k}$ are derivable.

The type system refines the one for the linear $\pi$-calculus [16], hence all the properties of the linear $\pi$-calculus (partial confluence, linear usage of channels, etc.) are still guaranteed. The added value is that the type system also guarantees deadlock/lock freedom.

Theorem 9.3. The following properties hold:

1. If $\emptyset \vdash_{0} P$, then $P$ is deadlock free.
2. If $\emptyset \vdash_{1} P$, then $P$ is lock free.

Example 3 (recursive Fibonacci function). Let $P$ be the process shown in Listing 9.1. Then it is possible to derive

$$
\text { fibo : \# [int, ! [int } \left.]_{0}^{0}\right]^{*} \vdash_{k} P
$$

if and only if $k=0$. It is not possible to find a derivation for $k=1$ since the type system cannot establish an upper bound to the time after which the continuation channel $c$ will be used in an invocation fibo! $(n, c)$. In fact, such upper bound depends on $n$ and on the fact that the recursion of the fibo service is well-founded This latter property requires a kind of analysis that eludes the capabilities of the type system.

### 9.3 Extended Examples

### 9.3.1 Fibonacci Stream Network

In this section we discuss an alternative modeling of system that computes the sequence of Fibonacci numbers and that is an example of stream network, that is a network of communicating processes that exchange infinite


Figure 9.1 Graphical representation of the Fibonacci stream network [8, 22].
sequences (streams) of messages. Figure 9.1 depicts the Fibonacci stream network $[8,22]$ where the boxes represent processing units and the arrows represent communication channels.

Each source $(n)$ process sends $n$ on the outgoing channel followed by each message received from the incoming channel. The copy process forwards each received message on each of its two outgoing channels. Finally, add sends on the outgoing channel the sum of corresponding messages received from the two incoming channels. Overall, it is easy to see that the stream of messages flowing on channel $a$ corresponds to the Fibonacci sequence $1,1,2,3,5, \ldots$.

The modeling of the Fibonacci stream network in Hypha's input language is shown in Listing 9.2. There is a service for each of the boxes in Figure 9.1, with source that makes use of an auxiliary service link that acts as a persistent message forwarder. The network itself is created on line 5 , where the services are invoked and connected by the channels $a$ through $e$. The most distinctive aspect of the modeling is the use of continuation passing for the representation of message streams: each channel that connects two combinators is in fact a linear channel (a channel that is meant to be used for one communication only); whenever a message is exchanged on the channel, the payload is paired with a fresh (linear) channel on which the subsequent message will be exchanged. This pattern can be clearly observed in the definitions of link, add, and copy. To improve readability, hereafter we write $\bar{x}$ for a channel name that is meant to represent the continuation of $x$.

```
*link?(x,y).x? (v,\overline{x}).source! (v,\overline{x},y)
```





```
source! (1,e,a) | copy! (a,b,c) | source! (0,b,d) | add! (d,c,e) }
```

Listing 9.2 Term representation of the Fibonacci stream network [8,22].

Hypha infers the following types for the channels used in the system

```
source : \# [int, ? \(\left[\text { int, } \mu \alpha . ?[\text { int, } \alpha]_{2}^{3}\right]_{1}^{2}\), ! \(\left.\left[\text { int, } \mu \beta \text {.? }[\text { int, } \beta]_{1}^{3}\right]_{0}^{0}\right]^{*}\)
    link : \#[?[int, \(\mu \alpha\). ? \(\left.[\text { int, } \alpha]_{2}^{3}\right]_{0}^{0}\), ! \(\left.\left[\text { int, } \mu \beta \text {. ? }[\text { int, } \beta]_{1}^{3}\right]_{1}^{1}\right]^{*}\)
        add : \#[? [int, \(\left.\mu \alpha . ?[\text { int, } \alpha]_{1}^{3}\right]_{0}^{0}\), ? \(\left[\text { int, } \mu \beta . ?[\text { int, } \beta]_{1}^{3}\right]_{0}^{1}\),
            ! \(\left.\left[\text { int, } \mu \gamma . ?[\text { int, } \gamma]_{2}^{3}\right]_{0}^{2}\right]^{*}\)
    copy : \# \(\left[\right.\) ? \(\left[\text { int, } \mu \alpha \text {. ? }[\text { int, } \alpha]_{1}^{3}\right]_{0}^{0}\),
            ! \(\left.[\text { int, } \mu \beta \text {.? [int, } \beta]_{2}^{3}\right]_{0}^{2}\), ! [int, \(\left.\left.\mu \gamma . ?[\text { int, } \gamma]_{1}^{3}\right]_{0}^{1}\right]^{*}\)
    \(a, d: \#\left[\text { int }, \mu \alpha \cdot ?[\text { int }, \alpha]_{1}^{3}\right]_{2}^{0}\)
    \(b, e: \#\left[\text { int }, \mu \alpha . ?[\text { int }, \alpha]_{2}^{3}\right]_{3}^{2}\)
        \(c: \#\left[\text { int }, \mu \alpha . ?[\text { int }, \alpha]_{1}^{3}\right]_{2}^{1}\)
```

confirming that the system is well typed and therefore lock free. In particular, Theorem 9.3(2) allows us to deduce that every number in the sequence of Fibonacci is computed in finite time. We make some observations concerning the inferred channel types: first, Hypha correctly distinguishes between the channels representing services (such as copy and source) from the linear channels that connect them (such as $a$ and $b$ ). Second, the levels associated with linear channels give hints concerning the order of synchronizations in the system. The synchronizations on $a$ and $d$ (with level 0 ) happen first, followed by that on $c$ (level 1), and then by that on $e$ (level 2). Note however, that the total order on levels does not necessarily reflect the partial order that represents dependencies between channels. For example, $b$ has a strictly greater level than $c$ and yet the synchronizations on these two channels may happen in any order. Concerning the ticket annotations, note that all linear channels require at least 2 tickets because they are used to connect 2 services. For example, $a$ connects source(1) and copy. By contrast, $b$ and $e$ need one more ticket because they are also forwarded by source to link.

### 9.3.2 Full-Duplex and Half-Duplex Communications

Many parallel algorithms use batteries of processes arranged in a grid that iteratively update array elements and communicate with processes assigned to neighbor elements (Figure 9.2). Processes may communicate according to one out of two modalities: when communication is full-duplex, processes simultaneously send messages to each other; when communication is half-duplex, only one message travels between two processes at any


Figure 9.2 Graphical representation of a $4 \times 3$ bi-dimensional stencil.
moment in time. Correspondingly, we can model the dotted grid fragment in Figure 9.2 as

$$
\begin{equation*}
e!(0, a, b) \mid f!(1, b, a) \tag{9.6}
\end{equation*}
$$

where $e$ and $f$ are service channels defined as either

$$
\text { *full? }(n, x, y) \text {.new } \bar{x} \text { in }\{x!(n, \bar{x}) \mid y ?(m, \bar{y}) . \text { full }!(n+m, \bar{x}, \bar{y})\}
$$

in case of full-duplex communication or as

$$
\text { *half? }(n, x, y) \cdot y ?(m, \bar{y}) \text {.new } \bar{x} \text { in }\{x!(n, \bar{x}) \mid \text { half! }(n+m, \bar{x}, \bar{y})\}
$$

in case of half-duplex communication. In both cases, $x$ is used for sending messages to, and $y$ for receiving messages from, a neighbor process. Each message sent on $x$ carries a payload $n$ as well as a continuation channel $\bar{x}$ used for the communication at the next iteration. Symmetrically, each message received from $y$ contains the neighbor's payload $m$ and a continuation $\bar{y}$. The difference between full and half is that, in the latter case, the sender waits for the message from its neighbor before sending its own.

Overall there are 4 possible configurations of the system (9.6) obtained by instantiating $e$ and $f$ with either full or half. It is easy to see that a configuration is lock free as long as at least one of $e$ or $f$ is instantiated with full. Indeed, Hypha infers the types

$$
a, b: \#\left[\text { int }, \mu \alpha \cdot ?[\text { int }, \alpha]_{1}^{1}\right]_{2}^{0}
$$

when $e=f=$ full and the types

$$
a: \#\left[\text { int }, \mu \alpha . ?[\text { int }, \alpha]_{1}^{2}\right]_{2}^{1} \quad b: \#\left[\text { int }, \mu \alpha \cdot ?[\text { int }, \alpha]_{1}^{2}\right]_{2}^{0}
$$

when $e=$ half and $f=$ full. The case when $e=$ full and $f=$ half is symmetric, while the one when $e=f=$ half is ill typed for deadlock freedom and, therefore, for lock freedom as well.


Figure 9.3 Master-worker (left) and producer-consumer (right).

### 9.3.3 Load Balancing

Figure 9.3 shows two network topologies aimed at taking advantage of parallelism by distributing multiple tasks to independent workers. They differ in that in the master-worker topology the same process that produces tasks is also the one that collects the results, whereas in the producer-consumer topology (sometimes called "farm") producer and consumer are different processes. The distinction between the two topologies has important consequences at the communication layer since the channels are bi-directional in the former network and uni-directional in the latter.

Listings 9.3 and 9.4 show the modeling of the network topologies in Figure 9.3, both of which are well-typed according to the lock freedom type system implemented in Hypha. For the master-worker network, Hypha infers the types

$$
\begin{aligned}
& a: \\
& b: \text { \# }\left[\text { int, } \mu \alpha .!\left[\text { int, }, \mu \alpha \cdot[\text { int, } \alpha]_{1}^{2}\right]_{0}^{1}\right]_{2}^{0} \\
& 1
\end{aligned}
$$

```
\{ *master? \((n, x, y)\).
    new \(\bar{x}, \bar{y}\) in \(\{x!(n, \bar{x})|y!(n+1, \bar{y})| \bar{x} ?(v, \overline{\bar{x}}) . \bar{y} ?(w, \overline{\bar{y}})\). master ! \((n+2, \overline{\bar{x}}, \overline{\bar{y}})\}\)
*worker? \((n, z) \cdot z ?(m, \bar{z})\).new \(\overline{\bar{z}}\) in \(\bar{z}!(m\) mod \(n, \overline{\bar{z}})\).worker! \((n, \overline{\bar{z}})\)
master! ( \(0, a, b\) ) | worker! (2,a) | worker! (3,b) \}
```

Listing 9.3 Term representation of master-worker (half-duplex channels).


```
*consumer?(x,y).x?(v,\overline{x}).y?(w,\overline{y}).{ print!v | print!w | consumer! (\overline{x},\overline{y})}
*worker?(n,x,y).x?(m,\overline{x}).new \overline{y}}\mathrm{ in { y!(m mod n, 文) | worker! ( }n,\overline{x},\overline{y})
producer!(0,a,b) | worker!(2,a,c) | worker!(3,b,d) | consumer!(c,d) }
```

Listing 9.4 Term representation of producer-consumer.
which describe a communication protocol whereby the master sends a task (represented as an integer number) to each worker along with a continuation channel. By using this continuation channel, the worker will answer back with the processed task (again represented as an integer number) and another continuation that the master uses for starting another iteration.

For the producer-consumer network Hypha infers the types

$$
\left.\left.\begin{array}{rlll}
a & : & \text { \#[int, } \left.\mu \alpha . ?[\text { int }, \alpha]_{1}^{2}\right]_{2}^{0} & c
\end{array}\right) \text { \#[int, } \mu \alpha . ?[\text { int }, \alpha]_{1}^{2}\right]_{2}^{1}
$$

again confirming that the network is lock free.

### 9.3.4 Sorting Networks

Figure 9.4 depicts an example of so-called sorting network, that is a network of communicating processes whose overall effect is that of sorting an input vector of fixed size, 6 in this case. The network is made of two different kinds of processes: comparators (the rectangular boxes) input two values and possibly swap them if the first happens to be larger than the second; buffers (the square boxes) simply forward the input value. The input values go through three identical phases; in each stage, the odd-indexed inputs and then the even-indexed inputs are compared to, and possibly swapped with, their successor.

The sorting network in Figure 9.4 is modeled in the linear $\pi$-calculus as shown in Listing 9.5. Note the use of auxiliary services odd and even corresponding to the two sub-phases of each phase and linked together by


Figure 9.4 Graphical representation of an odd-even 6-input sorting network.

```
{ *compare?(x,y,l,h).
    new z in { x?(v).z!v | y?(w).z?(v).if v<w then l!v | h!w else l!w | h!v}
*buffer?(x,y).x?(v).y!v
*provide?(x,n).x!n
*consume? (x,n) . x?(v).print! (n,v)
*even?( }\mp@subsup{x}{1}{},\mp@subsup{x}{2}{},\mp@subsup{x}{3}{},\mp@subsup{x}{4}{},\mp@subsup{x}{5}{\prime},\mp@subsup{x}{6}{},\mp@subsup{y}{1}{},\mp@subsup{y}{2}{},\mp@subsup{y}{3}{},\mp@subsup{y}{4}{},\mp@subsup{y}{5}{\prime},\mp@subsup{y}{6}{})
```



```
*odd?( }\mp@subsup{x}{1}{},\mp@subsup{x}{2}{},\mp@subsup{x}{3}{},\mp@subsup{x}{4}{},\mp@subsup{x}{5}{},\mp@subsup{x}{6}{},\mp@subsup{y}{1}{},\mp@subsup{y}{2}{},\mp@subsup{y}{3}{},\mp@subsup{y}{4}{},\mp@subsup{y}{5}{\prime},\mp@subsup{y}{6}{})
    { buffer! (x, , y1) | compare! (x, , x , , y2, 午)
    | buffer! (x, (x,y6) | compare! (x, (x, x5, y4, , y5) }
    *phase?( }\mp@subsup{x}{1}{},\mp@subsup{x}{2}{},\mp@subsup{x}{3}{},\mp@subsup{x}{4}{},\mp@subsup{x}{5}{},\mp@subsup{x}{6}{},\mp@subsup{y}{1}{},\mp@subsup{y}{2}{},\mp@subsup{y}{3}{},\mp@subsup{y}{4}{},\mp@subsup{y}{5}{\prime},\mp@subsup{y}{6}{}).new \mp@subsup{z}{1}{},\mp@subsup{z}{2}{},\mp@subsup{z}{3}{},\mp@subsup{z}{4}{},\mp@subsup{z}{5}{},\mp@subsup{z}{6}{}\mathrm{ in
    { even! ( }\mp@subsup{x}{1}{},\mp@subsup{x}{2}{},\mp@subsup{x}{3}{},\mp@subsup{x}{4}{},\mp@subsup{x}{5}{},\mp@subsup{x}{6}{},\mp@subsup{z}{1}{},\mp@subsup{z}{2}{},\mp@subsup{z}{3}{},\mp@subsup{z}{4}{},\mp@subsup{z}{5}{},\mp@subsup{z}{6}{}
    | odd! (z1,z2, z3,z4, z5, z6 , y1, y2, y3, y4, y5, y6) }
    phase! (a11, a}\mp@subsup{a}{21}{},\mp@subsup{a}{31}{},\mp@subsup{a}{41}{},\mp@subsup{a}{51}{},\mp@subsup{a}{61}{},\mp@subsup{a}{12}{},\mp@subsup{a}{22}{},\mp@subsup{a}{32}{},\mp@subsup{a}{42}{},\mp@subsup{a}{52}{},\mp@subsup{a}{62}{}
```



```
    phase! (a13, a}\mp@subsup{a}{23}{},\mp@subsup{a}{33}{},\mp@subsup{a}{43}{},\mp@subsup{a}{53}{},\mp@subsup{a}{63}{},\mp@subsup{a}{14}{},\mp@subsup{a}{24}{},\mp@subsup{a}{34}{},\mp@subsup{a}{44}{},\mp@subsup{a}{54}{},\mp@subsup{a}{64}{}
    provide! (a, (a,0) | provide! (a2, 1) | provide! (a, (a, 2)
    provide! (a41,3) | provide! (a51,4) | provide! (a61,5)
    consume! (a14,0) | consume! (a, (a4,1) | consume! (a34,2)
    consume! (a44,3) | consume! (a54,4) | consume! (a64,5) }
```

Listing 9.5 Term representation of an odd-even 6-input sorting network.
restricted channels $z_{i}$. This network is well-typed and Hypha infers the types

```
comparator : \#[?[int] \(0_{0}^{0}\),?[int] \(0_{0}^{0},![\mathrm{int}]_{0}^{2}\), ! [int] \(\left.{ }_{0}^{2}\right]^{*}\)
    buffer : \#[?[int] \(0_{0}^{0}\), ! [int] \(\left.]_{0}^{2}\right]^{*}\)
    \(a_{i j}: \#[i n t]_{2}^{4(j-1)}\)
```

confirming that each value sent on $a_{i 1}$ is eventually received on some $a_{j 4}$. More specifically, the level 12 assigned with the $a_{j 4}$ channels gives an upper bound to the number of synchronizations needed for producing the output.

Comparators input values in parallel (from the channels $x$ and $y$ ) and perform an internal synchronization (on a private linear channel $z$ ) to join the results of the two receptions and output the results. Alternatively, one could model comparators in such a way that the receive operations on $x$ and $y$ are performed in a fixed order. The choice of a particular modeling affects the levels associated with the input channels, but not the typeability of the network as a whole. This is not the case for buffers: they are operationally irrelevant and are usually omitted in standard presentations of sorting networks. Their use in Listing 9.5 is key for the lock freedom analysis to succeed as they make sure that the levels of the channels connecting one phase to the next one remain aligned.

## 9．3．5 III－typed，Lock－free Process Networks

In general，the problem of verifying whether a $\pi$－calculus process is （dead）lock free is undecidable．For this reason，the type system on which Hypha is based is necessarily incomplete，in the sense that there exist pro－ cesses satisfying Definitions 9.1 and 9.2 which are ill typed according to the type system described in Section 9．2．In this section，we discuss two repre－ sentative examples of processes that cannot be handled by our type system．In all cases，the inability to find a typing derivation is tightly related to the fact that the type system uses integer numbers for reasoning on the dependencies between linear channels and such numbers measure the（abstract）moment of time at which the synchronization occurs on these channels．

Listing 9.6 shows a process that computes the sequence of prime num－ bers．The process is modeled after Eratosthenes＇sieve：the from process emits the infinite stream of natural numbers starting from 2 ；the sequence goes through a growing pipeline of filters，each filter removing those numbers of the sequence that happen to be a multiple of a given prime number $m$ ；if a number $n$ manages to cross the entire pipeline and hits the emitter process output，then it is prime．In this case $n$ is sent on print and a new filter removing the multiples of $n$ is inserted at the end of the pipeline．Hypha is able to distinguish linear from service channels and to infer the type of messages exchanged therein，but the process is ill－typed for deadlock freedom even though it is deadlock free．The problem can be traced to the body of filter：when the received number $n$ turns out to be a multiple of $m$ ，the number is simply discarded and no output is sent on $y$ ．So，the recursive invocation of filter on line 3 reuses the same output channel $y$ that was received as input．Observe that $\bar{x}$ is received from $x$ ，meaning that the level of $\bar{x}$ must necessarily be greater than the level of $x$ ，and that the level of $\bar{x}$ must be strictly smaller than the level of $y$ ，since the input on performed on $\bar{x}$ at the next iteration of filter blocks the possible output on $y$ ．Given that the distribution of prime numbers is irregular，there is no upper bound to the number of inputs on $x$ that may be necessary before the next output

```
{ *from?(n,x).new \overline{x}}\mathrm{ in { x! (n, 利 | from! (n+1, 和)}
*filter?(m,x,y).x? (n,\overline{x}).{ if n mod m=0 then filter! (n,\overline{x},y)
    else new }\overline{y}\mathrm{ in { y! (n, y) | filter! (m, 何六)} }
*output?(x). x?(n,\overline{x}).{ print!n | new y in { filter! (n,\overline{x},y) | output!y } }
from! (2,a) | output!a}
```

Listing 9．6 Stream Network computing the sequence of prime numbers．
on $y$ is guaranteed to be performed. In general, the type system can handle those cases in which communications occur following a regular pattern that is independent of the content of messages themselves.

The second example we consider is a process stream network (Figure 9.5) that computes the so-called Thue-Morse sequence, that is the sequence of binary digits $011010011001 \cdots$ starting with 0 and obtained by appending the boolean complement of the sequence obtained thus far. The term representation of the process network (Listing 9.7) is modeled after its definition in terms of lazy streams [7] and makes use of a set of combinators some of which we have already used for the Fibonacci stream network (Section 9.3.1). The network is lock-free, as witnessed by the fact that the corresponding lazy stream definition can be shown to be productive [7], but also ill typed for deadlock freedom and hence for lock freedom as well. In this network the problematic combinator is zip, which interleaves on the output channel $f$ the digits received from the input channels $d$ and $e$ hence producing messages on $f$ at twice the rate at which they are consumed from $d$ and $e$. This means that there is no fixed offset between the levels of $d$ and $e$ and that of $f$ that could be dealt with by the typing rule [ T -out*]. Note that this phenomenon does not manifest in the Fibonacci stream network (Figure 9.1) despite its seemingly similar topology. The key difference is that in the Fibonacci network the add process combines the messages received from the two input channels into a single message sent on the output channel. The example is interesting also


Figure 9.5 Stream network computing the Thue-Morse sequence.

```
{ *zip?(x,y,z).x?(u,\overline{x}).new \overline{z}\mathrm{ in { z! (u, }\overline{z}) | zip! (y,\overline{x},\overline{z}) }
```



```
*tail?(x,y).x?(_, \overline{x}).link! (\overline{x},y)
*link?(x,y). x? (v,\overline{x}).source! (v,\overline{x},y)
```




```
source! (0,f,a) | copy! (a,b,c) | invert! (b,d) | tail!(c,e) | zip!(d,e,f) }
```

Listing 9.7 Term representation of the stream network computing the Thue-Morse sequence.
because the impossibility to type the term is due to an excess of produced messages rather than the lack thereof, as it was with the sequence of prime numbers.

### 9.4 Related Work

Binary session type disciplines [12] provide intra-session guarantees of lock freedom but cannot enforce this property in general when multiple sessions are interleaved for types do not carry any information concerning the dependencies between different sessions. Some session type systems [1, 24] are designed in such a way that a plain session type discipline is sufficient to guarantee deadlock freedom. However, only networks with a tree-like communication topology are well typed. Among the examples we have considered, just the recursive Fibonacci (Listing 9.1) and the master-worker (Listing 9.3) fall in this category.

Multiparty session type disciplines $[11,12]$ extend (dead)lock freedom to sessions involving multiple processes. In these framework a global type is used to describe the interactions between participants of a session as opposed to the actions that participants perform on the channel of the session. The global type is given explicitly by the system designer/programmer and a tool is then used to check the consistency of the global type against (a model of) the code that is meant to realize it. This top-down approach is complementary to the one we have pursued in this chapter: Hypha analyzes assemblies of processes knowing nothing about the intended communication topology. In general, global types have been designed for describing delimited interactions within sessions, but they cannot dispense completely from the need of interleaving different sessions, in which case they are unable to prevent (dead)locks. This has led to the study of hybrid approaches [2, 3] that keep track of the order in which different sessions interleave with the purpose of detecting mutual dependencies between sessions that could lead to (dead)locks.

The works most closely related to our own are those where each input/output operation described by a channel/session type is annotated with information that captures the dependencies between different channels/sessions. Such annotations come in the form of integer numbers as in our case, or as abstract events, or as combinations thereof. The original technique and the corresponding analysis tool TyPiCal, which our type system and Hypha are heavily inspired by, were described by Kobayashi [13-15].

These type systems and our own are uncomparable: on the one hand, in Kobayashi's works annotations can be used to reason about dependencies between arbitrary channels, whereas we focus on linear channels only. On the other hand, the form of level polymorphism allowed by rule [r-out*] enables the verification of cyclic networks of recursive processes (most of the examples we have examined in this chapter fall in this category) that cannot be successfully handled by Kobayashi's type systems [13-15]. A more recent work [9] improves the precision of the technique, although recursive types (hence recursive communication protocols) are not considered. The annotation-based technique has also been applied directly to binary [17,23] and multiparty sessions [21].

It has been shown that the approaches imposing a tree-like communication topology [1,24] are subsumed by those those annotating I/O actions in session types with dependency information [6].

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## References

[1] Luís Caires, Frank Pfenning, and Bernardo Toninho. Linear logic propositions as session types. Mathematical Structures in Computer Science, 26(3):367-423, 2016.
[2] Mario Coppo, Mariangiola Dezani-Ciancaglini, Luca Padovani, and Nobuko Yoshida. Inference of Global Progress Properties for Dynamically Interleaved Multiparty Sessions. In Proceedings of COORDINATION'13, LNCS 7890, pages 45-59. Springer, 2013.
[3] Mario Coppo, Mariangiola Dezani-Ciancaglini, Nobuko Yoshida, and Luca Padovani. Global Progress for Dynamically Interleaved Multiparty Sessions. Mathematical Structures in Computer Science, 26:238-302, 2016.
[4] Bruno Courcelle. Fundamental properties of infinite trees. Theoretical Computer Science, 25:95-169, 1983.
[5] Ornela Dardha, Elena Giachino, and Davide Sangiorgi. Session types revisited. In Proceedings of PPDP'12, pages 139-150. ACM, 2012.
[6] Ornela Dardha and Jorge A. Pérez. Comparing deadlock-free session typed processes. In Proceedings of EXPRESS/SOS'15, EPTCS 190, pages 1-15, 2015.
[7] Jörg Endrullis, Clemens Grabmayer, Dimitri Hendriks, Ariya Isihara, and Jan Willem Klop. Productivity of stream definitions. Theoretical Computer Science, 411(4-5):765-782, 2010.
[8] Marc Geilen and Twan Basten. Kahn process networks and a reactive extension. In Handbook of Signal Processing Systems, pages 1041-1081. Springer, 2013.
[9] Elena Giachino, Naoki Kobayashi, and Cosimo Laneve. Deadlock analysis of unbounded process networks. In Proceedings of CONCUR'14, LNCS 8704, pages 63-77. Springer, 2014.
[10] Kohei Honda. Types for dyadic interaction. In Proceedings of CONCUR'93, LNCS 715, pages 509-523. Springer, 1993.
[11] Kohei Honda, Nobuko Yoshida, and Marco Carbone. Multiparty asynchronous session types. Journal of the ACM, 63(1):9, 2016.
[12] Hans Hüttel, Ivan Lanese, Vasco T. Vasconcelos, Luís Caires, Marco Carbone, Pierre-Malo Deniélou, Dimitris Mostrous, Luca Padovani, António Ravara, Emilio Tuosto, Hugo Torres Vieira, and Gianluigi Zavattaro. Foundations of Session Types and Behavioural Contracts. ACM Computing Surveys, 49:3:1-3:36, 2016.
[13] Naoki Kobayashi. A type system for lock-free processes. Information and Computation, 177(2):122-159, 2002.
[14] Naoki Kobayashi. Type-based information flow analysis for the picalculus. Acta Informatica, 42(4-5):291-347, 2005.
[15] Naoki Kobayashi. A new type system for deadlock-free processes. In Proceedings of CONCUR'06, LNCS 4137, pages 233-247. Springer, 2006.
[16] Naoki Kobayashi, Benjamin C. Pierce, and David N. Turner. Linearity and the pi-calculus. ACM Transactions on Programming Languages and Systems, 21(5):914-947, 1999.
[17] Luca Padovani. From Lock Freedom to Progress Using Session Types. In Proceedings of PLACES'13, EPTCS 137, pages 3-19, 2013.
[18] Luca Padovani. Deadlock and Lock Freedom in the Linear $\pi$-Calculus. In Proceedings of CSL-LICS'14, pages 72:1-72:10. ACM, 2014.
[19] Luca Padovani. Type Reconstruction for the Linear $\pi$-Calculus with Composite Regular Types. Logical Methods in Computer Science, 11:1-45, 2015.
[20] Luca Padovani, Tzu-Chun Chen, and Andrea Tosatto. Type Reconstruction Algorithms for Deadlock-Free and Lock-Free Linear $\pi$-Calculi. In Proceedings of COORDINATION'15, LNCS 9037, pages 83-98. Springer, 2015.
[21] Luca Padovani, Vasco T. Vasconcelos, and Hugo Torres Vieira. Typing Liveness in Multiparty Communicating Systems. In Proceedings of COORDINATION’14, LNCS 8459, pages 147-162. Springer, 2014.
[22] John H. Reppy. Concurrent Programming in ML. Cambridge University Press, 1999.
[23] Hugo Torres Vieira and Vasco T. Vasconcelos. Typing progress in communication-centred systems. In Proceedings of COORDINATION'13, LNCS 7890, pages 236-250. Springer, 2013.
[24] Philip Wadler. Propositions as sessions. Journal of Functional Programming, 24(2-3):384-418, 2014.

