Compressive Imaging Using RIP-Compliant CMOS Imager Architecture and Landweber Reconstruction

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Abstract-In this paper, we present a new image sensor architecture for fast and accurate compressive sensing (CS) of natural images. Measurement matrices usually employed in CS CMOS image sensors are recursive pseudo-random binary matrices. We have proved that the restricted isometry property of these matrices is limited by a low sparsity constant. The quality of these matrices is also affected by the non-idealities of pseudo-random number generators (PRNG). To overcome these limitations, we propose a hardware-friendly pseudo-random ternary measurement matrix generated on-chip by means of class III elementary cellular automata (ECA). These ECA present a chaotic behavior that emulates random CS measurement matrices better than other PRNG. We have combined this new architecture with a block-based CS smoothed-projected Landweber reconstruction algorithm. By means of single value decomposition, we have adapted this algorithm to perform fast and precise reconstruction while operating with binary and ternary matrices. Simulations are provided to qualify the approach.

Index Terms-CMOS image sensor architecture, compressive sensing, Landweber reconstruction, power spectral density, random binary matrix RIP proof, single value decomposition, ternary measurement matrix.

I. INTRODUCTION

▼OMPRESSIVE Sensing (CS) is a data acquisition technique that can be used to represent the content of an image with fewer samples than required by Shannon-Nyquist theorem. These samples are called compressed samples.

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Reconstruction algorithms exploit the sparsity of the original image in order to recover it by finding the unique solution to an underdetermined linear system [1]. This system can be expressed in matrix notation as:

$$\mathbf{y} = \mathbf{\Phi}\mathbf{x} \tag{1}$$

where $\mathbf{y} \in \mathbb{R}^M$ is the set of *M* compressed samples, $\mathbf{x} \in \mathbb{R}^N$ represents the N pixels values folded in a column vector, i.e. the unknowns of the system of equations, and $\Phi \in$ $\mathbb{R}^{M \times N}$ ($M \ll N$) is referred to as measurement matrix. It has been shown in [2] that, if Φ holds the Restricted Isometry Property (RIP), then the solution to this inverse linear problem, by means of convex optimization, is unique and error free [3].

CS is a field of research that is located in-between the areas of analog microelectronics and of signal processing. The link between these areas is given by the feasible yet RIP-compliant design of measurement matrices. Measurement matrices affect both the performance of reconstruction algorithms as well as the architecture of CS CMOS image sensors (CS-CIS).

CS differs from standard acquisition-plus-compression techniques in that the image is not captured and then digitized before compression. To benefit from the characteristics of CS, the image needs to be sensed and compressed simultaneously. For that reason, in CS-CIS, compressed samples are formed in the analog domain and digital conversion occurs just before their delivery.

Since each pixel has different coefficients for each compressed sample, if the coefficients of a measurement matrix (Φ) were real numbers, a CS-CIS would require the implementation of in-pixel analog multipliers. This solution would be impractical in terms of pixel sensitivity and spatial resolution. Furthermore, each compressed sample is a linear combination of the weighted readings of all pixels (N), which means it is the sum of N products. The upper bound on the amount of bits required to represent theses linear combinations is:

$$B_{\rm CS} = \left\lceil \log_2 N \right\rceil + (B_{\Phi} + B_{\rm I}) \tag{2}$$

where $\left[\cdot\right]$ denotes the smallest integer greater than the argument and B_I and B_{Φ} are the number of bits used to describe the pixel values and the coefficients of the measurement matrix, respectively.

It is virtually impossible to design analog to digital converters (ADCs) with such resolutions in standard technologies. In fact, one of the main drawbacks of most CS-CIS

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implementations [4]–[6] is the lack of dynamic range to properly represent compressed samples.

The use of binary measurement matrices and block based compressed sampling (BCS) [7], which divides a pixel array into smaller sub-arrays that are digitized independently, is essential for the implementation of practical measurement matrices in CS-CISs.

In signal processing literature, there are many works that study deterministic binary measurement matrices; i.e. cyclic matrices [8], sparse bipartite graphs [9] or other forms of deterministic matrices that try to match the performance of Gaussian random matrices [10]. These matrices are usually coined hardware-friendly by their authors, but, even if they are binary, given the technology at our disposal, they cannot be implemented in application specific integrated circuits (ASICs). As compressive sampling an acquisition technique, the fact that we cannot apply these theories to actively design sensors is, in itself, the limitation of these works. This happens because, even if these matrices are binary in nature, they still disregards the fact that, on-chip, they need hardware resources to be generated, transmitted and/or stored. To store a binary measurement matrix in its integrity a large memory would be required. This exceeds nowadays CS-CIS capabilities in term of area occupation. If, on the contrary, we were to introduce a connection between sensor and reconstruction system to continuously send the rows of the matrix, the amount of exchanged data would void CS benefits as a compression technique. For this reason, measurement matrices designed in analog microelectronics [5], [6] solely focus on the use of pseudo random binary numbers generators (PRNG) to recursively create the rows of the matrix sample after sample. This solution employs the minimum possible amount of on-chip resources and does not need data feedback between sensor and reconstruction system. These matrices though do not ascertain the quality of the resulting samples because their primary focus is placed almost entirely on electrical parameters such as power consumption and compactness of the implementation.

PRNG are non-linear spatially discrete and temporally discrete dynamic systems made of binary logic elements that show high sensitivity to initial conditions and evolve in time according to a divergent and fractal behavior [11]. A new state in their time evolution is derived from their actual state using a feedback mechanism that promotes this instability. Each state corresponds to a row of the binary measurement matrix. Each row of a measurement matrix is generated by the CS-CIS from the previous row and there is no need to store the whole binary matrix on chip or to receive it from outside the chip.

In this paper, the performance of binary measurement matrices obtained using binary PRNG is evaluated in terms of RIP. We introduce a new method for the generation of a hardware-friendly measurement matrix that takes into account both technology limits and quality of the resulting matrix. A differential pixel readout system and an elementary cellular automaton (ECA) are proposed to recursively create a ternary measurement matrix in a row by row fashion. The performance of the generated ternary measurement matrix is analyzed and compared with other binary measurement matrices typically found in analog microelectronics. To do so we introduce a new reconstruction algorithm that uses this matrix to deliver reconstruction results close to those of the more refined matrices typically found in signal processing.

II. RIP OF BINARY MATRICES PRODUCED BY PSEUDO RANDOM BINARY GENERATORS

PRNG commonly used to implement measurement matrices into CS-CIS are Linear Feedback Shift Registers (LFSRs) [12], [13]. LFSRs consist of a series of sequentially connected flip-flops. Some of the outputs of these flip-flops, besides being connected to the input of the flip-flop that they precede, are also connected, by means of XOR logic gates, to the input of the very first one. When considered individually, the output of each flip-flop in this sequence evolves in time with a behavior that resembles a symmetric probability distribution. Using one flip-flop for each pixel it is possible to sequentially create the pseudo-random binary coefficients of the measurement matrix one row after the other. The resulting matrix is an approximation of a very efficient type of measurement matrix popular in CS: a matrix in which each element is derived from the normalized Gaussian distribution. Early implementations of CS-CIS binary measurement matrices were carried out incorporating a flip-flop inside each pixel. These flip-flops were then connected sequentially to form LFSRs. This solution though posed a heavy burden on the design specifications of image sensors due to the large area required at pixel level. Recent solutions have overcome this problem placing LFSRs around the pixel array as row and column drivers (Fig. 1) [6], [14], [15].

Inside of each pixel, the flip-flops are replaced by simple and more compact selectors. These selectors are driven by LFSRs surrounding the array. Selectors can be implemented with few transistors, using for instance switches or gated inverters. This simplification was possible because, at pixel level, we can consider random row and column selection as the multiplication of two independent binary random variables. The multiplication of random variables still delivers random outputs. Using PRNG, such as LFSRs, the recursive row and column drivers also reduce the amount of on-chip memory needed to store their elements to $N_g = 2\sqrt{N}$ bits. Since this recursive pseudo-random sequence can be reproduced from an initial N_g bits seed, it allows the independent generation of the same measurement matrix in the system in charge of reconstruction, without continuous transmission of the measurement matrix from the sensor. It is important to notice that LFSRs produce binary matrices that are neither normalized nor orthogonalized. To understand the quality of the compressed samples that they produce, it is necessary to analyze them in terms of RIP.

Suppose that an area of size $\sqrt{N} \times \sqrt{N}$ pixels is sampled by a measurement matrix generated row by row as described above. Let us consider the simplest possible case in which an element of the pseudo random generator is assigned to each pixel of the imager so that each row of the measurement matrix Φ corresponds to a time step in the discrete evolution of the generator itself. Mathematically, this sampling process



Fig. 1. Conceptual floor plan of a CS-CIS.

is formulated as Eq.(1), where $\mathbf{x} \in \mathbb{R}^N$ is the $\sqrt{N} \times \sqrt{N}$ array of pixels folded in vector form. The reconstruction of the original signal \mathbf{x} from the compressed samples \mathbf{y} is an ill-posed problem. By making use of sparsity, a well-known characteristic existing in natural signals, a *k*-sparse¹ signal, like images and videos, can be reconstructed from a few samples obtained using an appropriate measurement matrix.

A sufficient condition for the unique and exact recovery of the signal is the RIP of the measurement matrix $\mathbf{\Phi}$ [2]. A matrix $\mathbf{\Phi} \in \mathbb{R}^{M \times N}$ satisfies the RIP of order k if there is constant δ_k ($0 < \delta_k < 1$) such that, for all vectors $\mathbf{x} \in \mathbb{R}^N$ with $\|\mathbf{x}\|_0 \le k$ (i.e. k-sparse signals²), it holds:

$$(1 - \delta_k) \|\mathbf{x}\|_2^2 \le \|\mathbf{\Phi}\mathbf{x}\|_2^2 \le (1 + \delta_k) \|\mathbf{x}\|_2^2$$
(3)

The smallest non-negative value for δ_k is called restricted isometry constant (RIC) of order k. The construction of a measurement matrix which satisfies the RIP is a central problem in CS. If sparse signals with maximum possible sparsity level k can be recovered exactly and stably, it is said the measurement matrix Φ has sparsity order k. An upper bound for the sparsity level is $k \leq CM/\log(N/M)$ where C is a constant [16]. The signals with k above this bound can only be approximately reconstructed. In practice, to recover a signal **x** with a large k, is desirable to have a measurement matrix with a δ_k as small as possible. The limit imposed on the RIP of random binary measurement matrices has already been studied in [17] Such binary matrices satisfy the RIP with:

$$\delta_k = \frac{(3k-2)m}{(k-2)m+4d} \quad \text{if } 3 \le d \le M/2 \text{ and } 2 \le m \le d$$
 (4)

where d is the estimated number of non-zero elements in each column of Φ and m is the maximum inner product

between two distinct columns. The distribution of 1's among the elements of random binary matrices, generated by pseudo random generators, can be associated with a Bernoulli probability distribution in which each element has a probability P of being 1 and $\bar{P} = 1 - P$ of being 0. Since these pseudo random measurement matrices try to emulate the normalized Gaussian distribution, which is a symmetric probability distribution, usually, circuit designers choose LFSR because their distribution is akin to $P = \bar{P} = 0.5$.

Let us consider the ideal case of an ideal pseudo random generator capable of recursively producing independent binary elements with a symmetric Bernoulli probability distribution having P = 0.5. If we consider a large enough pseudo random binary generator (a large enough number of pixels N), since each of its elements, by construction, is independent from the others and the probability distribution is symmetrical, we will obtain a measurement matrix Φ with an equal amount of 0s and of 1s row-wise. If the acquisition process is long enough (a large enough number of acquired compressed samples M), we will have an equal amount of 0s and of 1s columnwise as well. Such measurement matrix, whose entries are randomly drawn from pseudo random generators, satisfies the RIP with Eq.(4). Following [17], let us define the mutual coherence of Φ as:

$$\mu\left(\mathbf{\Phi}\right) = m \big/ d \tag{5}$$

We can derive *m* and *d* as functions of the number of pixels in the image *N*, the sampling subrate of the sensor S = M/N, and the probability of each element of being different from 0, P. From the definition of subrate (*S*), it is straight forward that $M = S \times N$. Moreover, using the probability that each element of the matrix has of being different from 0, we can define the number of non-zero elements in a column of Φ as:

$$d = S \cdot N \cdot \mathbf{P} \tag{6}$$

Mutual coherence $\mu(\Phi)$ of a matrix represents the maximum absolute value of the cross-correlations among its normalized columns and is defined as:

$$\mu \left(\boldsymbol{\Phi} \right) = \max_{j \neq k} \left| \boldsymbol{\varphi}_{j}^{H} \boldsymbol{\varphi}_{k} \right|$$
(7)

where φ_k is the *k*-th normalized column of matrix Φ and φ_j^H is the conjugate transpose of the *j*-th normalized column of the same matrix. For a matrix to be column-wise normalized it means that for each column: $\varphi_j^H \varphi_j = 1$. Given the definition of mutual coherence and the randomness of φ_j^H and φ_k , to approximate *m*, we will make use of the most probable outcome of a dot product between normalized columns of Φ :

$$m = \mathbf{E}\left[\boldsymbol{\varphi}_{j}^{H}\boldsymbol{\varphi}_{k}\right]d\tag{8}$$

Remember that $\varphi_j^H \varphi_k$ is the sum of M element by element products. Each one of these M products will be different from zero if and only if the two elements that are being multiplied differ from 0 as well. By construction, we are multiplying independent random variables; the probability that their product be different from 0 is equal to the joint

¹**x** is called *k-sparse* or a signal with sparsity level *k*, if it has no more than *k* non-zero components with $k \ll N$.

 $^{||\}mathbf{x}||_0$ norm denotes the number of non-zero components in **x**.

probability of the single elements. For this reason, the expected amount of non-zeros among the M products is:

$$E\left[\operatorname{Tr}\left(\boldsymbol{\varphi}_{j}^{H}\cdot\boldsymbol{\varphi}_{k}\right)\right] = \mathbf{P}^{2}\cdot S\cdot N \tag{9}$$

where Tr (·) is the trace of the matrix obtained by the vector multiplication of φ_j^H and φ_k . Furthermore we can derive the value of each product in terms of P, N and S by using the definition of normalization and remembering that Φ is binary. Normalizing a vector involves dividing each one of its non-zero elements by the Euclidean norm of the vector itself. Since each non-zero element in a binary vector has to take the value of 1, all elements of a normalized binary vector will be equal to the inverse of the vector Euclidean norm itself. For this reason, if the *i*-th elements in column φ_j and column φ_k are non-zeros, then their normalized product will be:

$$\boldsymbol{\varphi}_{j}^{H}{}_{i}\boldsymbol{\varphi}_{k_{i}} = \frac{1}{\mathbf{P}\cdot N\cdot S} \tag{10}$$

Combining Eq.(9) and Eq.(10), we can deduce the most probable cross-correlation between two columns of a binary matrix Φ This correlation only depends on P and it is independent from the number of pixels and the sampling subrate:

$$\mathbf{E}\left[\boldsymbol{\varphi}_{j}^{H}\boldsymbol{\varphi}_{k}\right] = \frac{\mathbf{P}^{2}\cdot\boldsymbol{S}\cdot\boldsymbol{N}}{\mathbf{P}\cdot\boldsymbol{N}\cdot\boldsymbol{S}} = \mathbf{P}$$
(11)

Joning Eq.(4), Eq.(5) and Eq. (11) we obtain:

$$\delta_k = \frac{(3k-2)}{(k-2)+4/P} \text{ if } 3/(S \cdot N) \le P \le 1/2 \text{ and } 2/(S \cdot N) \le P^2 \le P$$
(12)

For P = 0.5 and $N \ge 6$, it is possible to see that all of the conditions hold and that random binary matrices that follow a symmetric probability distribution will have RIP ($\delta_k \le 1$) only if $k \le 4$.

A recovery algorithm can deliver error free reconstructions only if the measurement matrix holds RIP. Eq. (12) poses a harsh limitation on matrices that need to be implemented in CS-CISs. Block based CS-CIS, in many occasions, need small blocks in order to maintain acceptable errors during ADC. ADC dynamic range is determined by noise and the maximum signal range. It is very difficult to allocate large dynamic range. Small blocks reduce the requirement for signal range and consequently making it feasible for implementation. This in turn introduces asymmetries in the pixel array. These asymmetries create artefacts in the captured images in correspondence of the borders of the blocks. Reconstruction of block based compressive sampled images usually entails smoothing of those regions. When using a CS-CIS the smoothing process needs to take care of physical discontinuities as well. Because these two effects combine themselves they can potentially deteriorate the quality of the recovered images. It would be desirable to implement a measurement matrix that did not add errors during reconstruction to facilitate the smoothing process. But, since the RIP of random binary matrices holds only if $k \leq 4$, errors during reconstruction are to be expected.

III. TERNARY MATRICES GENERATED USING CLASS III Elementary Cellular Automata

We have bound the RIP of binary measurement matrices usually implemented in a CS-CIS with a limit ($k \le 4$) that, in practice, is too small to grant the error free reconstruction of natural images. As explained in the introduction, PRNG generated measurement matrices are necessary to design feasible CS-CIS architectures. However, to overcome the limitation imposed by Eq.(12), we propose to use them to generate ternary measurement matrices instead of the commonly used binary ones.

The resulting ternary matrices present smaller coherence than the aforementioned binary ones of same size thus improving RIP and diminishing reconstruction errors. Similar results have been obtained using binary matrices with binary correlations between columns applied to CS in [18].

Ternary measurement matrices can be generated by delivering two independent binary coefficients to each pixel: one to define the number either 0 or 1 and the other to define the sign of the contribution. As mentioned in section II, if one set of row/column selectors (or driving signal) can be used to produce a binary matrix having one bit per element we could use two sets (or driving signals) to produce a ternary matrix that needs two bits per element. If each pixel received two driving signals, it would be possible to use one of them to determine if the pixel took part in a compressed sample and, in case it did, use the other to select the sign of its contribution. Since recent CS-CIS examples [6], [14], [15] have shown that as few as $N_g = 2\sqrt{N}$ bits of information are needed to generate binary measurement matrices on-chip, the amount of resources (number of transistors) needed to incorporate a ternary matrix would still be implementable in a CS-CIS design. Each coefficient of the resulting matrix would have a probability distribution of:

$$\begin{cases} P_{+1} = 0.25 \\ P_0 = 0.5 \\ P_{-1} = 0.25 \end{cases}$$
(13)

where P_{-1} represents the probability that a pixel has of contributing negatively in a compress sample, P₊₁ represents the probability that a pixel has of contributing positively and P₀ represents the probability that a pixel has of not contributing at all. Furthermore, a ternary measurement matrix would have a positive effect on the limit, imposed by Eq. (2), on the amount of bits required to represent a compressed sample. In hardware, the sign associated to a pixel contribution can be implemented by means of a differential readout system that routes the output of the pixels through one of two output lines [5]. These lines, outside of the pixel array can then be digitized separately or used as input of differential circuits, such as analog subtractors or transimpedance amplifiers. Using a differential readout system to divide the pixel contribution to different output lines to be treated separately would in fact reduce the amount of bits needed for ADC thus relaxing the converter design parameters.

Another important factor to study in order to improve the quality of CS-CISs measurement matrices are LFSRs performances. Even if LFSRs are good candidates to emulate random probability distributions thanks to their low impact on on-chip area consumption, ultimately the matrices that they produce still follow a row-by-row deterministic pattern. For this reason it is necessary to analyze this pattern to understand how well the generated measurement matrix approximates the functionality of one that is extracted directly from a real binary probability distribution. Moreover, LFSRs are not the only example of circuits that can serve as PRNG in a CS-CIS. A valid alternative are elementary cellular automata (ECA) [19]. An ECA is a spatially and temporally discrete dynamic system made of identical interconnected cells. Each ECA cell is a triple-input single-output binary unit. Based on this setup there are 256 possible ECA configurations. Each configuration, also known as rule, corresponds to a particular logic implementation. We will focus our attention on rules that belong to class III, in particular rule 30 [20]. We will study rule 30 hardware implementation and the matrices that it produces in order to establish if it is better suited than LFSRs to be used as PRNG in the design of CS-CIS.

Let us consider the simple case of binary measurement matrices. This simplification poses no loss of generality because the elements of the aforementioned ternary matrices are generated following the same pattern used for binary measurement matrices and the bits are grouped in sets of two only at pixel level. Let us define $\Phi_{\mathbf{G}} \in \{0, 1\}^{M \times N}$ as a binary measurement matrix obtained using a PRNG and $\Phi_R \in$ $\{0, 1\}^{M \times N}$ as a binary measurement matrix obtained selecting each element at random from a binary probability distribution with $P = \bar{P} = 0.5$. Φ_G will be a good approximation of Φ_R if it holds three characteristics: the number of non-zero elements in a row of Φ_G must approximate P \approx 0.5; the temporal evolution of the elements of the PRNG do not present repeating patterns; and the temporal evolution of the elements of the PRNG present no correlation with one another. The first and second requirements bind the number of non-zero elements in Φ_G with the average of the probability distribution used to create Φ_R . The second and third requirements can be used to bind the mutual coherence of Φ_G as:

$$E\left[\mu\left(\mathbf{\Phi}_{G}\right)\right] \approx E\left[\mu\left(\mathbf{\Phi}_{R}\right)\right] = \mathbf{P} \tag{14}$$

We will analyze how binary measurement matrices generated using LFSRs and ECA fulfill these criteria in order to compare the performances of the two types of PRNGs in CS imagers.

A. Density of the PRNG Output

To study the number of non-zero elements in a row of Φ_G let us define the density of a PRNG output (DO(t)) as the average of all the states of its binary elements at a given discrete time step t (following a notation similar to [21]):

DO
$$(t) = \frac{1}{N} \sum_{h=1}^{N} \varphi_h(t)$$
 where $t = 1, 2, \cdots, M$ (15)

being $\varphi_h(t)$ the state of the *h*-th element of the PRNG at time *t* as well as the *t*-th element of the *h*-th column of Φ_G , *N* the number of elements of the PRNG as well as the number of columns in Φ_G and *M* the number of discrete time steps of the PRNG as well as the number of rows in Φ_G . Let t = 1



Fig. 2. Density of the output over a varying random initial seed for rule-30 ECA and LFSR.

be the initial condition when the first row of Φ_G coincide with the seed that has been loaded in the memory on-chip. Since the elements of Φ_R are extracted from a symmetric probability distribution, an optimal seed should have half of its elements set to 0 and half set to 1 and as such DO (1) = 0.5.

To analyze the performance of LFSRs and ECA it is necessary to evaluate how fast their states can reach DO (t) = 0.5 in the eventuality of having a suboptimal initial seed in which the number of elements set to 0 differs from the number of those set to 1 (Fig. 2). To do so we devise a MATLAB experiment in which we evolve a 64-cells rule-30 ECA and a 64-flip-flops LFSR using 65 different initial seeds having an density that varies from DO (1) = 0 all the way to DO (1) = 1.

In the result of the experiment we represent DO (t) using greyscale elements in which DO (t) = 0 is colored black and DO (t) = 1 is colored white. In Fig. 2 the top line represents these initial seeds with increasing density, from the leftmost DO (1) = 0 to the rightmost DO (1) = 1. Each step underneath shows the temporal evolution of the PRNG output density evolving from each initial seed configuration.

Fig. 2 shows that LFSRs density changes in time at a much slower rate than class III ECA. This implies that in the presence of sub-optimal initial seed, the DO of LFSRs would take several time steps longer than ECAs to achieve a stable situation centered around DO (t) = 0.5. As reported in [22], the behavior of LFSRs is more similar to class IV ECA, where randomness in the evolution is linked to the randomness of the initial configuration, rather than class III ECA where the chaotic evolution is introduced by the rule itself. For this reason, in order to approximate Φ_R using LFSRs much care should be placed in the selection of an appropriate initial seed. ECA appears to be a safer choice to generate a Φ_G rather than LFSRs at least in terms of number of distribution of non-zero elements starting from seeds that are not ideal.

Fig. 2 also shows an interesting behavior for ECA when its seed is either DO(1) = 0 or DO(1) = 1 and for LFSR when its seed is DO(1) = 0. These particular seeds represent the forbidden states of these PRNGs. All PRNGs have forbidden states. A forbidden state is a state in which the PRNG continuously delivers a fixed output. These states though do not pose any issues in real-world implementations because it is impossible for a PRNG to stumble upon



Fig. 3. Power spectral density of rule-30 ECA and LFSR.

them accidentally. Even though these states do not pose any problem during standard operation, to use PRNGs in CS-CIS, it is important to be aware of their presence in order to avoid sending them accidentally. The only particular concern regarding forbidden states happens when power is first applied to a circuit. Since each register can randomly start up containing either a zero or a one, a PRNG could power up containing its forbidden state, but this can be quickly taken care of initializing the PRNG with an opportune seed value.

B. Power Spectral Density

To understand if the rows in Φ_G present repeating patterns and to compare dynamical behaviors of LFSRs and rule-30 ECAs we will use Power Spectral Density (PSD) analysis. This technique has been extensively applied to analyze discrete dynamical systems such as ECA [21] or LFSRs [13]. For a spatially discrete and temporally discrete dynamic system, the DFT can be expressed as:

$$\phi_h(f) = \frac{1}{M} \sum_{i=1}^{N} \varphi_h(t) e^{-i2\pi t f_i} M \text{ where } t = 1, 2, \cdots, M$$
(16)

where, following a notation similar to [21], $\phi_h(f)$ is the DFT value of the *h*-th element of the PRNG at frequency *f*, *M* once again is the number of discrete time steps of the PRNG as well as the number of rows in Φ_G and $\varphi_h(t)$ is the state of the *h*-th element of the PRNG at time *t* as well as the *t*-th element of the *h*-th column of Φ_G .

PSD expresses the distribution of the energy of a waveform among its different frequency components. Any peak in a graphic of PSD (f) over f at a given frequency f_p would represent a strong repeating pattern of period $1/f_p$ among the rows of Φ_G Given all $\phi_h(f)$ PSD can be computed as:

$$PSD(f) = \frac{1}{N} \sum_{e=1}^{N} |\phi_h(f)|^2$$
(17)

where N is the number of elements of the PRNG as well as the number of columns in Φ_G .

The PSD profile of a suitable and efficient PRNG for CS should closely resemble white noise since its energy should equally spread throughout the entire spectrum. Once again we devise a MATLAB experiment in which we evolve a 64-cells rule-30 ECA and a 64-flip-flop LFSR. Fig. 3 shows the PSD profiles of rule-30 ECA and LFSR. These graphics have been

produced for t = 1000 time steps starting from an initial random binary seed with DO (1) = 0.5.

The first thing that we can notice is that both PSD profiles are similar to white noise. That was to be expected because otherwise neither of them would be suitable for CS but, the amplitude of the pattern of the LFSR presents a greater variation than that of rule-30 ECA as well as some unwanted fluctuation.

Rule-30 ECA appears to a better choice over LFSRs to extract quasi-independent coefficients from a random like distribution. Furthermore, as we said in the introduction, a necessary requirement to design a measurement matrix for CS is that said matrix respects the RIP which means that it must be nearly orthonormal, at least when operating on sparse vectors [2]. Orthogonal matrices have uncorrelated columns and rows. For this reason, even though RIP and mutual coherence are two different concepts, a matrix Φ_G , compliant with RIP, and its transpose Φ_G^T will both have low mutual coherence by definition [23]. Class III ECA PSD profile offers better guaranties over LFSRs that the mutual coherence of the generated matrix will be low. We opted to use a single class III rule elementary cellular automaton over other solutions like multi-rule hybrid cellular automata for two reasons: to facilitate the circuit design and because we are interested in the patterns that the individual cells produce and not the generated number in its decimal representation.

C. Hardware Implementation

To analyze the correlations among temporal evolutions of the elements of a PRNG is equivalent to analyze mutual coherence of Φ_G^T . To do so it is necessary to study how these patterns are generated by the feedback mechanism that promotes the PRNG's instability.

Fig. 4 is an example that represents the feedback needed to evolve an 8-bit LFSR. This feedback is imposed on the first flip-flop of the sequence through XOR gates tapping flip-flops 4, 5, 6 and 8. The flip-flops that must be tapped to allow an *N*-bit LFSR to cycle through all $2^N - 1$ combinations are tied to primitive polynomials with binary coefficients and degree equal to *N* [24].

On the contrary, the feedbacks needed for the evolution of a cell in a cellular automaton are spatially local. The block diagram of Fig. 5 is an example of a cell of a rule-30 ECA, in it we have defined as S the actual state of the cell. L and R are the actual states of its two closest neighbors, to the left and to the right respectively. NS corresponds to the cell's next state.

This is in sharp contrast with the feedback of LFSRs which is unique for all its elements. The only drawback that ECA present with respect to LFSRs is that while the basic element of a LFSR is a flip-flop, to implement a cell of a rule-30 ECA it takes two extra logic gates.

Even though ECA require more resources (transistors) to be implemented than LFSRs it has been shown in [22] that, due to the shifting of data, inside large LFSRs, the outputs of the individual flip-flops have strong correlation between each other. A consequence of this fact is that the mutual coherence of Φ_G



Fig. 4. 8-bit LFSR.



Fig. 5. ECA cell implementation of rule 30.

could inadvertently increase, thus reducing k, the maximum amount of non-zero coefficients, in an appropriate domain, that the signal to be sampled can have before incurring in reconstruction errors.

IV. RECOVERY ALGORITHM FOR PRNG GENERATED MEASUREMENT MATRICES

When the signal to be acquired is a large image, the receiver needs a large memory to store the generated measurement matrix. Furthermore, the reconstruction algorithm undergoes a huge computational burden due to this large size. BCS [7], [25], [26] solves these issues by dividing the scene to be acquired into *H* non-overlapping blocks of size $\sqrt{N_b} \times \sqrt{N_b}$. With BCS, the acquisition is achieved independently, block by block, using the same measurement matrix $\Phi \in \mathbb{R}^{M_b \times N_b}$. For each block we can reformulate Eq. (1) as:

$$\mathbf{y}_i = \mathbf{\Phi} \mathbf{x}_i \tag{18}$$

where $\mathbf{x}_i \in \mathbb{R}^{N_b}$ is the vector representation of the *i*-th block of the image and $\mathbf{y}_i \in \mathbb{R}^{M_b}$ are the corresponding samples. At the receiver side, each block can be reconstructed separately.

In order to avoid creating unpleasant blocking artefacts, recently, several BCS reconstruction algorithms have been designed [25], [27]–[29]. BCS with smoothed-projected Landweber reconstruction (BCS-SPL) is a fast and efficient reconstruction algorithm [25]. It is based on successive projections and thresholding in the transform domain and provides a good trade-off between computational complexity and reconstruction quality. While this method is simple to derive, convergence is only guaranteed for a limited class of matrices that hold the strong RIP condition, e.g. Gaussian random measurement matrices with real coefficients. Recently, Akbari and Trocan [26] proposed a way to improve the performance of the BCS-SPL algorithm to a wide range of random measurement matrices, including Bernoulli and Gaussian measurement matrices. Binary PRNG such as those used in hardware implementations deliver matrices that, a priori, would destabilize the BCS-SPL algorithm. In this paper, we propose one way to adapt this algorithm to work with these generators. To this purpose, we combine the BCS-SPL algorithm with singular value decomposition in order to design a more robust and stable reconstruction algorithm, called BCS-SVD. Let $\hat{\mathbf{x}}_i^{[k]}$ be the estimated image at the *k*-th iteration, partitioned into *H* blocks. Furthermore, suppose that the SVD decomposition of the measurement matrix $\boldsymbol{\Phi}$ is given by:

$$\mathbf{\Phi} = \mathbf{U}\mathbf{D}\mathbf{V}^T \tag{19}$$

where **U** and **V** are orthogonal matrices of size $M_b \times M_b$ and $N_b \times N_b$ respectively, and their column are known as the singular vectors of the matrix $\mathbf{\Phi}$. **D** is a diagonal matrix of size $M_b \times N_b$ and its element along its diagonal are known as the singular values [30]. The proposed algorithm updates the *i*-th block $\hat{\mathbf{x}}_i^{[k]}$ iteratively by means of the following approximating operator:

$$\hat{\mathbf{x}}_{i}^{[k+1]} = \hat{\mathbf{x}}_{i}^{[k]} + \boldsymbol{\Phi}^{\mathrm{T}} \mathbf{U} \mathbf{S} \mathbf{U}^{T} \left(\mathbf{y}_{i} - \boldsymbol{\Phi} \hat{\mathbf{x}}_{i}^{[k]} \right)$$
(20)

where **S** is a diagonal matrix of size $M_b \times M_b$ whose entries are obtained by taking the reciprocal of the corresponding squared non-zero elements of the matrix **D**. The most useful feature of the approximating operator in BCS-SVD is that it partially generalizes matrix inversion of the measurement matrix Φ . This operator is based on the error between the original samples of each block and the corresponding approximated samples, obtained by the estimated block at the previous iteration. Using this residual, it forms a correction equation for which this process is repeated.

In the next step, all the reconstructed blocks at iteration k are put back into the full-image plane to update the reconstructed image $\hat{\mathbf{x}}^{[k+1]}$ using the same smoothing and thresholding operations, proposed in the BCS-SPL algorithm [25]. The smoothing operation is done by Wiener filtering which reduces the blocking artefacts. The aim of thresholding process is controlling the local sparsity that is well-known in the natural image and video signals. To this purpose, soft thresholding is done in the transform domain Ψ by:

$$\tilde{\mathbf{x}}^{[k+1]} = \begin{cases} \tilde{\mathbf{x}}^{[k+1]} & |\tilde{\mathbf{x}}^{[k+1]}| \ge \tau \\ 0 & else \end{cases}$$
(21)

where τ is a predetermined threshold and $\tilde{\mathbf{x}}^{[k+1]}$ is the transformed image. A wide range of sparse transforms can be used, such as discrete cosine and wavelet transforms. The most

TABLE I BCS-SVD Algorithm

Input: Φ , $[\mathbf{y}_i]_{i=1}^H, \Psi(.)$
Output: x̂
Initialize: $k = 0$ and $\hat{\mathbf{x}}^{[0]} = 0$
SVD decomposition: $\Phi = UDV^T$
While $\ \hat{\mathbf{x}}^{[k+1]} - \hat{\mathbf{x}}^{[k]}\ \leq \varepsilon$:
$\left[\hat{\mathbf{x}}_{i}^{[k]}\right]_{i=1}^{H} = \mathbf{R}\left(\hat{\mathbf{x}}^{[k]}\right)$
For $i = 1$ to H
$\hat{\mathbf{x}}_{i}^{[k+1]} = \hat{\mathbf{x}}_{i}^{[k]} + \mathbf{\Phi}^{T} \mathbf{U} \mathbf{S} \mathbf{U}^{T} (\mathbf{y}_{i} - \mathbf{\Phi} \hat{\mathbf{x}}_{i}^{[k]})$
End For
$\hat{\mathbf{x}}^{[k+1]} = \mathbf{R}^{-1} \left(\left[\hat{\mathbf{x}}_{i}^{[k+1]} \right]_{i=1}^{H} \right)$
$\hat{\mathbf{x}}^{[k+1]} = \mathbf{Wiener}(\hat{\mathbf{x}}^{[k+1]})$
$\tilde{\mathbf{x}}^{[k+1]} = \mathbf{\Psi}(\hat{\mathbf{x}}^{[k+1]})$
$\tilde{\mathbf{x}}^{[k+1]} = \mathbf{Threshold}(\tilde{\mathbf{x}}^{[k+1]})$
$\tilde{\mathbf{x}}^{[k+1]} = \mathbf{R}^{-1} \left(\tilde{\mathbf{x}}^{[k+1]} \right)$
End While

effective one, as proposed in [25], is the dual-tree discrete wavelet transform (DDWT). Such a procedure is iterated until the difference of successive approximations of the image is a very small value, *i.e.* $\|\hat{\mathbf{x}}^{[k+1]} - \hat{\mathbf{x}}^{[k]}\| \le \varepsilon$. The details of the proposed BCS-SVD algorithm are summarized in Table 1.The operator $\mathbf{R}(\cdot)$ splits the image into H non-overlapping blocks and the operator $\mathbf{R}^{-1}(\cdot)$ puts back the reconstructed blocks into the corresponding positions in the reconstructed image, padded with zeros elsewhere.

Eq. (20) is derived to solve the following minimization problem

$$\operatorname{argmin}_{\mathbf{x}_{i}}\{\|\mathbf{y}_{i} - \mathbf{\Phi}\mathbf{x}_{i}\|_{2} + \lambda \|\mathbf{x}_{i}\|_{1}$$
(22)

where λ is the regularization parameter. Eq. (20) is the enhanced version of the projected Landweber step proposed in BCS-SPL [25] which, with the thresholding operator of Eq. (21), iteratively improves the reconstruction quality. This iterative threshing process has been analyzed extensively in the literature. Different theoretical analyses of the BCS-SPL algorithm applied to the CS recovery problem have been given in [31]–[33]. BCS-SPL is guaranteed to converge to a local minimum of Eq. (22). The argument for BCS-SVD follows the same line as that in [31].

V. EXPERIMENTAL RESULTS

In this section, the performance of the CS measurement matrix, described in sections III, and the BCS-SVD algorithm, proposed in the section IV, will be evaluated via a suite of simulations carried out on 8-bit grayscale standard images of size 512×512 pixels. Here, as shown in Fig. 6, we report the results of four classic images often used in image processing: Boat, Lena, Livingroom, and Baboon.³ The Peak Signal-to-Noise Ratio (PSNR) is chosen as objective quality measure for our experimental framework. PSNR is an approximation to human perception of reconstruction quality from lossy compression. When used for this purpose the original data is considered the





Fig. 6. Test images: (a)Boat, (b)Lena, (c) Livingroom, (d)Baboon.

signal and the error introduced by compression is the noise:

$$PSNR = 10 \log_{10} \frac{\max\left(\mathbf{x}\right)^{2}}{\frac{1}{MN} \left\|\mathbf{x} - \hat{\mathbf{x}}\right\|_{2}}$$
(23)

being max (\mathbf{x}) the highest possible pixel value. The simulations were carried out in MATLAB. In each set of simulations, 30 trials are performed due to the random nature of generated measurement matrices and the average PSNR is computed. For the BCS sampling, we use the block dimension of size 32×32 , *i.e.* $N_b = 1024$. The value of threshold in the reconstruction algorithm is set to $\varepsilon = 10^{-4}$. We remind the reader that dividing the pixel array into smaller subarrays that can be digitized independently is essential for two reasons. It helps the implementation of practical measurement matrices in CS-CISs and it reduces software resources needed during reconstruction. Usually fast and accurate reconstruction algorithms opt for small blocks, typically 8×8 . On the other hands, blocks that are too small deteriorate the quality of the sensor introducing asymmetries within the pixel array. We chose a block size of 32×32 as a compromise between the two divergent necessities. We have set the value of threshold in the reconstruction algorithm to be $\varepsilon = 10^{-4}$ because we saw that it was a good compromise between reconstruction time and accuracy of the reconstruction result.

Experiments are designed with two goals: first, the performance of ECA-based ternary matrices for CS should be compared with other types of matrices; second, the stability and robustness of the proposed measurement matrices with different recovery algorithms should be evaluated. This test set up includes 480 trials. More examples can be found in our online repository.

A. Validation of the Proposed Measurement Matrices

In this subsection, the performance of the proposed ternary and binary pseudo random measurement matrices based on



Fig. 7. PSNR of the reconstructed images as a function of subrate (S).

ECA is compared to performance of the pseudo random measurement matrices based on LFSR. In addition, we have included the Gaussian random matrices, whose entries are randomly selected from a Gaussian distribution, as a reference. We conducted a series of test during which we applied the CS using four matrices: $\Phi_{Gaussian}$ a Gaussian random matrix chosen i.i.d. from $\mathcal{N}(0, 1)$ and then normalized; Φ_{LFSR}^B a binary pseudo random measurement matrix based on LFSR; Φ_{ECA}^B a Binary pseudo random measurement matrix based on ECA; and Φ_{ECA}^T a ternary pseudo random measurement matrix based on ECA.

First, the test images are divided into non-overlapping blocks of size 32×32 pixels and each block is compressed with the abovementioned measurement matrices. Then, the test images are recovered using the proposed recovery algorithm in section IV. This process is repeated several times per image and then the results are averaged.

Fig. 7 demonstrates the quality of reconstructed images in terms of PSNR as a function of subrate (S), where S varies from 0.1 to 0.5. As shown in Fig. 7, ternary matrices can be used to compressive sample the images and their performances degrade when the subrate decreases. An interesting observation is that the ternary matrix Φ_{ECA}^{T} outperforms



binary matrices almost reaching the Gaussian random matrix $\Phi_{Gaussian}$. Fig. 9 represent Lena reconstructed using a ternary measurement matrix generated using ECA with a subrate of 0.1. We will provide more images and results along with the MATLAB code used to obtain them in the online repository. Depending on the subrate, on average, the proposed ternary matrix improves the PSNR of the reconstructed images from 0.08 dB to 0.35 dB when compared with the binary measurement matrix Φ^{B}_{ECA} and from 0.27 dB to 1.90 dB when compared with the binary measurement matrix Φ^{B}_{ECA} . As can be seen, the performance the binary measurement matrix Φ^{B}_{ECA} is lower than that of the ternary matrix Φ^{T}_{ECA} at lower subrates. However, it shows a good performance when higher subrates are considered. For example, at S = 0.5, the PSNR of reconstructed images using the samples obtained from the matrix Φ_{ECA}^{B} matrix is almost 0.08 dB lower on average than that of the matrix Φ_{ECA}^{T} . Finally, it should be particularly noted that the ternary matrix $\mathbf{\Phi}_{\mathrm{ECA}}^{\mathrm{T}}$ competes with the performance of the Gaussian random matrix, which is an optima theoretical measurement matrix. It should be reminded that the proposed ternary matrix is much easier to implement in hardware and that adds a valuable advantage to it.



Fig. 8. PSNR of the reconstructed images using different recovery algorithms as a function of subrate (S).

B. Stability of the Ternary Measurement Matrices

The aim of the simulation in this section is to show that the images can be recovered almost perfectly from the compressed samples obtained by the proposed ternary measurement matrix, when different recovery algorithms are used. To this purpose we use NESTA algorithm [34], gradient projection for sparse reconstruction (GPSR) [35], and sparsity adaptive matching pursuit (SAMP) [36] and we compare their performances with that of BCS-SVD algorithm. We chose these algorithms due to the fact that they are robust and perform well for recovering images sampled using sub-optimal matrices obtained from PRNGs. During this simulation, the test images are divided into non-overlapping blocks of size 32×32 pixels. We applied the measurement process of CS with the ternary measurement matrix $\Phi_{\text{ECA}}^{\text{T}}$. Then, the test images are recovered using NESTA and BCS-SVD respectively. The same measurement matrix is used for both algorithms.

Fig. 8 shows the quality of reconstructed images in terms of PSNR at different subrates. It can be observed that both algorithms can recover the images, although the proposed BCS-SVD recovery algorithm has better performance in terms of PSNR compared with the other algorithms. As another result, at the same subrate S, the test images like Boat and Lena are recovered with ~1dB gain w.r.t. NESTA. This is due the

fact that these images are more compressible, i.e. have a higher sparsity level in the wavelet domain. A visual comparison of the various algorithms is shown in Fig. 9.

C. Complexity

Finally, it should be noted that the proposed scheme is valuable because it reduces the complexity of both the measurement and recovery processes of CS; at the sensor side, encoding is achieved without a huge memory requirement and, at the decoder side, the proposed BCS-SVD recovery algorithm is able to provide a fast and accurate solutions. The BCS-SVD algorithm is very simple. Each iteration involves the application of two operators, $\mathbf{\Phi}$ and $\mathbf{\Phi}^{\mathrm{T}}$, as well as two vector additions. It should be noted that SVD decomposition is carried out once for the whole recovery and its complexity is ignored. The thresholding process Eq. (21) also involves a partial ordering of the elements $\hat{\mathbf{x}}_{i}^{[k]}$ in magnitude and has ignorable complexity. Apart from the storage of samples y_i , we only require the storage of vector $\hat{\mathbf{x}}_{i}^{[k]}$ for each block, which is of length N_b . The bottle neck, both in terms of storage and computation time, is due to the operators Φ and $\Phi^{T}UU^{T}$. So, the computational complexity of each iteration is $O(M_b \times N_b)$. To justify our claim regarding the fast implementation recovery using the BCS-SVD algorithm, we have



Fig. 9. The reconstructed image *Lena* at subrate = 0.1: (a)BCS-SVD, (b) SAMP, (c)NESTA, (d)GPSR.

 TABLE II

 Lena Reconstruction Time for Subrate=0.3

Algorithm	NESTA	SAMP	GPSR	BCS-SVD
Time (s)	65	41	97	2.5

compared the required time for reconstructing the test image *Lena* using the above-mentioned algorithms at the subrate 0.3 in Table 2. As can be seen in Table 2, the proposed algorithm indicates a lower order of complexity.

VI. CONCLUSIONS

In this paper we have presented a new hardware-friendly ternary measurement matrix for CS acquisition of natural images and a reconstruction algorithm capable of exploiting this matrix for fast and accurate recovery. We have modeled through a set of equations the challenges and limitations that arise during the design of compressive sampling CMOS image sensors and we have established why PRNG are essentials for the practical implementation of this type of circuits. We have bound the RIP of the binary measurement matrices that these generators produce with a constant that is very strict. We overcame this shortcoming presenting a novel sensor's topology capable of delivering ternary pseudo-random matrices by means of ECA and a differential readout system. The proposed measurement matrix approaches closely the ideal case of normalized Gaussian random measurement matrix. Further, a fast and accurate acquisition and reconstruction system is proposed with the BCS-SVD algorithm. The experiments are promising and show the possibility of using this system in real time applications such as video surveillance or data transmission with channels that have a high noise and/or limited bandwidth.

REFERENCES

- J. A. Tropp and J. S. Wright, "Computational methods for sparse solution of linear inverse problems," *Proc. IEEE*, vol. 98, no. 6, pp. 948–958, Jun. 2010.
- [2] E. J. Candès, "Compressive sampling," in Proc. Int. Congr. Math., Madrid, Spain, Aug. 2006, pp. 1433–1452.
- [3] R. Baraniuk, V. Cevher, and M. Wakin, "Low-dimensional models for dimensionality reduction and signal recovery: A geometric perspective," *Proc. IEEE*, vol. 98, no. 6, pp. 959–971, Jun. 2010.
- [4] M. F. Duarte et al., "Single-pixel imaging via compressive sampling," IEEE Signal Process. Mag., vol. 25, no. 2, pp. 83–91, Mar. 2008.
- [5] V. Majidzadeh, L. Jacques, A. Schmid, P. Vandergheynst, and Y. Leblebici, "A (256×256) pixel 76.7 mW CMOS imager/ compressor based on real-time in-pixel compressive sensing," in *Proc. IEEE Int. Symp. Circuits Syst. (ISCAS)*, Paris, France, May 2010, pp. 2956–2959.
- [6] Y. Oike and A. El Gamal, "CMOS image sensor with per-column Σ Δ ADC and programmable compressed sensing," *IEEE J. Solid-State Circuits*, vol. 48, no. 1, pp. 318–328, Jan. 2013.
- [7] L. Gan, "Block compressed sensing of natural images," in Proc. Int. Conf. Digit. Signal Process., Cardiff, U.K., Jul. 2007, pp. 403–406.
- [8] R. A. DeVore, "Deterministic constructions of compressed sensing matrices," J. Complex., vol. 23, nos. 4–6, pp. 918–925, 2007.
- [9] S. T. Xia, X. J. Liu, Y. Jiang, and H. T. Zheng, "Deterministic constructions of binary measurement matrices from finite geometry," *IEEE Trans. Signal Process.*, vol. 63, no. 4, pp. 1017–1029, Feb. 2015.
- [10] S. Jafarpour, M. F. Duarte, and R. Calderbank, "Beyond worst-case reconstruction in deterministic compressed sensing," in *Proc. IEEE Int. Symp. Inf. Theory (ISIT)*, Cambridge, MA, USA, Jul. 2012, pp. 1852–1856.
- [11] G. Boeing, "Visual analysis of nonlinear dynamical systems: Chaos, fractals, self-similarity and the limits of prediction," *Systems*, vol. 4, no. 37, p. 37, Nov. 2016.
- [12] S. Saravanan, M. Lavanya, R. V. Sai, and R. Kumar, "Design and analysis of linear feedback shift register based on various tap connections," *Elsevier, Procedia Eng.*, vol. 38, pp. 640–646, Sep. 2012.
- [13] G. Mazzini, R. Rovatti, and G. Setti, "Randomly-flipped linear feedback shift registers: Spectral analysis and conjectures," in *Proc. IEEE Int. Symp. Spread Spectr. Techn. Appl.*, Bologna, Italy, Aug. 2008, pp. 751–755.
- [14] B. Kaliannan and V. S. R. Pasupureddi, "A low power CMOS imager based on distributed compressed sensing," in *Proc. 13th Int. Conf. Embedded Syst., VLSI Design, 27th Int. Conf.*, Jan. 2014, pp. 534–538.
- [15] M. Dadkhah, M. J. Deen, and S. Shirani, "CMOS image sensor with area-efficient block-based compressive sensing," *IEEE Sensors J.*, vol. 15, no. 7, pp. 3699–3710, Jul. 2015.
- [16] P. Indyk, "Explicit constructions for compressed sensing matrices," in *Proc. 19th Annu. ACM-SIAM Symp. Discr. Algorithms*, Jan. 2008, pp. 30–33.
- [17] W. Lu, W. Li, K. Kpalma, and J. Ronsin. (Oct. 2013). "Nearoptimal binary compressed sensing matrix." [Online]. Available: https://arxiv.org/abs/1304.4071
- [18] W. Lu and S.-T. Xia, "Construction of ternary matrices with small coherence for compressed sensing," *Electron. Lett.*, vol. 52, no. 6, pp. 447–448, Mar. 2016.
- [19] E. Jen, "Aperiodicity in one-dimensional cellular automata," Phys. D, Nonlinear Phenomena, vol. 45, nos. 1–3, pp. 3–18 Sep. 1990.
- [20] S. Wolfram, "Statistical mechanics of cellular automata," *Rev. Mod. Phys.*, vol. 5, no. 3, pp. 601–644 Jul. 1983.
- [21] S. Ninagawa, "Power spectral analysis of elementary cellular automata," *Complex Syst. Publications, Inc.*, vol. 17, no. 4, pp. 399–411, Dec. 2008.
- [22] S. Dhingra, "Comparison of LFSR and CA for BIST," Ph.D. dissertation, Dept. Elect. Comput. Eng. Auburn Univ., Auburn, AL, USA, 2005. [Online]. Available: http://www.researchgate.net/publication/245717746
- [23] R. Obermeier and J. A. Martínez-Lorenzo, "Sensing matrix design via mutual coherence minimization for electromagnetic compressive imaging applications," *IEEE Trans. Comput. Imag.*, vol. 1, no. 2, pp. 217–229, Jun. 2017.
- [24] B. A. Ndaw, D. Sow, and M. Sanghare, "Construction of maximum period linear feedback shift registers (LFSR) (primitive polynomials and linear recurring relations)," *Brit. J. Math., Comput. Sci.*, vol. 11, no. 4, pp. 403–406, 2015.
- [25] S. Mun and J. E. Fowler, "Block compressed sensing of images using directional transforms," in *Proc. IEEE Int. Conf. Image Process.*, Cairo, Egypt, Nov. 2009, pp. 3021–3024.

- [26] A. Akbari and M. Trocan, "Robust image reconstruction for block-based compressed sensing using a binary measurement matrix," in *Proc. IEEE Int. Conf. Image Process.*, Athens, Greece, 2018, pp. 1832–1836.
- [27] A. Akbari, D. Mandache, M. Trocan, and B. Granado, "Adaptive saliency-based compressive sensing image reconstruction," in *Proc. IEEE Int. Conf. Multimedia Expo Workshops*, Seattle, WA, USA, Jul. 2016, pp. 1–6.
- [28] M. Trocan, T. Maugey, J. Fowler, and B. Pesquet-Popescu, "Disparitycompensated compressed-sensing reconstruction for multiview images," in *Proc. IEEE Conf. Multimedia Expo*, Singapore, Jul. 2010, pp. 1225–1229.
- [29] A. Akbari, M. Trocan, and B. Granado, "Residual based compressed sensing recovery using sparse representations over a trained dictionary," in *Proc. Int. ITG Conf. Syst., Commun. Coding*, Hamburg, Germany, Feb. 2017, pp. 1–6.
- [30] K. B. Petersen and M. S. Pedersen, *The Matrix Cookbook*. Lyngby, Denmark: Technical Univ. Denmark, Nov. 2012.
- [31] T. Blumensath and M. E. Davies, "Iterative thresholding for sparse approximations," *J. Fourier Anal. Appl.*, vol. 14, nos. 5–6, pp. 629–654, Dec. 2008.
- [32] K. Bredies and D. A. Lorenz, "Linear convergence of iterative softthresholding," J. Fourier Anal. Appl., vol. 14, no. 5, pp. 812–837, 2008.
- [33] A. Akbari, M. Trocan, and B. Granado, "Sparse recovery-based error concealment," *IEEE Trans. Multimedia*, vol. 19, no. 6, pp. 1339–1350, Jun. 2017.
- [34] S. Becker, J. Bobin, and E. J. Candès, "NESTA: A fast and accurate first-order method for sparse recovery," *SIAM J. Imag. Sci.*, vol. 4, no. 1, pp. 1–39, 2011.
- [35] M. A. T. Figueiredo, R. D. Nowak, and S. J. Wright, "Gradient projection for sparse reconstruction: Application to compressed sensing and other inverse problems," *IEEE J. Sel. Areas Commun.*, vol. 1, no. 4, pp. 586–597, Dec. 2007.
- [36] T. T. Do, L. Gan, N. Nguyen, and T. D. Tran, "Sparsity adaptive matching pursuit algorithm for practical compressed sensing," in *Proc.* 42th Asilomar Conf. Signals, Syst., Comput., Pacific Grove, CA, USA, Oct. 2008, pp. 581–587.



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He co-founded the Instituto de Microelectrónica de Sevilla, a joint undertaking of the Consejo Superior

de Investigaciones Científicas and the Universidad de Sevilla, and started a Research Lab on Analog and Mixed-Signal Circuits for Sensors and Communications. He has always been looking for a balance between long-term research and industrial innovation. In 2001, he was the main promotor of AnaFocus, Ltd., where he has served as the CEO until 2009, when the company reached maturity as a worldwide provider of smart CMOS imagers. AnaFocus was founded on the basis of his patents on vision chip architectures. He also participated in the foundation of the Hungarian start-up AnaLogic, Ltd. He has filed ten patents. His research interests include smart imagers, vision chips, and biomedical circuits, with an emphasis on system integration. His Lab designed many high-performance mixed-signal chips in the framework of Spanish, European, and USA R&D programs. These included three generation of vision chips, analog front ends for XDSL MoDems, ADCs for wireless communications. ADCs for automotive sensors, chaotic signals generators, and complete MoDems for power-line communications. Many of these chips were state-of-the-art in their respective fields. Some of them entered mass production. He also pioneered the application of chaotic dynamics in instrumentation and communications. His team completed the design and prototyping of the first integrated circuits with controllable chaotic behavior and the design and prototyping of the first, world-wide, chaos-based communication MoDem chips. His team made also significant contributions to the area of structured analog and mixed-signal design and the area of data converter design, including the elaboration of advanced teaching materials on this topic for different industrial courses and the production of two widely quoted books on the design of high-performance CMOS sigma-delta converters. He produced teaching materials on data converters that were delivered to companies and got the Quality Label of EuroPractice.

He has authored/edited 11 books, 43 chapters in contributed books, including original tutorials on chaotic integrated circuits, the design of data converters, and the design of chips for vision, and some 160 journal articles in the peer-review specialized publications. He has presented some 50 invited plenary lectures at different international conferences and has received a number of international awards for his research work. His publications have some 9200 citations and several awards, including the IEEE Guillemin-Cauer Best Paper Award, two Wiley's IJCTA Best Paper Awards, two IEEE ECCTD Best Paper Awards, one IEEE-ISCAS Best Paper Award, one SPIE-IST Electronic Imaging Best Paper Award, the IEEE ISCAS Best Demo-Paper Award, and the IEEE ICECS Best Demo-Paper Award. He has an h-index of 48 and an i10-index of 179 (Google Scholar).

Dr. Rodríguez-Vázquez has chaired different international IEEE and SPIE conferences. Among others, he has served as the TPC Chair for the IEEE ESSCIRC 1992 and 2010 and the General Chair for the IEEE NDES 1996, IEEE CNNA 1996, IEEE ECCTD 2007, IEEE ESSDERC-ESSCIRC 2010, and IEEE ICECS 2012. He has served as the VP Region 8 of the IEEE CASS from 2009 to 2012 and the Chair for the IEEE CASS Fellow Evaluation Committee in 2010 and from 2012 to 2015. He has been appointed as the General Chairman of the IEEE ISCAS 2020. He has served and is currently serving as an Editor, and Associate Editor, and the Guest Editor for different IEEE and non-IEEE journals. He is on the committee of many international journals and conferences.



Ricardo Carmona-Galán received the Licenciado (B.Sc.) and Doctor (Ph.D.) degrees in electronic physics from the University of Seville, Spain, in 1993 and 2002, respectively. He designed his first chip, a smart CMOS image sensor delivering the Radon Transform of an input image binarized with an automatic threshold, as his graduation project. From 1994 to 1996, he was a Graduate Student with the Institute of Microelectronics of Seville. From 1996 to 1998, he was a Research Assistant with the Prof. Chua's Laboratory, EECS Department,

University of California at Berkeley. From 1999 to 2005, he was an Assistant Professor with the School of Engineering, University of Seville. He was awarded with a Certificate of Teaching Excellence.

Since 2005, he has been a Tenured Scientist at the Institute of Microelectronics of Seville (CSIC). His main research interests include vision chips, in particular smart CMOS imagers for low-power vision applications, such as robotics, vehicle navigation, and vision-enabled wireless sensor networks. He is also interested in CMOS-compatible sensing structures for LWIR and MWIR imaging, single-photon detection, and detectors for high-energy physics. He also held a Post-doctoral position at the Prof. Porod's Laboratory, University of Notre Dame, Notre Dame, IN, USA, from 2006 to 2007, where he studied interfaces for CMOS-compatible IR-light sensing nanostructures.

He has participated in many international projects, holds several patents, and has collaborated with the start-up companies: AnaFocus, Seville, and Eutecus, Berkeley. He has authored more than 160 papers in refereed journals and conferences and several book chapters. He received best paper awards from the *International Journal of Circuit Theory and Applications*, and the IS&T/SPIE Electronic Imaging Conference on Image Sensors, and the IEEE ISCAS Technical Committee on Sensory Systems.

Dr. Galán is a Senior Member of the IEEE and the Circuits and Systems, Solid-State Circuits, and Computer Societies. He is also a member of the ACM, HiPEAC, and the IEEE-CASS Technical Committees on Cellular Nanoscale Networks and Array Computing and on Sensory Systems, of which he is the Secretary-Elect. He also belongs to the IEEE Sensors Council. He is a member of the Steering Committee of the Workshop on Architecture of Smart Cameras. He has been the General Chair of the 9th International Conference on Distributed Smart Cameras (ICDSC 2015) in cooperation with ACM SIGBED. He has served as an Associate Editor for the IEEE TRANSACTIONS ON CIRCUITS AND SYSTEMS-I: REGULAR PAPERS from 2012 to 2013. He is currently an Associate Editor of the Journal of Real-Time Image Processing (Springer). Dr. Galán is the Coordinator of ACHIEVE-ITN, an Innovative Training Network (H2020 Marie Skłodowska-Curie Action) dedicated to research on Advanced Hardware/Software Components for Integrated/Embedded Vision Systems. This research and training programme started in 2017 and ends in 2021.