Charge Dynamics and Spin Blockade in a Hybrid Double Quantum Dot in Silicon

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Electron spin qubits in silicon, whether in quantum dots or in donor atoms, have long been considered attractive qubits for the implementation of a quantum computer because of silicon's "semiconductor vacuum" character and its compatibility with the microelectronics industry. While donor electron spins in silicon provide extremely long coherence times and access to the nuclear spin via the hyperfine interaction, quantum dots have the complementary advantages of fast electrical operations, tunability, and scalability. Here, we present an approach to a novel hybrid double quantum dot by coupling a donor to a lithographically patterned artificial atom. Using gate-based rf reflectometry, we probe the charge stability of this double quantum-dot system and the variation of quantum capacitance at the interdot charge transition. Using microwave spectroscopy, we find a tunnel coupling of 2.7 GHz and characterize the charge dynamics, which reveals a charge T_2^* of 200 ps and a relaxation time T_1 of 100 ns. Additionally, we demonstrate a spin blockade at the inderdot transition, opening up the possibility to operate this coupled system as a singlet-triplet qubit or to transfer a coherent spin state between the quantum dot and the donor electron and nucleus.

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I. INTRODUCTION

Quantum physics, when applied to computing, is projected to usher in a new computational paradigm with great potential applications in cryptography and simulation [1]. Most of the research effort has focused on the creation of the building blocks of quantum computing, called qubits: elements that have to be resistant to environmental disturbance in order to preserve the coherent information encoded in them. Many different media have been investigated to create such qubits, from superconducting devices [2] to atomlike spins in semiconductors [3]. In the last decade, silicon has attracted particular interest [4] because of its ability to create qubits out of implanted donor atoms [5], as well as the compatibility of a silicon quantum architecture with the microelectronics industry [6].

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Interest in donor-based spin qubits in silicon has been motivated by their exceptionally long electron spin coherence times, exceeding 1 s in isotopically enriched ²⁸Si [7]. Additionally, the donor electron spin can be a gateway to access the donor nuclear spin, which has longer coherence times [8], even at room temperature [9], and the potential to serve as a quantum memory [10]. Moreover, the single-shot read-out of single electron [11] and nuclear [12] spins, a milestone for donor-based quantum computing [6,13,14], has recently been demonstrated in nanoelectronic silicon devices.

On the other hand, artificial atoms such as electrostatically defined quantum dots offer complementary advantages as qubits, notably in their tunability [15], flexible coupling geometries [16], and opportunities for fast electrical [17–20] and magnetic [21,22] control of spin. It is therefore attractive to investigate the possibility of *hybrid* architectures, which bring together the advantages of these two systems by coupling a quantum dot to a donor atom [23,24]. Such a double dot could take advantage of fast spin manipulations using gate voltage to form a hybrid singlet-triplet qubit coupled to the long-lived quantum memory offered by its nuclear spin. In addition, this hybrid architecture could be used to create spin buses with

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quantum-dot chains to mediate quantum information stored in donor qubits over long distances [25,26].

In this work, we present measurements taken on a silicon nanowire transistor indicating a coupled system formed by a single phosphorus atom and a quantum dot. We show that this hybrid system behaves as a double quantum dot, and we characterize the high-frequency admittance of the coupled system by rf gate-based sensing. Quantumcapacitance changes at the interdot charge transition (ICT) and microwave (MW) spectroscopy allow us to characterize the tunnel coupling and the charge dynamics. Finally, we demonstrate spin blockade effects between singlet and triplet states by applying a magnetic field that changes the resonator response at the ICT. These results demonstrate the potential of a donor-dot system as a new singlet-triplet qubit.

II. CHARACTERIZATION OF THE DEVICE

A. Device fabrication and measurement setup

The nanowire transistor device, sketched in Fig. 1(a), is fully compatible with the CMOS industry. It is fabricated from an silicon on insulator substrate composed of a 145-nm buried oxide layer and a 11-nm-thick silicon layer doped with phosphorus at a concentration of 5×10^{17} cm⁻³. The doped silicon layer is etched to create a 200-nm-long and 30-nm-wide nanowire by means of deep-UV lithography. A 30-nm-wide wraparound top gate is defined using a SiO₂(0.8 nm)/HfSiON(1.9 nm) stack for the gate dielectric followed by TiN(5 nm)/poly-Si(50 nm) as the top gate material. The self-aligned source and drain (not represented on the sketch) are formed by ion implantation after the deposition of 20-nm-thick Si₃N₄ spacers (see Fig. S1).

Measurements are performed at the base temperature of an Oxford Instruments Triton 200 cryogen-free dilution refrigerator (30 mK). High-sensitivity charge detection is achieved by radio-frequency reflectometry on a tank circuit composed of a surface-mounted inductance (390 nH), a parasitic capacitance to ground (0.75 pF), and the device capacitance between the transistor top gate and the channel [27]. Rf reflectometry is performed close to the resonance frequency (294 MHz), with the input power set to -85 dBm and the reflected signal amplified by a low-noise cryogenic amplifier anchored at 4 K. The signal is further amplified and demodulated at room temperature, as shown in Fig. 1(b). An on-board bias tee is used to apply both dc and rf voltages on the top gate. The undoped silicon substrate is activated by flashing a surface-mounted blue LED to generate free carriers, and it can then be used as a back gate [28].

B. Charge stability diagram

We first characterize the device charge stability diagram as a function of top-gate (V_{tg}) and back-gate voltages (V_{bg}). Rf reflectometry is used to detect a change in the sample impedance due to a dissipative or dispersive event

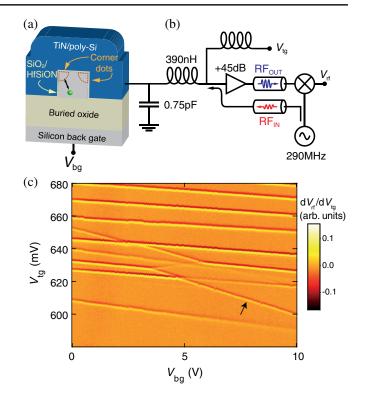


FIG. 1. (a) A sketch of the device, showing a cross section of the nanowire channel and wraparound gate, with quantum dots localized in the top corners of the nanowire and a dopant located deeper in the channel. For clarity, the source and drain are not represented on this sketch. (b) In our rf reflectometry measurements, an rf signal is sent through a directional coupler to a resonator made from the device capacitance, parasitic capacitance, and surface-mounted inductor. The reflected signal is amplified at low temperatures and demodulated using a reference signal to give $V_{\rm rf}$. (c) The derivative of the demodulated resonator response as a function of $V_{\rm tg}$ and $V_{\rm bg}$ gives the charge stability diagram. Transitions with weak V_{bg} dependence are attributed to changes in the charge occupancies of the two distinct (and uncoupled) corner dots. The charge transition indicated with a black arrow is attributed to a phosphorus atom in the bulk of the nanowire.

occurring below the top gate. As a consequence, it can probe a change of resistance due to, for instance, a charge tunneling between the source and a localized state [27,29,30], or a change of quantum capacitance induced by a charge tunneling between two quantum dots [31,32]. Figure 1(c) presents the Gaussian-filtered numerical derivative of the reflected resonator signal amplitude as a function of V_{tg} and V_{bg} . It shows a set of charge transitions with a small dependence on V_{bg} corresponding to single charge tunneling from the source or drain to quantum dots localized below the top gate. We attribute these charge transitions to the so-called corner quantum dots, formed in the top corners of the nanowire where the electric field is maximum, when the channel is in the subthreshold regime [33]. These dots have relatively large charging energies, $E_c \sim 18$ meV, and a strong top-gate lever arm, $\alpha_{tg} \sim 0.85$, similar to what has been reported elsewhere [27,33].

However, one single transition, indicated with a black arrow in Fig. 1(c), is more coupled to the back gate than the other charge transitions: This corresponds to a localized state lying further away from the nanowire-top-gate interface. Such subthreshold resonances are commonly attributed to single donors located in the channel [34,35]. This is likely to be the case in the present sample and in the one presented in Fig. S5 (see Ref. [36]), as the charging event occurs very close to the threshold (650 mV) indicating an impurity state close to the conduction band edge, as expected for phosphorus atoms in silicon [37]. In addition, from the stability diagram and the back-gate or top-gate lever-arm ratio, we can place the impurity far below the surface, eliminating any interface charge trap or electron puddle, while at the same time, the number of bulk defects in the nanowire active region $(30 \text{ nm} \times 30 \text{ nm} \times 11 \text{ nm})$ is a few orders of magnitude lower than the average number of dopants that were implanted (about 5 dopants in the channel). Finally, no subthreshold signatures were observed in any of the undoped devices (see Ref. [27]).

III. CHARGE DYNAMICS

A. Interdot charge transition and quantum capacitance

In Fig. 2(a) we focus on a particular region of interest in the charge stability diagram, showing the classic signature of a double quantum dot through the presence of an extra ridge at the intersection between the charge transitions of a corner dot and the donor. The charge number (1,1)corresponds to one electron located on the corner dot and one at the donor site, and (0,2) indicates that both electrons are on the donor. This charge assignment is deduced from two complementary measurements. First, we exploit the phase contrast of the reflected signal on the stability diagram (see Ref. [36], Fig. S2), to assign the two corner dots' charge occupancy. We deduce that the corner dot transition, seen in Fig. 2(a), corresponds to a transition from (0,1) to (1,1). Second, the charge transition shifts to higher gate voltage with the magnetic field (see Ref. [36], Fig. S3). This shift indicates that the Zeeman energy increases the donor chemical potential; as a result and in agreement with previous literature [38], we attribute the charge degeneracy to a $D^0:D^-$ transition, where D^0 is the neutral donor and D^- the anion. Moreover, we observe this transition very close to the threshold, as expected [39].

We now investigate the coupling between the donor and one of the corner dots. The reflectometry signature of a charge tunneling between the donor and a corner dot is obtained by measuring the resonator response at an intersection between their charge transitions. The signal amplitude A at the ICT can be shown to be a function of the quantum capacitance; its model is described in Ref. [40] and depicted in Figs. 2(b) and 2(c). When the energy levels

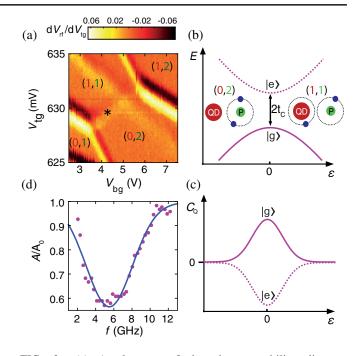


FIG. 2. (a) A close-up of the charge stability diagram showing the charge transition between a corner dot and a donor, with charge occupancies labeled as (corner dot, donor). The "asterisk" indicates the avoided level crossing sketched in (b) the energy levels of the hybrid double quantum dot, where the quantum dot and the P atom form bonding $|g\rangle$ and antibonding $|e\rangle$ states when the detuning ϵ is near zero. For simplicity, spin effects are omitted here and will be discussed later. For $|\epsilon| \gg 0$, the double dot has ionic-type wave functions, where the charges are localized on the dot or donor. (c) States $|g\rangle$ and $|e\rangle$ have opposite values of quantum capacitance, and hence, monitoring [panel (d)] the interdot charge transition (asterisk), as a function of applied microwave frequency, shows a reduction in signal amplitude upon resonance with the $|g\rangle$: $|e\rangle$ transition (see S4 for more details).

of the donor and the corner dot are brought into resonance, the tunnel coupling between the two quantum systems gives rise to a set of molecular orbitals: a bonding state and an antibonding state. The quantum capacitance is directly proportional to the curvature of the eigenenergies with respect to detuning [31],

$$C_q = -(e\alpha)^2 \frac{\partial^2 E}{\partial \epsilon^2},\tag{1}$$

where α is the coupling between the resonator and the double quantum dot, *E* the eigenenergies, and *e* the detuning energy. The quantum capacitance is then maximum for $\epsilon = 0$, where the curvature of the eigenenergies is maximum. Notably, the quantum capacitances of the bonding state $|g\rangle$ and antibonding state $|e\rangle$ are of opposite sign.

B. Tunnel coupling and charge dephasing time

We exploit these quantum capacitance signatures to characterize the tunnel coupling between the donor and the corner dot, as well as the charge dynamics in the system, starting by performing MW spectroscopy using a local antenna coupled to the top gate. Figure 2(d) shows the relative change of signal, A/A_0 , at the ICT as a function of the MW frequency, where A_0 is the signal amplitude without any MW applied. The MW excitation causes a fraction of the ground-state population to be promoted to $|e\rangle$, reducing the averaged quantum capacitance. As a result, the maximum change of signal occurs when the MW frequency matches the tunnel splitting, which in our case gives $\Delta = 2t_c = 5.5$ GHz. We use a Gaussian function to fit the data, from which we extract the charge coherence time $T_2^* \sim 200$ ps, very similar to the one measured in a double-donor system in the same type of nanowire transistors [35]. The present dephasing rate is close to the tunnel splitting; therefore, implementation of a quantum protocol that uses coherent charge transfer to mediate spin information, such as coherent transfer by adiabatic passage (CTAP) [41], would have limited fidelity. However, while the tunnel splitting might be tunable in alternative device architectures [35], the coherence time may be improved either by going to lower temperatures (if it is limited by charge relaxation) or by improving the charge stability of the device (if it is charge noise limited).

C. Charge relaxation

To confirm that the coherence time is not limited by charge relaxation, we present a measurement of the charge relaxation time T_1 between the bonding and antibonding states. We use MW excitation to populate $|e\rangle$ and measure how fast it decays to the ground state. The relaxation time is expected to be short compared to our demodulator and voltage amplifier bandwidth (about a few MHz), making a transient measurement impossible in our case. Instead, we use a procedure developed by Petta *et al.* [42], where the MW excitation is chopped at some frequency $1/\tau$ with a 50% duty cycle while time averaging the signal at the ICT. We define the charge polarization as $P = (P_g - P_e)$, with P_g and P_e the ground- and excited-state populations.

When the period τ is long compared to T_1 , the timeaveraged polarization is $\langle P \rangle \approx 1/2(P_{sat} + P_{th})$. This is because the polarization is, to a good approximation, at saturation, P_{sat} , during the first part of the cycle (MW on) and at thermal equilibrium, P_{th} , during the second part (MW off). When τ is short compared to T_1 , the system has no time to relax to the ground state, and it is then at saturation throughout the cycle, giving $\langle P \rangle \approx P_{sat}$. Charge relaxation takes the system between these extremes, giving

$$\langle P \rangle = \frac{(P_{\text{sat}} + P_{\text{th}})}{2} + (P_{\text{sat}} - P_{\text{th}}) \frac{T_1(1 - e^{-\tau/2T_1})}{\tau}.$$
 (2)

In the present study, the normalized amplitude A/A_0 at the ICT is directly proportional to $\langle P \rangle / P_{\text{th}}$; thus, $P_{\text{sat}} / P_{\text{th}}$ is obtained from the ICT signal under continuous MW

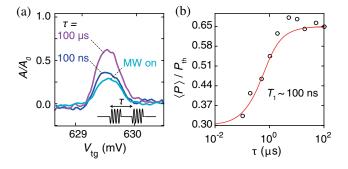


FIG. 3. (a) Interdot charge transitions measured under continuous resonant MW (light blue), and chopped MW at 10 MHz (dark blue) and at 10 kHz (purple). (b) Renormalized average charge-state population difference as a function of the chopping period τ . The data are fitted using Eq. (2) to give the charge relaxation time T_1 .

excitation [see Fig. 3(a)]. The power dependence of the saturation level is plotted in Ref. [36], Fig. S3. Also shown in Fig. 3(a) is the ICT under MW excitation chopped with 100-ns and $100-\mu$ s time periods. As expected, the signal amplitude is greater when the charge has more time to relax.

The time-averaged polarization recorded for different values of τ is presented in Fig. 3(b) and is fitted to give $T_1 \sim 100$ ns. Since $T_1 \gg T_2^*$, charge coherence is not limited by charge relaxation. Moreover, the phonon-induced dephasing rate in silicon is expected to be in the MHz range [43]. Therefore, background charge fluctuations or noise in the gate voltages are assumed to be the main dephasing sources as they induce fluctuations in both tunnel coupling and detuning energy [44].

IV. SPIN BLOCKADE

We now investigate spin-related effects in the system, in order to demonstrate unambiguously that the ICT is of even parity. Figure 4(a) displays the ICT as a function of V_{tg} and the magnetic field, B. It shows that the reflectometry signal disappears with an increasing magnetic field and that, above about 1.2 T, the variation of quantum capacitance at the ICT has completely vanished. This result may be explained as follows: (i) at $\epsilon = 0$, as the magnetic field is increased, the population of the singlet state decreases while the population of the T_{-} triplet state increases (and eventually dominates for $g\mu_B B \gg k_B T$), and (ii) the triplet state has a linear dependence on detuning and hence a zero quantum capacitance signature according to Eq. (1). A similar response has been observed in an InAs double quantum dot coupled to a rf resonator [40] and has been theoretically investigated in Ref. [45].

The shift in the maximum amplitude position observed at a low field [see Fig. 4(c)] can then be understood by considering the detuning value at which the singlet and T_{-} states intersect, ϵ_{ST} , which shifts with magnetic field, in

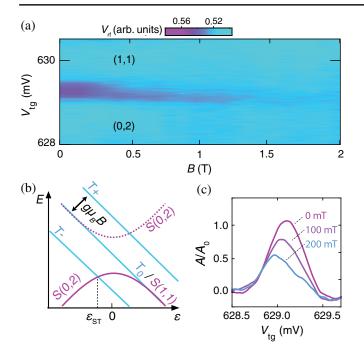


FIG. 4. (a) The quantum capacitance signature of the interdot charge transition (ICT) becomes strongly suppressed with increasing magnetic field, as the T_{-} triplet state becomes the ground state, as shown in the energy-level diagram [panel (b)] near the (1,1) to (0,2) transition in the presence of a magnetic field. $e_{\rm ST}$ denotes the value at which the singlet and T_{-} states intersect, and is responsible for the shift and asymmetry observed in (c), the ICT measured at 0 mT, 100 mT and 200 mT.

accordance with Fig. 4(b). For $\epsilon < \epsilon_{ST}$, there is a singlet ground state and the quantum capacitance has a finite value, albeit one that decreases for more negative ϵ_{ST} . For $\epsilon > \epsilon_{ST}$, there is a T_{-} ground state and the quantum capacitance vanishes. The asymmetric line shape observed is therefore a signature of the system with a significant population, which follows the ground state, even at the S:T₋ intersection. This may be understood by the presence of an avoided level crossing combined with the fact that these measurements are inherently multipassage experiments. The mixing between these two states is generally induced by the magnetic-field gradient created by the nuclear spin bath [17]. In contrast, in this silicon hybrid double dot, the mixing should be dominated by the hyperfine interaction with the nuclear spin localized at the donor site. As a result, neglecting the Overhauser field created by the ²⁹Si, the electron located on the donor is detuned by $\delta = A\mathbf{S} \cdot \mathbf{I}$, where A is the hyperfine coupling constant, S the electron spin, and I the donor nuclear spin. In the case of phosphorus, I = 1/2 and A = 117 MHz, the S(1, 1)-T₀ coherent evolution at a very low exchange coupling could be driven at around 50 MHz. In the case of bismuth, I = 9/2 and A = 1.48 GHz, the driving frequency should be even higher, ranging from 300 MHz up to 3 GHz depending on the nuclear spin state.

V. OUTLOOK AND CONCLUSION

In conclusion, we have investigated a hybrid double-dot system formed by a single donor and a corner dot in a single silicon nanowire transistor. Combining rf reflectometry with microwave spectroscopy has allowed us to determine a tunnel splitting $2t_c = 5.5$ GHz as well as to characterize the charge dynamics. The charge dephasing rate $1/T_2^* \sim 5$ GHz is similar to the tunnel splitting and could be reduced by removing the nitride spacers in the device, which are known to possess a large trapped charge density. Also, the tunnel coupling might be tunable in a different architecture, with a split top gate for instance, such as that used in Ref. [35]. Furthermore, we have demonstrated a spin blockade at the interdot charge transition due to the presence of singlet and triplet states. The time evolution of such a singlet-triplet qubit should be governed by the donor nuclear spin, enabling controlled rotation gates when combined with NMR excitation [46]. Finally, exploiting the interdot exchange coupling would allow a SWAP operation between the corner dot and the donor atom spin state and, eventually, its storage in the donor nuclear spin using electronuclear double-resonance techniques.

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