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Optimisation Validation

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Abstract

We introduce the idea of *optimisation validation*, which is to formally establish that an instance of an optimising transformation indeed improves with respect to some resource measure. This is related to, but in contrast with, *translation validation*, which aims to establish that a particular instance of a transformation undertaken by an optimising compiler is semantics preserving. Our main setting is a program logic for a subset of Java bytecode, which is sound and complete for a resource-annotated operational semantics. The latter employs *resource algebras* for measuring dynamic costs such as time, space and more elaborate examples. We describe examples of optimisation validation that we have formally verified in Isabelle/HOL using the logic. We also introduce a type and effect system for measuring static costs such as code size, which is proved consistent with the operational semantics.

Keywords: Compiler Optimisation, Translation Validation, Program Logic, Java Virtual Machine Language, Cost Modelling, Resource Algebras, Lightweight Verification.

1 Introduction

We are interested in certifying the resource usage of mobile code for the Java platform. In previous work [3,1,6] we have described a proof-carrying code infrastructure which accomplishes this for memory usage. A class file is accompanied by a proof certificate which describes the resource usage of the main method of the program;

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we use a program logic with judgments of the form $\triangleright e : \{P\}$, stating that expression e satisfies assertion P . For example

$$\triangleright e_{\text{main}} : \{r = F(h)\}$$

where h is the starting heap of the program (in particular, containing the arguments to the `main` method) and r is the memory consumption of the program expressed as a function of the size of the arguments in h .

In this paper we investigate a significant extension of this framework and a particular application. First, we generalise the form of resources so that a wider range of notions is covered, in a uniform fashion. Second, we consider *orderings* on resources, which allow us to talk about *optimisation validation*, in the sense that we can establish when one program consumes fewer resources than another.

This turns out to be of interest for the compiler community, where much research has been invested in trying to select (the order of) the best compiler transformation in the current context, given the available resources. However, this may be problematic:

“[...] current optimisation strategies do not always achieve the performance goals. Indeed, it is well known that optimizations may degrade performances in certain circumstances. The difficulty is that current techniques cannot always determine when it is beneficial or harmful to apply an optimization.” [31]

This is where optimisation validation comes to the rescue: technically, it is inspired by the idea of *translation validation* [23], an alternative to the wholesale verification of translators and compilers. In this approach, one instead constructs a validation mechanism that, after every run of a compiler, formally confirms that the target code produced on that run is a correct translation of the source producing

“[...] the same result while (*hopefully*) executing in less time or space or consuming less power.” [24]

(our emphasis). Here, optimisation validation take the improvement in resource usage as being the primary motivation, and therefore, what should be checked. This is appropriate in scenarios such as the safety policies considered in proof-carrying code, where resource usage may even be a more important concern than correctness, because it encompasses the security requirements of the domain.

1.1 Notions of optimisation.

To consider validating optimisations, we must first define what we mean by optimisation in our setting. We suppose that a program is given as a collection of classes, one of which includes a nominated `main` method. A simple notion of *dynamic* optimisation refers to every terminating execution of this method. Let P_1 be the program before optimisation and P_2 be the program after:

$$P_1 \longrightarrow P_2$$

We only need to consider the costs for the bodies of the main method in each program,

$$e_1 \longrightarrow e_2$$

Changes in other methods may be optimising, neutral or even non-optimising; at this point we do not study optimisations within nested program contexts. To be considered an optimisation, we want to establish that the transformation is improving with respect to a cost model. We capture the latter with the notion of *resource algebra* \mathcal{R} , which contains components for measuring the cost of executing each kind of instruction, along with an ordering on those costs. The overall (dynamic) cost may depend on the input of the program, and it is measured by execution in a operational semantics annotated with calculations using \mathcal{R} . If for all input heaps both e_1 and e_2 converge, then the resource consumption of e_2 should improve on that of e_1 :

$$h \vdash e_1 \Downarrow r_1 \ \wedge \ h \vdash e_2 \Downarrow r_2 \implies r_2 \leq r_1$$

where the ordering \leq refers to the ordering from \mathcal{R} . We may assume, without loss of generality, that the input pointer for the argument(s) to `main` is fixed on every execution.

1.2 Optimisation sequences.

The above defines our notion of a single-step optimisation. For several optimisations in sequence, it is enough to consider an optimisation between the initial and final program for the resource algebra of interest \mathcal{R} . However, we often want to decompose a sequence of optimisations into several transformations which are individually optimising. Then we can show the existence of a sequence of optimising steps:

$$P_1 \longrightarrow P_2 \longrightarrow \dots \longrightarrow P_n$$

where each $P_i \longrightarrow P_{i+1}$ is an optimisation for some particular resource algebra \mathcal{R}_i . Additionally, each step in the optimisation should be non-increasing for the target cost model \mathcal{R} . A *proper* optimisation sequence has at least one step for which costs in \mathcal{R} strictly decrease from some P_i to P_{i+1} .

1.3 Validating optimisations by program logic.

To state and prove (dynamic) cost optimisations, we use a program logic that provides assertions about functions bounding the resources consumed. We must find assertions of the form:

$$ST_1 \triangleright e_1 : \{F_1(h) \leq r\} \qquad ST_2 \triangleright e_2 : \{r \leq F_2(h)\}$$

where the *specification tables* ST_i associate an assertion to each method and loop in the program, providing the appropriate invariant. The assertions state that the resources consumed when executing P_1 are bounded from below by some function F_1 of the input heap, and that the resources consumed by P_2 are bounded from

above by a function F_2 . To show that P_2 is an optimisation of P_1 we must now prove that:

$$\forall h. F_2(h) \leq F_1(h)$$

(in particular, this holds trivially in case $F_1 = F_2$).

1.4 Static optimisations.

Static costs such as code size are commonly used as metrics for optimisation and some dynamic costs can be usefully approximated with static measurements. We cover both possibilities by introducing a notion of *static* resource algebra \mathcal{S} . To measure static costs, we use a type system with effects. For two function bodies e_1 and e_2 we must find a type t and effects s_1 and s_2 such that:

$$\Gamma_{\text{main}} \vdash_{\Sigma_1} e_1 : t, s_1 \qquad \Gamma_{\text{main}} \vdash_{\Sigma_2} e_2 : t, s_2$$

where the typing context for the body of main has the form $\text{args} : \text{String}[]$ and Σ_1 and Σ_2 are the resource typing signatures of programs P_1 and P_2 respectively, see Sect. 5. For P_2 to be a static optimisation of P_1 we should establish that $s_2 \leq s_1$, where the ordering \leq now refers to the ordering on static costs. An ideal notion of optimisation would be w.r.t. a pair $(\mathcal{R}, \mathcal{S})$ of target dynamic and static cost models; a sequence of optimisations might alternate dynamic and static reductions as appropriate. A typical example is to use time *and* code size to validate optimisations such as loop unrolling, see Sect. 4.1. To simplify exposition here we consider the costs separately.

This paper is organized as follows. In Sect. 2 we present the dynamic semantics of our language, introduce resource algebras, and describe some typical instantiations. In Sect. 3, we present a program logic that generalizes the logic presented in [1] to arbitrary resource algebras. Sect. 4 gives example optimisation validations, including standard compiler optimisation steps, tail-call optimisation and an application specific one. Sect. 5 examines the static system, while Sect. 6 concludes with a summary and discussion of related work.

2 Resource annotated operational semantics

We use a functional form of Java bytecode called Grail [7], although the approach would work for other languages endowed with a structural operational semantics. Grail retains the object and method structure of JVM, but represents method bodies as sets of mutually tail-recursive first-order functions. The language is built from values v , arguments a , and function body expressions e (in this paper we do not

mention static fields and virtual invocation, which are accounted for elsewhere [1]):

$$\begin{aligned}
v ::= & () \mid l_C \mid i \mid \text{null}_C \\
a ::= & v \mid x \\
e ::= & a \mid \text{prim } a \ a \mid \text{new } C \mid x.f \mid x.f := a \mid e ; e \mid \text{let } x=e \text{ in } e \\
& \mid \text{if } e \text{ then } e \text{ else } e \mid \text{call } g \mid C.m(\bar{a})
\end{aligned}$$

Here, C ranges over Java class names, f over field names, m over method names, x over variables (method parameters and locals) and g over function names (which correspond to instruction addresses in bytecode). Values consist of integer constants i , typed locations l_C , the unique element $()$ of type unit and the nullary reference null_C . As in JVM, the booleans $bool$ are defined as $\text{true} \stackrel{\text{def}}{=} 1$, $\text{false} \stackrel{\text{def}}{=} 0$.

The (impure) call-by-value functional semantics of Grail coincides with an imperative interpretation of its direct translation into JVM, provided some syntactic conditions are met. In particular, actual arguments in function calls must coincide with the formal parameters of the function definitions. Sample Grail programs are shown in Fig. 1 and 2 in their Isabelle format and in the concrete syntax of our compiler in Fig. 3.

To model consumption of computational resources, our semantics is annotated with a resource counting mechanism based on *resource algebras*.

Definition 2.1 A *resource algebra* \mathcal{R} is a partially ordered monoid $(R, 0, +, \leq)$, i.e. $(R, 0, +)$ is a monoid and (R, \leq) a partially ordered set, where

- (i) 0 is the minimum element: $0 \leq x$;
- (ii) $+$ is order preserving on both sides: $x \leq y$ entails $x + z \leq y + z$ and $z + x \leq z + y$.

Moreover, \mathcal{R} has constants in R for each expression former: \mathcal{R}^{int} , $\mathcal{R}^{\text{null}}$, \mathcal{R}^{var} , $\mathcal{R}^{\text{prim}}$, $\mathcal{R}_C^{\text{new}}$, $\mathcal{R}^{\text{getf}}$, $\mathcal{R}^{\text{putf}}$, $\mathcal{R}^{\text{comp}}$, \mathcal{R}^{let} , \mathcal{R}^{if} , $\mathcal{R}^{\text{call}}$ and a monotone operator $\mathcal{R}_{C,m,\bar{v}}^{\text{meth}} : R \rightarrow R$.

Each constant denotes the cost associated to an instruction, which are then composed via the monoidal operation. The operator $\mathcal{R}_{C,m,\bar{v}}^{\text{meth}}$ calculates a cost for method calls. For some applications, we might parameterise the constants with additional pieces of syntax, for example if we are tracking read/writes of certain variables or charge differently selected function calls and/or primitive operations. For all the resource algebras considered here, composition is commutative; however, for examples where it is not, the order of the operation in the rules is important and matches the evaluation order.

A useful operation on such algebras is the *product*, which we simply under-specify as monoidal product; for $\mathcal{R} = (R, 0, +, \leq)$ and $\mathcal{R}' = (R', 0', +', \leq')$, define $\mathcal{R} \times \mathcal{R}'$ as $(R \times R', \langle 0, 0' \rangle, \odot, \leq^*)$, where:

$$(i) \langle r_1, r'_1 \rangle \odot \langle r_2, r'_2 \rangle \equiv \langle r_1 + r_2, r'_1 +' r'_2 \rangle;$$

(ii) \leq^* is any partial order on $R \times R'$, satisfying the conditions in Def. 2.1.

This allows us to compose various resource algebras without committing to the ordering induced by the product of posets. For instance we may want to take the ordering on $\mathcal{R} \times \mathcal{R}'$ to be lexicographic or simultaneous orderings, see for example the product algebra introduced on page 16. Conversely, we can define the *projection* $\pi_i(\mathcal{R}^n)$ of a product as expected.

The operational semantics defines a judgement

$$E \vdash h, e \Downarrow h', v, r$$

which relates expressions e to environments E (maps from variables to values), initial and final heaps h, h' , result values v and costs $r \in R$. Heaps are partial maps from locations l to objects, where an object is represented as a class name C together with a field table (a map from field names f to values v). We use the following notations for heaps:

$h(l)$	class name of object at l ;
$h(l).f$	field lookup of value at f in object at l ;
$h[l.f \mapsto v]$	field update of f with v at l .

Argument evaluation in an environment E is defined by $eval_E(x) = E(x)$ and $eval_E(v) = v$, while costs are defined by

- $cost() = cost(l_C) = 0$;
- $cost(\text{null}_C) = \mathcal{R}^{\text{null}}$;
- $cost(i) = \mathcal{R}^{\text{int}}$;
- $cost(x) = \mathcal{R}^{\text{var}}$.

The function $\text{fields}(C)$ returns the sequence \overline{f} of fields in the class C , while initval_{f_i} denotes the initial value of the field f_i . For functions and methods, we write body_g and $\text{body}_{C,m}$ to stand for the definition of g and $C.m$, respectively. The complete listing of the operational semantics rules follows:

$$\begin{array}{c}
 \frac{a \neq l_C}{E \vdash h, a \Downarrow h, eval_E(a), cost(a)} \\
 \\
 \frac{E \vdash h, a \Downarrow h, v_a, r_a \quad E \vdash h, a' \Downarrow h, v'_a, r'_a}{E \vdash h, \text{prim } a \ a' \Downarrow h, \text{prim}(v_a, v'_a), r_a + r'_a + \mathcal{R}^{\text{prim}}} \\
 \\
 \frac{l = \text{freshloc}(h) \quad \text{fields}(C) = \overline{f}}{E \vdash h, \text{new } C \Downarrow h[l.f_i \mapsto \text{initval}_{f_i}], l, \mathcal{R}_C^{\text{new}}}
 \end{array}$$

$$\begin{array}{c}
\frac{E \vdash h, x \Downarrow l, h, r_x}{E \vdash h, x.f \Downarrow h, h(l).f, r_x + \mathcal{R}^{\text{getf}}} \\
\\
\frac{E \vdash h, x \Downarrow l, h, r_x \quad E \vdash h, a \Downarrow v, h, r_a}{E \vdash h, x.f := a \Downarrow h[l.f \mapsto v], (), r_x + r_a + \mathcal{R}^{\text{putf}}} \\
\\
\frac{E \vdash h, e_1 \Downarrow h_1, (), r_1 \quad E \vdash h_1, e_2 \Downarrow h_2, v, r_2}{E \vdash h, e_1 ; e_2 \Downarrow h_2, v, r_1 + \mathcal{R}^{\text{comp}} + r_2} \\
\\
\frac{E \vdash h, e_1 \Downarrow h_1, v_1, r_1 \quad E \langle x := v_1 \rangle \vdash h_1, e_2 \Downarrow h_2, v, r_2}{E \vdash h, \text{let } x = e_1 \text{ in } e_2 \Downarrow h_2, v, r_1 + \mathcal{R}^{\text{let}} + r_2} \\
\\
\frac{E \vdash h, e \Downarrow h', 1, r_e \quad E \vdash h', e_1 \Downarrow h'', v, r}{E \vdash h, \text{if } e \text{ then } e_1 \text{ else } e_2 \Downarrow h'', v, r_e + \mathcal{R}^{\text{if}} + r} \\
\\
\frac{E \vdash h, e \Downarrow h', 0, r_e \quad E \vdash h', e_2 \Downarrow h'', v, r}{E \vdash h, \text{if } e \text{ then } e_1 \text{ else } e_2 \Downarrow h'', v, r_e + \mathcal{R}^{\text{if}} + r} \\
\\
\frac{E \vdash h, \text{body}_g \Downarrow h', v, r}{E \vdash h, \text{call } g \Downarrow h', v, \mathcal{R}^{\text{call}} + r} \\
\\
\frac{\text{eval}_E(\bar{a}) = \bar{v} \quad \bar{x} := \bar{v} \vdash h, \text{body}_{C,m} \Downarrow h', v, r}{E \vdash h, C.m(\bar{a}) \Downarrow h', v, \text{cost}(\bar{a}) + \mathcal{R}_{C,m,\bar{v}}^{\text{meth}}(r)}
\end{array}$$

2.1 Resource algebra examples

Some example resource algebras are shown in Table 1. The **Time** algebra models an instruction counter that approximates execution time; each Grail expression form is charged according to the number of JVM instructions to which it expands⁴. The **Heap** algebra counts the size of heap space consumed during execution (ignoring the possibility of garbage collection, which cannot be assumed for an arbitrary JVM). Only the **new** instruction consumes heap. The **Frames** algebra counts the maximal number of frames on the stack during execution. The **MethCnts** algebra traces invocations by accumulating a multiset of invoked method names.

⁴ There are zero costs for the *if* instructions because they are compiled as test and branches; similarly, sequential composition has zero cost in these example algebras.

	Time	Heap	Frames	MethCnts	MethFreq ^{Id}	MethGuard
$ \mathcal{R} $	\mathcal{N}	\mathcal{N}	\mathcal{N}	$MS(Id)$	$\mathcal{N} \times \mathcal{N}$	$\{\text{tt}, \text{ff}\}$
\mathcal{R}^{int}	1	0	0	\emptyset	(1, 0)	tt
$\mathcal{R}^{\text{null}}$	1	0	0	\emptyset	(1, 0)	tt
\mathcal{R}^{var}	1	0	0	\emptyset	(1, 0)	tt
$\mathcal{R}^{\text{prim}}$	1	0	0	\emptyset	(1, 0)	tt
$\mathcal{R}_C^{\text{new}}$	3	$size(C)$	0	\emptyset	(3, 0)	tt
$\mathcal{R}^{\text{getf}}$	2	0	0	\emptyset	(2, 0)	tt
$\mathcal{R}^{\text{putf}}$	3	0	0	\emptyset	(3, 0)	tt
$\mathcal{R}^{\text{comp}}$	0	0	0	\emptyset	(0, 0)	tt
\mathcal{R}^{let}	1	0	0	\emptyset	(1, 0)	tt
\mathcal{R}^{if}	0	0	0	\emptyset	(0, 0)	tt
$\mathcal{R}^{\text{call}}$	1	0	0	\emptyset	(1, 0)	tt
$\mathcal{R}_{C,m,\bar{v}}^{\text{meth}}(r)$	$ \bar{v} + 2 + r$	r	$r + 1$	$r \cup_+ \{C.m\}$	$\text{Freq}_{C,m, \bar{v} }(r)$	$G_{C,m}(\bar{v}) \wedge r$
$0_{\mathcal{R}}$	0	0	0	\emptyset	(0, 0)	tt
$+\mathcal{R}$	+	+	max	\cup_+	$+\text{Freq}$	\wedge
$\leq_{\mathcal{R}}$	\leq	\leq	\leq	\subseteq_+	\leq_{Freq}	\leq_{Guard}

The notation $|\bar{v}|$ denotes the length of the list $v_1 \dots v_n$. For method counts, \cup_+ and \subseteq_+ are multiset union and subset respectively. For frequencies, we define $\text{Freq}_{Id,n}(t, p) = (0, \max(t, p))$ and $\text{Freq}_{C,m,n}(t, p) = (n + 2 + t, p)$ for $C.m \neq Id$. Composition in this case is $(t, p) +_{\text{Freq}}(t', p') = (t + t', \max(p, p'))$ and the ordering $(t, p) \leq_{\text{Freq}}(t', p')$ iff $p \leq p'$. For guards, $G_{C,m}(\bar{v})$ is a boolean valued function for each C, m and $b \leq_{\text{Guard}} b'$ iff $b = \text{tt}$ or $b = b' = \text{ff}$.

Table 1
Example resource algebras

The MethFreq^{Id} algebra calculates a measure of the frequency of calls to the method Id (a long identifier $C.m$), by accumulating the maximal period between successive calls; this is an example of an application specific algebra (see Sect. 4.3 for a motivating example).

Finally, the MethGuard algebra does not calculate a quantitative resource, but rather maintains a boolean monitor that checks whether arbitrary guards $G_{C,m}(\bar{v})$ are satisfied at invocations of method m in class C . If guards are considered as resource usability preconditions (for example, to check that a method parameter lies within some limits), then we may consider an optimisation to be a transformation that ensures the resource preconditions are always satisfied.

In this last case, the resource operator $\mathcal{R}_{C,m,\bar{v}}^{\text{meth}}$ depends on the run-time values v_i , whereas in the other examples it is fixed – only the length of the argument list matters and it is specified by the definition of the method. In general, resource algebras such as this that depend on runtime values can collect traces along the path of computation. The resulting word may be constrained by further policies, specified for example by security automata [27] or by formulae from logics over linear structures. These can be encoded in the higher-order assertion language of our program logic, introduced next.

3 Resource-aware program logic

Our primary basis for optimisation validation is a general-purpose program logic for Grail where assertions are boolean functions over all semantic components occurring in the operational semantics, namely the input environment E and initial heap h ,

the post heap h' , the result value v , and the resources consumed r . An assertion P thus belongs to the type $\mathcal{E} \times \mathcal{H} \times \mathcal{H} \times \mathcal{V} \times \mathcal{R} \rightarrow \text{BOOL}$. A judgement $G \triangleright e : P$ in the logic relates a Grail expression e to an assertion P , dependent on a context $G = \{(e_1, P_1), \dots, (e_n, P_n)\}$ that stores assumptions for recursive program structures, in the spirit of Hoare's original proof rule for procedures [17]. The program logic comprises one rule for each expression form, an axiom and a consequence rule, where we use the following syntactical conventions:

- the square-bracket notation $P[E, h, h', v, r]$ indicates the instantiation of a predicate P ;
- The notation $G \triangleright e : \{\Phi(x)\}$ for a formula Φ with some occurrence of an implicitly universally quantified variable x stands for $G \triangleright e : \lambda x. \Phi(x)$.

$$\frac{(e, P) \in G}{G \triangleright e : P} \qquad \frac{G \triangleright e : P \quad P \longrightarrow Q}{G \triangleright e : Q}$$

$$\frac{}{G \triangleright a : \{h' = h \wedge v = \text{eval}_E(a) \wedge r = \text{cost}(a)\}}$$

$$\frac{}{G \triangleright \text{prim } a_1 a_2 : \{h' = h \wedge v = \text{prim}(\text{eval}_E(a_1), \text{eval}_E(a_2)) \wedge r = \text{cost}(a_1) + \text{cost}(a_2) + \mathcal{R}^{\text{prim}}\}}$$

$$\frac{}{G \triangleright \text{new } C : \{v = \text{freshloc}(h) \wedge h' = h[v.f_i \mapsto \text{initial}_{f_i}] \wedge r = \mathcal{R}_C^{\text{new}}\}}$$

$$\frac{}{G \triangleright x.f : \{h = h' \wedge (\exists l. E(x) = l \wedge v = h(l).f) \wedge r = \text{cost}(x) + \mathcal{R}^{\text{getf}}\}}$$

$$\frac{}{G \triangleright x.f := a : \{(\exists l. E(x) = l \wedge h' = h[l.f \mapsto \text{eval}_E(a)]) \wedge v = () \wedge r = \text{cost}(x) + \text{cost}(a) + \mathcal{R}^{\text{putf}}\}}$$

$$\frac{G \triangleright e_1 : P_1 \quad G \triangleright e_2 : P_2}{G \triangleright e_1 ; e_2 : \{\exists h_1 r_1 r_2. P_1[E, h, h_1, (), r_1] \wedge P_2[E, h_1, h', v, r_2] \wedge r = r_1 + \mathcal{R}^{\text{comp}} + r_2\}}$$

$$\frac{G \triangleright e_1 : P_1 \quad G \triangleright e_2 : P_2}{G \triangleright \text{let } x = e_1 \text{ in } e_2 : \{\exists h_1 v_1, r_1 r_2. P_1[E, h, h_1, v_1, r_1] \wedge P_2[E[x := v_1], h_1, h', v, r_2] \wedge r = r_1 + \mathcal{R}^{\text{let}} + r_2\}}$$

$$\frac{G \triangleright e_1 : P_1 \quad G \triangleright e_2 : P_2 \quad G \triangleright e_3 : P_3}{G \triangleright \text{if } e_1 \text{ then } e_2 \text{ else } e_3 : \{\exists h_1 v_1 r_1 r_2. P_1[E, h, h_1, v_1, r_1] \wedge (v_1 = \mathbf{1} \implies P_2[E, h_1, h', v, r_2]) \wedge (v_1 = \mathbf{0} \implies P_3[E, h_1, h', v, r_2]) \wedge r = r_1 + \mathcal{R}^{\text{if}} + r_2\}}$$

$$\frac{G \cup \{(\text{call } g, P)\} \triangleright \text{body}_g : P[E, h, h', v, \mathcal{R}^{\text{call}} + r]}{G \triangleright \text{call } g : P[E, h, h', v, r]}$$

$$\frac{G \cup \{(C.m(\bar{a}), P)\} \triangleright \text{body}_{C,m} : P[\bar{x} := \text{eval}_E(\bar{a}), h, h', v, \mathcal{R}_{C,m,\text{eval}_E}^{\text{meth}}(r)]}{G \triangleright C.m(\bar{a}) : P[E, h, h', v, r]}$$

Before demonstrating how the program logic is used to verify the resource consumption of programs, we summarise some basic meta-theoretical properties. These have been formally proven by representing the operational semantics and the program logic in the proof assistant Isabelle/HOL; for more details, see [1,2]; the results here are a generalisation with resource algebras of those presented there. First, semantic validity, which has a partial correctness interpretation:

Definition 3.1 An assertion P is *valid* for expression e , written $\models e : P$, if for all E, h, h', v and r $E \vdash h, e \Downarrow h', v, r$ implies that the assertion $P[E, h, h', v, r]$ holds. A context G is *valid*, $\models G$, if for all pairs (e, P) in G , it holds that $\models e : P$. Assertion P is *valid for e in context G* , if $\models G$ implies $\models e : P$.

Indeed, the proof system is sound with respect to the operational semantics:

Theorem 3.2 (Soundness) *If $G \triangleright e : P$ then $G \models e : P$.*

The proof of Theorem 3.2 proceeds by induction on the height of derivations, employing suitably relativised notions of (context) validity.

Given the adopted partial correctness interpretation, it is clear that non-terminating programs satisfy their specifications vacuously. To verify resource consumption of such programs, an auxiliary termination logic have developed [2].

The treatment of logical completeness, as well as the actual proving methodology, benefits from some admissible rules concerning the proof context. Beyond the usual weakening rule(s), other rules allow one to discharge the proof context, i.e. to derive judgements in the absence of contextual assumptions. This uses a *specification table* ST , which maps function and method calls into assertions. We say that a context G *respects* the specification table ST , notation $ST \models G$, if all entries in it consist of a function or method call together with its assertion in the table; moreover their bodies satisfy a corresponding assertion. See [2] for the formal definition.

$$\frac{ST \models G \quad (e, P) \in G}{\triangleright e : P} \quad (\text{SPECTABLE})$$

$$\frac{ST \models G \quad (C.m(\bar{a}), ST(C, m, \bar{a})) \in G}{\triangleright C.m(\bar{b}) : ST(C, m, \bar{b})} \quad (\text{ADAPT})$$

The SPECTABLE rule accounts for the verification of (possibly mutually recursive) program fragments using the specification table, while ADAPT may be used to adjust the actual arguments when extracting method specifications.

To prove relative completeness, we define a context G_{strong} that associates to each function and method call its strongest specification.

Definition 3.3 The strongest specification for e is

$$SSpec(e) \equiv \{E \vdash h, e \Downarrow h', v, r\}$$

Lemma 3.4 For any e , $G_{strong} \triangleright e : SSpec(e)$.

Furthermore, G_{strong} satisfies $ST_{strong} \models G_{strong}$, where ST_{strong} is the specification table defined by $(\lambda g. SSpec(\text{call } g), \lambda C m \bar{a}. SSpec(C.m(\bar{a})))$. From this, we obtain:

Theorem 3.5 (Completeness) For any e and P , $\models e : P$ implies $\triangleright e : P$.

The completeness result means that we can conceivably derive any provable assertion using the rules of the program logic, following the structure of the program.

4 Validated optimisations

The program logic presented in the previous section can be used to justify program transformations that are routinely applied in optimising compilers [20], *provided* they are in fact improving. In this section we give some example optimisations and sketch the proofs of their validation. While the transformations and the examples we consider in this paper are fairly simple, they serve the purpose of demonstrating our methodology.

4.1 Standard low-level optimisations

We first consider the motivating program of [24],

```
i <- 0; x <- 1; y <- 2;
WHILE i < 24 DO {i <- i + x + y ; g <- 2 * i}
EXIT
```

Our formal verification refers to a translation of this code into (the Isabelle representation of) our language. The result of this (manual) translation is the method `R.calc0`:

```
method static int R.calc0() =
  let i=0 in let x=1 in let y=2 in let g=0 in call f
  fun f(int i, int x, int y, int g) = if i < 24 then call h else var g
  fun h(int i, int x, int y, int g) =
    let j=i+x in let i=j+y in let g=2*i in call f
```

which differs from the original code only in minor ways: we extended the loop prelude by an assignment to variable g , converted the loop into two functions which represent basic blocks, and turned the EXIT statement into a return statement of the final value of g .

Using the resource algebra \mathbf{Time} defined previously, we now outline our Isabelle proof of the judgement

$$\triangleright \mathbf{R.calc}_0([\]): \{r = 213\} \quad (1)$$

which states that an invocation of $\mathbf{R.calc}_0$ requires 213 units of time. We first define two auxiliary (semantic) functions

$$cost_f(n) = 24 * n + 10$$

$$cost_h(n) = 24 * n + 1$$

that describe the costs of evaluating functions f and h , respectively, where n is the number of loop iterations. Next, we define a specification table ST_0 for \mathbf{calc}_0 and its local functions f and h .

$$\left[\begin{array}{l} \mathbf{R.calc}_0 \mapsto \{r = 11 + cost_f(8)\} \\ \text{call } f \mapsto \{\forall J. (E(x) = 1 \wedge E(y) = 2 \wedge E(i) = 3 * J \wedge J \leq 8) \longrightarrow \\ \quad r = cost_f(8 - J)\} \\ \text{call } h \mapsto \{\forall J. (E(x) = 1 \wedge E(y) = 2 \wedge E(i) = 3 * J \wedge J \leq 7) \longrightarrow \\ \quad r = cost_h(8 - J)\} \end{array} \right]$$

The first line defines the specification of $\mathbf{R.calc}_0$ in terms of the auxiliary function $cost_f$, while the entries for $\text{call } f$ and $\text{call } h$ ensure that the auxiliary functions correctly model the costs of the executing the local functions. In both cases, the specifications depend on the value of the variable i ; intuitively, the universally quantified variable J represents the number of loop iterations that have already been performed.

Next, we define a context, G_0 that associates the specification table entries to the relevant function and method calls, e.g.

$$G_0 = \{(\mathbf{R.calc}_0([\]), ST_0 \mathbf{R.calc}_0), (\text{call } f, ST_0 \text{ call } f), (\text{call } h, ST_0 \text{ call } h)\}$$

The core of the verification consists in establishing $ST_0 \models G_0$. From that, it is just a matter of calling the `SPECTABLE` rule to conclude the proof of (1). The former, in turn, requires us to prove that each entry in G_0 is justified: for each entry $(\text{call } f, P)$ – and similarly for method entries – we need to show that the body $body_f$ satisfies $G_0 \triangleright body_f : P[E, h, h', v, \mathcal{R}^{\text{call}} + r]$. Using the rules of our program logic, these proofs proceed syntax-directed similarly to the way a Verification Condition Generator would work, leaving side conditions involving numeric constraints. Ideally, we would delegate the solution of those verification conditions to a fully automated (external) solvers. Currently, instead, the proof assistant often needs directions when facing large case-splits and quantifier instantiations beyond decision procedures.

In the same fashion, we have established Isabelle proofs of specifications $\triangleright \mathbf{R.calc}_i([\]): \{r = r_i\}$ for methods $\mathbf{R.calc}_1 \dots \mathbf{R.calc}_7$ which arise from applying the code trans-

formations described in [24] to R.calc_0 . The resulting code is shown in Fig. 1, while Table 2 summarises the costs r_i obtained for each transformation step.

```

class R {
...
method static int calc1() = let i=0 in let x=1 in let y=2 in let g=0 in call f
  fun f(int i, int x, int y, int g) = if i < 24 then call h else var g
  fun h(int i, int x, int y, int g) = let i=i+3 in let g=2*i in call f

method static int calc2() = let i=0 in let g=0 in call f
  fun f(int i, int g) = if i < 24 then call h else var g
  fun h(int i, int g) = let i=i+3 in let g=2*i in call f

method static int calc3() = let i=0 in let g=0 in call h
  fun h(int i, int g) = let i=i+3 in let g=2*i in if i < 24 then call h else var g

method static int calc4() = let g=0 in call h
  fun h(int g) = let g=g+6 in if g < 48 then call h else var g

method static int calc5() = let g=0 in call h
  fun f(int g) = let g=g+6 in if g < 48 then call h else var g
  fun h(int g) = let g=g+6 in if g < 48 then call f else var g

method static int calc6() = let g=0 in call h
  fun h(int g) = let g=g+6 in let g=g+6 in if g < 48 then call h else var g

method static int calc7() = let g=0 in call h
  fun h(int g) = let g=g+12 in if g < 48 then call h else var g}

```

Figure 1. A sequence of low level transformations

i	t_i	Transformation
0	213	
1	197	Constant propagation and constant folding
2	193	Dead assignment elimination
3	176	Branch movement, inlining, redundant test elimination
4	126	Induction variable elimination
5	126	Loop unrolling <i>without</i> code sharing
6	82	Dead code elimination
7	66	Expression folding

Table 2
Costs associated to low level transformations

```

class REV {
method static LIST App(LIST l, int i) = call app
  fun app(LIST l, int i) = if l = null then call app_0 else call app_1
  fun app_0(int i) = let l = null in let x = new LIST in
    x.HD:=i ; x.TL:=1 ; var x
  fun app_1(LIST l, int i) = let h=1.HD in let t=1.TL in
    let t = REV.App(t, i) in 1.TL:=t ; var l

method static LIST Rev1(LIST l) = call rev1
  fun rev1(LIST l) = if l = null then null else call rev1_1
  fun rev1_1(LIST l) = let h=1.HD in let t=1.TL in
    let t = REV.Rev1(t) in REV.App(t, h)

method static LIST Rev2(LIST l, LIST acc) = call rev2
  fun rev2(LIST l, LIST acc) = if l = null then var acc else call rev2_1
  fun rev2_1(LIST l, LIST acc) = let h=1.HD in let t=1.TL in
    1.TL:=acc ; REV.Rev2(t, l)

method static LIST Rev3(LIST l, LIST acc) = call rev3
  fun rev3(LIST l, LIST acc) = if l = null then var acc else call rev3_1
  fun rev3_1(LIST l, LIST acc) = let t=1.TL in 1.TL:=acc ;
    let acc = var l in let l = var t in call rev3}

```

Figure 2. Class REV

In general, proofs of functional correctness of arbitrary code fragments may be required to verify statements about resource consumption in this case-study. However, this is not the case for the specific transformations we considered: none of the specifications involved constrains the result values v . Furthermore, none of the transformations increases the dynamic resources consumed. Indeed, except for loop unrolling (the conversion $\text{calc}_4 \rightarrow \text{calc}_5$), all transformations reduce the costs⁵.

4.2 Tail-call optimisation

Next, we consider a recursive program involving heap structures. Figure 2 defines the class REV with method App for appending an element to a list, and methods Rev₁, ..., Rev₃ for reversing a list. We assume that objects of class LIST contain fields HD and TL of type int and LIST, respectively.

Concentrating our attention on the required height of the frame stack, we observe that method Rev₁ is formulated using method recursion and employs the auxiliary method App. As all its recursive invocations are nested, Rev₁ requires a frame stack of a height that depends linearly on the length of the input list. To express this dependency we define a predicate $h, v \models_X n$ that specifies when a reference value v

⁵ The loop unrolling performed in [24] actually *increases* the dynamic costs, because it jumps to a shared code block instead of duplicating the continuation code. Using our formalism of static resources we could characterise this as a static optimisation instead, namely reducing code size.

REV.App(\bar{a})	$\mapsto \{\forall x y n X.$ $(\bar{a} = [x, y] \wedge h, E(x) \models_X n) \longrightarrow$ $(h', v \models_{\{\text{freshloc}(h)\} \cup X} n + 1 \wedge h = \text{dom}(h) \setminus X \ h' \wedge r = r_{\text{App}}(n))\}$
REV.Rev ₁ (\bar{a})	$\mapsto \{\forall x n X.$ $(\bar{a} = [x] \wedge h, E(x) \models_X n) \longrightarrow (\exists Y. h', v \models_Y n \wedge r = r_{\text{Rev1}}(n))\}$
REV.Rev ₂ (\bar{a})	$\mapsto \{\forall x n X y m Y.$ $(\bar{a} = [x, y] \wedge h, E(x) \models_X n \wedge h, E(y) \models_Y m \wedge X \cap Y = \emptyset) \longrightarrow$ $(\exists Z. h', v \models_Z n + m \wedge r = r_{\text{Rev2}}(n))\}$
REV.Rev ₃ (\bar{a})	$\mapsto \{\forall x n X y m Y.$ $(\bar{a} = [x, y] \wedge h, E(x) \models_X n \wedge h, E(y) \models_Y m \wedge X \cap Y = \emptyset) \longrightarrow$ $(\exists Z. h', r \models_Z n + m \wedge r = r_{\text{Rev4}}(n))\}$
call rev3	$\mapsto \{\forall n X m Y.$ $(h, E(1) \models_X n \wedge h, E(\text{acc}) \models_Y m \wedge X \cap Y = \emptyset) \longrightarrow$ $(\exists Z. h', v \models_Z n + m \wedge r = r_{\text{Rev4}}(n))\}$

Table 3
Specification table for class REV.

represents a non-cyclic (integer) list of length n in a heap region $h \downarrow_X$.

$$h, v \models_{\emptyset} 0 \equiv v = \text{null}$$

$$h, v \models_{v \uplus Y} (n + 1) \equiv v \in \text{dom}(h) \wedge h(v) = \text{LIST} \wedge h(v).\text{TL} = t \wedge h, t \models_Y n$$

We can now prove a specification that relates the length of the list to the stack depth. The quantitative specification we will prove also indicates that the runtime, the number of jumps, and the number of method invocations all grow quadratically with the length of the input list:

$$\triangleright \text{REV.Rev1}([a]) : \{\forall n X. h, E(a) \models_X n \longrightarrow r_{\text{Frames}} = n + 1\}$$

Method **Rev₂** arises from **Rev₁** by introducing an accumulator that eliminates the invocation to **App** and formulates the recursion as tail recursion. Its specification imposes some well-structuredness conditions on both arguments: pointers must represent lists, which moreover should be *non-overlapping* in the heap. The frame depth depends only on the length of the first argument, which gives the same overall depth cost as **Rev₁** (but a considerable saving in allocated space):

$$\triangleright \text{REV.Rev2}([a, b]) : \{\forall n X m Y. h, E(a) \models_X n \wedge h, E(b) \models_Y m \wedge X \cap Y = \emptyset$$

$$\longrightarrow r_{\text{Frames}} = n + 1\}$$

In **Rev₃**, the method-level tail recursion is converted into a method-internal loop and the redundant field is eliminated, resulting in a program whose execution only requires a single frame.

$$\triangleright \text{REV.Rev3}([a, b]) : \{\forall n X m Y. h, E(a) \models_X n \wedge h, E(b) \models_Y m \wedge X \cap Y = \emptyset$$

$$\longrightarrow r_{\text{Frames}} = 1\}$$

The verification of the three specifications follows the same strategy as before. This is an overall optimisation for the resource algebra **Frames**, but we verified the intermediate steps using a more informative *product* resource algebra, **Frames** \times **MethCntsAll** \times **Time** \times **Heap**, where **MethCntsAll** is similar to the **MethCnts** algebra shown in Table 1, except that only the *size* of the multisets is considered (thus we sum calls to all methods), and we stipulate that $\text{size}(\text{LIST}) = 1$. We obtain the following resource tuples, which show the costs in terms of the length n of the (first) input list.

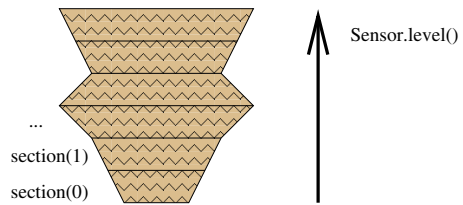
	r_{Frames}	$r_{\text{MethCntsAll}}$	r_{Times}	r_{Heap}
$r_{\text{App}}(n) \equiv$	$(n + 1,$	$n + 1,$	$22n + 22,$	$1)$
$r_{\text{Rev1}}(n) \equiv$	$(n + 1,$	$n(n + 1)/2,$	$11n^2 + 29n + 11,$	$n)$
$r_{\text{Rev2}}(n) \equiv$	$(n + 1,$	$n + 1,$	$20n + 11,$	$0)$
$r_{\text{Rev3}}(n) \equiv$	$(1,$	$1,$	$18n + 11,$	$0)$

Under the lexicographic order for the product algebra, both steps are optimising. To preserve datatype representation conditions across method calls, we need stronger invariants in the specifications than shown above. The full specification table is given in Table 3, which also contains an invariant of the loop represented by the function call `rev3`.

4.3 Optimisation of method call frequency

As well as standard optimisations, our framework can be used to validate optimisations that are custom specified for a particular application.

Consider the hypothetical scenario of a process-control application where there is an irregularly shaped chemical tank (illustrated opposite) whose contents must be carefully monitored to ensure sufficient reagent. When the amount reaches a critical low point, the reaction must be temporarily halted while the tank refills.



An embedded controller runs a program that which monitors the level gauge. A suitable notion of optimisation in this setting would be to transform the program into one which checks the tank level more frequently, so reducing the latency between noticing a tank empty condition and triggering the refill cycle. Thus the frequency of invoking the `Sensor.level()` method is a suitable resource measure. Using the same methodology as previously, and with the resource algebra **MethFreq**, we can validate the transformation of a naive implementation of a program which calculates the amount of reagent into the tank into a better one which checks the level more frequently.

The full listing of the example program (naive version) is in Fig. 3. The method `calc(n)` calculates the amount of reagent left in `n` sections of the tank. It is invoked from the `runloop` method, which is supposed to be the process control loop; in reality, this loop would be run indefinitely and involve other tasks besides level calculation.

```

class ChemCalc {
  field static int alarm
  field static int[] section
  field static int critical_amount
  method static void runloop() =
  let
    val n = 1000
    fun raise_alarm () = putstatic <int ChemCalc.alarm> 1
    fun loop_check(int n) = if n>0 then loop(n) else ()
    fun loop(int n) =
    let
      val chem_level = invokestatic <int Sensor.level()> ()
      val chem_amount = invokestatic <int ChemCalc.calc(int)> (chem_level)
      val n = sub n 1
      val critical = getstatic <int ChemCalc.critical_amount>
    in
      if chem_amount < critical then raise_alarm() else loop_check(n)
    end
  in loop_check(n) end
  method static int calc(int n) =
  let
    val a = 0
    fun sumup(int n, int a) =
    let
      val cs = getstatic <int[] ChemCalc.section>
      val x = get cs n
      val a = add x a
      val n = sub n 1
    in
      sumup_check (n,a)
    end
    fun sumup_check(int n, int a) =
      if n>0 then sumup(n,a)
      else a
  in sumup_check(n,a) end}

```

Figure 3. Grail code for an embedded controller

Numerous optimisations are possible in this example to increase the rate of testing the sensor level. For example, we might sum up the section sizes only until we find out that the critical level has been safely exceeded. Or (supposing the dimensions of the tank are fixed during the run of the process), we may calculate the sums for the container sections in advance to avoid looping over the `section` array each time we test the sensor level. We have not yet undertaken the formal verification of this example, as it goes slightly beyond our formal presentation of the logic as it makes use of arrays; however, the extension is straightforward.

5 Static semantics

A static resource algebra \mathcal{S} is defined exactly as in Def. 2.1, except that the resource constructors depend on the typing context only; in particular, the method operator does not depend on the values of its arguments. For a fixed signature the judgment $\Gamma \vdash e : t, s$ assigns type t and effect s to Grail expression e in a straightforward way; an example is the rule for *if* expressions:

$$\frac{\Gamma \vdash e : \text{bool}, s_e \quad \Gamma \vdash e_1 : t, s_1 \quad \Gamma \vdash e_2 : t, s_2}{\Gamma \vdash \text{if } e \text{ then } e_1 \text{ else } e_2 : t, s_e + \mathcal{S}^{\text{if}} + s_1 + s_2}$$

$$\begin{array}{c}
\frac{}{\Gamma \vdash a : \text{type}_\Gamma(a), \text{scost}(a)} \qquad \frac{\Gamma \vdash e : t, s \quad s \leq s'}{\Gamma \vdash e : t, s'} \\
\\
\frac{\Gamma \vdash a_1 : t_1, s_1 \quad \Gamma \vdash a_2 : t_2, s_2 \quad \Sigma(\text{prim}) = t_1 \times t_2 \rightarrow t_3}{\Gamma \vdash \text{prim } a_1 a_2 : t_3, s_1 + s_2 + \mathcal{S}^{\text{prim}}} \\
\\
\frac{}{\Gamma \vdash \text{new } C : C, \mathcal{S}_C^{\text{new}}} \qquad \frac{\Gamma \vdash x : C, s \quad \Sigma(C.f) = t}{\Gamma \vdash x.f : t, s + \mathcal{S}^{\text{getf}}} \\
\\
\frac{\Gamma \vdash x : C, s_x \quad \Gamma \vdash a : t, s_a \quad \Sigma(C.f) = t}{\Gamma \vdash x.f := a : \text{unit}, s_x + s_a + \mathcal{S}^{\text{putf}}} \\
\\
\frac{\Gamma \vdash e_1 : \text{unit}, s_1 \quad \Gamma \vdash e_2 : t_2, s_2}{\Gamma \vdash e_1 ; e_2 : t_2, s_1 + \mathcal{S}^{\text{comp}} + s_2} \\
\\
\frac{\Gamma \vdash e_1 : t_1, s_1 \quad \Gamma, x : t_1 \vdash e_2 : t_2, s_2}{\Gamma \vdash \text{let } x = e_1 \text{ in } e_2 : t_2, s_1 + \mathcal{S}^{\text{let}} + s_2} \\
\\
\frac{\Gamma \vdash e : \text{bool}, s_e \quad \Gamma \vdash e_1 : t, s_1 \quad \Gamma \vdash e_2 : t, s_2}{\Gamma \vdash \text{if } e \text{ then } e_1 \text{ else } e_2 : t, s_e + \mathcal{S}^{\text{if}} + s_1 + s_2} \\
\\
\frac{\Sigma(g) = t_1 \times \dots \times t_n \rightarrow t, s}{\Gamma \vdash \text{call } g : t, \mathcal{S}^{\text{call}} + s} \\
\\
\frac{\Gamma \vdash a_i : t_i, s_i \quad \Sigma(m) = t_1 \times \dots \times t_n \rightarrow t, s}{\Gamma \vdash C.m(\bar{a}) : t, \Sigma_i s_i + \mathcal{S}_{C,m,\bar{a}}^{\text{meth}}(s)}
\end{array}$$

Figure 4. Typing rules

Notice that this is different from usual type and effect systems, where the effect on both branches would be the same. See Fig. 4 for the full listing, where argument typing and static cost are defined as follows:

$$\begin{array}{ll}
\text{type}_\Gamma(x) & = \Gamma(x) & \text{scost}(x) & = \mathcal{S}^{\text{var}} \\
\text{type}_\Gamma(i) & = \text{int} & \text{scost}(i) & = \mathcal{S}^{\text{int}} \\
\text{type}_\Gamma() & = \text{unit} & \text{scost}() & = 0 \\
\text{type}_\Gamma(l_C) & = C & \text{scost}(l_C) & = 0 \\
\text{type}_\Gamma(\text{null}_C) & = C & \text{scost}(\text{null}_C) & = \mathcal{S}^{\text{null}}
\end{array}$$

Many of the standard properties of type and effect systems hold, culminating in subject reduction. All proofs are standard and hence omitted. First, it is immediate

to show that our system is conservative w.r.t. the effect-free system defined, as usual, by erasure. Further, a canonical forms property holds.

Fact 5.1 (Canonical forms) *Assume that $\Gamma \vdash v : t, s$; then:*

- if $t = \text{int}$ then $v = i$ and $s = \mathcal{S}^{\text{int}}$.
- if $t = \text{unit}$ then $v = ()$ and $s = 0$.
- if $t = C$ then either $v = \text{null}_C$ and $s = \mathcal{S}^{\text{null}}$ or $v = l_C$ and $s = 0$.

From this we observe that every value has *constant* effect.

Weakening is admissible as is a specialised form of substitution, in which arguments play the role of variables and the effect is increased accordingly. This generalises *value* substitution in type and effects systems, which holds because values are *pure* (i.e. have zero effect).

We now introduce a generalisation of the well-known relation between static effects and dynamic traces [30], which is key in the statement and proof of subject reduction.

Definition 5.2 Given two resource algebras \mathcal{S} and \mathcal{R} , an *approximation* is a relation \sqsubseteq included in $\mathcal{S} \times \mathcal{R}$, which reads “static effect s approximates dynamic resource r ”, with the following properties:

- (i) $0^{\mathcal{S}} \sqsubseteq 0^{\mathcal{R}}$ and for every constructor c , it holds $\mathcal{S}^c \sqsubseteq \mathcal{R}^c$.
- (ii) If $s \sqsubseteq r$, then $\mathcal{R}_{C,m,\bar{a}}^{\text{meth}}(s) \sqsubseteq \mathcal{R}_{C,m,\bar{a}}^{\text{meth}}(r)$.
- (iii) $s_1 \sqsubseteq r_1 \wedge s_2 \sqsubseteq r_2$ entails $s_1 +^{\mathcal{S}} s_2 \sqsubseteq r_1 +^{\mathcal{R}} r_2$;
- (iv) $s \sqsubseteq r$ entails $s +^{\mathcal{S}} s' \sqsubseteq r$.

For example, consider the static approximation $\text{SG} = \langle \mathbf{S}, \{\text{tt}\}, \cup, \leq_{\text{SG}} \rangle$ of the MethGuard algebra, where the carrier \mathbf{S} is $\{\{\text{tt}\}, \{\text{tt}, \text{ff}\}\}$ and the ordering is defined as $b \leq_{\text{SG}} b'$ iff $b = \{\text{tt}\}$ or $b = b' = \{\text{tt}, \text{ff}\}$. The approximation relation is \in^{-1} , which trivially satisfies the above conditions.

Let $E : \Gamma$ be the usual correspondence between environment and typing. Further, say that a heap h is well-typed if $\Sigma(C.f) = t$ implies $h(l_C).f : t$. Finally, we say that a signature is *well-typed* if for all $g, m \in \Sigma$ it holds

$$\begin{aligned} \Sigma(g) = t_1 \times \cdots \times t_n \rightarrow t, s &\implies x_1 : t_1 \dots x_n : t_n \vdash \text{body}_g : t, s \\ \Sigma(C.m) = t_1 \times \cdots \times t_n \rightarrow t, s &\implies x_1 : t_1 \dots x_n : t_n \vdash \text{body}_{C,m} : t, s \end{aligned}$$

Theorem 5.3 (Subject reduction) *Assume a well-typed signature, algebras \mathcal{S} and \mathcal{R} as above. Suppose further that $\Gamma \vdash e : t, s$ and for a well typed heap h it holds that $E \vdash h, e \Downarrow h', v, r$ and $E : \Gamma$; then $\Gamma \vdash v : t, \text{scost}(v)$ and $s \sqsubseteq r$.*

The above result ensures the consistency of the operational semantics with the type system; it is a basis for approximating dynamic measurements using type checking instead of theorem proving.

6 Conclusions

We have presented a framework and methodology for *optimisation validation*, based on generic forms of dynamic and static resource costs. We have formalised most of the setting in Isabelle/HOL, particularly including the soundness and completeness of the program logic, which was applied to validate the specific optimisations in Sect. 4.

One can argue against our approach in various ways. For example, validating optimisation with disregard of behavioural equivalence seems pointless (often, the empty program is the ultimate optimisation). Yet, we see resource improvement validation as orthogonal to translation validation; in some settings one may check both things. Optimising compilers usually employ heuristics to decide what to do with code, but in some cases a sequence of transformations may not actually result in improvement even if correctness is preserved; the optimisation is then pointless (as noted in [31]). In others settings, such as our PCC application, the safety policy that we care most about is captured by our resource consumption notion and so resource usage preservation is more crucial than functional equivalence.

6.1 Related Work.

By now there is an extensive literature on verifying compiler correctness and optimisations (e.g. [10,11,20]), but as far as we know, no previous work on *formal* and *static* methods for verifying that optimisations in fact improve resource usage. The closest are an early formal approach to performance estimation and monitoring for space and time complexity w.r.t. OO programs [26] and, at the other end of the spectrum, a framework for *predicting* the impact of optimizations, via models for the latter as well for code and resources [31]. This is empirically tested and used to select the right combination and application strategy of given optimizations.

Specific instances of machine checked correctness proofs have also been pursued: some recent examples concern code elimination [8], tokenization and componentization transformations [13].

One of the most well-developed approaches is David Sands' *Improvement Theory* [25], a specialisation of the standard theory and reasoning principles of *observational* equivalence, in which basic observations include some intensional information about computational cost. This is extended to space improvements for effects-free call-by-need languages in [15]. "Paper and pencil" proofs are mostly equational and require considerable ingenuity even in the simplest cases. Our direction is inspired by *translation validation* (TV) [23], mainly implemented in the automatic TVOC tool [4]. It addresses both *reordering transformation* such as loop fusion and *structure preserving* ones, such as constant folding, where statements can be inserted or deleted. In both cases sound rules generate verification conditions entailing a bisimulation between source and target w.r.t. observable variables. Those VC's are then fed to a theorem prover, in particular *CVC*. TV subsumes Rinard's *credible compilation* [24], which instead requires full code instrumentation. Necula [22] demonstrates an approach to TV based on symbolic execution in the context of the

GNU C compiler intermediate language. Similarly to Rinard, he uses simulation of execution paths, but instead of compiler annotation, a constraint-based algorithm heuristically tries to infer a simulation. The system is robust enough to allow the author to verify structure preserving optimisations in `gcc` itself.

Several other researchers have considered *program logics* going beyond traditional functional correctness specification. For example [28] presents a generic Hoare calculus for reasoning about computational monads and is formalised in *HasCasl*. With a similar aim as us, Denney and Fischer [12] introduce a framework for safety policies: given a semantically defined safety property (such as “no division by zero”) and an operational semantics, the aim is to derive specialised Hoare rules to enforce the property; however, for “stateful” properties, such as memory writes limits, the approach becomes technically quite involved.

Since we consider optimisation from one program to another, a natural approach suggests itself, namely using a logic which relates two programs at once. In Benton’s relational Hoare logic [5] judgements $\{R\} c_1 \sim c_2 \{S\}$ refer to the execution of two (possibly) different programs c_1 and c_2 while the pre- and post-conditions are relations (rather than predicates) over states. As a special case, the program logic contains a proof system in which (functional) equivalence of programs can be directly verified, in contrast to our approach where separate judgements are needed. A similar logic is used in Rinard’s report [24]. In both cases, proofs of soundness are included while completeness is not examined.

Elsewhere, forms of *cost algebras* (monads) and *partial orders* similar to ours have been investigated for analysis of resource consumptions, e.g., [19,14] and optimisation [21]. General static analysis techniques having similarities with the setup of our type and effect system include [16,29]. There is also considerable work on specific static analysis for different notions of resource usage: to name one, the use of abstract interpretation for certification of bound memory usage in Java byte code [9], but a more complete survey would lead us well beyond the scope of this overview.

6.2 Future work.

There are several avenues for pursuing this work. First, by considering finer-grained transformations individually, perhaps by generalising Improvement Theory to resource algebras. Second, it would be noteworthy if our static analysis was able to validate optimisations directly and avoid the need for the program logic: this is in fact possible in restricted (e.g. boolean) domains, but further assumptions are needed in the general case. To scale our techniques to routine application we would need either an automatic technique based on the type system or better automatic assistance for using the program logic. Endowing relational Hoare logics with a notion of resource algebra seems also a swift way to combine semantics preservation with optimisation validation.

Finally, the considerable generality of resource algebras allows examples that are less directly related to optimisation, but useful for validating other safety properties (including correspondence properties in protocols, or resource usage analysis in the

sense of [18]), and we would like to apply our general techniques to those examples too.

The sources for the core logic (and much more) are available at:

<http://www.tcs.ifi.lmu.de/~hwloidl/mrg/MRG-infra-0805.tgz>

The examples presented in this paper can be downloaded from:

<http://homepages.inf.ed.ac.uk/amomigli1/papers/cocv06.tar>

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References

- [1] Aspinnall, D., L. Beringer, M. Hofmann, H.-W. Loidl and A. Momigliano, *A program logic for resource verification*, in: K. Slind, A. Bunker and G. Gopalakrishnan, editors, *TPHOLs2004*, LNCS **3223** (2004), pp. 34–49.
- [2] Aspinnall, D., L. Beringer, M. Hofmann, H.-W. Loidl and A. Momigliano, *A program logic for resources* (2005), to appear in *Theoretical Computer Science*.
- [3] Aspinnall, D., S. Gilmore, M. Hofmann, D. Sannella and I. Stark, *Mobile resource guarantees for smart devices*, in: G. Barthe, L. Burdy, M. Huisman, J.-L. Lanet and T. Muntean, editors, *CASSIS 2004*, LNCS **3362** (2005), pp. 1–26.
- [4] Barrett, C. W., Y. Fang, B. Goldberg, Y. Hu, A. Pnueli and L. D. Zuck, *TVOC: A Translation Validator for Optimizing Compilers*, in: K. Etessami and S. K. Rajamani, editors, *CAV*, LNCS **3576** (2005), pp. 291–295.
- [5] Benton, N., *Simple relational correctness proofs for static analyses and program transformations*, in: N. D. Jones and X. Leroy, editors, *POPL* (2004), pp. 14–25.
- [6] Beringer, L., M. Hofmann, A. Momigliano and O. Shkaravska, *Automatic certification of heap consumption*, in: A. V. Franz Baader, editor, *LPAR 2004*, LNCS **3425** (2005), pp. 347–362.
- [7] Beringer, L., K. MacKenzie and I. Stark, *Grail: a functional form for imperative mobile code*, in: *Foundations of Global Computing*, number 85.1 in ENTCS (2003), pp. 1–21.
- [8] Blech, J. O., L. Gesellensetter and S. Glesner, *Formal verification of dead code elimination in Isabelle/HOL*, in: B. K. Aichernig and B. Beckert, editors, *SEFM* (2005), pp. 200–209.
- [9] Cachera, D., T. Jensen, D. Pichardie and G. Schneider, *Certified memory usage analysis*, in: J. Fitzgerald, I. J. Hayes and A. Tarlecki, editors, *FM'05*, LNCS **3582** (2005), pp. 91–106.
- [10] Cousot, P. and R. Cousot, *Systematic design of program transformation frameworks by abstract interpretation*, in: *POPL*, 2002, pp. 178–190.
- [11] Dave, M. A., *Compiler verification: a bibliography*, SIGSOFT Softw. Eng. Notes **28** (2003), pp. 1–4.
- [12] Denney, E. and B. Fischer, *Correctness of source-level safety policies*, in: K. Araki, S. Gnesi and D. Mandrioli, editors, *FME 2003*, LNCS **2805** (2003), pp. 894–913.
- [13] Genet, T., T. P. Jensen, V. Kodati and D. Pichardie, *A Java Card CAP converter in PVS*, ENTCS **82** (2003).

- [14] Grobauer, B., *Cost recurrences for DML programs*, in: *ICFP'01* (2001), pp. 253–264.
- [15] Gustavsson, J. and D. Sands, *Possibilities and limitations of call-by-need space improvement*, in: *ICFP'01* (2001), pp. 265–276.
- [16] Hankin, C. and D. L. Métayer, *A type-based framework for program analysis*, in: *SAS*, 1994, pp. 380–394.
- [17] Hoare, C. A. R., *Procedures and parameters: An axiomatic approach*, in: E. Engeler, editor, *Symp. Semantics of Algorithmic Languages*, Notes in Mathematics 188 (1971), pp. 102–116.
- [18] Igarashi, A. and N. Kobayashi, *Resource usage analysis*, *ACM SIGPLAN Notices* **37** (2002), pp. 331–342.
- [19] Jay, C. B., M. Cole, M. Sekanina and P. Steckler, *A monadic calculus for parallel costing of a functional language of arrays*, in: C. Lengauer, M. Griebel and S. Gorlatch, editors, *Euro-Par*, *LNCS* **1300** (1997), pp. 650–661.
- [20] Kennedy, K. and J. R. Allen, “Optimizing compilers for modern architectures: a dependence-based approach,” Morgan Kaufmann Publishers Inc., 2002.
- [21] Knoop, J., O. Rüthing and B. Steffen, *Optimal code motion: Theory and practice*, *ACM TOPLAS* **16** (1994), pp. 1117–1155.
- [22] Necula, G. C., *Translation validation for an optimizing compiler*, in: *PLDI '00* (2000), pp. 83–94.
- [23] Pnueli, A., M. Siegel and E. Singerman, *Translation validation*, in: B. Steffen, editor, *TACAS*, *LNCS* **1384** (1998), pp. 151–166.
- [24] Rinard, M., *Credible compilation*, Technical Report MIT-LCS-TR-776, MIT Laboratory for Computer Science (1999).
- [25] Sands, D., *Improvement theory and its applications*, in: A. D. Gordon and A. M. Pitts, editors, *Higher Order Operational Techniques in Semantics*, Publications of the Newton Institute, Cambridge University Press, 1998 pp. 275–306.
- [26] Schmidt, H. W. and W. Zimmermann, *A complexity calculus for object-oriented programs*, *Journal of Object-Oriented Systems* **1** (1994), pp. 117–147.
- [27] Schneider, F. B., *Enforceable security policies*, *ACM Transactions on Information and System Security* **3** (2000), pp. 30–50.
- [28] Schröder, L. and T. Mossakowski, *Monad-independent Hoare logic in HasCASL*, in: M. Pezze, editor, *FASE*, *LNCS* **2621** (2003), pp. 261–277.
- [29] Skalka, C. and S. F. Smith, *History effects and verification*, in: W.-N. Chin, editor, *APLAS*, *LNCS* **3302** (2004), pp. 107–128.
- [30] Wadler, P. and P. Thiemann, *The marriage of effects and monads*, *ACM Trans. Comput. Log.* **4** (2003), pp. 1–32.
- [31] Zhao, M., B. R. Childers and M. L. Soffa, *Predicting the impact of optimizations for embedded systems*, in: *LCTES* (2003), pp. 1–11.