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# dc magnetometry of niobium thin film superconductors deposited using high power impulse magnetron sputtering

S. Wilde,<sup>1,3</sup> R. Valizadeh,<sup>1</sup> O. B. Malyshev,<sup>1</sup> G. B. G. Stenning,<sup>2</sup> T. Sian,<sup>1,4</sup> and B. Chesca<sup>3</sup>

<sup>1</sup>ASTeC, STFC Daresbury Laboratory, Warrington, WA4 4AD Cheshire, United Kingdom

<sup>2</sup>ISIS, STFC Rutherford Appleton Laboratory, Didcot OX11 0QX, United Kingdom

<sup>3</sup>Department of Physics, Loughborough University, Loughborough LE11 3UE, United Kingdom

<sup>4</sup>The University of Manchester, Manchester M13 9PL, United Kingdom

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We performed a systematic investigation of the dc magnetic properties of superconducting niobium thin films deposited by high power impulse magnetron sputtering (HiPIMS) as a function of the main deposition parameters: the temperature, T, of the heated substrate and the applied dc bias voltage, V, during the sputtering process. The measured dc magnetization curves between 0 and 1000 mT were used to calculate the relative volume of each sample into which the applied magnetic field had penetrated,  $(\Delta V/V)_M$ . The sample deposited at 700°C with -80 V biased substrate exhibited the least penetration by the magnetic field.  $(\Delta V/V)_M$  appeared to be highly dependent on the bias voltage at both room temperature and 500°C; however, a broad range of bias voltages showed comparatively similar results at increased temperatures of 700°C. Samples deposited at 700°C exhibit smaller upper critical fields,  $H_{C2}$ , than samples deposited at room temperature and 500°C, with the lower temperatures exhibiting a greater dependency on the applied bias. The films deposited at 700°C also display a more stable magnetization curve suggesting that an enhanced flux pinning was achieved when compared to lower temperatures. Consequently, films with stable pinning were found to have the most repeatable dc magnetic behavior. Our results are particularly relevant to the superconducting radio-frequency accelerator scientific community where thin films have been suggested as a technology which may ultimately surpass the performance of bulk niobium. They are also relevant to the fundamental area of superconducting thin films and any applied area where thin films produced by HiPIMS are used, such as superconducting electronics.

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# I. INTRODUCTION

There are three parameters which can be used to rate superconducting radio-frequency (SRF) cavity performance, the maximum attainable acceleration gradient, E, above which the cavity will quench, the attainable quality factor, Q, as a function of the accelerating field and its maximum  $Q_0$ .

The theoretical limit of acceleration gradient is capped at the superheating field ( $H_{SH}$ ) as it is the upper limit to which a superconductor can remain in the Meissner state. Current technology already allows bulk niobium test cavities to reach accelerating fields which are close to the  $H_{SH}$  of niobium with a corresponding magnetic field strength, H, of 220 mT [1]. New surface treatments such as nitrogen doping have improved the quality factor of bulk niobium cavities however at the expense of a reduction in the maximum acceleration gradient [2]. High field nonlinear dissipation could explain the monopoly of bulk niobium in SRF applications since niobium has the highest value of the first critical field,  $H_{C1}$  (around 175 mT at a temperature of 2 K) among all superconductors: high  $H_{C1}$  material is mandatory to prevent early vortex penetration on surface defects. Attempts to use superconductors with higher critical temperature,  $T_C$ , or upper critical field,  $H_{C2}$ , have failed so far, due to their low values of  $H_{C1}$ , which allows early penetration of magnetic vortices resulting in high surface dissipation.

Superconducting thin films have been suggested as a technology which may ultimately surpass the performance of bulk niobium. The best niobium thin film cavities have already achieved higher Q at low fields than that of bulk niobium [3]. Thin film cavities have also been shown to be less sensitive to trapped flux when compared to bulk niobium [4]. "Dirty" niobium thin films with large Ginzburg-Landau parameters can be integrated into multi-layer superconductor-insulator-superconductor films which have been proposed as a way to use magnetic shielding layers to increase both acceleration gradient and Q factor [5]. Other benefits of superconducting thin films are the

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possibility of substrates such as oxygen-free copper to dissipate heat more efficiently than bulk niobium and which are less expensive than bulk niobium therefore reducing material costs [6].

The current capabilities of thin film niobium only allow its use for low field application. Q factors become unacceptably small above 17 MV m<sup>-1</sup>. There are theories regarding the mechanisms which explain the sharp Q(E) slope; however, none are widely accepted as unequivocally proven [4,7–10]. It is therefore imperative that thin film niobium is fully characterized using a wide range of analytical techniques to fill in the gaps in our scientific understanding towards the goal of erasing the Q slope.

The objective of this study was to systematically analyze the dc magnetic properties of niobium thin films deposited by HiPIMS as a function of the deposition parameters. Some preliminary results have been reported by us previously [11–13]. HiPIMS is a variant of the commonly used pulsed dc sputtering. However HiPIMS achieves much higher target currents by using short pulses and comparatively long off times. The resulting HiPIMS plasma can have peak currents which are up to 2 orders of magnitude larger than dc. The enhanced plasma current at the target surface allows for a high ion to neutral ratio of the target material and synthesizes comparable films to ion assisted deposition [14]. The effect of changing both the substrate temperature and bias voltage will have been to change the surface mobility of sputtered material arriving at the substrate [15], allowing us to study the influence of temperature and ion bombardment on the rates of production and annihilation of defects.

# **II. EXPERIMENTAL**

#### A. Sample preparation and deposition

The niobium thin films were each deposited onto asreceived polycrystalline oxygen-free copper substrates. Each substrate disk was 2 mm thick with a diameter of 52 mm. The substrates were first cleaned in ultrasonic baths of acetone, then isopropanol, then methanol and finally rinsed with deionized water.

The HiPIMS deposition facility consists of two chambers: deposition and loadlock chambers. Up to seven samples can be inserted into the cartridge inside the loadlock vacuum chamber. Prior to deposition, the entire deposition facility was baked to 150°C for four days; the base pressure of  $5 \times 10^{-10}$  mbar was reached after cooling to room temperature. Inserting multiple sample substrates into the loadlock vacuum chamber allows one baking of all samples without the need to vent and bake the system between depositions. After all samples are deposited then new substrates are inserted and only the loadlock vacuum chamber was baked again.

Films were deposited by HiPIMS using a water cooled three-inch planar magnetron which was oriented at 45° from the substrate plane. The target material was 99.95% purity

niobium. The distance between the substrate and target was 150 mm. Krypton sputter gas was used at a pressure of  $7 \times 10^{-3}$  mbar. Krypton was chosen because it is purer than commercially available argon whilst is less costly than xenon. Krypton has been shown to produce sputtered niobium films which contain lower volumes of implanted sputter gas atoms when compared to the other noble gases [4]. The sample substrate was set to continuously rotate at 4 rpm.

The HiPIMS power supply was set to pulse with constant settings for every deposited film. The voltage was set to pulse at 200 Hz with a pulse length of 100  $\mu$ s. The average current was maintained at 600 mA which resulted in peak currents for each pulse of approximately 40 A. Each deposition was performed for 4 h resulting in the film thickness of approximately 1.4  $\mu$ m.

The variable parameters of the experiment were the substrate temperature and a dc substrate bias voltage. Eighteen samples were deposited in total. Two main deposition parameters were varied, sample temperature and the bias applied to the sample substrate. Samples were deposited at room temperature,  $500^{\circ}$ C or  $700^{\circ}$ C with either a grounded substrate, or with an applied dc bias of -20, -50, -80, -100 or -120 V. The desired substrate temperature was reached 30 min prior to the start of film deposition so that the substrate temperature was homogeneous once the film began to grow.

### B. dc magnetic hysteresis measurements

All deposited niobium films were analyzed using a quantum design MPMS XL-7 to measure magnetic hysteresis loops in a dc magnetic field at 4.2 K. In application to SRF cavities, the magnetic field should be parallel to the surface. All samples were oriented as close to parallel to the plane of the magnetic field as was possible to achieve. The error in the sample alignment with the magnetic field was  $\pm 1^{\circ}$  ( $\pm 17$  mrad).

The sensitivity of the Magnetic property measurement system (MPMS) system is  $10^{-7}$  emu; however, in the reported experiment the noise level of the magnetic moment measurements was observed to be  $10^{-4}$  emu at zero field.

Signal to noise ratio can be increased by increasing the sample volume. Since the volume of deposited thin films is very small then copper substrates were first etched from the films using nitric acid so that the sample thickness could be reduced, allowing three  $5 \pm 1 \text{ mm} \times 5 \pm 1 \text{ mm}$  pieces of the same film to be placed one on top of another inside the MPMS measurement space and then enhance the signal. The nitric acid was of >99.99% purity with a concentration of 70% and was mixed in a ratio of 1:1 with deionized water.

#### C. Residual resistivity ratio

The residual resistivity ratio (RRR) was measured for each deposited film. RRR is the ratio of the resistance of the sample at 300 and 10 K. Samples with the largest RRR have the fewest defect or impurity densities.

Each sample was measured using a purpose built fourpoint probe by passing 10 mA through the outer current probes and measuring the voltage drop across the inner probes, allowing the resistance to be calculated. The substrate was first etched from each sample using the same process as for the dc magnetic measurements to ensure the resistance of the copper was not measured. The resistance was measured every 5 s as each sample was cooled from room temperature down to below its superconducting transition temperature ( $T_C$ ).

# **D.** Morphological characterization

Each deposited film was analyzed by grazing incidence x-ray diffraction (GI-XRD) at an angle of 2°. Samples were measured with a Bruker D8 Discover system using a cobalt x-ray source to produce radiation with a wavelength of 1.78901 Å. Niobium thin film samples were measured both still attached to the copper substrate and after the copper substrate had been etched away.

The angle of the largest diffraction peak (preferred growth orientation),  $\theta$ , from each measurement was used to calculate the lattice parameter,  $a_0$ , of each film by first using Eq. (1) to calculate the lattice spacing, d, and then Eq. (2) to calculate  $a_0$ . A relative difference in the length of the lattice parameter from one film to another suggests disparity in the inherent film stresses, where the stress is attributed to the distribution of defects as the film grows:

$$n\lambda = 2d\sin\theta \tag{1}$$

$$a_0 = \frac{d_{hkl}}{(h^2 + k^2 + l^2)}.$$
 (2)

A selection of films were also imaged by scanning electron microscope (SEM). Images were taken of the film surface using a backscatter detector to give a better understanding of the film microstructure.

#### **III. RESULTS**

# A. First quadrant hysteresis

The results of magnetization measurements are shown in Figs. 1(a), 1(b) and 1(c) for the samples deposited at room temperature, 500 °C and 700 °C, respectively. Each figure displays results for bias voltages of 0, -20, -50, -80, -100 and -120 V. Measurements start at 0 mT and stop at fields above  $H_{C2}$ . The measured magnetic moment, M, displays an initial linearity as a function of applied field, H, followed by a varying rate of change in the measured moment before reaching its maximum value and finally falling back to zero. Only a selection of samples exhibit smooth curves, deposited at 700°C with bias voltages of -80, -100 and -120 V. Multiple small value magnetic flux jumps can be observed in all other samples. Such flux jumps represent unstable flux pinning and rapid reorientation of vortices into more energetically stable pinning locations as the magnetic field strength changes as discussed later in Sec. IV.



FIG. 1. dc magnetic moment against applied magnetic field at 4.2 K for niobium samples which were deposited at (a) room temperature, (b) 500°C and (c) 700°C.

# **B.** First detection of flux entry and the second critical field

When the sample is in the full Meissner state the magnetic moment,  $M_i$ , increases linearly with the applied magnetic field, H, and can be described as

$$M_i = \beta H, \tag{3}$$

where  $\beta$  is proportional to the undefined sample volume.

The first detection of flux entry into a superconducting film occurs when the gradient of the hysteresis curve changes after an initial linear increase [16]. To find  $\beta$  for each sample, straight fitting lines were plotted for the linear section of the hysteresis curve and any deviation from the predicted moment was calculated using the root mean squares error (RMSE) method. The RMSE is initially large ( $\approx 0.00009$  emu) at low field due to small signal to noise ratio, before reaching a minimum ( $\approx 0.00001$  emu) during the linear section of the curve, then finally continuing to increase once flux begins to enter the sample.

The field at which the magnetization curve returns to zero is equivalent to the  $H_{C2}$  of a superconductor.  $H_{C2}$  is reached when the magnetic field becomes large enough to extinguish superconductivity.

An example of the first detected flux entry (first deviation from linear),  $H_{dev}$ , the maximum magnetization,  $H(M_{max})$ , and  $H_{C2}$  are shown in Fig. 2. Table I gives the calculated RMSE for the measurement shown in Fig. 2.

The error in the measured moment becomes larger when the moment is smaller, as has already been discussed, therefore the field at which the magnetization effectively reaches zero has been quoted with an error set to  $\pm 10\%$ .



FIG. 2. Example of a raw dc hysteresis curve. The film was deposited at 700 °C with a -80 V biased substrate. The dashed lines denote either  $H_{dev}$ ,  $H(M_{max})$  or  $H_{C2}$ .

TABLE I. Data taken from the hysteresis curve shown in Fig. 2. The RMSE begins a continuous increase at 65 mT  $(H_{dev})$  indicating that flux has already entered the sample at that field.

Field [mT]	Moment [emu]	Predicted moment [emu]	Difference [emu]	RMSE [emu]
0	0.00000	-0.00009	0.00009	0.00009
10	-0.00315	-0.00313	-0.00002	0.00001
20	-0.00624	-0.00615	-0.00009	0.00002
30	-0.00931	-0.00917	-0.00014	0.00003
41	-0.01274	-0.01265	-0.00009	0.00002
49	-0.01507	-0.01503	-0.00005	0.00001
61	-0.01846	-0.01854	0.00007	0.00001
65	-0.01952	-0.01970	0.00018	0.00003
69	-0.02062	-0.02092	0.00030	0.00005
73	-0.02172	-0.02208	0.00036	0.00006
77	-0.02285	-0.02328	0.00043	0.00007
81	-0.02387	-0.02444	0.00057	0.00009
85	-0.02487	-0.02560	0.00073	0.00011
89	-0.02591	-0.02681	0.00089	0.00013
92	-0.02680	-0.02795	0.00115	0.00017
96	-0.02768	-0.02910	0.00142	0.00020
100	-0.02859	-0.03028	0.00169	0.00024

Figures 3 and 4 show plots of  $H_{dev}$  and  $H_{C2}$  data respectively for all samples.  $H_{dev}$  was at its maximum of  $65 \pm 4$  mT for the sample deposited at 700°C with -80 V biased substrate. A peak in  $H_{dev}$  occurred at a -80 V bias for all temperatures.  $H_{C2}$  peaked at 1000 mT for films deposited at room temperature without bias and with -20 V bias and at 500°C without bias.  $H_{C2}$  dropped to between 800 and 600 mT for all other bias voltages at room temperature and 500°C. The smallest  $H_{C2}$  values were between 400 and 500 mT and occurred for all samples



FIG. 3. The magnetic field  $H_{dev}$  at which the gradient of the hysteresis curve first changes from linearity for each deposited film.



FIG. 4. The magnetic field  $H_{C2}$  for each deposited film.

deposited at 700°C. The maximum magnetization  $H(M_{max})$  was recorded for each deposited film however showed no consistent trends relating to either substrate temperature or applied bias; the results are displayed in Fig. 5.

### C. Data normalization

A simple algorithm was applied to compare all hysteresis curves. Coefficients  $\beta$  allow the normalization of each set of measurements, M, to the ideal moment, M<sub>i</sub>, of the sample if it is in the Meissner state. Thus  $M/M_i = 1$  corresponds to a sample in the Meissner state. Any deviation from  $M/M_i = 1$  will therefore be proportional to the fraction of the sample volume which is no longer in the Meissner state and contains magnetic flux,  $(\Delta V/V)_M$ . The linear section of the raw data, where  $M/M_i = 1$ , was defined as the



FIG. 5. The maximum recorded magnetization  $H(M_{\text{max}})$  for each deposited film.



FIG. 6. The normalized dc hysteresis curves for samples deposited at room temperature with substrate bias of 0 to -120 V.

minimum RMSE as described for one sample in Table I. The results are shown in Figs. 6–8.

### D. Meissner state ratio

Meissner state ratios for films deposited at room temperature are shown in Fig. 6. The film deposited at room temperature and with a -80 V bias displayed M/M<sub>i</sub> which is consistently larger than for any other film up to a magnetic field of 150 mT, at which point the films biased at -80 and -100 V converge. The films deposited with bias voltages of 0, -20 and -120 V display the smallest M/M<sub>i</sub> up to fields of approximately 350 mT.



FIG. 7. The normalized dc hysteresis curves for samples deposited at 500°C with substrate bias of 0 to -120 V.



FIG. 8. The normalized dc hysteresis curves for samples deposited at 700°C with substrate bias of 0 to -120 V.

Films deposited with a substrate heated to 500°C are presented in Fig. 7.  $M/M_i$  was largest for the film deposited with a -80 V bias, displaying a similar curve to the film deposited with the same bias at room temperature. The films deposited at 500 °C without bias and with -120 V bias displayed the smallest average  $M/M_i$  of all films deposited at that temperature.

 $M/M_i$  for all films deposited at 700°C are shown in Fig. 8.  $M/M_i$  was largest for the film deposited with -80 V bias and displayed the sharpest transition from the Meissner state to  $H_{C2}$  of any films deposited during the study.  $M/M_i$  for the films deposited at -100 and -120 V displayed very similar properties over the entire curve. The smallest  $M/M_i$  was observed for the films deposited with bias voltages of either 0 or -20 V.

Figure 9 illustrates all deposited samples together at magnetic fields of 30, 60, 100 and 180 mT. All films achieved an  $M/M_i$  above 0.8 at 30 mT.  $M/M_i$  is largest at



FIG. 9. Compilation of the measured  $M/M_i$  for every sample at magnetic fields of 30, 60, 100 and 180 mT. The bias voltage applied to each sample is denoted as  $-V_B(V)$ .

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bias voltages of -50 to -100 V for all temperatures at 30 mT.

A distinct peak occurs at -80 V bias for films deposited at room temperature, 500°C and 700°C with an applied field strength of 60 mT. The shape of the M/M<sub>i</sub> curve is similar for 500°C and 700°C at bias voltages of -20 to -100 V however the M/M<sub>i</sub> for room temperature films deposited at -20 and -100 V is relatively smaller. M/M<sub>i</sub> is larger at 700 °C relative to both room temperature and 500 °C for the unbiased samples and those biased at -50and -120 V.

Within the error margins,  $M/M_i$  is the same at each bias voltage for room temperature and 500°C films at 100 mT, exhibiting a peak at -80 V.  $M/M_i$  for films deposited at 700°C has become greatly enhanced relative to room temperature and 500°C for the unbiased films and bias voltages of -80 to -120 V.

 $M/M_i$  is no larger than 0.55 for any film at 180 mT and a peak can still be observed at -80 V for all temperatures.

### E. RRR

RRR for each deposited film is shown in Fig. 10. The smallest RRR values of 9 were measured for the films deposited at room temperature and 500°C without an applied bias voltage. RRR peaked at 19 to 21 for the films deposited at room temperature with bias voltages of -50 to -120 V. RRR peaked at 29 to 31 for the films deposited at 500°C with bias voltages of -20 to -80 V. The largest RRR values of between 52 and 54 occurred at a substrate temperature of 700°C with bias voltages of 0, -50, -80 and -120 V with slightly smaller values of 43 and 42 for the -20 and -100 V biased samples, respectively.



FIG. 10. RRR of niobium thin films deposited with substrate temperature ranging from room temperature to 700°C and with dc bias voltages ranging 0 to -120 V.



FIG. 11. GI-XRD measurement of a niobium thin film sample after the copper substrate had first been etched away. The sample was originally deposited at a substrate temperature of 700°C with -80 V substrate bias.

#### F. Lattice parameters and surface imaging

All deposited samples grew with the same body centered cubic structure with preferred (110) growth orientation corresponding to a peak at approximately 44° to 45°. Small peaks relating to the (200), (211), (210) and (310) orientations were also observed in every deposited film. A typical GI-XRD measurement is shown in Fig. 11. The film was deposited at 700°C with -80 V biased substrate during deposition and the substrate had been etched away before the GI-XRD measurement was performed.

The lattice parameters of all deposited films are shown in Fig. 12. The samples with the copper substrate etched away display similar lattice parameters of 3.31 to 3.33 Å for films deposited at room temperature with bias voltages of 0 to -100 V and at 500°C with bias voltages of 0, -50, -80, -100 and -120 V. The film deposited at -120 V at room temperature exhibits a large lattice parameter at 3.37 Å and the film deposited at 500°C with -20 V bias a small lattice parameter of 3.26 Å. The lattice parameters of films deposited at 700°C are larger than at room temperature and 500°C. The film biased at -120 V at 700°C has the largest lattice parameter of all films at 3.38 whilst all other films deposited at that temperature range 3.35 to 3.36 Å.

The lattice parameters of the films with the copper substrate still in place are different from those with the substrate etched away. Films measured on copper that were deposited at room temperature show little variation in lattice parameter with all bias voltages ranging from 3.27 to 3.30 Å. The lattice parameter of the films deposited at 500°C and 700°C with bias voltages of 0 and -20 V have larger lattice parameters than was displayed at room temperature whilst the lattice parameter of the sample deposited at 500°C with -120 V is smaller. The lattice



FIG. 12. The calculated lattice parameters of niobium thin films deposited at either room temperature,  $500^{\circ}$ C or  $700^{\circ}$ C with bias voltages of 0, -20, -50, -80, -100 or -120 V with (a) the copper substrate removed by etching or (b) with the copper substrate still in place.

parameters of the films deposited at 500°C and 700°C with bias voltages of -50 to -100 V are comparable to those which were measured for films deposited at room temperature with similar bias voltage.

Planar SEM images of the films deposited at either 0 or -80 V bias for each of the three temperatures considered are shown in Fig. 13. The films deposited at room temperature and 500°C without an applied substrate bias grow with columnar grains whereas those deposited at -80 V bias appear to have grains which are more densely packed together. Grain sizes of all films deposited at room temperature and 500°C appear to be of the order of submicrons; however, grain size does appear slightly enhanced at the larger substrate temperature. The visible grain structure of films deposited at 700°C do not display significant changes from the unbiased sample and that biased at -80 V. Grains



FIG. 13. Planar SEM images of niobium thin film samples deposited (a) at room temperature and no bias, (b) at room temperature with -80 V bias, (c) at 500°C with no bias, (d) at 500°C and -80 V bias, (e) at 700°C with no bias and (f) at 700°C with -80 V bias.

have grown much larger at 700°C relative to room temperature and 500°C with grains of up to approximately 4–5 microns across.

# **IV. DISCUSSION**

The interpretation of the dc magnetic hysteresis measurements is perfectly valid and unambiguous only for cases when the thin film investigated is parallel to the applied magnetic field. Indeed, the magnetic behavior of superconducting thin films can be very different for samples which are aligned perfectly with the field and for films which are inclined to the magnetic field. Superconducting quantum interference device measurement of superconducting films with H parallel to the sample plane is fairly difficult to analyze because of the existence of a strong transverse signal, as shown in [17]. Moreover, regardless of any sample anisotropy, a purely geometric effect is to be expected due to edge effects. The magnetic moment is very sensitive to any small disorientation of the sample, which will have a dramatic effect on



FIG. 14. Schematic representation of magnetic field lines for (a) superconducting sample parallel to the applied magnetic field lines and (b) superconducting sample inclined with angle  $\alpha$ .

the signal intensity. Figure 14 illustrates a superconducting thin film oriented either parallel or inclined to the applied magnetic field. The area of samples which are perpendicular to the applied magnetic field is equal to  $A \sin \alpha$ . Therefore, the superconducting thin film ideally positioned parallel to the magnetic field will interfere much less than a thin film placed with an angle,  $\alpha > 0$ . The film can be considered parallel to the magnetic field if  $\alpha < d/L$ . We can use Eq. (4) to approximate an upper limit for  $\alpha$  in the parallel orientation to be 0.2 mrad, assuming a film thickness of 1  $\mu$ m and length of 5 mm:

$$\alpha(rad) \approx \frac{d}{L} \approx \frac{1 \ \mu m}{5000 \ \mu m} \approx 0.2 \text{ mrad},$$
 (4)

The value of 0.2 mrad is much smaller than the error in sample alignment of  $\pm 17$  mrad therefore samples in our experiment cannot be considered parallel to the magnetic field. If the sample is not perfectly parallel to the magnetic field, then the magnetic field near the edges is enhanced with respect to the rest of the sample. Thus, the magnetic field penetrates early into the edges of the sample, whilst the rest of the sample remains in the Meissner state.

The limitations of the sample alignment dictate that it is difficult to separate the two phenomena of flux penetration due to edge effects and flux penetration due to whole sample properties.

Multiple flux jumps were observed in Figs. 1(a) and 1(b) for all films deposited at room temperature and 500°C and in Fig. 1(c) for a selection of films deposited at 700°C. Such flux jumps represent unstable flux pinning and rapid reorientation of vortices into more energetically stable pinning locations as the magnetic field strength changes [18]. In extreme cases of multiple flux jumps it can lead to the so-called vortex avalanches [19]. Types of pinning locations are precipitates of nonsuperconducting material, precipitates of superconducting material with reduced

transition temperature, lattice dislocations, interplanar defects or grain boundaries. Vortices which move between pinning locations act as a viscous fluid and can produce areas of local heating which in turn can promote Cooper pair breaking. Enhanced numbers of unpaired electrons will increase the possibility of a quench [20]. Superconductors which display stable flux pinning are likely to have better thermal conductivity than superconductors which exhibit unstable flux pinning.

A range of films was deposited and tested multiple times and the results showed that the sample to sample variation in  $M/M_i$  was highly repeatable for samples which did not exhibit flux jumps, but the measured moment could fluctuate about the approximate magnitude of the flux jumps for those which did exhibit jumps. Samples which demonstrate either no jumps or one or two jumps show the highest average  $M/M_i$  values over the range of between 60 mT and  $H_{C2}$ . The films which showed the least flux jumps were the 700°C films with bias voltages of -80 to -120 V. This would indicate that either a minimum amount of energy must be supplied to the growing film to reduce the defect density to a level which is required for stable flux pinning, or changes occur within the substrate at 700°C, such as growth of grains or removal of the surface oxide layer, which could promote the growth of films that can exhibit stable flux pinning. Studies have in fact already shown that the substrate condition can greatly affect the properties of niobium films [4], where oxide-free copper surfaces, as would be expected at 700°C, produced much larger grain size than films grown onto the native oxide of copper. The morphological characterization of our films confirmed that both grain size and RRR was much larger for films deposited at 700°C when compared to the lower temperatures.

The field at which the gradient first changes for each hysteresis curve indicates the first detectable change in the magnetic properties of the sample and is therefore the first detectable sign of flux entry into a sample. The results indicate that no film remained in the Meissner state above 65 mT which is well below the  $H_{SH}$  of niobium, at 220 mT. The early flux penetration occurred due to the sample geometry within the applied magnetic field which produces the edge effects.

Results describe an enhancement in the field where the first flux penetration can be detected within sample films which were deposited with a -80 V bias at all temperatures. The gradual increase in H<sub>dev</sub> from no bias to -80 V bias and subsequent reduction in H<sub>dev</sub> at higher bias voltages can be explained using well-known thin film growth theory [15]. Films with relatively few defects will be deposited if the transferred energy due to ion bombardment is above the surface displacement energy but below the bulk displacement energy of niobium [21]. The initial application of a bias voltage will accelerate charged ions towards the substrate and so promote the breaking of

atomic bonds at the films surface. Ion bombardment then increases surface atom mobility and diffusion whilst also annihilating surface defects. The rate of surface diffusion rises with increased bias voltage, further reducing defect densities up to the threshold of -80 V. The images of the surface of the samples show how the films deposited at room temperature and 500°C change from columnar films deposited with a grounded substrate to more densely packed grains with a bias of -80 V. Reduced H<sub>dev</sub> at -100 and -120 V then suggests that high energy ion bombardment produces more defects than are being annihilated. The types of defect which are produced by high energy bombardment can be both a reduction in grain size (more grain boundaries) and random complex dislocations.  $H_{dev}$  increases with temperature for bias voltages of -100and -120 V where the heat energy enhances the probability of annealing out defects within the film and reduces the volume of grain boundaries.

The RRR results for samples deposited at 500°C display an increase from 0 to -80 V, then a drop at -100 and -120 V and was like observations of H<sub>dev</sub>. The same drop in RRR was not seen for films deposited at room temperature with bias voltages of -100 and -120 V; however, RRR rose from 9 without bias to a plateau of between 19 and 21 with biases of -50 to -120 V indicating there is a finite limit to the improvement in crystallinity as the bias is increased. The slight inconsistency between observations of RRR and H<sub>dev</sub> for films deposited at room temperature with biases of -100 and -120 V supports the theory that more random complex defects can form within a deposited film as the ion energy is increased past a certain threshold. RRR is consistently large at 700°C and results from the dramatic enlargement of grain size when compared to films deposited at room temperature and 500°C, with and without an applied bias.

The lattice parameters give an idea of the nature of the defects which may be present within films. Lattice parameters were different in every instance for films measured on copper relative to those which had been etched, indicating that the substrate has a large influence on the stresses within a growing film. Bias voltages of -50 to -100 V display consistent values between 3.27 and 3.29 Å for each temperature when measured on the copper substrate. The lattice parameters of etched films with bias voltage of -50to -100 V are still consistent with each other, however slightly larger when compared to those of the films measured on copper, and are largest for the films deposited at 700°C. A consistency of the lattice parameter indicates a consistency in the types of defect present in the film microstructure and is likely determined by either the densities of grain boundaries or impurity atoms. However, seemingly random distributions of the lattice parameter are most likely caused by the presence of random complex dislocations. The lattice parameter for the -120 V biased films varies widely as the deposition temperature

changes from room temperature up to 700°C for both the films measured on copper and after it was etched. The inconsistent and varying lattice parameters of the films deposited with -120 V bias strengthen the argument that random defects are produced at high bias voltage, as discussed for the H<sub>dev</sub> and RRR results. The lattice parameter of the films deposited with both 0 and -20 V bias displays much larger values at 500 and 700°C than for the other tested bias voltages when measured on copper and there was a relatively small lattice parameter displayed by the etched film deposited with -20 V bias at 500°C. The inconsistencies in lattice parameter for films deposited with either no bias or very small bias indicate that random defects are frequent within these films as the energy of bombarding ions was not high enough to annihilate them.

The films deposited at room temperature and 500°C without substrate bias display similar  $H_{C2}$  of 1000 mT.  $H_{C2}$ then drops at either room temperature with bias voltages of -50 V or larger or at 500°C with bias voltages of -20 V or larger, to a minimum value of 617 mT.  $H_{C2}$  is consistently smaller for a deposition temperature of 700°C at between 400 and 500 mT for all bias voltages. The results suggest that  $H_{C2}$  is determined by the defect and impurity densities within films which act as magnetic pinning centers, especially those found at grain boundaries. The samples with the largest  $H_{C2}$  of 1000 mT coincide with the three films which had the smallest RRR, between 9 and 13, of all the deposited samples.  $H_{C2}$  was then smaller for the films which had larger RRR at room temperature and 500°C. A consistently small  $H_{C2}$  was measured for the films deposited at 700°C where grain size and RRR were largest.

 $M/M_i$  is proportional to the fraction of the sample volume which contains magnetic vortices and gives an alternative description of the behavior of the flux entry into the sample. Films which have the largest  $M/M_i$  can be assumed to be more resistant to penetration by an applied magnetic field. Samples which show the largest  $M/M_i$  relative to the applied field strength are therefore good candidates for further rf testing as they have a smaller percentage volume which contains pinned magnetic vortices. Pinned magnetic vortices oscillate within rf electric fields and lead to dissipative heating and ultimately a cavity quench when used in SRF application [22].

The  $M/M_i$  curves describe little difference between deposited films at fields of 30 mT; however, differences can be observed once the field reaches 60 mT. A pattern emerges where films deposited at each temperature show a minimum volume containing magnetic flux for films with a bias voltage at -80 V.

A more distinct pattern can be observed at the higher field of 100 mT. The ratio of  $M/M_i$  is the same within the bounds of error for room temperature and 500°C films for every bias voltage. The 700°C films developed a broad  $M/M_i$  peak which covers a range of bias voltages at 100 mT.

The same thin film growth mechanisms described in detail for the observations of H<sub>dev</sub>, RRR and lattice parameter also apply for the  $M/M_i$  curves. The application of a bias will initially reduce defect densities before high energy ion bombardment produces more defects than are being annihilated. The effects of substrate heating become noticeable at 700°C with enhanced M/M<sub>i</sub> for a broad range of bias voltages. Again, as discussed in detail earlier for results of H<sub>dev</sub>, RRR and the SEM images, the higher substrate temperature reduces the volume of defects found at grain boundaries and enhances the probability of annealing out defects which occur due to high energy ion bombardment. Ideally, the experiment would use much higher temperatures to further anneal complex defects from films; however, the maximum deposition temperature is restricted by the melting temperature of 1085 °C for the copper substrate. Thin film growth dynamics which occur at the interface between film and substrate are less predictable once the deposition temperature gets close to the melting temperature of the substrate.

It is important to note that, although our results are very important to understand the physics related to dc magnetic fields penetration in thin films deposited by HiPIMS they may not be directly extrapolated to thin film SRF cavities which are subjected to varying rf magnetic fields. Another significant difference is that the parallel magnetic field in rf cavities is applied to one side of the film only, whereas the magnetic field is applied to all sides of the sample in our dc magnetometer measurements. Further experiments are required to properly link both the dc and rf magnetic behaviors of superconducting thin films.

It should also be made clear that the results presented here are for films deposited by HiPIMS using specific process conditions. The HiPIMS power supply was set to pulse with a repetition rate of 200 Hz, with a pulse length of 100  $\mu$ s and with an average current of 600 mA. Different HiPIMS settings and other deposition techniques may yield different results.

## **V. CONCLUSIONS**

The effects of substrate heating and applied bias during HiPIMS deposition on niobium thin film superconducting properties were tested using dc squid magnetometry. Films were later analyzed for RRR, lattice parameter and imaged by SEM so that the dc magnetometry results could be interpreted.

The values of  $M/M_i$  for both room temperature and 500°C films remained constant for similar bias voltages therefore demonstrating that the applied bias had a more significant effect on the growing film than the substrate heating had up to 500°C. The increased  $M/M_i$  at -80 V is expected to be a result of increased rates of surface diffusion and defect annihilation due to ion bombardment. The measured lattice parameters and correlations to RRR measurements suggest that ion bombardment has become

too energetic at bias voltages of -120 V and therefore more random complex defects are produced than are removed from the niobium lattice.

Average increases of  $M/M_i$  for films deposited at 700°C relative to room temperature and 500°C reveal that the energy contribution towards diffusion within and at the surface of the film has become significant. There is a broad range of bias voltage which produces large  $M/M_i$  at 700°C suggesting that there is less dependence on the applied bias at larger temperatures. RRR was also observed to be consistently larger for films deposited at 700°C than for room temperature and 500°C. The most likely cause of the enhancement of  $M/M_i$  and RRR at 700°C is the enlargement of the niobium grain size as shown in SEM images.

Samples deposited at 700°C exhibit smaller  $H_{C2}$  than samples deposited at room temperature and 500°C and is most likely due to the increase in grain size.

The study concluded that the films which are most suitable for SRF applications are those deposited with a -80 V substrate bias at room temperature, 500°C and 700°C as these films display similarly large M/M<sub>i</sub>.

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