

*pin1,3;4;7*

PIN1::gPIN1:GFP

ATML1::cPIN1:GFP

PIN1::cPIN1:GFP

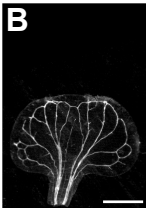
SHR::cPIN1:GFP

SCL32::cPIN1:GFP

**A**



**B**



**C**



**D**



**E**

