Capacitively Coupled Silicon-Organic Hybrid Modulator for 200 Gbit/s PAM-4 Signaling

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Abstract: We demonstrate capacitively coupled silicon-organic hybrid (SOH) modulator with a π -voltage-length product of 1.3 V mm and 3 dB EO bandwidth exceeding 65 GHz. The modulator is used for 200 Gbit/s (100 GBd) PAM-4 signaling.

OCIS codes: (230.4110) Modulators; (130.3120) Integrated optics devices; (200.4650) Optical interconnects

1. Introduction

High-speed, low-cost electro-optic modulators with small footprint and low drive voltage are essential for short-reach links within data-center and campus-area networks. In this context, the silicon photonic (SiP) platform is attractive, offering mature high-yield fabrication techniques well suited for highly scalable mass production. Although conventional depletion-type SiP modulators have been pushed to bandwidths of about 50 GHz, their efficiency is still rather low, leading to π -voltage-length products of the order¹ of 10 V mm. Silicon-organic hybrid (SOH) devices can overcome these limitations by combining nanophotonic silicon slot waveguides with highly efficient electro-optic (EO) materials, leading to ultra-low $U_{\pi}L$ products² of 0.32 V mm and symbol rates³ of 100 GBd. However, the analogue bandwidth of such devices is still limited³ to, e.g., 25 GHz by the finite conductivity of the doped silicon slabs that are used to electrically contact the rails of the slot waveguide. Plasmonic-organic hybrid (POH) do not suffer from these limitations and offer bandwidths⁴ of more than 300 GHz, but suffer from high optical losses. In an alternative approach, SiP waveguides can be combined with high- κ dielectric materials instead of doped silicon slab⁵. Experimental demonstrations of this concept⁶, however, were still limited to devices with $U_{\pi}L$ products of 14 V mm, measured at a frequency of 1 kHz.

In this paper, we demonstrate a capacitively coupled SOH (CC-SOH) modulator, where the resistive Si slabs of a conventional SOH modulator are replaced by a high- κ dielectric, BaTiO₃ (BTO). The BTO layer is used to confine the modulating RF signal to the EO material in the slot, thereby overcoming the bandwidth limitations of conventional resistively-coupled SOH devices. We show that the EO frequency response is extremely broad having a 3 dB bandwidth of more than 65 GHz, and the π -voltage-length product is as small as 1.3 V mm. Using a 1 mm-long device, we generate PAM-4 signals at line rates (symbol rates) of 200 Gbit/s (100 GBd) with a bit error ratio (BER) of 9×10^{-3} . To the best of our knowledge, this is the first demonstration of a CC-SOH modulator with competitive performance parameters, leading to the highest PAM-4 data rate so far demonstrated with modulators based on dielectric waveguides on the silicon photonic platform.

2. Capactively Coupled Silicon-Organic Hybrid (CC-SOH) Modulator

The CC-SOH Mach-Zehnder modulator (MZM) is fabricated on the silicon-on-insulator (SOI) platform, see Fig. 1(a). Each MZM arm consists of a SiP strip waveguide (500 nm × 220 nm) which is transformed into a slot waveguide through a logarithmic taper, see Inset of Fig. 1(a). A cross-section of the slot waveguide is shown in Fig. 1(b). It comprises two 200 nm-wide and 220 nm-high silicon rails separated by a 100 nm-wide slot. The RF signal is supplied by a co-planar transmission line made from gold (yellow) in ground-signal-ground (GSG) configuration, see Fig. 1(a). Amorphous BTO slabs (red) having a height of 150 nm, a width of 1 μ m, and a high electric permittivity⁷ of $\varepsilon_r > 50$ are deposited between the optical slot waveguide and the transmission line to enhance capacitive coupling of the RF signal to the slot. The finite difference time domain (FDTD) simulations in Fig. 1(b) and (c) show that the optical quasi-TE mode and the RF mode are tightly confined to the slot region and hence have a strong overlap. An organic EO polymer (YLD124) is filled into the silicon slots and is activated by a one-time poling process. To this end, the device is heated and a voltage is applied between the (floating) ground electrodes, allowing the MZM to operate in push-pull mode³. A geometrical length difference of 80 μ m between the MZM arms allows to select the operating point by tuning the wavelength. For a 1 mm-long CC-SOH MZM, we measure a U_{π} of 1.3 V.

3. Bandwidth Measurement of CC-SOH Modulator

CC-SOH modulators overcome the RC limitations of conventional SOH devices by replacing the resistive slabs with a high- κ dielectric material. For short devices, the lumped element equivalent circuit results in a small slot capacitor which is connected to the RF signal lines by a large coupling capacitor rather than a resistor. This coupling is not associated with any time constant, and the bandwidth is only limited by the RF propagation losses of the modulation signal. Fig. 1(d) shows the electro-optic-electric (EOE) response of a 1 mm-long CC-SOH modulator with 150 nm

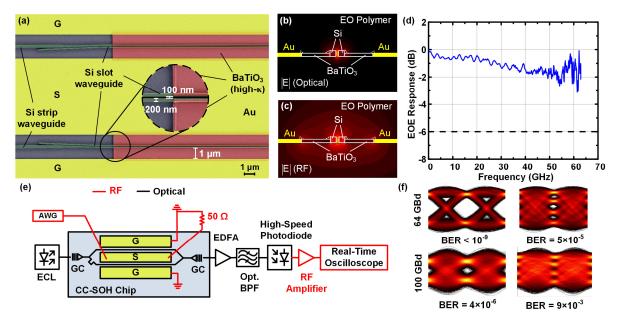


Fig. 1: (a) False-color scanning electron micrograph of CC-SOH MZM. Inset: Silicon slot waveguide surrounded by layers of BaTiO₃. (b), (c) Electric field profiles of the optical quasi-TE mode and the RF mode, both tightly confined to the slot region. (d) Measured electro-opticelectric (EOE) response of a 1 mm-long CC-SOH modulator showing a 6 dB EOE bandwidth well beyond 65 GHz. (e) Setup for data transmission experiments. (f) Extracted eye diagrams and bit error ratio (BER) for NRZ and PAM-4 signals at symbol rates of 64 GBd and 100 GBd.

thick BTO. The 6 dB electro-optical-electrical (EOE) bandwidth corresponds to the 3 dB electro-optic (EO) bandwidth, which is well beyond the 65 GHz that is accessible by the vector network analyzer used in our setup.

4. Data Transmission Experiments

To evaluate the transmission performance of the CC-SOH modulator, we use the setup depicted in Fig. 1(e). The electrical signals are generated by a 120 GSa/s arbitrary-waveform generator (AWG, Keysight M8194A, analog bandwidth 45 GHz). The resulting signal drives the GSG electrodes of the MZM via microwave probes having a nominal bandwidth of 67 GHz. An external 50 Ω impedance terminates the coplanar transmission line. We use sinc-like pulse shapes featuring a raised-cosine spectrum for generating signals with symbol rates of 64 GBd and 100 GBd. The peakto-peak on-chip drive voltage is less than 1 V for 100 GBd signaling. An external-cavity laser (ECL) provides the optical carrier at a wavelength near 1550 nm. Light is coupled in and out of the CC-SOH chip via grating couplers. An erbium-doped fiber amplifier (EDFA) compensates the fiber-to-fiber insertion loss of the device, which amounts to 22 dB. Note that this loss is mainly caused by non-optimum grating couplers (9 dB total) and waveguide design, both of which can be further improved. The modulated optical carrier is filtered by a 2 nm optical band-pass filter and fed to a high-speed photodiode with a bandwidth of 70 GHz (Finisar XPDV3120R). The resulting electrical signal is amplified by an RF amplifier and recorded with an oscilloscope having an analog bandwidth of 100 GHz (Keysight UXR1004A). A programmed pre-compensation in the AWG accounts for the strong frequency roll-off of the AWG and other RF components at the transmitter, excluding the modulator. Fig. 1(f) shows the eye diagrams for on-off keying (OOK) and four-state pulse amplitude modulation (PAM-4) signals at symbol rates of 64 GBd and 100 GBd. At 64 GBd, the BER is below 10^{-9} for OOK and reaches 5×10^{-5} for PAM-4. At 100 GBd OOK, a BER of 4×10^{-6} is measured, which is still well below the forward error correction (FEC) limit with 7 % overhead. For 100 GBd PAM-4 (200 Gbit/s line rate), we obtain a BER of 9×10^{-3} , which is just below the threshold of FEC with a 20 % overhead. To the best of our knowledge, this represents the highest PAM-4 data rate so far demonstrated with modulators based on dielectric waveguides on the silicon platform.

5. Summary

We presented a capacitively coupled silicon-organic hybrid modulator with 3 dB EO bandwidth beyond 65 GHz and a $U_{\pi}L$ -product of 1.3 V mm. We demonstrated 100 GBd PAM-4 signaling with BER below the 20 % FEC limit. This work is supported by the European Research Council (ERC Consolidator Grant 'TeraSHAPE', # 773248), by the DFG project PACE (# KO 4192/9-1) within the Priority Programme "Electronic-Photonic Integrated Systems for Ultrafast Signal Processing" (SPP 2111), by the DFG project HIPES (# KO 4192/6-1), by the Alfried Krupp von Bohlen und Halbach Foundation, by the Karlsruhe Nano Micro Facility (KNMF), and by the European Regional Development Fund (ERDF).

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