

Noise Reduction on the LEP bunch current measurement system

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Abstract

The lifetimes of coasting beams in LEP are computed from the difference of the bunch currents measured during a time interval of a few seconds. The quality of these measurements is presently limited by the noise figure of the bunch to bunch acquisition system. This paper describes comparative studies on the acquisition system in order to improve the noise figure by one order of magnitude. For this purpose three different principles, i.e. integrating measurements, peak hold circuits with internal timing (autotrigger) or peak hold circuits with external timing were studied.

The paper describes the results of these studies.

1 INTRODUCTION

The LEP bunch current monitoring system has been described in previous publications [1] [2] [3]. The system is used for daily LEP operation and an online display of the measured bunch currents and computed beam lifetimes is available in the main control room. Fig.1 shows a screen dump of the online display for the positron bunches during a LEP physics coast with four bunchtrains of four bunches in each train. In addition to the bunch currents the lifetime averaged over all bunches is displayed (56.4 hours in fig.1) and the time evolution of the lifetime is shown as the yellow trace. Although the beam conditions had not changed a significant variation of the measured lifetime is visible in the graph. This variation is linked to noise contributions in the individual current measurements and will be discussed in the following section.

Fig.2 illustrates how the lifetime is computed from two consecutive bunch current measurements, which themselves are averages of many beam revolutions during a time

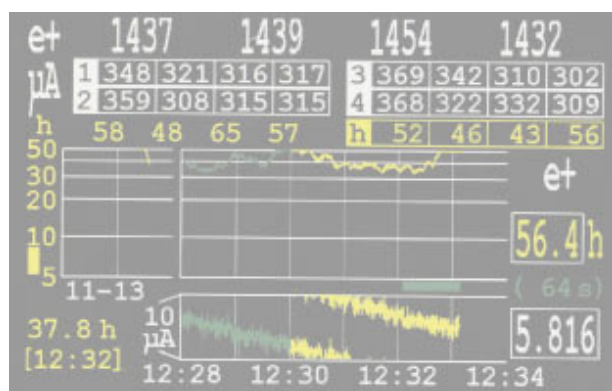


Figure 1: Online Display in the main control room of the LEP-BCT

interval Δt . Error propagation yields for the measurement error on the lifetimes:

$$\frac{\sigma_{\tau}}{\tau} = \frac{\tau \cdot \sigma_{\Delta I}}{I \cdot \Delta t^{3/2}} \quad (1)$$

where $\sigma_{\Delta I}$ is the variance of the measured current difference, which can be evaluated as $\sqrt{2}$ times the variance of individual current measurements. The above equation shows the strong influence of the integration time interval Δt . In the present LEP system this is exploited by having a dynamic adjustment of the integration time. For long lifetimes the integration time is increased up to 128 seconds, and only for short lifetimes integration times of 1 second are used (see details in [4]).

Of course a better solution to the problem would be to reduce drastically the variance of individual current measurements, such that the system could be operated with a fixed integration time of 1 or 2 seconds. This is very convenient for operators, as the recovery time of the instrument after sudden lifetime changes is much quicker.

Using equation 1 with an integration time of 2 seconds and asking for 5 % of measurement error on lifetimes of about 20 hours requires a current measurement noise of about 20 ppm. This has to be compared with the value of the present BCT system of 100 - 200 ppm.

The following chapters describe measurement results on different acquisition systems in order to obtain the required factor 10 improvement in noise performance.

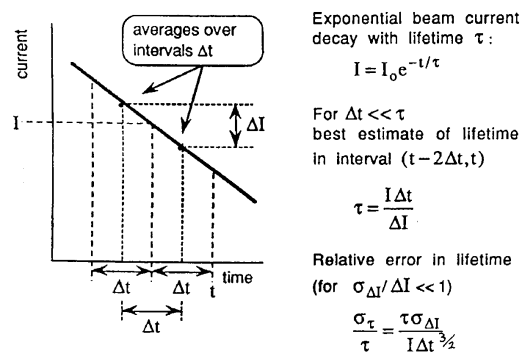


Figure 2: Illustration of lifetime calculation from two consecutive beam current measurements

2 ACQUISITION ELECTRONICS

2.1 Signal Integration

This circuit is used in the present BCT system and its design was aimed at obtaining maximum absolute precision. The bunch signal (1) is measured as difference between the integrated beam signal and an integration of the baseline (pedestal) of the detector. The subtraction is done as analog circuit. Two "Integrate and Hold" Circuits, which are controlled by two timing gates (2) & (3) of equal length, are used for the integration.

2.2 Peak Hold Circuits

Compared to the previous circuit a peak hold circuit is not optimum in terms of absolute precision as the baseline of the signal is neglected, on the contrary the circuit can be optimized for best noise performance. The signal to noise ratio depends largely on the bandwidth of the input signal and the jitter on the sampling gate. In bunch train operation the spacing between two bunches of the same polarity is 335 nsec. In order to have less than 1% crosstalk between bunches the transformer signal was filtered with a lowpass of 12 MHz corner frequency. The noise requirements of 20 ppm translate into the specification of having a jitter on the sampling gate of less than 3 nsec rms.

Two circuits with a different circuit for the sampling gate were tested. All tests were done by using modified digitizing cards of the BOSC system [5], which is presently used for the SPS tune measurements.

2.2.1 Internal timing

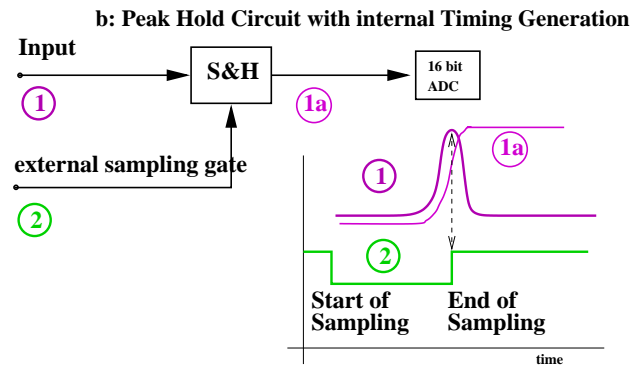
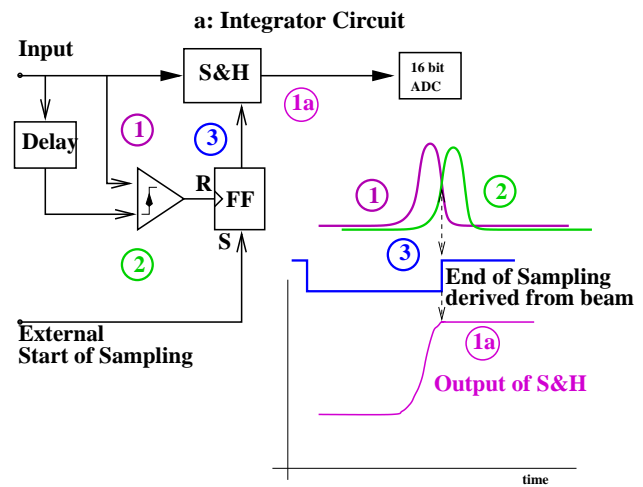
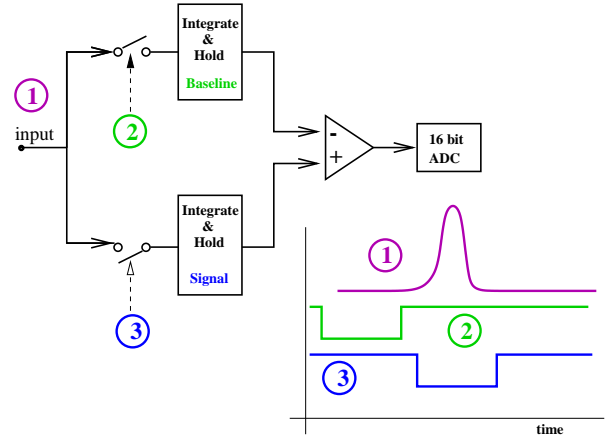
The objective of this design was to render the system almost independent of the LEP filling scheme as the sampling gate is derived from the beam signal itself. The signal is split into a direct path (1) and into a delayed pass (2). A comparator circuit detects the cross over between direct and delayed signal. By adjusting the amplitude of the delayed signal the cross over point can be set such that the end of the sampling gate coincides with the crest of the input signal at the S & H circuit.

2.2.2 External Timing

In this case the "end of sampling gate" is applied from the outside to the circuit. For the tests a signal directly derived from the Rf-system was used. This signal had a timing jitter below 3 nsec rms.

3 RESULTS

Fig.4 shows measurements of the the bunch currents using the three different electronics as described in the previous chapter. Each point in the graphs corresponds to the average of 5622 machine turns, i.e. the equivalent of 0.5 seconds of data.



c: Peak Hold Circuit with external Timing Generation

Figure 3: Block Diagram and Timing Sequence of the three different electronics circuits, top: integrator, middle: peak hold with internal timing, bottom: peak hold with external timing

A straight line fit is applied to the data and the rms spread around the fitted curve is displayed in the measurement graph. The obtained rms values depend on the selection of the input data, as the straight line fit implies a constant life time during the measurements. With different data sets the following range for the noise figures were obtained: integrator: 120 - 300 ppm, peak hold internal timing: 80 - 140 ppm, peak hold external timing: 19 - 37 ppm.

4 DISCUSSION

The above results show clearly that both peak hold circuits give better noise performance than the present integrator circuit. Since the prototype design in 1989 the present BCT system has the integrator circuits and the digitizing electronics in separate chassis making it very difficult to avoid ground loops or other noise sources. A modern and more compact circuit design would probably give also better results.

The choice between the two peak hold circuits is almost evident. Only the external timing circuit yields the required performance in terms of rms noise. Another argument in favour of the external timing circuit is the required dynamic range. LEP is filled with bunches between 1 μA bunch current in special machine experiments up to 1 mA bunch currents in high intensity operation. In case of an internal autotrigger circuit this dynamic range of 60 dB can not be realized without switching the gain of a preamplifier. The best result for the dynamic range of the autotrigger, which we achieved in the laboratory was 42 dB (for the bandwidth considerations as described in the previous chapter).

So in conclusion the new LEP BCT system scheduled for 1997 operation will make use of peak hold circuits with external timing reference. With a short integration window of 2 seconds on the bunch current measurements (see fig.1) and 20 ppm rms noise lifetimes of 20 hours will be measured with a precision of 5% rms.

5 REFERENCES

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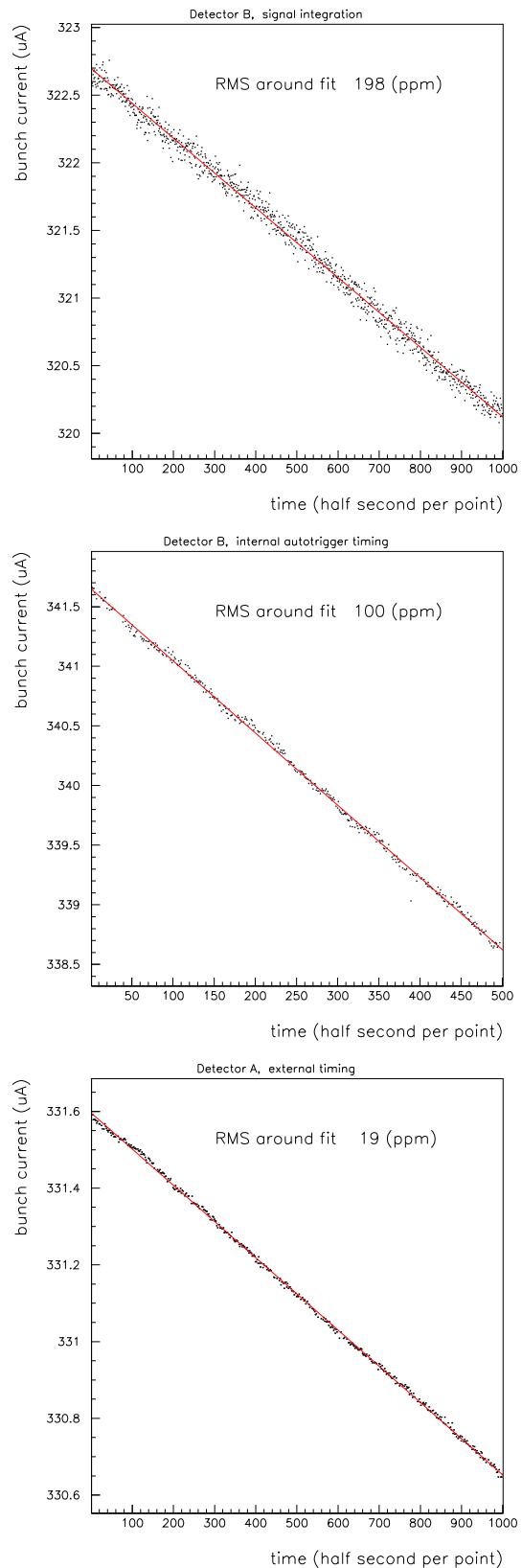


Figure 4: Noise measurements on the three different electronic circuits: top: integrator, middle: peak hold with internal timing, bottom: peak hold with external timing reference