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OPEN Solitonic Josephson-based meminductive systems

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Memristors, memcapacitors, and meminductors represent an innovative generation of circuit elements whose properties depend on the state and history of the system. The hysteretic behavior of one of their constituent variables, is their distinctive fingerprint. This feature endows them with the ability to store and process information on the same physical location, a property that is expected to benefit many applications ranging from unconventional computing to adaptive electronics to robotics. Therefore, it is important to find appropriate memory elements that combine a wide range of memory states, long memory retention times, and protection against unavoidable noise. Although several physical systems belong to the general class of memelements, few of them combine these important physical features in a single component. Here, we demonstrate theoretically a superconducting memory based on solitonic long Josephson junctions. Moreover, since solitons are at the core of its operation, this system provides an intrinsic topological protection against external perturbations. We show that the Josephson critical current behaves hysteretically as an external magnetic field is properly swept. Accordingly, long Josephson junctions can be used as multi-state memories, with a controllable number of available states, and in other emerging areas such as memcomputing, i.e., computing directly in/by the memory.

Circuit elements, specifically, resistors, capacitors, and inductors with memory¹⁻⁸, i.e., elements with characteristics that depend on the past states through which the system has evolved, have recently received increasing attention. Beyond the obvious applications in storing information, these elements can be combined in complex circuits to perform logic⁹ and unconventional computing operations¹⁰⁻¹⁶ in massive parallel schemes¹⁷, and in the same physical location where storing occurs. Superconducting circuits that store and manipulate information are particularly appealing in view of their low-energy operation. Among these, a superconducting tunnel junction-based memristor was recently suggested¹⁸⁻²⁰. However, this type of element does not feature controllable multiple states that can be easily protected against unavoidable noise, due to a stochastic drift of the memory¹⁵.

Our proposal instead is based on a long rectangular tunnel Josephson junction (LJJ) subject to a suitable periodical driving. A tunnel Josephson junction is a quantum device formed by sandwiching a thin insulating layer between two superconducting electrodes, and "long" refers to the physical length of the junction (\mathcal{L}) which is supposed to exceed the Josephson penetration depth (λ_I). A scheme of a LJJ with an in-plane magnetic field (H_{ext}) is shown in Fig. 1a. A LJJ is the prototypical system to investigate solitons^{21,22} in a fully solid-state environment, and the history-dependent behavior that we envision stems from how solitons rearrange their configuration along the junction under the effect of an external magnetic field^{23,24}.

Results and Discussion

The phase dynamics of a LJJ is described by the sine-Gordon equation²⁵⁻²⁸:

$$\frac{\partial^2 \varphi(x,t)}{\partial t^2} + \alpha \frac{\partial \varphi(x,t)}{\partial t} - \frac{\partial^2 \varphi(x,t)}{\partial x^2} = -\sin[\varphi(x,t)].$$
(1)

Above, φ is the macroscopic quantum phase difference between the superconductors, α denotes the intensity of the damping effect, x is the spatial coordinate along the junction, and t is the time (see SI). The boundary conditions of equation (1) read

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Figure 1. Solitonic Josephson-based meminductive system. (a) A superconductor-insulator-superconductor (SIS) rectangular long Josephson junction (LJJ) excited by an homogeneous external periodical magnetic field H_{ext} . Here, we refer to the normalized field H in place of H_{ext} (see SI). The length and the width of the junction are $\mathcal{L} > \lambda_J$ and $\mathcal{W} \ll \lambda_J$, respectively, where λ_J is the Josephson penetration depth. A LJJ excited by a magnetic flux falls into the category of field-controlled meminductive systems, since the input and output variables are the applied magnetic field and the Josephson critical current, respectively. The symbol used to represent the *solitonic* Josephson-based meminductive system (SJMS) is shown. Fluxons (Φ_0) within the junction surrounded by supercurrent loops are also represented. (b) Schematic of a possible memory drive formed by an ensemble of SJMSs. The core of the device is a LJJ excited by an in-plane magnetic field, with specific read-out electronics for the critical current. As an example, we display here a junction with length $L = \mathcal{L}/\lambda_J = 10$ by which a 4-state memory element can be defined. These distinct states are labelled by the number of solitons arranged along the junction. The peaks in the $d\varphi/dx$ curves (see SI) and the number of loops of Josephson current surrounding the fluxons are indicated as well.

$$\frac{d\varphi(0,t)}{dx} = \frac{d\varphi(L,t)}{dx} = H(t),$$
(2)

where H(t) is the normalized time-dependent external magnetic field, and $L = \mathcal{L}/\lambda_J$ is the normalized length of the junction. By varying H(t), the phase φ evolves according to equations (1) and (2). For a spatially homogeneous supercurrent density, the Josephson critical current $I_s^m(t)$ of the junction shows a "Fraunhofer-like" diffraction pattern consisting of overlapping lobes as the magnetic field is increased, and described by the following equation²⁹⁻³¹:

$$I_s^m(t) = \frac{I_c}{L} \left| \int_0^L dx \cos \varphi(x, t) \right|,\tag{3}$$

where I_c is the zero-field, zero-temperature junction critical current. This behavior is shown in Fig. 2a as the driving magnetic field is swept "forward" from zero. A diffraction lobe corresponds to a specific number of solitons present along the junction^{23,31}. When the external magnetic field penetrates the junction edges it induces Josephson vortices along the weak-link, according to the nonlinearity of equation (1). These vortices, i.e., solitons, are induced by persistent supercurrent loops carrying a quantum of magnetic flux, $\Phi_0^{21,22}$. The critical current, and the resulting patterns as the driving field is swept, are the physical quantities on which we focus since they can be measured with conventional techniques. In all forthcoming calculations we use parameters typical of Nb/AlOx/Nb tunnel junctions as the ideal materials combination to implement solitonic Josephson-based meminductive structures.

Figure 2b shows the diffraction pattern of the critical current when the magnetic field direction is reversed. The resulting "backward" diffraction pattern markedly differs from the forward pattern shown in Fig. 2a. For a given magnetic field H, the current state in which the system is found depends on the field history. This is a remarkable feature of the dissipative solitonic dynamics described by equation (1). Different current states correspond to different numbers of solitons arranged along the junction, and the transition from a diffraction lobe to another corresponds to the injection, or the ejection, of solitons³¹. As in any dissipative dynamics, the state of the system is not only determined by the value of the drive but it also depends on the path followed by the system. This induces the forward-backward asymmetry, and the hysteretic diffraction patterns shown in Fig. 2a,b. In the forward pattern, the first lobe corresponds to the Meissner state, i.e., zero solitons in the junction, whereas by exceeding a threshold value $H_c \simeq 2$ the second lobe begins and solitons in the form of magnetic fluxons penetrate into the junction. This value of the critical field characterizes the diffraction patterns of the Josephson critical current in both overlap and inline LJJs^{26,32,33}. For H > 0, the backward dynamics is strictly described by N-solitons solutions, with $N \ge 1$. The amount of solitons exited depends on both the field intensity and the length of the junction.

Figure 2b–d display the forward-backward diffraction patterns as a function of the junction length. Specifically, by increasing the length, the number of lobes forming the pattern grows, and the hysteretic asymmetry between



Figure 2. Josephson critical current diffraction patterns. (a,b), Normalized Josephson critical currents I_s^f/I_c and I_s^b/I_c as the driving field *H* is swept forward from H=0 to H=5 (right half of panel a), then backward from H=5 to H=-5 (panel b) and again forward from H=-5 to H=0 (left half of panel (a). The inset in panel (a) shows one period (T_H) of the driving field. The critical current as a function of H(t) exhibits a diffraction-like pattern formed by lobes which are directly related to the number of solitons arranged along the junction. By sweeping the magnetic field forward and then backward leads to the appearance of a clear hysteretic behavior. This is a distinctive signature of any memdevice. According to this hysteretic behavior, the Josephson junction can be effectively used as a multi-state memory. For any specific range of magnetic field values, each state of the memory is represented by a forward or backward diffraction lobe, labeled by the number of excited solitons present along the junction. (**c**,**d** and **e**), Diffraction patterns for a few junction lengths *L*. The number of memory states provided by the SJMS can be changed by varying the junction length. The memory states associated with current lobes are indicated with the same notation as in Fig. 1b.

forward and backward patterns is enhanced. Notably, *L* can be tuned as well by changing the junction operation temperature (*T*) owing to the temperature dependence of $\lambda_I(T)$.

The presence of both the hysteretic behavior of the critical current and highly-distinguishable current states suggests possible applications of the LJJ. For instance, this device can be used as a field-controlled memelement^{1,5,7}, in which the time-dependent input/output related variables are the external magnetic field H(t) and the Josephson critical current $I_s^m(t)$, respectively. We envisage here a memelement with distinct memory states which make use of the lobes of the forward/backward diffraction patterns. For a given applied magnetic field, the memelement state is determined by the value of the critical current, the latter keeping track of the field history, and pointing to a specific number of solitons present in the junction. Since the critical supercurrent and the magnetic field are the variables yielding the history-dependent behavior, our junction can be regarded as a *meminductive* system^{5,7,34-37}, specifically, a field-controlled solitonic Josephson-based meminductive system (SJMS).

More generally, the LJJ can be thought as a multi-state memory in which each memory state is represented by a specific diffraction lobe, and labeled by the number of excited solitons (see Fig. 1b). For example, by referring to the diffraction patterns shown in Fig. 2a,b, three backward lobes can be easily recognized within the range $H \in [0, 2]$ in clear contrast to one single forward lobe, by which a 4-state memory could be built.

On general grounds, a good memelement has to read/write in short times, and has to be sufficiently robust against external fluctuations (noise) that tend to destroy the stored information. On the one hand, reading the state of the SJMS can be performed by conventional well-established techniques, or via a Josephson sensor³⁸, based on the variations of the kinetic inductance of a junction working in the dissipationless regime inductively coupled with a SQUID, or even by an interferometer reading the magnetic flux variations through the JJ. On the other hand, the writing process of each memory state depends on the operating frequency (ω_H) of the magnetic field, and on the ability of the system to follow a fast periodic driving. To quantify the LJJ memdevice performance as the driving frequency and the temperature are changed we make use of a figure of merit defined by the difference between the forward and backward critical currents, $\delta I_i = |I_s^f(H_i) - I_s^b(H_i)|/I_c$, where H_i is the magnetic field at the midpoint of the *i*-th backward diffraction lobe, as shown in Fig. 3a for i = 1, 2, 3. For large δI_i one can safely distinguish distinct memory states, namely, the current states. Furthermore, to further characterize our memdevice we have included a Gaussian thermal fluctuation term in equation (1) (see SI) thereby making the SJMS a *stochastic* memory element⁷ whereas a noiseless driving field source was considered. The relevant super-current differences ($\delta \overline{I}_i$) are then calculated between the averaged diffraction patterns. Recently, the effects of the noise on the performance of several memory devices has been investigated^{15,18,39-45}.



Figure 3. Frequency response of the memory states. (a) Forward and backward diffraction patterns for $H \in [0, 2]$ and L = 10. For each backward diffraction lobe, we have considered the middle magnetic field value H_i , and calculated the current difference $\delta I_i = |I_s^f(H_i) - I_s^b(H_i)|/I_c$ (i = 1, 2, 3), where $I_s^f(H_i)$ and $I_s^b(H_i)$ are the corresponding forward and backward critical currents. (b) Difference $\delta \overline{I}_i$ (i = 1, 2, 3) between average forward and backward diffraction patterns $I_s^f(H_i)$ and $\overline{I}_s^b(H_i)$, computed by averaging over $N_{exp} = 100$ numerical realizations of the Josephson critical current, as a function of the driving frequency ω_H for T = 1.2 K. The memory states are stable up to $\omega_H \sim 0.5$ GHz. At higher frequencies, i.e., $\omega_H \gtrsim 1$ GHz, the system is no more able to respond to the fast driving.

Independently of the physical mechanism defining the state of the device, the memelement response is usually strongly dependent on the frequency of the input drive^{10,46,47}. At low frequencies, the system has enough time to adjust its state to the instant value of the drive, so that the device non-linearly behaves and a hysteretic evolution results. Conversely, at high frequencies, there is not enough time for any change during an oscillation period of the drive.

Figure 3b shows $\delta \overline{I}_i$ as a function of the driving frequency ω_H , for T = 1.2 K. The memory states defined in Fig. 3a are stable up to a driving frequency $\omega_H \sim 0.5$ GHz. At higher frequencies, i.e., for $\omega_H \gtrsim 1$ GHz, the system is not able to respond anymore to the fast driving. In this region of frequencies, $\delta \overline{I}_i$ tends to increase (see SI), the diffraction patterns are not stable, and therefore cannot be used to safely distinguish the memory states.

As expected, due to its topological nature the LJJ memory shows remarkable robustness against thermal disturbances: being a soliton-based memelement, it is intrinsically protected against small fluctuations. Indeed, the states of the memory are associated to the number of solitons present in the LJJ and, therefore, are quantized³¹. The creation of a soliton is a macroscopic quantum phenomenon involving crossing of a potential barrier³¹. Far away from the superconducting critical temperature (T_c), the presence of an energy barrier in a damped dynamics prevents noise-induced state degradations, i.e., the so-called "stochastic catastrophe"¹⁵.

Figure 4 emphasizes the robustness of the SJMS against thermal fluctuations, as the driving frequency is set to $\omega_H \sim 0.04$ GHz. Specifically, here we show how the temperature affects the forward (Fig. 4a) and backward (Fig. 4b) diffraction patterns. In particular, by increasing the temperature leads to a smoothing of the interference patterns with broadened transitions between lobes due to noise-induced creation or destruction of solitons. Nevertheless, the memory states tend to degrade only for somewhat high temperatures approaching T_c (see the results for T > 4.2 K in Fig. 4a,b).

Finally, the stability of our Josephson-based memory as the temperature is changed is quantified in Fig. 4c. In particular, the memory states turn out to be stable against large temperature variations, i.e., $\delta \overline{I}_i$ is roughly constant



Figure 4. Effects of the temperature. (a,b) Average forward and backward diffraction patterns I_s^f/I_c and I_s^b/I_c , respectively, calculated for a few temperatures, L = 10, and $\omega_H \sim 0.04$ GHz. The patterns are computed by averaging over $N_{exp} = 100$ numerical realizations of the critical current as the magnetic field is swept forward and backward when thermal fluctuations are taken into account. The legend in panel (b) refers to both panels. (c) Differences $\delta \overline{I}_i$ (i = 1, 2, 3) for L = 10 and $\omega_H \sim 0.04$ GHz calculated in correspondence of the temperatures set to obtain the results shown in panels (a,b). By approaching the superconducting critical temperature ($T_c \simeq 9.2$ K for a Nb/AlOx/Nb JJ) the forward and backward diffraction patterns tend to superimpose, and $\delta \overline{I}_i$ vanishes.

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as long as $T \leq 4.2$ K. For higher temperatures, the average forward/backward diffraction patterns tend to superimpose so that $\delta \overline{I}_i$ vanishes with the following suppression of the memory states at the critical temperature.

Conclusion

In summary, we have suggested long Josephson junctions excited by an external magnetic field as prototypical multi-state superconducting memories. Our proposal for a memory element is based on the characteristic hysteretic behavior of the critical supercurrent as the driving field is swept. The resulting memelement realizes a multi-state memory with a number of states controllable via the effective length of the junction. The solitonic nature at the origin of the critical current hysteresis makes these memory states stable and robust against thermal fluctuations. Our memory scheme represents the first endeavor to combine superconductivity and solitons

physics in one single memelement, and could find potential application in various emerging areas such as logic in memory and unconventional computing^{16,17}.

Methods

Computational Details. The phase dynamics and the behaviors of the Josephson critical current presented in this work were calculated numerically by using a Fortran computer program.

To give a realistic estimate of the physical quantities used in the computations, both the superconductors and the insulator making the junction have to be chosen. Therefore, let us set a Nb/AlO/Nb junction, characterized by a resistance per area $R_a = 50 \,\Omega\mu m^2$ and a specific capacitance $C_s = 50 fF/\mu m^2$. Moreover, a

length-to-Josephson-penetration-depth ratio equal to L = 10 is considered. At low temperatures, the critical current reads²⁶ $I_c = \frac{\pi}{2} \frac{\Delta(0)}{eR} = \frac{\pi}{2} \frac{\Delta(0)}{eR_a} \mathcal{A}$, where $\Delta(T)$ is the BCS superconduct-ing energy gap and \mathcal{A} is the junction area. Accordingly, $J_c = \frac{\pi}{2} \frac{A(0)}{eR_a} \sim 0.44 \times 10^8 \text{ A/m}^2$, $T_c = 9.2 \text{ K}$ being the Nb critical temperature. The effective magnetic thickness is equal to $t_d = 2\lambda_L^0 + d \sim 161$ nm, the London penetration depth of a Nb thin film being $\lambda_L^0 \sim 80$ nm and setting d = 1 nm. Through the Josephson penetration depth $\lambda_J = \sqrt{\frac{\Phi_0}{2\pi\mu_0} \frac{1}{t_d f_c}} \sim 6 \,\mu\text{m}$, the linear dimensions $\mathcal{L} = 10\lambda_J = 60 \,\mu\text{m}$ and $\mathcal{W} = 1 \,\mu\text{m}$ and the area $\mathcal{A} = \mathcal{WL} = 60 \ \mu\text{m}^2$ of the junction can be set. Therefore, the capacitance and the resistance of the device can be estimated, $C = C_s \times \mathcal{A} = 3 \text{ pF}$ and $\underline{R} = R_a/\mathcal{A} \sim 0.82 \Omega$, respectively. Consequently, the plasma frequency and the damping parameter read $\omega_p = \sqrt{\frac{2\pi}{\Phi_0} \frac{I_c}{C}} \sim 1.63 \text{ THz and } \alpha = \frac{1}{\omega_p RC} \sim 0.24$, respectively. The driving external field is equal to $H = \frac{2\pi\mu_0}{\Phi} t_d \lambda_I H_{ext}$, so that $H_{ext} \sim 3.4 \text{ HOe}$.

Some of these quantities have an explicit dependence on the temperature. In particular, for identical superconductors²⁶, the effective magnetic thickness $t_d(T)$ depends on T through the London penetration depth $\lambda_i(T) = \lambda_L^0 / \sqrt{1 - (T_i/T_c)^4}$, and the Josephson critical current $I_c(T)$ depends on T through the Ambegaokar and Baratoff formula²⁶. Accordingly, the values used in the numerical calculations for the plasma frequency $\omega_p(T)$, the damping parameter $\alpha(T)$, the Josephson penetration depth $\lambda_I(T)$, and the normalized length $L(T) = \mathcal{L}/\lambda_I(T)$ are adjusted by changing the temperature.

Concerning the normalized length, if, for instance, $L(T \to 0) = 10$, as the temperature is increased to $T^* = 0.8 T_c$ the corresponding normalized length becomes $L(T^*) = \frac{\lambda_j(0)}{\lambda_j(T^*)} L(0) \sim 8$.

References

- 1. Chua, L. Memristor the missing circuit element. IEEE Trans. Circuit Theory 18, 507-519 (1971).
- 2. Chua, L. O. & Kang, S. M. Memristive devices and systems. Proc. IEEE 64, 209-223 (1976).
- 3. Yang, J. J. et al. Memristive switching mechanism for metal/oxide/metal nanodevices. Nature Nanotech. 3, 429-433 (2008).
- 4. Strukov, D. B., Snider, G. S., Stewart, D. R. & Williams, R. S. The missing memristor found. Nature 453, 80-83 (2008).
- 5. Di Ventra, M., Pershin, Y. V. & Chua, L. O. Circuit elements with memory: memristors, memcapacitors, and meminductors. Proc. IEEE 97, 1717-1724 (2009).
- 6. Biolek, D., Biolek, Z. & Biolková, V. SPICE modeling of memristive, memcapacitative and meminductive systems. Proc. of ECCTD'09, European Conference on Circuit Theory and Design 249-252 (2009).
- 7. Pershin, Y. V. & Di Ventra, M. Memory effects in complex materials and nanoscale systems. Adv. Phys. 60, 145-227 (2011).
- 8. Radwan, A. G. & Fouda, M. E. On the Mathematical Modeling of Memristor, Memcapacitor, and Meminductor, Studies in Systems, Decision and Control Vol. 26 (Springer International Publishing, Cham 2015).
- 9. Borghetti, J. et al. 'Memristive' switches enable 'stateful' logic operations via material implication. Nature 464, 873-876 (2010).
- 10. Pershin, Y. V. & Di Ventra, M. Solving mazes with memristors: A massively parallel approach. Phys. Rev. E 84, 046703 (2011).
- 11. Pershin, Y. V. & Di Ventra, M. Newblock Neuromorphic, digital, and quantum computation with memory circuit elements. Proc. IEEE 100, 2071-2080 (2012).
- 12. Linn, E., Rosezin, R., Kugeler, C. & Waser, R. Complementary resistive switches for passive nanocrossbar memories. Nature Mater. 9, 403-406 (2010).
- 13. Linn, E., Rosezin, R., Tappertzhofen, S., Böttger, U. & Waser, R. Beyond von Neumann-logic operations in passive crossbar arrays alongside memory operations. Nanotechnology 23, 305205 (2012).
- 14. Yang, J. J., Strukov, D. B. & Stewart, D. R. Memristive devices for computing. Nature Nanotech. 8, 13-24 (2013).
- 15. Di Ventra, M. & Pershin, Y. V. On the physical properties of memristive, memcapacitive and meminductive systems. Nanotechnology 24, 255201 (2013).
- 16. Traversa, F. L. & Di Ventra, M. Universal memcomputing machines. IEEE Trans. Neural Netw. Learn. Syst. 26, 2702-2715 (2015).
- 17. Di Ventra, M. & Pershin, Y. V. The parallel approach. Nature Phys. 9, 200-202 (2013).
- 18. Peotta, S. & Di Ventra, M. Superconducting memristors. Phys. Rev. Applied 2, 034011 (2014).
- 19. Salmilehto, J., Deppe, F., Di Ventra, M., Sanz, M. & Solano, E. Quantum memristors with superconducting circuits, arXiv:1603.04487.
- 20. Shevchenko, S. N., Pershin, Y. V. & Nori, F. Qubit-Based Memcapacitors and Meminductors. Phys. Rev. Applied 6, 014006 (2016).
- Parmentier, R. D. The New Superconducting Electronics, Vol. 251 of NATO Advanced Study Institute, Series E: Applied Sciences, (H. 21. Weinstock & R. W. Ralston (Kluwer, Dordrecht), 221-248 1993).
- 22. Ustinov, A. V. Solitons in Josephson junctions. Physica D 123, 315-329 (1998).
- 23. Kuplevakhsky, S. V. & Glukhov, M. Static solitons of the sine-Gordon equation and equilibrium vortex structure in Josephson junctions. Phys. Rev. B 73, 024513 (2006).
- 24. Kuplevakhsky, S. V. & Glukhov, M. Exact analytical solution of a classical Josephson tunnel junction problem. Low Temp. Phys. 36, 1012 (2010).
- 25. McLaughlin, D. W. & Scott, A. C. Perturbation analysis of fluxon dynamics. Phys. Rev. A 18, 1652 (1978).
- 26. Barone, A. & Paternò, G. Physics and Applications of the Josephson Effect (Wiley, New York, 1982).
- Lomdahl, P. S., Soerensen, O. H. & Christiansen, P. L. Soliton excitations in Josephson tunnel junctions. Phys. Rev. B 25, 5737-5748 (1982).
- 28. Valenti, D., Guarcello, C. & Spagnolo, B. Switching times in long-overlap Josephson junctions subject to thermal fluctuations and non-Gaussian noise sources. Phys. Rev. B 89, 214510 (2014).
- Giazotto, F., Martínez-Pérez, M. J. & Solinas, P. Coherent diffraction of thermal currents in Josephson tunnel junctions. Phys. Rev. B 29. 88,094506 (2013).
- 30. Martínez-Pérez, M. J. & Giazotto, F. A quantum diffractor for thermal flux. Nat. Commun. 5, 3579 (2014).

- 31. Guarcello, C., Giazotto, F. & Solinas, P. Coherent diffraction of thermal currents in long Josephson tunnel junctions. *Phys. Rev. B* 94, 054522 (2016).
- 32. Owen, C. S. & Scalapino, D. J. Vortex structure and critical currents in Josephson junctions. Phys. Rev. 164, 538-544 (1967).
- Cirillo, M., Doderer, T., Lachenmann, S. G., Santucci, F. & Grønbech-Jensen, N. Dynamical evidence of critical fields in Josephson junctions. *Phys. Rev. B* 56, 11889–11896 (1997).
- Biolek, D., Biolek, Z. & Biolková, V. PSPICE modeling of meminductor. *Analog Integr. Circ. Sig. Process.* 66, 129–137 (2011).
 Biolek, D., Biolková, V. & Kolka, Z. Spice modeling of meminductor based on its constitutive relation. In *Proceedings of the 10th*
- international conference on instrumentation, measurement, circuits and systems (IMCAS'11), 76–79 (2011).
 36. Fouda, M. E. & Radwan, A. G. Meminductor response under periodic current excitations. *Circuits Syst. Signal Process.* 23, 1–11 (2013)
- 37. Han, J. et al. Realization of the meminductor. ACS Nano 8, 10043-10047 (2014).
- 38. Giazotto, F. et al. Ultrasensitive proximity josephson sensor with kinetic inductance readout. Appl. Phys. Lett. 92, 162507 (2008).
- 39. Stotland, A. & Di Ventra, M. Stochastic memory: Memory enhancement due to noise. Phys. Rev. E 85, 011116 (2012).
- 40. Slipko, V. A., Pershin, Y. V. & Di Ventra, M. Changing the state of a memristive system with white noise. *Phys. Rev. E* 87, 042103 (2013).
- 41. Patterson, G. A., Fierens, P. I., García, A. A. & Grosz, D. F. Numerical and experimental study of stochastic resistive switching. *Phys. Rev. E* 87, 012128 (2013).
- 42. Patterson, G. A., Fierens, P. I. & Grosz, D. F. On the beneficial role of noise in resistive switching. Appl. Phys. Lett. 103 (2013).
- 43. Patterson, G. A., Sangiuliano Jimka, F., Fierens, P. I. & Grosz, D. F. Memristors under the influence of noise and temperature. *Phys. Status Solidi c* 12, 187–191 (2015).
- 44. Patterson, G. A., Grosz, D. F. & Fierens, P. I. Noise on resistive switching: a Fokker-Planck approach. J. Stat. Mech.: Theory Exp. 2016, 054043 (2016).
- 45. Mikhaylov, A. N. *et al.* Field- and irradiation-induced phenomena in memristive nanomaterials. *Phys. Status Solidi c* 13, 870–881 (2016).
- Fouda, M., Elwakil, A. & Radwan, A. Pinched hysteresis with inverse-memristor frequency characteristics in some nonlinear circuit elements. *Microelectron. J.* 46, 834–838 (2015).
- 47. Biolek, Z., Biolek, D. & Biolková, V. Specification of one classical fingerprint of ideal memristor. *Microelectron. J.* 46, 298–300 (2015).

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Author Contributions

C.G., P.S., and F.G. conceived the ideas and designed the study. C.G. developed the algorithm and carried out the simulations. C.G., P.S., M.D., and F.G. discussed the results and wrote the paper.

Additional Information

Supplementary information accompanies this paper at http://www.nature.com/srep

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