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Power Sharing Method Based on Droop Control for Three-Phase UPS Systems

Eng. Mohamed M. Ramadan* Prof. Dr. Magdy A. Koutb

Dept. of Industrial Electronics and Control Eng., Faculty of Electronic Eng., Menoufia University, Egypt

Abstract

The main objective of this paper is to improve the power sharing capability and to achieve synchronization between three-phase Uninterruptible power supply (UPS) units in the presence of load interruption. This paper presents a droop-controlled scheme in such a way that the computation of the instantaneous value of the active power and the reactive power are taken as feedback signals to the frequency and voltage restoration control system. The restored frequency and voltage are introduced to voltage controller circuit, which produces a suitable control signal to sinusoidal pulse width modulation circuit (SPWM). Thus producing a suitable trigger pulses to the inverter gate in order to guarantee synchronization between three-phase UPS units. Simulation of two UPS units with the same ratings (4 KW) are carried out using MATLAB. The results show the effectiveness of the proposed control system in achieving synchronization and improving the power sharing capability in the presence of load interruption. **Keywords:** Uninterruptible power supply; power sharing; parallel operation; droop control.

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1. Introduction

Uninterruptible power supplies are used to make electrical power system more reliable and continuous. Recently, UPSs usage is widespread in the applications that require power quality and continuous electrical feeding such as computers used for controlling important processes, some medical equipment and data-processing system. UPSs have two modes of operation; stand-alone mode and modular UPS mode, [1]. In stand-alone mode, where full backup of other units are devoted, very high costs are involved with high power UPS. Therefore, using several smaller rated UPS units (modular UPS) in parallel instead of one big unit is more proper, [2].

For several years, great efforts have been devoted to the study of power sharing control strategies for modular UPS. Previous studies indicate that the control strategies require communication lines between the modules such as concentrated method, master slave method and distributed logic control method. This results in increasing the cost of the system, making it difficult to expand the system and the long distance communication lines will be easier to get interfered, thereby reducing system reliability and expandability, [3], [4], [5].

In recent years, research on control strategies without communication links has become very popular. These strategies based on droop concept which improve reliability requirements in addition to avoiding the complexity and high costs. Such a system is easier to expand because of the plug-and-play feature of the modules, [4], [6], [7].

The output of the UPS inverter is required to be sinusoidal signal with minimum distortion. Thus, a combination of pulse width modulation (PWM) circuit and a low pass filter are introduced at the output of the inverter. The PWM has a well performance with linear loads but it doesn't guarantee low distortion of the load voltage with non-linear loads. One way of achieving a clean sinusoidal load voltage is by using a sinusoidal pulse width modulation (SPWM), [8].

This paper presents a droop-controlled scheme based on measuring the instantaneous values of the active power and the reactive power on the tie line and then taken these values as feedback signals to the frequency and voltage restoration control system. The frequency and voltage control signals are introduced to voltage regulator circuit which produces a suitable control signal to SPWM. Thus producing a suitable trigger pulses to the inverter gate in order to guarantee synchronization between three-phase UPS units.

In order to validate the performance of the system in the presence of load interruption, simulation of two parallel-connected 3-phase UPS units with the same ratings is carried out using MATLAB/SIMULINK.

This paper is organized as follows: Section 2 presents the mathematical modeling of the active power-frequency $(P-\omega)$ droop and the reactive power-voltage (Q-V) droop characteristics. Section 3 discusses the restoration process. Simulation results are presented in section 4. Concluding remarks are introduced in Section 5 followed by the list of references.

2. Mathematical model

2.1 System description

 $P-\omega$ droop and Q-V droop controllers have been successfully adopted in the UPS systems, [10]. Figure 1 shows the connection diagram of distributed inverters of UPS units connected in parallel and sharing power through a tie line.

The equations that describe the active power and the reactive power drawn from each inverter unit to the load

can be expressed as, [9], [11]

$$P = \frac{3}{2} \frac{VE}{x} \sin \theta \qquad (1)$$

$$Q = \frac{3}{2} \frac{VE \cos \theta - V^2}{x} \qquad (2)$$

where, X is the output reactance of an inverter $X = \omega L = 2\pi f L$; ω angular frequency, L inductance, θ is the phase angle between the output voltage of the inverter and the voltage of the tie line, E and V are the amplitude of the output voltage of the inverter and the grid voltage, respectively.

Low pass filters is introduced at the output terminals of each inverter to smooth out the waveform and makes it as close as possible to the required sine wave, [14].



Figure 1. Simplified circuit of the system.

2.2 $P - \omega$ droop and Q - V droop characteristic

As can be seen from Figure 2, the basic idea of this control level is to mimic the behavior of a synchronous generator, which is to reduce the frequency as the active power increases, [6], [11]. The reactive power drawn from the inverter increases, the voltage decreases, [12], [13].

$$\begin{split} \omega_{i} &= \omega_{2,i} - m_{P} (p_{2} - p_{i}) \\ V_{i} &= V_{2,i} - n_{Q} (Q_{2} - Q_{i}) \end{split} \ \ (3)$$

where, m_P is the slope of the $P-\omega$ characteristics, n_Q is the slope of the Q-V characteristics; i the index representing each UPS, ω_i is the rated angular frequency of the UPS unit at the rated output active power $P_i \cdot \omega_{2,i}$ is the angular frequency when the inverter works at the output active power of P_2 . V_i is the rated voltage of the UPS unit at the rated output reactive power $Q_i \cdot V_{2,i}$ is the voltage when the inverter works at the output reactive power of Q_2 .

To achieve power sharing, synchronization between uninterruptible power supply must be achieved. If we have two UPS units working in parallel at the same rated frequency and voltage, then:

3. Control design

In the droop-controlled scheme the active power (P) and the reactive power (Q) are measured, then the instantaneous values of P and Q are taken as feedback signals to the frequency and voltage restoration control system. The restored frequency and voltage are introduced to gate pulse generator circuit in which the voltage regulator produces a suitable control signal to sinusoidal pulse width modulation (SPWM). Thus, producing a suitable trigger pulses to the inverter gates which guarantee synchronization between three-phase UPS units. Figure 3 shows the droop-controlled scheme of one UPS unit.

A restoration mechanism is also proposed to bring frequency ω and voltage V to its rated value in order to satisfy synchronization between UPS units, [10].



(a) Active power - frequency droop characteristics

(b) Reactive power - voltage droop characteristics.



Figure 3. The droop-controlled scheme of one UPS unit.

3.1 Frequency droop restoration control

Initially, the UPS unit operates with nominal frequency (ω_0) at the active power level of (P_0) on the line L_1 . If the load power reduces to P_1 , the frequency of the unit shifts to a new value (ω_0) . In order to restore the frequency back to the nominal value, the droop line should be shifted down as shown in Figure 4. The new line L_2 has the same slope as the original one. The output of each inverter must have the same frequency (rated one) after controlled and from equation (3).

$$\omega_0 = m_{p1} P_1 = m_{p2} P_2 \tag{7}$$

The active power shared between inverters are changed due to the change of the load active power ΔP_L . $\Delta \omega_i = m_{pi} \Delta P_i$ (8)

where,
$$\Delta P_i$$
 is the change in active power and $\Delta \omega_i$ is the change in the frequency of the ith inverter unit.
In equation (3), $P_{2,i}$ is changed for each unit to restore the frequency during load sharing.

$$\Delta P_{2i} = (K_{res} * P_{Ri}) * \Delta \omega_i \qquad (9)$$

$$P_{2,i} = (K_{res} * P_{Ri}) * \int \Delta \omega_i$$
(3)

The " $K_{res}P_{Ri}$ " coefficients in these equations determine the frequency restoration ratio. Equations (3), (8) and (10) determine the block diagram shown in Figure 5 of the frequency restoration control.



Figure 4. Frequency restoration by shifting the active power-freq. droop characteristics.



Figure 5. Frequency restoration block diagram.

3.2 Voltage droop restoration control

Similar to frequency restoration algorithm, voltage restoration is required upon the change of the reactive power of the load. In equation (4), $Q_{2,i}$ is changed for each unit to restore the voltage during load sharing.

$$\Delta Q_{2,i} = (Y_{res} * Q_{Ri}) * \Delta V_i$$
(11)

$$Q_{2,i} = (Y_{res} * Q_{Ri}) * \int \Delta V_i$$
(12)

The " $Y_{res}Q_{Ri}$ " coefficients in these equations determine the Voltage restoration ratio (gain). Equations (4) and (12) determine the block diagram shown in Figure 6 of the voltage restoration control.

4. Simulation results

In order to validate the performance of the proposed system in the presence of load interruption, MATLAB/SIMULINK is used for the simulation of two parallel-connected 3-phase UPS units with the same ratings (4 KW). The simulation is made for 2 Seconds. Initially, the loads z_1 and z_2 are connected on the tie line and the UPS units worked in the normal case. When the time reaches 0.5 Sec, an interruption is introduced by adding

additional load z_{tr} connected on the tie line in parallel with the loads z_1, z_2 . When the time reaches 1 Sec the load z_{tr} is disconnected from the tie line and the units returned to work in the normal case. Table 1 shows the parameters used in the simulation.

Figure 7 shows the output voltage and current of the UPS₁ and UPS₂. The voltage is maintained at its rated value 380 Volt as shown in Figure 7-a and Figure 7-c. When the load z_{tr} is connected on the tie line in parallel with the loads z_1 , z_2 at T=0.5 Sec the total impedance decreased so the current is increased. At T= 1 Sec the load z_{tr} is disconnected so the current is returned back to the first value as shown in Figure 7-b and Figure 7-d.



Figure 6. Voltage restoration block diagram.

Table 1. Simulation parameters.			
Rated Frequency (f ₀)	50 Hz	Rated Voltage (V_0)	380 Volt
Freq. restoration gain	900	voltage restoration gain	600
m _P	-0.04	n _Q	-0.02
PWM_switching freq.	1000 Hz	$z_{1,2}=140\Omega+13 mH$	$z_{tr} = 80\Omega + 1 mh$
Filter	$R=1 \Omega$	L=5 mH	C=1200 µF





Figure 8 shows the effect of load interruption on the active power and frequency. The change in the active power of UPS_1 and UPS_2 occurred at T= 0.5 Sec and T=1 Sec as shown in Fig. 8-a. The frequency is instantaneously interrupted then returned back to its initial value 50 Hz as shown in Fig. 8-b. This behavior is due to the frequency restoration control.

Fig. 9 shows the effect of load interruption on the reactive power and voltage. The change in the reactive power of UPS₁ and UPS₂ occurred at T=0.5 Sec and T=1 Sec as shown in Fig. 9-a. The voltage is instantaneously interrupted then returned back to its initial value 380 Volt as shown in Fig. 9-b. This behavior is due to the voltage restoration control. Thus, the restoration droop circuits regulate the system frequency and voltage (which satisfy synchronization) in the presence of load interruption and the UPS units are sharing load correctly.



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Since the UPS₁ and UPS₂ units have the same ratings, they share the total load equally. Thus, the two UPS units have the same voltage, current, active power, reactive power and frequency waveforms as shown in Fig.7, Fig. 8 and Fig. 9.

Figure 10 shows the root mean square error (RMSE) of the output voltage magnitude variation of UPS₁ and UPS₂ units when load change occurred at 0.5 Sec and 1 Sec. The desired value of voltage magnitude = 380 Volt. The RMSE of the output voltage variation of UPS unit reaches zero within a short period. This means that the system response goes to the desired value. Thus, the droop-controlled method succeeded in improving the power sharing capability in the presence of load interruption.

5. Conclusion

The effectiveness of the droop-controlled scheme in improving synchronization and power sharing capability in the presence of load interruption is presented. In this control system the instantaneous values of the active power and the reactive power are taken as feedback signals to the frequency and voltage restoration control system. The restored frequency and voltage are introduced to gate pulse generator circuit, which produces a suitable control signal to the inverter gate. Simulations of two parallel-connected 3-phase UPS units with the same ratings (4 KW) are carried out using MATLAB and the results show that the restoration droop circuits regulate the system frequency and voltage efficiently in the presence of load interruption. The RMSE of the output voltage magnitude of UPSs unit reaches zero within a short period. Since the UPS1 and UPS2 units have the same ratings, they share the total load equally.



Figure 10 RMSE of output voltage magnitude variation of UPS_{1,2} units.

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Mohamed M. Ramadan received the B. Sc. degrees in industrial electronics and control Engineering from Faculty of Electronic Engineering, Menoufia University, Egypt in 2013.

Work as an Electrical Engineer at Ministry of housing - New Urban Communities authority – Sadat city authority since 2015. His research interests include UPS microgrids, power control of hybrid solar/wind systems and modern control algorithms.

Magdy A. Koutb received his B.Sc. and M.Sc. degrees in Industrial Electronics and Control Engineering from the Menoufia University, Egypt, in 1977 and 1981, respectively, and the Ph.D. degree in control systems from Poland, in 1985. He was a vice dean of the faculty of electronic engineering for postgraduate studies and research from 2003 to 2009. Currently, he is a professor emeritus in the Department of industrial electronics and control Engineering at Menoufia University, Egypt.

He has published about 100 refereed journal and conference papers. His research interest covers robotics, feedback control systems, electrical power systems and control theory.