

OPERATION ANALYSIS OF RESISTIVE SWITCHING OF CBRAM USING IN-SITU TEM

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Resistive random access memories (ReRAMs) have great potential as a candidate for next-generation nonvolatile memories for the high speed, high density storage per cost [1] and their ability to the neural network devices. In order to analyze the reliability of ReRAMs and to find out the origin of the failures, it is indispensable to understand the resistive switching mechanism. Since the transmission electron microscopy (TEM) provides a high resolution images of the nanostructure, *in-situ* TEM should be a powerful tool for the analysis. In our *in-situ* TEM system [2, 3], repeatable switching characteristics, are achieved together with clear images of formation and rupture of conductive filaments corresponding to the low and high resistance states.

In this work, we used several kinds of Cu-based ReRAM (CBRAM: Conductive Bridge RAM). TEM samples are fabricated by two methods. One is an ion-shadow method [4, 5], which is an ion milling technique with carbon mask particles. The other is FIB that is a conventional technique to make a sample observable in TEM. Almost the same characteristics as those measured at the outside of TEM by the use of real ReRAM cells are achieved in TEM by the both method. Fig. 1 shows an example of *I-V* switching characteristics of Cu-Te based ReRAM [6, 7] and the corresponding TEM images [2, 3]. It was clearly shown that a dark spot corresponding to a conductive filament appeared by SET and erased after RESET. These resistive switching characteristics by *I-V* sweep were reproducible at least 60 cycles in TEM. In addition, SET/RESET pulse operation more than 100k times are confirmed during TEM observation as shown in Fig. 2.

These results clearly indicate that the *in-situ* TEM will be a powerful tool to guarantee the reliability of ReRAMs.

References

- [1] D. Sacchetto, et al.: "Applications of multi-terminal memristive devices: A Review," *IEEE Circuits and Systems Magazine, Second Quarter*, 23 (2013).
- [2] M. Kudo, et al.: "Visualization of conductive filament during write and erase cycles on nanometer-scale ReRAM achieved by *in-situ* TEM," *IMW* (2015).
- [3] Y. Takahashi, et al.: Visualization of conductive filament of ReRAM during resistive switching by *in-situ* TEM, *ECS Transactions* 69 (10), p.299 (2015).
- [4] M. Kudo, et al.: "Filament formation and erasure in molybdenum oxide during resistive switching cycles," *Appl. Phys. Lett.* 105, 173504 (2014).
- [5] M. Kudo, et al.: "Preparation of resistance random access memory samples for *in situ* transmission electron microscopy experiments," *Thin Solid Films*, 533, 48 (2013).
- [6] K. Aratani, et al.: "A novel resistance memory with high scalability and nanosecond switching," *IEDM*, p.783 (2007).
- [7] W. Fackenthal, et al.: "A 16Gb ReRAM with 200MB/s Write and 1GB/s Read in 27nm Technology," *ISSCC*, p.338 (2014)

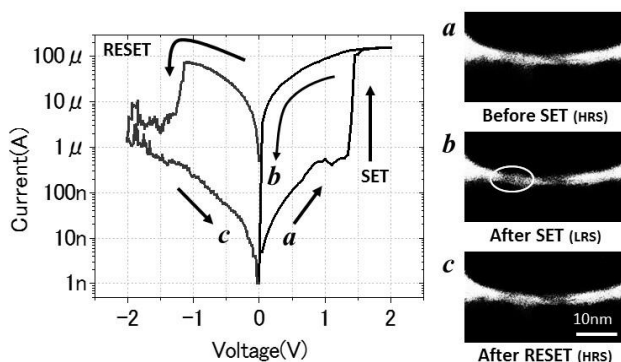


Fig. 1. *I-V* characteristics measured in TEM with current compliance of 150 μ A, and TEM images just before SET, just after SET and just after RESET.

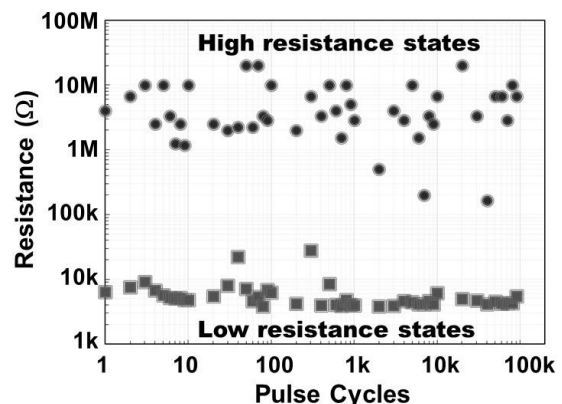


Fig. 2. SET/RESET pulse cycles measured in TEM.