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Accurate Location of Evolving Faults on Transmission Lines Using Sparse Wide Area Measurements

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Abstract:

In electric power systems, not all fault conditions remain unchanged during faults. An evolving fault has one characteristic initially and changes to a different condition subsequently. Locating evolving faults is challenging due to the change in fault type shortly after the fault initiation. This paper presents a new approach for estimating the locations of evolving faults on transmission lines. By using sparse wide area voltage measurements, this method is able to accurately locate evolving faults without requiring measurements from either end of the faulted line. There is no need to detect whether a fault is an evolving fault or not. Fault type information is not a necessity either, and the change of fault phases does not affect the estimation accuracy. In addition, the algorithm is applicable to both single-circuit and double-circuit lines, and the transmission lines can be either transposed or untransposed. Distributed parameter line model is adopted to fully consider the shunt capacitances of the transmission lines. Electromagnetic Transient Program (EMTP) is employed to simulate transmission system, and quite accurate results have been achieved.

Keywords: fault location, evolving fault, wide-area measurement

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1 Introduction

Electric power transmission systems occasionally suffer from various short-circuit faults along transmission lines. Precise and fast fault location can facilitate maintenance, and reduce the outage time and accelerate power system restoration [1, 2]. Numerous algorithms have been developed in the past to estimate fault location in the transmission system [1–12, 14–16]. Most of the existing fault location algorithms assume the fault types are stationary over fault periods.

An evolving fault is defined as a fault where the phases involved change over time [17]. For example, an evolving fault begins as a single line-to-ground fault, and could become a line-to-line-to-ground fault after a few power frequency cycles [18, 19]. Evolving faults may happen when the object causing a single line-to-ground fault touches conductor(s) of additional phase or phases [19].

There have been several articles discussing evolving faults in transmission grid [20–24]. In [20], a few evolving faults recorded by transmission line relays are presented. It is learned from these practical experiences that evolving faults can delay distance element tripping which may result in longer duration of existence of faults. The authors of [21] use the ratio of zero sequence fault current over negative sequence fault current as the criterion to detect evolving faults. Due to evolving fault's changing nature, fault classification is not easy. In [22], the impact of evolving fault on the fault phase selector is analysed, and it is shown that various factors such as fault position and fault resistance could cause incorrect phase selection. The authors of [23] use artificial neural networks (ANN) to select fault phase of evolving faults. Locating evolving faults is less studied compared with stationary faults, and this paper for the first time puts forwards a wide-area evolving fault location method. Reference [19] presents a time-domain method to locate evolving faults in distribution systems. This method requires accurate fault classification that is a demanding task itself. Incorrect fault classification would yield erroneous estimation of where the fault happens. In [24], a method based on ANN is designed to locate evolving faults on transmission lines without knowing fault type information, where the network is trained under a variety of possible fault conditions. A method based on wide area measurements for locating faults is provided in [25]. This paper will further develop this approach to deal with evolving faults.

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In order to obviate the dependency on fault classification and find the fault location by using analytical approach, this paper proposes a new fault location algorithm for evolving faults. Changing fault phases would impose little impact on the estimation result. There is no need to detect whether a fault is an evolving fault or not. This method is also immune to fault resistance. In other words, even both the fault phase and fault resistance are not constant, accurate fault location can still be computed. This algorithm is applicable to both single-circuit and double-circuit lines, and is able to deal with both transposed and untransposed cases. Another feature of this method is that it utilizes sparse wide area measurements that do not have to be captured from the ends of the faulted line. Distributed line parameter model is adopted in order to accurately model the line distance transmission line. In practical applications, sparse wide area measurements can be obtained from recording devices installed across the system such as phasor measurement units, digital fault recorders, etc [26, 27]. These measurements are transferred to a central office, based on which the proposed fault location method can be applied to estimate the fault location.

The rest of the paper is organized as follows. Section 2 introduces more backgrounds of evolving faults, including characteristics and causes, and describes the proposed wide-area location method. Evaluation of the proposed algorithm is presented in Section 3. The simulated power system and obtained data are exhibited. In Section 4, the conclusions are drawn.

2 Proposed fault location algorithm

2.1 Characteristic of evolving faults

Evolving faults are faults where the phases involved change over time [17, 24]. The characteristic of an evolving fault is that its faulted phases change over time, while other types of faults' faulted phases remain unchanged. Whether a fault can be referred to as an evolving fault depends on its faulted phase(s), rather than other fault conditions such as fault resistance. For example, a fault with time-dependent fault resistance is not an evolving fault if its faulted phases do not change.

In overhead systems, an evolving fault may happen when a tree limb first touches one, then two or all three phases in succession, making the fault changes from single line-to-ground fault to double line-to-ground fault [20, 24]. For example, an evolving fault may begin as an AG fault, and changes to ABG fault after a few power cycles. The sample plots of fault voltage and current during an example evolving fault are shown in Figure 1(a) and Figure 1 (b), respectively. The fault initially occurs as a single line-to-ground fault in phase A, and evolves to a double line-to-ground fault between phases A and B within around four cycles. Typically, the location of the fault would not change during the evolving fault [21]. Evolving faults occur less often than commonly occurring shunt faults. However, in post-fault power networks, evolving faults can easily happen since the systems are under abnormal conditions [19, 21].

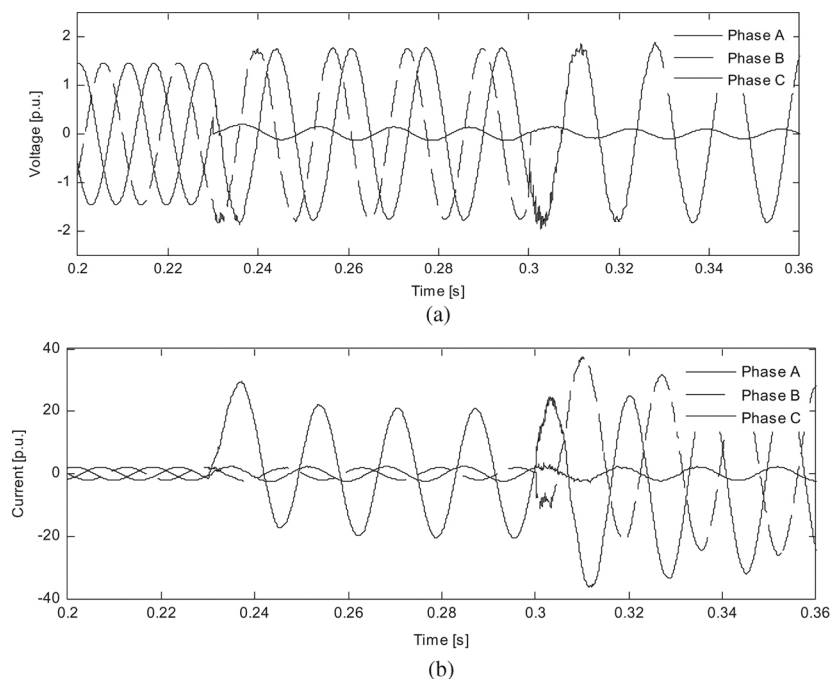


Figure 1: Waveforms of (a) voltage and (b) current during an example AG-to-ABG evolving fault.

2.2 Derivation of fault location

The method to be presented is based on bus impedance matrix concept [25], and is intended to locate evolving faults on transmission lines. The following part of this section will first discuss the derivation of faulted network's bus impedance matrix, then express the fault current and voltage in terms of fault location, and finally determine the fault location.

Figure 2 presents a section of a power transmission system between buses p and q , where the remaining part of the system is not shown. Bus r is added as the fault bus. The following notations are adopted. n is the total number of nodes in the entire transmission system without the fault nodes. $p_1, p_2, p_3, q_1, q_2,$ and q_3 are the nodes of the sample transmission line. The nodes of the fictitious fault bus are represented by $r_1, r_2,$ and r_3 , where $r_1 = n + 1, r_2 = n + 2, r_3 = n + 3$. The per-unit fault distance from bus p to the fault bus r is denoted as m . \mathbf{Z}_0 is the bus impedance matrix of the pre-fault network in phase domain, while \mathbf{Z} is the bus impedance matrix of the during fault network in the phase domain. The element in the k th row and l th column of \mathbf{Z} is written as Z_{kl} . \mathbf{z} and \mathbf{y} represent the sample line's per unit length impedance matrix and admittance matrix, respectively.

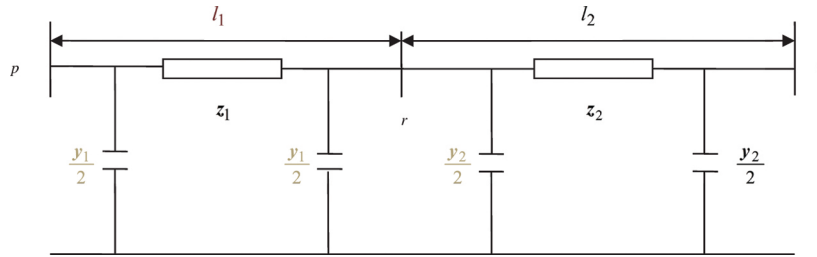


Figure 2: A section of a power transmission system.

The prefault network's bus impedance \mathbf{Z}_0 can be readily developed. The first n rows and first n columns of \mathbf{Z} are identical with \mathbf{Z}_0 . The other part of \mathbf{Z} includes the driving point impedances of faulted nodes, the transfer impedances between a faulted node and a non-fault node, and the transfer impedances between a fault node and another fault node. These impedances will be derived in the following paragraphs.

Based on the general analysis of multi-phase system, the equivalent Pi circuit parameters of the line between p and r can be derived as functions of m [28]:

$$\mathbf{z}_1 = \mathbf{z} \mathbf{B} \text{diag}[\sinh(\boldsymbol{\gamma} m l)] \cdot \boldsymbol{\gamma} \mathbf{B}^{-1} \quad (1)$$

$$\mathbf{y}_1/2 = \mathbf{B} \text{diag}[\tanh(\boldsymbol{\gamma} m l/2)] \cdot \boldsymbol{\gamma} \mathbf{B}^{-1} \mathbf{y} \quad (2)$$

where \mathbf{B} is the eigenvector of $\mathbf{y}\mathbf{z}$; $\text{diag}[\cdot]$ returns a square diagonal matrix with the elements of input vector on its diagonal; \cdot/ \cdot stands for element-wise division; $\boldsymbol{\gamma}$ is a vector composed of γ_j , which is the square root of the j th eigenvalue of $\mathbf{y}\mathbf{z}$; $j = 1, 2, 3$ for single-circuit lines, and $j = 1, 2, \dots, 6$ for double-circuit lines.

It is noted that the parameters derived are phase domain matrices, and thus are applicable to untransposed lines. Similarly, the equivalent Pi circuit parameters for the line between q and r can also be derived as functions of m .

Assume k is a measurable non-fault bus in the system, which includes nodes $k_1, k_2,$ and k_3 . The transfer impedance between the node $k_i, i = 1, 2, 3$ and the fault nodes can be obtained as

$$\mathbf{Z}_{k,r} = (\mathbf{y}_1/2 + \mathbf{y}_2/2 + \mathbf{z}_1^{-1} + \mathbf{z}_2^{-1})^{-1} (\mathbf{z}_1^{-1} \mathbf{Z}_{k,p} + \mathbf{z}_2^{-1} \mathbf{Z}_{k,q}), \quad i = 1, 2, 3 \quad (3)$$

The driving point impedances and the transfer impedances related to the fault nodes are

$$\mathbf{Z}_{r,r_i} = (\mathbf{y}_1/2 + \mathbf{y}_2/2 + \mathbf{z}_1^{-1} + \mathbf{z}_2^{-1})^{-1} (\mathbf{z}_1^{-1} \mathbf{Z}_{p,r_i} + \mathbf{z}_2^{-1} \mathbf{Z}_{q,r_i} + \mathbf{u}_i), \quad i = 1, 2, 3 \quad (4)$$

where \mathbf{u} is a three by three identity matrix, whose i th column is denoted by \mathbf{u}_i .

It is seen from eqs (3) and (4) that $\mathbf{Z}_{k,r}$ and $\mathbf{Z}_{r,r}$ are both functions of the unknown fault distance m , and are independent of the fault resistance and the fault type.

The fault current \mathbf{I}_f is calculated based on the following equation

$$\mathbf{I}_f = -(\mathbf{Z}_{kr}^T \mathbf{Z}_{kr})^{-1} (\mathbf{Z}_{kr}^T \Delta \mathbf{E}_k) \quad (5)$$

where ΔE_k is the superimposed voltage at bus k .

The prefault voltages at the fault bus, E_{r0} , can be estimated based on the prefault network conditions. Therefore, the during-fault voltage at the fault bus, E_r , is obtained as

$$E_r = E_{r0} - Z_{rr} I_f \quad (6)$$

Based on I_f and E_r , the reactive power consumed by the fault resistance can be computed as

$$Q = \text{Imag} \left\{ -E_r [(Z_{kr}^T Z_{kr})^{-1} (Z_{kr}^T \Delta E_k)]^* \right\} \quad (7)$$

where $\text{Imag} \{ \cdot \}$ returns the imaginary part of its argument, and $(\cdot)^*$ denotes the complex conjugate. It can be observed that m is the only unknown variable in eq. (7). Since the fault resistance only consumes real power, eq. (7) should equal to zero. The fault location m can then be solved by the Newton-Raphson technique.

This approach is applicable when measurements from one bus are available. When measurements from two buses are available, an alternative method is derived in the following subsection.

2.3 Cases when measurements from two buses are available

A two-bus algorithm can be implemented when measurements from two buses are available. Suppose voltage measurements are available from non-fault buses k and k' , and the measurements are synchronised. It can be derived that

$$(Z_{k'r}^T Z_{k'r})^{-1} (Z_{k'r}^T \Delta E_{k'}) - (Z_{kr}^T Z_{kr})^{-1} (Z_{kr}^T \Delta E_k) = 0 \quad (8)$$

Equation (8) only includes one unknown variable, m . Thus, the fault location can be derived.

If the measurements at bus k and k' are not synchronised, by equalling the magnitudes of the fault current derived based on the two sets of measurements, the fault location can still be obtained. The fault current calculated based on measurements from bus k is $-(Z_{kr}^T Z_{kr})^{-1} (Z_{kr}^T \Delta E_k)$, and the value based on measurements from bus k' is $-(Z_{k'r}^T Z_{k'r})^{-1} (Z_{k'r}^T \Delta E_{k'})$. Equalling their magnitude yields

$$\left| (Z_{kr}^T Z_{kr})^{-1} (Z_{kr}^T \Delta E_k) \right| - \left| (Z_{k'r}^T Z_{k'r})^{-1} (Z_{k'r}^T \Delta E_{k'}) \right| = 0 \quad (9)$$

where $|\cdot|$ returns magnitude of its argument. The fault location can be obtained by solving eq. (9).

2.4 Cases when the faulted line is a double-circuit line

When the faulted line is a double-circuit line, a fictitious bus r containing six nodes are added to the network. Bus r is at the assumed fault location. The equivalent Pi circuit line parameters can be calculated following eqs (1)-(2). It is noted that $\mathbf{z}_1, \mathbf{y}_1, \mathbf{z}_2, \mathbf{y}_2$ are six by six matrices in the case of double-circuit line. The transfer impedances between nodes on bus k and the faulted nodes can be derived as

$$Z_{k,r} = (\mathbf{y}_1/2 + \mathbf{y}_2/2 + \mathbf{z}_1^{-1} + \mathbf{z}_2^{-1})^{-1} (\mathbf{z}_1^{-1} [Z_{k,p}, Z_{k,p}]^T + \mathbf{z}_2^{-1} [Z_{k,q}, Z_{k,q}]^T), \quad i = 1, 2, \dots, 6 \quad (10)$$

The transfer impedances and the driving point impedances related to the faulted nodes are expressed as

$$Z_{r,r_i} = (\mathbf{y}_1/2 + \mathbf{y}_2/2 + \mathbf{z}_1^{-1} + \mathbf{z}_2^{-1})^{-1} (\mathbf{z}_1^{-1} [Z_{pr_i}, Z_{pr_i}]^T + \mathbf{z}_2^{-1} [Z_{qr_i}, Z_{qr_i}]^T + \mathbf{v}_i), \quad i = 1, 2, \dots, 6 \quad (11)$$

where Z_{pr_i} and Z_{qr_i} can be calculated based on eq. (10), and \mathbf{v} is a six by six identity matrix whose i th column is denoted as \mathbf{v}_i .

The prefault voltage at bus r is

$$E_{r0} = (\mathbf{y}_1/2 + \mathbf{y}_2/2 + \mathbf{z}_1^{-1} + \mathbf{z}_2^{-1})^{-1} (\mathbf{z}_1^{-1} [E_{p0}, E_{p0}]^T + \mathbf{z}_2^{-1} [E_{q0}, E_{q0}]^T) \quad (12)$$

The during-fault voltage at bus r and the fault current I_f are calculated following eqs (6) and (5). The fault location can then be solved based on eq. (7). When measurements from two buses are available, eq. (8) can be adopted to derive the fault location.

3 Evaluation studies

The presented fault-location algorithm has been validated by using the Electromagnetic Transients Program (EMTP) simulation data of faults in a 27-bus, 345-kV, 60 Hz transmission system. The diagram of the studied system is shown in Figure 3 [25]. The bus numbers are labelled, and the line lengths in miles are given in parenthesis. Matlab is utilized to implement the proposed methods. The estimation accuracy is measured by the percentage error as

$$\%Error = \frac{|ActualLocation - EsimationLocation|}{TotalLengthofFaultedLine} \times 100 \tag{13}$$

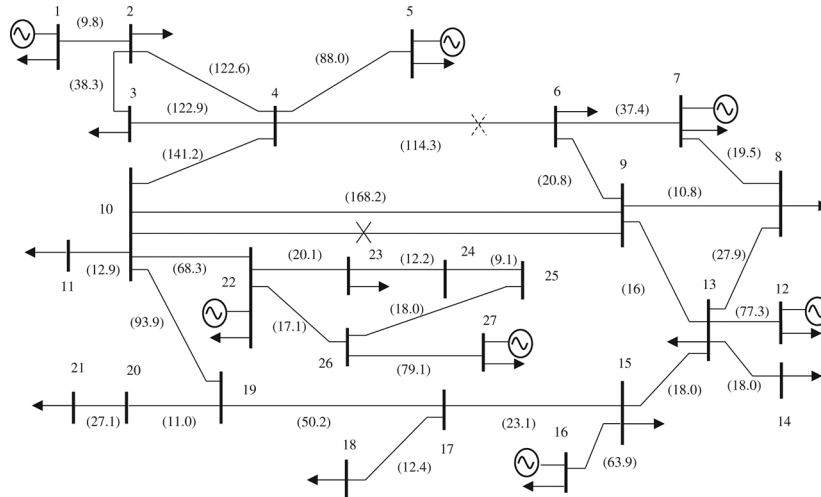


Figure 3: Diagram of the studied system.

3.1 Fault location results by two-bus algorithm

For single-circuit line fault location study, the evolving fault is assumed to happen on the line between bus 4 and bus 6, and is labelled by dashed cross in Figure 3. For double-circuit line fault location study, the evolving fault is imposed on the untransposed line between bus 9 and bus 10, and is denoted by solid cross in Figure 3. An example case study will be demonstrated below, followed by the evaluation under various conditions.

An AG fault occurs on the single-circuit line, 34.3 miles, i. e. 0.3 p.u., away from bus 4, and evolves to an ABG fault after 35 ms. Voltage measurements are available at two buses: bus 5 and bus 8. The three phase transient voltages observed at bus 5 and bus 8 are shown in Figure 4.

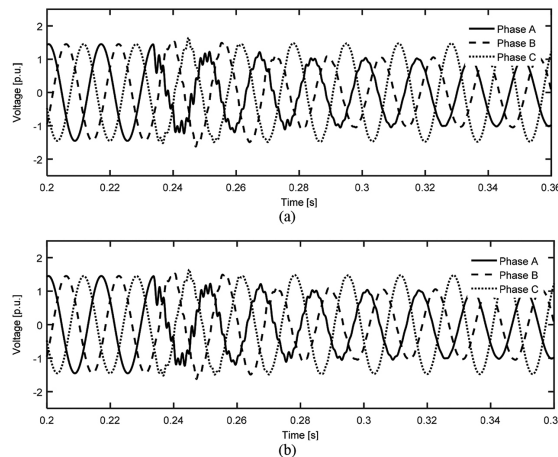


Figure 4: Voltage measurements at (a) bus 5 and (b) bus 8 during an AG-to-ABG evolving fault.

Now, the two-bus fault location algorithm developed in Section 2 is applied to estimate the fault location. The locus of estimated fault location versus time is drawn in Figure 5. It can be observed that due to changing

fault types and transients, the estimated fault location locus fluctuates at the beginning, and then levels off at 0.3 p.u. which is the actual fault location. The locus indicates a quick convergence of the proposed fault location method. It is noted that the same fault location method is applied without the need of fault type identification.

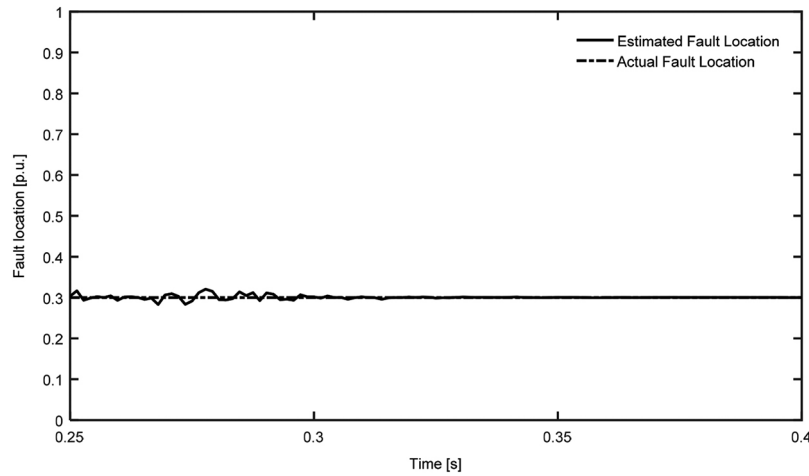


Figure 5: Locus of estimated fault location for an AG-to-ABG evolving fault.

For the purpose of evaluating the proposed algorithm’s feasibility, different evolving faults under various conditions have been studied.

Table 1 presents the representative conditions of the evolving faults on the single-circuit line and corresponding fault location results. In the table, the first two columns list the fault type information. The fault resistance values for the initial and secondary faults are given in column three and four. The fault location is the distance from bus 4 to the fault, and is presented in column five. The time intervals between the initial and secondary faults are given in column six, and they are chosen below three cycles. The percentage errors of estimations by using measurements from two buses are shown in the last three columns in Table 1. The voltage measurements from buses 5&8 and 7&22 are considered as synchronised and the measurements from bus 12&24 are unsynchronised. The secondary faults may differ from the initial fault in terms of fault type or fault resistance or both. It can be observed that quite accurate fault location results are achieved, regardless of these changes.

Table 1: Single-circuit line evolving fault location results using two-bus algorithm.

Initial fault type	Secondary fault type	Initial fault res. (Ω)	Secondary fault res. (Ω)	Fault loca. (p.u.)	Time interval (ms)	Est. error (%) using measurements from buses		
						5&8 (syn.)	7&22 (syn.)	12&24 (unsyn.)
AG	AG	10	1	0.8	15	0.06	0.14	0.12
AG	ABG	10	10	0.3	10	0.03	0.07	0.19
AG	ABCG	5	5	0.4	26	0.02	0.05	0.17
BG	BCG	15	5	0.5	26	0.01	0.18	0.14
BG	ABCG	20	15	0.5	10	0.01	0.16	0.11
AB	ABG	10	5	0.3	8	0.03	0.10	0.18
BC	ABCG	1	1	0.7	17	0.01	0.16	0.12
BC	BCG	50	50	0.5	17	0.09	0.21	0.22
BC	ABC	10	10	0.2	30	0.01	0.05	0.09

In Table 2, columns 1–6 are initial fault type, secondary fault type, initial fault resistance, secondary fault resistance, actual fault location and the time interval between faults. It is seen that the developed algorithm works well with double-circuit lines. By using synchronised measurements from buses 7&12 and unsynchronised measurements from 15&22 and 19&26, accurate estimates are produced.

Table 2: Double-circuit line evolving fault location results using two-bus algorithm.

Initial fault type	Second. fault type	Initial fault res. (Ω)	Sec-ond. fault res. (Ω)	Fault loca. (p.u.)	Time interval (ms)	Est. error (%) using measurements from buses		
						5&8 (syn.)	7&22 (syn.)	12&24 (unsyn.)
6								

						7&12 (syn.)	15&22 (unsyn.)	19&26 (unsyn.)
AG	AG	5	1	0.5	33	0.04	0.10	0.11
AG	ABG	10	5	0.7	20	0.06	0.09	0.09
AG	ABCG	50	50	0.7	20	0.04	0.35	0.41
BG	BCG	5	5	0.3	28	0.05	0.09	0.09
BG	ABCG	15	15	0.6	15	0.05	0.10	0.12
AB	ABG	5	1	0.3	25	0.02	0.09	0.09
BC	BCG	15	15	0.7	30	0.04	0.06	0.07
BC	ABC	5	5	0.8	15	0.06	0.07	0.11
BC	ABCG	20	20	0.2	10	0.23	0.26	0.31

3.2 Fault location results by one-bus algorithm

An example evolving fault is assumed to evolve from BG fault to ABCG fault 8 ms after the initial fault. The fault occurs on the double-circuit untransposed line between bus 9 and bus 10. The fault resistance is 5 ohms. Figure 6 depicts the voltage measurement from bus 11.

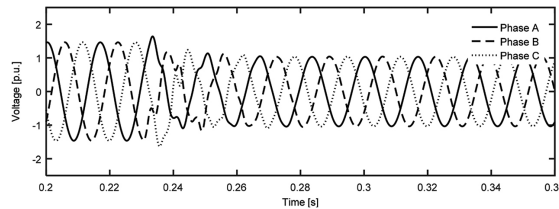


Figure 6: Voltage measurements at bus 11 during a BG-to-ABCG evolving fault.

The fault location estimation locus is drawn in Figure 7. The estimation locus first oscillates around the red reference line that represents the actual fault location. Afterwards, the estimation locus and the reference line coincide with each other. In other words, quite accurate estimation is achieved quickly.

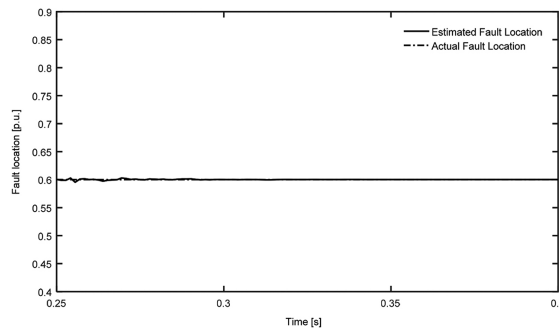


Figure 7: Locus of estimated fault location for a BG-to-ABCG evolving fault.

Similarly, different evolving faults are utilized to validate the proposed method. The following section exhibits the percentage fault location errors for one-bus algorithm. Evolving faults happening on both single-circuit and double-circuit lines are included in the study. Extensive fault conditions have been tested, and representative results are provided here.

In Table 3 and Table 4, the columns 1–6 are the initial fault type, secondary fault type, initial fault resistance, secondary fault resistance, actual fault location, and the time interval. The other columns are the fault location estimation errors. It is shown that the proposed method has achieved quite accurate results.

Table 3: Single-circuit line evolving fault location results using one-bus algorithm.

Initial fault type	Secondary fault type	Initial fault res. (Ω)	Secondary fault res. (Ω)	Fault loca. (p.u.)	Time interval (ms)	Est. error (%) using measurements from buses		
						3	12	22
AG	AG	5	15	0.2	9	0.19	0.11	0.21
AG	ABG	10	5	0.5	10	0.12	0.04	0.32

AG	ABCG	5	5	0.3	26	0.03	0.03	0.05
BG	BCG	10	15	0.7	26	0.12	0.04	0.33
BG	ABCG	10	10	0.6	15	0.01	0.05	0.03
AB	ABG	50	50	0.8	10	0.05	0.04	0.04
BC	ABCG	10	1	0.5	17	0.02	0.01	0.03
BC	BCG	15	15	0.6	17	0.15	0.07	0.29
BC	ABC	5	5	0.8	30	0.03	0.02	0.02

Table 4: Double-circuit line evolving fault location results using one-bus algorithm.

Initial fault type	Secondary fault type	Initial fault res. (Ω)	Secondary fault res. (Ω)	Fault loca. (p.u.)	Time interval (ms)	Est. error (%) using measurements from buses		
						11	19	21
AG	AG	1	10	0.3	10	0.15	0.12	0.11
AG	ABG	20	10	0.8	10	0.05	0.06	0.08
AG	ABCG	5	5	0.3	26	0.02	0.04	0.12
BG	BCG	15	5	0.6	26	0.05	0.08	0.09
BG	ABCG	5	15	0.6	8	0.05	0.22	0.09
AB	ABG	10	1	0.3	30	0.08	0.05	0.09
BC	ABCG	5	5	0.7	17	0.03	0.19	0.06
BC	BCG	10	10	0.5	17	0.05	0.08	0.04
BC	ABC	15	15	0.2	30	0.02	0.05	0.15

3.3 Discussion

The proposed fault location method uses voltage phasors to calculate the fault location. So accuracy of phasor estimation will affect the accuracy of fault location. This paper uses Fourier transform method to extract phasors. For improved accuracy, advanced phasor extraction method such as least squares based approach may be used to extract phasors [29]. This paper focuses on the fault location method and hence does not detail phasor extraction methods. However, impact of measurement noises or phasor estimation error on the proposed fault location method is examined below.

To simulate measurement noises, 1 % error is added to the voltage measurements and then the fault location method is applied. The fault location results are listed in Table 5. It is shown that the fault location estimation is still quite accurate.

Table 5: Fault location results based on noisy measurements.

Initial fault type	Second. fault type	Initial fault res. (Ω)	Second. fault res. (Ω)	Fault loca. (p.u.)	Time interval (ms)	Faulted line circuit	Est. error (%) using measurements from buses		
							3&11	15	19
AG	AG	1	10	0.3	21	Single	0.44	0.43	0.33
AG	ABG	20	10	0.8	32	Single	0.67	0.51	0.49
AG	ABCG	5	5	0.3	42	Double	0.34	0.80	0.47
BG	BCG	15	5	0.6	12	Double	0.95	0.74	0.65
BG	ABCG	5	15	0.6	28	Single	0.70	0.84	0.46
AB	ABG	10	1	0.3	13	Single	0.30	0.67	0.68
BC	ABCG	5	5	0.7	35	Double	0.77	0.53	0.65
BC	BCG	10	10	0.5	2	Double	0.46	0.41	0.30

It is also noted that since the proposed method is applicable regardless of the fault type, a fault location locus versus time can be achieved. Using the obtained fault location, other fault conditions including the fault type versus time can be further derived. Thus, we will have a better understanding of the faulted system, and be more confident about the obtained fault location than if we only have one single fault location obtained using the last cycle of the waveforms.

4 Conclusion

The fault phases in an evolving fault change during the fault. Fault location methods that depend on fault type information may give unreliable results if incorrect fault type is obtained. This paper presents a method to locate evolving faults that is applicable regardless of time varying fault conditions. Neither fault type information nor fault resistance is a prerequisite for this algorithm. By using sparse wide area measurements, quite accurate estimation can be obtained even when the measurements are not available from the terminals of the faulted lines. This method is applicable to both single-circuit and double-circuit lines, and fully considers the shunt capacitance of the transmission lines. Evaluation studies have demonstrated that the proposed methods yield quite accurate results.

Appendix

This section provides the model data of the studied 27-bus system. The per-unit system is based on a based voltage of 345kV and base volt-ampere of 100MVA. The transmission line data, generator data, and load data are listed in Table 6-Table 9.

Table 6: Transmission line data excluding the double-circuit line.

From Bus	To Bus	Length (mile)	R1(p.u.)	X1(p.u.)	B1(p.u.)	R0(p.u.)	X0(p.u.)	B0(p.u.)
1	2	9.8	0.00054	0.00498	0.08169	0.00162	0.01494	0.04084
2	3	38.3	0.00214	0.01929	0.32695	0.00642	0.05787	0.16348
2	4	122.6	0.00667	0.06199	1.03274	0.04906	0.1542	0.613
3	4	122.9	0.0068	0.06255	1.03066	0.04913	0.155	0.6145
4	5	88	0.00484	0.04472	0.73934	0.03108	0.1028	0.44
4	6	114.3	0.00633	0.05754	0.97111	0.0557	0.1832	0.5715
4	10	141.2	0.0077	0.0717	1.1612	0.0572	0.1779	0.79229
6	7	37.4	0.00166	0.01852	0.32361	0.00498	0.05556	0.16181
6	9	20.8	0.00075	0.01014	0.18298	0.0095	0.0352	0.104
7	8	19.5	0.0009	0.00959	0.17028	0.0073	0.0243	0.0975
8	9	10.8	0.00048	0.00536	0.09336	0.0042	0.0196	0.054
8	13	27.9	0.00151	0.01378	0.24124	0.0079	0.0527	0.1395
9	13	16	0.00087	0.00793	0.13882	0.0046	0.0303	0.08
10	11	12.9	0.0008	0.0077	0.1237	0.00542	0.01682	0.0645
10	19	93.9	0.00513	0.04479	0.83974	0.0348	0.1706	0.4695
10	22	68.3	0.0035	0.03436	0.58279	0.02428	0.09307	0.36804
12	13	76	0.0042	0.0371	0.65336	0.0126	0.1113	0.32668
13	14	18	0.001	0.0089	0.1523	0.003	0.0267	0.07615
13	15	18	0.001	0.0089	0.1523	0.003	0.0267	0.07615
15	16	63.9	0.0034	0.0317	0.5398	0.0102	0.0951	0.2699
15	17	23.1	0.0012	0.0111	0.1999	0.0036	0.0333	0.09995
17	18	12.4	0.00066	0.00596	0.10713	0.00198	0.01788	0.05356
17	19	50.2	0.00294	0.02484	0.43528	0.01914	0.08901	0.251
19	20	11	0.00064	0.00543	0.09518	0.0042	0.0195	0.055
20	21	27.1	0.0015	0.0134	0.2293	0.0045	0.0402	0.11465
22	23	20.1	0.00042	0.00969	0.1822	0.0066	0.02668	0.12114
22	26	17.1	0.00083	0.00825	0.15134	0.00527	0.02517	0.08945
23	24	12.2	0.00026	0.00584	0.11141	0.00384	0.01727	0.07131
24	25	9.1	0.00044	0.00455	0.07723	0.0029	0.01311	0.04554
25	26	18	0.00037	0.00864	0.16156	0.005	0.02638	0.09106
26	27	79.1	0.00374	0.03812	0.69902	0.02414	0.11857	0.41408

In Table 6, the first two columns are the two bus numbers for each branch. The per-unit positive-sequence resistance, positive-sequence reactance, positive-sequence susceptance, zero-sequence resistance, zero-sequence reactance, and zero-sequence susceptance for each branch excluding the untransposed double-circuit line are listed.

Since the transmission line between bus 9 and bus 10 is untransposed double-circuit lines, there are six modes involved to represent this line, and the modal values used to simulate such line is listed in Table 7.

Table 7: Transmission line data for the line between bus 9 and bus 10.

Modes	Modal values of the untransposed double-circuit line
1	$0.000472451729722 + 0.003011594685278j$
2	$0.000074001058003 + 0.000783717824966j$
3	$0.000057640900543 + 0.000519020785647j$
4	$0.000045966799966 + 0.000506206323198j$
5	$0.000040415648413 + 0.000476514356441j$
6	$0.000037474354557 + 0.000480355357462j$

In Table 8, the first column represents the bus number that the generator is connected to. Columns 2–5 show the zero-sequence source resistance, zero-sequence source reactance, positive-sequence source resistance, and positive-sequence source reactance.

Table 8: Generator data of the power system.

Bus No.	R0(p.u.)	X0(p.u.)	R1(p.u.)	X1(p.u.)
1	0.00332,695	0.01794547	0.00306751	0.0158382
5	0.00188591	0.05335358	0.00236455	0.0486705
7	0.00592615	0.02806461	0.01679126	0.0407814
12	0.00604318	0.06398748	0.00671002	0.0525554
16	0.00424012	0.02669784	0.00925217	0.030113
22	0.00359328	0.02391867	0.0005041	0.0349924
27	0.00635514	0.03437664	0.00376854	0.0195658

Table 9: Load data of the power system.

Bus No.	Load Impedance (p.u.)
1	$1.225 + 0.2487j$
2	$3.1 + 1.2252j$
3	$1.65 + 0.235j$
5	$2.425 + 0.6078j$
6	$0.845 + 0.3314j$
7	$3.19 + 0.9779j$
8	$1.056 + 0.3469j$
11	$1.92 + 0.56j$
12	$0.648 + 0.1567j$
13	$4.85 + 1.2155j$
14	$3.28 + 0.5939j$
15	$3.28 + 0.5939j$
16	$0.64 + 0.1867j$
18	$1.96 + 0.398j$
21	$2.475 + 0.3527j$
22	$1.0722 + 0.2914j$
23	$1.3857 + 0.3473j$
27	$1.056 + 0.3469j$

In Table 9, the first column represents the bus number that the load is connected to. The second column exhibits the equivalent load impedance in per unit.

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