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Fall 2018

# FED 101 - ECE Fundamentals of Engineering Design

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## Helen and John C. Hartmann Department of Electrical and Computer Engineering New Jersey Institute of Technology Course Syllabus

**Course Number and Title:** ECE 101-002 Introduction to Electrical & Computer Engineering

*Instructor:* Ryoko Mathes

E-mail: mathes@njit.edu, Office Location: ECEC 235, Telephone: 973-596-3510

**Office Hours:** By Appointment- Please call the office (972-596-3510)

Text book: None required

Pre-requisite: None

Days and Times: Tuesdays, 5:00 pm- 6:00 pm

Location: WEST LECT 1

**ECE 101 Course Description**: This freshman level course is designed to familiarize students with various disciplines, career opportunities and curricula in electrical and computer engineering. Invited speakers include faculty and industrial representatives.

**Learning Outcome:** Upon completion of this course, students will demonstrate:

- The understanding of variety career options available for electrical and computer engineers
- The understanding of curriculum and requirements of the ECE curriculum

<u>Assignments:</u> There are two assignments in this course. The details of the assignments will be announced on Moodle one week prior to the due date. <u>All assignments are due at the beginning of the class on the due date.</u>

Assignment 1: Reflection on student work and research experience panel

Due: January 31

Assignment 2: Career and Educational Action Plans

Due: April 18

**Grading:** The course grade will be "S," Satisfactory, or "U," Unsatisfactory based on attendance and assignments.

- Attendance is mandatory and missing class is grounds for the unsatisfactory grade. Attendance is taken every week. Tardiness is unacceptable in this course and will be counted as absent. The door closes at 5:00 pm and no late comers will be allowed in the class.
- All excused absences will need to be reported to the Dean of Students Office
- No phones and laptop allowed in the class

• Assignments must be turned in at the beginning of the class. No late assignments will be accepted. Missing assignments are ground for and unsatisfactory grade for the course.

The NJIT Honor Code is strictly enforced in this course. No student is allowed to sign-in or submit the lecture notes for who is not present.

### Course Schedule:

Date	Lecture	Speaker	<b>Assignment Due</b>
1/17/2017	Introduction to Electrical and Computer Engineering	Dr. Leonid Tsybeskov, Chair	
1/24/2017	Student Panel Coop and research experience	ECE students	
1/31/17	Guest Speaker	TBA	Reflection on student work and research experience panel due
2/7/2017	Guest Speaker	TBA	
2/14/2017	Guest Speaker	TBA	
2/21/2017	Student Panel	IEEE	
2/28/2017	Guest Speaker		
3/7/2016	ECE Curriculum and advising	Ryoko Mathes	
3/14/2016	Spring Recess-No Class		
3/21/2016	Guest Speaker	Brian Kiernan	
3/28/2016	Guest Speaker	Leon Baptise	
4/4/2016	Guest Speaker	Kevin Carswel, Solid State Cooling Systems	
4/11/2016	Guest Speaker	David Haessig, BAE	
4/18/2016	Guest Speaker	Doru Popescu, Keysights Technologies	Career and Educational Action Plans Due
4/26/2016	Wrap up		