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Contributions to modeling, simulation, and analysis of three-phase dual active bridge converters for dc-grid integration

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Thèse présentée en vue de l'obtention du diplôme de Philosophiæ Doctor

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RÉSUMÉ

Le convertisseur cc-cc bidirectionnel isolé triphasé à deux ponts actifs (3p-DAB) est largement étudié pour son utilisation dans les réseaux à courant continu de nouvelle génération. Son intégration nécessite une modélisation précise de ses caractéristiques petit et grand signal. De plus, sa caractérisation précise en mode dégradé d'opération est un sujet important pour le développement de réseaux fiables et résilients. L'application de stratégies de modélisation bien connues au convertisseur 3p-DAB, tant en fonctionnement normal que dégradé, n'est pas triviale du fait de sa structure cc-ca-cc, des différentes connexions possibles de transformateurs triphasés et du nombre plus élevé d'interrupteurs par rapport à d'autres topologies connues. L'absence de modèles petits signaux précis ainsi que de modèles grands signaux numériquement efficace du 3p-DAB est la principale motivation des développements présentés dans cette thèse. Trois contributions principales se dégagent de ce travail.

Cette thèse contribue d'abord à l'identification des limites de l'approche conventionnelle de moyennage en espace d'état (SSA) pour la détermination des impédances d'entrée Z_D et Z_N du convertisseur 3p-DAB. Ces deux fonctions de transfert sont nécessaires à l'application du théorème d'éléments supplémentaires (EET) de Middlebrook qui est largement utilisé par les concepteurs en électronique de puissance pour prévenir la dégradation des performances dynamiques ainsi que les conditions d'instabilité lors de l'ajout d'un filtre d'entrée. La détermination précise de Z_D et Z_N est également importante pour déduire les caractéristiques petit signal en boucle fermée du convertisseur à partir de son modèle en boucle ouverte. Bien qu'il soit démontré que l'approche généralisée de moyennage en espace d'état (GSSA) peut être utilisée pour surmonter les limites de la méthode SSA pour l'évaluation de Z_D , un nouveau modèle hybride combinant SSA et GSSA est proposé pour le calcul de Z_N .

La deuxième contribution de cette thèse est le développement d'un modèle moyenné généralisé (GAM) précis et efficace permettant la simulation de réseaux utilisant des convertisseurs 3p-DAB dans des logiciels d'analyses de transitoires électromagnétiques. Le modèle proposé est comparé en détail à d'autres techniques de modélisation, soit le modèle idéal en commutation, la technique des fonctions de commutation (SWF) et l'approche de moyennage en espace d'état (SSA). En somme, il est conclu que le modèle GAM fournit une solution optimale lorsqu'on

V

prend en compte la précision de la réponse transitoire, la réduction du temps de calcul et la largeur de bande de la réponse fréquentielle. Finalement, un réseau de simulation à grande échelle est développé dans le logiciel Electromagnetic Transient Program (EMTP) pour valider les performances du modèle GAM pour l'analyse du courant de défaut et de la tension de transitoire de rétablissement dans un système grande échelle.

La troisième contribution de cette thèse est la proposition et la caractérisation d'un nouveau mode dégradé pour le convertisseur 3p-DAB. Des travaux antérieurs ont montré que le 3p-DAB peut être utilisé dans un mode de défaillance de panne dit à phase gelée, c'est-à-dire lorsque les deux transistors d'une même phase sont forcés à l'état ouvert par les protections internes de leurs pilotes de grille. Puisque les diodes de roue libre sont laissées en autocommutation, la caractérisation analytique du convertisseur pour toutes les conditions de tension et de charge n'est pas triviale. Dans cette thèse, il est proposé d'ouvrir la phase en défaut de manière à éliminer l'interaction des diodes de roue libre au niveau de la phase défectueuse. En éliminant l'interaction de ces diodes, le 3p-DAB peut être opéré dans un mode de fonctionnement caractérisable pour toutes les conditions de tension et de charge. De plus, en raison de la circulation de courant réactif dans la phase défectueuse en fonctionnement à phase gelée, il est démontré que les contraintes de courant sont plus élevées et que la capacité de transfert de puissance du convertisseur est réduite par rapport au fonctionnement à phase ouverte proposé dans cette thèse.

ABSTRACT

The three-phase dual active bridge (3p-DAB) isolated-bidirectional dc-dc converter (IBDC) is widely investigated in next-generation dc-grids. Its successful integration requires an accurate representation of its small- and large-signal characteristics. Furthermore, accurate characterization of converters behavior in degraded-mode is an important topic for the development of reliable and resilient dc-grids. The application of well-known modeling strategies to 3p-DAB converters in both normal and degraded operations is not trivial due to its dc-ac-dc structure, the different possible three-phase transformer connections and the higher number of switches compared to other well-known IBDC topologies. The absence of accurate small-signal models as well as computational efficient large-signal models of the 3p-DAB has motivated the developments presented in this thesis. Three main contributions are emerging from this work.

This thesis first contributes to the identification of the limitations of the basic state-space averaging (SSA) approach for the determination of the driving point Z_D and null driving point Z_N input impedances for 3p-DAB converters. These two transfer functions are necessary for the application of Middlebrook's extra element theorem (EET) which is broadly used by practicing power electronic designers to avoid dynamic performance degradation or instability conditions in the presence of an additional input filter. The accurate determination of Z_D and Z_N is also important to derive the closed-loop small-signal characteristics of converters from its open-loop characteristics. While it is shown that the generalized state-space averaging (GSSA) approach can be used to overcome SSA limitations to evaluate Z_D , a new hybrid SSA and GSSA multi-input multi-output (MIMO) model, which combines SSA and GSSA, is proposed to evaluate Z_N .

The second contribution of this thesis is the development of an accurate and computational efficient generalized average model (GAM) which enables system-level simulation of dc-grids with 3p-DAB converters in electromagnetic transient type (EMT-type) programs. The proposed model is rigorously compared with alternative modeling techniques: ideal-model, switching-function (SWF) and state-space averaging (SSA). It is concluded that the GAM model provides an optimal solution when accuracy of transient response, reduction in computation time, and wideband response factors are considered. A large-scale test system is implemented in

Electromagnetic Transient Program (EMTP) to validate the performance of the GAM model for fault current and transient recovery voltage analysis in a practical size system.

The third contribution of this thesis is the proposal and the characterization of a new degraded mode for the 3p-DAB converter. Previous work has shown that the 3p-DAB can be operated in a frozen leg fault-tolerant mode, i.e. with the two transistors of the same phase being opened by their gate driver internal protections. Because the free-wheeling diodes are left self-commutated, the analytical characterization of the converter for all voltage and loading conditions is not trivial. In this thesis, it is proposed to open the faulty-phase such as it eliminates the interaction with the faulty-phase free-wheeling diodes. By removing the interaction of the free-wheeling diodes, the 3p-DAB falls in a characterizable operating mode for all voltage and loading conditions. Furthermore, because of the circulation of reactive current in the faulty-phase in frozen leg operation, it is demonstrated that the current stress is higher, and the power transfer capability is reduced over the proposed open-phase operation.

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LIST OF SYMBOLS AND ABBREVIATIONS

1p-DAB	Single-phase dual active bridge
3p-DAB	Three-phase dual active bridge
AC	Alternating current
ADC	Analog-to-digital converter
CPL	Constant power load
CPU	Central processing unit
DAB	Dual active bridge
DC	Direct current
DPS	Dual-phase-shift
EET	Extra element theorem
EMC	Electromagnetic compatibility
EMT	Electromagnetic transient
EMTP	Electromagnetic transient program
EPS	Extended-phase-shift
ESR	Equivalent series resistance
FD	Fault-diagnosis
FET	Field effect transistor
FFT	Fast Fourier transform
FHA	First harmonic approximation
FPGA	Field programmable gate array
FT	Fault-tolerant
GaN	Gallium nitride
GAM	Generalized average model

GSSA	Generalized state-space averaging
IBDC	Isolated-bidirectional dc-dc converter
KVL	Kirchhoff's voltage law
MANA	Modified-augmented-nodal analysis
MIMO	Multi-input multi-output
MOV	Metal oxide varistor
OS	Overshoot
PI	Proportional integral
PLM	Piecewise-linear modeling
RAM	Random access memory
RMS	Root mean square
SiC	Silicon carbide
SPS	Single-phase-shift
SSA	State-space averaging
SST	Solid-state transformer
SWF	Switching-function
TAB	Triple active bridge
TPS	Triple-phase-shift
UPS	Uninterruptible power supply
ZVS	Zero-voltage switching

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CHAPTER 1 INTRODUCTION

1.1 Motivation

There is a surge of interest in academic and industrial research on dc-grids mainly due to their wellknown advantages over ac-grids in several applications [1], [2]. DC distributions are being developed for applications such as transportation [3], [4], commercial and industrial buildings [5], [6], data centers [7], generating stations [8], and integration of distributed resources [9].

Power electronic interfaces play a key role in dc-grids [10], their primary functions being power flow control, galvanic isolation and voltage conversion between different grid voltage levels. Isolated-bidirectional dc-dc converters (IBDCs) are highly investigated for these applications because their structures naturally meet these three functions. IBDC topologies are classified into seven (7) categories depending on the number of switches that constitutes their switching stage (Figure 1.1).

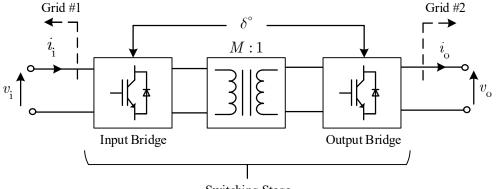
Number of switches						
Two (2)	Three (3)	Four (4)	Five (5)	Six (6)	Eight (8)	Twelve (12)
dual-flyback,	forward-flyback	dual-push-pull	full-bridge-forward	half-full-bridge	single-phase-dual-	three-phase-
dual-Cuk		push-pull-forward,	-		active-bridge	dual-active-
Zeta-Sepic		push-pull-flyback				<u>bridge</u>
		dual-half-bridge				

Figure 1.1 : Classification of isolated-bidirectional dc-dc converters (adapted from [11])

Among these topologies, dual active bridge (DAB) isolated bidirectional dc-dc converters (Figure 1.2) are broadly considered due to their high flexibility, and their high efficiency owing to their inherent zero-voltage switching (ZVS) capabilities [12], [13]. DAB converters are proposed to be employed in many recent conversion applications such as distributed resources interface [14], uninterruptible power supply (UPS) systems [15], automotive [16], [17], passenger rail transportation [18], solid-state transformers (SST) [19], airborne wind turbines [20], and flexible load emulators [21].

Both single-phase (1p-DAB) and three-phase (3p-DAB) versions have been proposed and protected by a patent in the early 1990s [22]. These two topologies are now public domain and are gaining a broad interest especially with the introduction of emerging semiconductor technologies which tends to reduce the negative effects of having more switches when compared to other IBDC topologies. Regardless of the higher number of switches, the 3p-DAB (Figure 1.3) provides many

advantages over the 1p-DAB which is why it tends to be favored in many modern flexible dcgrids [23]. Due to its three-phase structure, the ripple frequency is increased, thus reducing the sizing of the filter capacitors. It also reduces the current through the switches and provides a better utilization of the transformer copper and core materials.



Switching Stage

Figure 1.2 : DAB topology for dc-grids interface

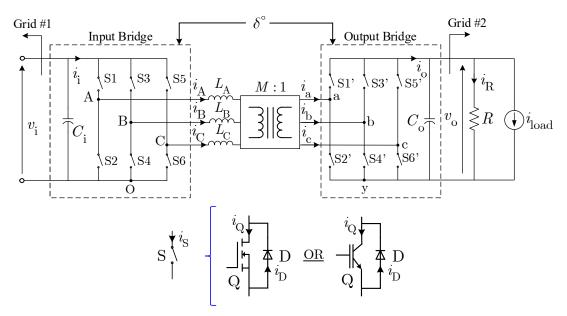


Figure 1.3 : Three-phase dual active bridge (3p-DAB) converter

The integration of power electronic converters in dc-grids brings many challenges in terms of power system control and stability [25]–[27]. Small-signal and large-signal analyses are necessary steps for successful integration of power electronics-based systems [28]. These analyses require the development of efficient and accurate models which can be implemented in modern computer programs.

Small-signal frequency-domain modeling of converters is important in controller and filter designs, as well as stability and dynamic performance assessments [29]. Converter open-loop small-signal characteristics are generally defined by a set of standard transfer functions: the control-to-output transfer function $G_{\rm vd}$, the input-to-output transfer function $G_{\rm vg}$, the driving point $Z_{\rm D}$ and null driving point $Z_{\rm N}$ input impedances, and the output impedance $Z_{\rm o}$. For the stability analysis of multiple converters interacting with other network components, it is also required to determine the closed loop small-signal characteristics of the converter such as the loop gain T and the closed-loop input and output impedances [30].

As for traditional ac-grids, the integration of power electronics in dc-grids also requires timedomain system-level studies such as general transient behavior, voltage regulation performance with load variations, transient stability, and short-circuit [28], [31]–[33]. Since the behavior of a converter depends on both its topology and its controller, closed-loop modeling of converters is necessary for system-level studies [34].

Each converter topology has its own particularities which must be considered in the modeling process. As it will be emphasized in this thesis, the application of well-known modeling strategies to the 3p-DAB converter is not trivial due to its dc-ac-dc structure, the different possible three-phase transformer connections and the higher number of switches compared to other IBDC topologies. As it will also be shown in this thesis, there is currently a lack of accurate small-signal models for stability analysis of the 3p-DAB in the literature. It is also the case for efficient and accurate large-signal models of the 3p-DAB for system-level studies in electromagnetic transient type (EMT-type) programs.

Furthermore, there is also limited literature on degraded-mode operation of the 3p-DAB converter. Recently, its has been identified in [35] that the 3p-DAB converter can be operated in a frozen leg degraded-mode. However, the analytical characterization of the converter behavior under all voltage and loading conditions is not trivial. Accurate characterization of the converter behavior in degraded-mode is an important topic for the development of reliable and resilient dc-grids [36], [37].

1.2 The three-phase dual active bridge (3p-DAB)

1.2.1 Control of the 3p-DAB

The basic three-phase dual active bridge (3p-DAB) topology is shown in Figure 1.3. The transformer voltage ratio M is used to match the nominal voltage levels between the two grids. Due to its three-phase structure, the 3p-DAB also allows using different winding connections (Y-Y, Y- Δ , and Δ - Δ). It is, however, demonstrated in [24] that the Y- Δ transformer offers better performance in terms of stress on switches, transformer utilization, and filter capacitor requirements. The Y- Δ version (Figure 1.4 a)) has therefore been selected for investigation in this thesis.

Voltage control is performed by controlling the duty cycle of the primary and secondary bridges, and/or the phase-shift between the two bridges [38], [39]. Single-phase-shift (SPS) control has been the first proposed control method for the DAB converter. However, with SPS, both the 1p-DAB and the 3p-DAB have a limited ZVS soft-switching range under wide voltage and loading conditions [40]. For the 1p-DAB, extended-phase-shift (EPS) [41], dual-phase-shift (DPS) [42], and triple-phase-shift (TPS) [43] control strategies have been introduced to decrease the current stress, and improve the converter efficiency by extending its ZVS range. Due to the coupling between the three phases of the 3p-DAB, it has been shown that some TPS modulation methods [38] cannot be used without operating the 3p-DAB as a 1p-DAB converter. Even if it addresses the problem of poor partial load efficiency by extending the ZVS range, for the 3p-DAB, these strategies almost nullify the 3p-DAB advantages regarding the reduction of the output filter size [17]. The generic TPS modulation method of [43] has been used in [44] for the Y-Y 3p-DAB converter. While the results show a better efficiency at light load, it does not reduce the RMS current in the transformer as well as the maximum total losses at high load. It is not widely adopted in the literature and SPS still remains the most commonly used control method for the 3p-DAB.

The Y- Δ 3p-DAB converter under study in this thesis is therefore operated using SPS control with a fixed switching frequency $\omega_s = 2\pi f_s$ and a 180° (or 6-step) modulation method as shown in Figure 1.4 b) and c). The phase-shift δ between the gating signals applied to the two bridges is the variable used to regulate the voltage of either one of the two ports. For example, in forward operation ($0 \le \delta \le \pi/2$), the active power is transferred from the input bridge to the output bridge such that v_0 is regulated to reach its reference value V_0^{ref} . Reverse operation consists in reversing the sign of δ and regulating v_i to reach a reference value V_i^{ref} .

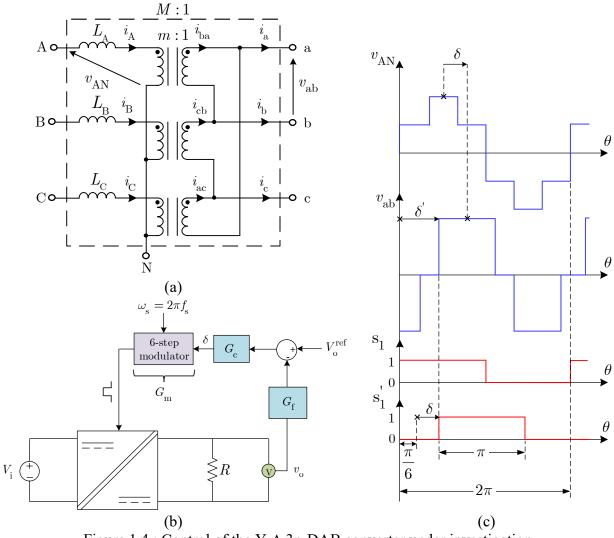


Figure 1.4 : Control of the Y- Δ 3p-DAB converter under investigation

1.2.2 Variants of the 3p-DAB topology

Different topologies of the 3p-DAB are also being proposed with different objectives. In [45], a six-leg inverter is used on the low-voltage side with the main objective of increasing the converter current capability while preserving standard modulation techniques and DAB advantages. The six-leg structure results in three decoupled voltages applied on the low-voltage side of the three-phase transformer.

Moreover, a 3p-DAB featuring a Y- Δ transformer and a current-tripler unit on the low-voltage side is proposed in [46] for increasing the converter conversion ratio while limiting the required transformer winding-ratio which has the negative effect of increasing the transformer leakage inductance and parasitic capacitance. The main drawback with the current-tripler version is that it does not offer ZVS on the low-voltage side. In [47] a current-fed topology is developed for achieving high efficiency over wide input voltage and loading ranges with a reduction in the circulation of reactive current in the Y-Y transformer.

The triple active bridge (TAB) topology using Y-YY and Y-Y Δ transformer configurations is also analyzed and the Y-Y Δ configuration is selected because it shows a better transformer utilization at high-power load [48], [49]. The TAB can be used to control the power flow between three different dc voltage sources, or to reduce the current through the output bridge switches by connecting the two output bridges in parallel.

1.3 Contributions

The main contributions of this thesis are summarized here. Each contribution is presented along with a literature review to clearly position this thesis with respect to previous work.

<u>Contribution #1</u>: Hybrid average modeling of three-phase dual active bridge converters for stability analysis [50]

The most popular technique to derive the analytical small-signal model of dc-dc converters is the state-space averaging (SSA) method [33], [51]. It is based on three main assumptions: 1) the switching ripple is small, 2) the ac variations are small, and 3) the modulation frequency $\omega_{\rm m}$ is much smaller than the switching frequency $\omega_{\rm s}$ [30]. It leads to a dc-averaged model of the converter. Due to the high-frequency ac-link in DAB converters, SSA has limitations in deriving an accurate small-signal model for DAB converters [52], [53]. SSA has been first applied to approximate the control-to-output $G_{\rm vd}$, and the input-to-output $G_{\rm vg}$ transfer functions of the 3p-DAB in [54]. While it can be easily extended to the derivation of the output impedance $Z_{\rm o}$, there is currently no resource in the literature to evaluate the driving point input impedance $Z_{\rm D}$ and the null driving point input impedance $Z_{\rm N}$ for 3p-DAB converters with SSA. These two transfer functions are necessary for the application of Middlebrook's extra element theorem (EET) [55] which is broadly used by power electronic designers to analyze dc-dc converters in the presence of an additional input filter.

To overcome the limitations of the SSA method for DAB converters, generalized state-space averaging (GSSA) models [56] have been developed in [53], [57] and [58]. These models are not extended to the derivation of Z_D and Z_N . The derivation of Z_N is particularly challenging for DAB converters modeled with GSSA. The methodology presented in [52] is the first attempt to derive both Z_D and Z_N for 1p-DAB converters modeled with GSSA. The methodology requires determining the closed-loop input impedance Z_{in}^c with a dummy controller G_c before extracting Z_N by analytically removing the controller from the set of equations. The application of this methodology to 3p-DAB converters modeled with GSSA is neither shown nor trivial.

This thesis first contributes to the identification of the limitations of SSA in the modeling of 3p-DAB converters especially for the determination of Z_D and Z_N . While it will be shown that GSSA can be used to evaluate Z_D , a new hybrid SSA and GSSA multi-input multi-output (MIMO) model, which combines SSA and GSSA, is proposed to evaluate Z_N . The proposed modeling approach is validated with time-domain simulations in Electromagnetic Transient Program (EMTP) [59]. Through the application of Middlebrook's EET for additional input filter [55], it is demonstrated that the proposed approach can effectively reduce the number of time-domain simulation runs as well as the number of experimental tests required to predict degradation of dynamic performance and instability conditions. The use of Middlebrook's theorem [55] not only guarantees stability, but also ensures that the introduction of an input filter does not substantially alter the converter transfer functions G_{vd} , G_{vg} , and Z_o [60]. SSA, GSSA, and hybrid methodologies are also thoroughly compared to provide a comprehensive review of their advantages, limitations, and overall performance.

<u>Contribution #2</u>: Large-signal modeling of three-phase dual active bridge converters for electromagnetic transient analysis in dc-grids [61]

As previously mentioned, large-signal modeling of the 3p-DAB converter is not trivial because of its dc-ac-dc structure, the various three-phase transformer connections and the higher number of switches compared to other IBDC topologies. The methodology presented in [62] is the first attempt to reduce the computation time for system-level studies with 3p-DAB converters in EMT-type programs. A discretized switching-function (SWF) model [63] in the $\alpha\beta$ frame is implemented. It reduces the number of electrical nodes while preserving the switching-effect. However, it still requires time-steps in the range of 1/1000th of the switching period to maintain numerical stability. The reduction in the number of electrical nodes cannot be used alone as an indicator for the reduction in computation time.

The ideal-model approach is well known for the analysis of 3p-DAB converters and other topologies because its implementation using ideal switches is straightforward in circuit simulation tools. The ideal-model approach is a recognized reference for the validation of other models [28]. However, because the circuit topology is time-variant, the computation time is generally high for system-level studies. The SWF approach [63] has been proposed as an alternative methodology to ideal-model in which the converter circuit is replaced by controlled voltage and current sources. Since the circuit topology is now time-invariant, the SWF method reduces the computation

time [32]. However, because it keeps the switching-effect, small time-steps are still required. Averaged models [51], [56] are well known for neglecting the switching-effect and allowing the use of larger time-steps which significantly reduce the computation time.

In this thesis, a generalized averaging model (GAM) for time-domain simulation of 3p-DAB converters is developed, implemented and compared with the ideal, SWF [63] and SSA [51] methods in EMTP. In Figure 1.5, the simulation approaches investigated in this thesis are marked with a star (*). The ideal-model is used as a reference. Detailed-mode simulation in which detailed semiconductor models are used is not considered because such level of sophistication is generally not necessary for system-level studies.

This thesis contributes to provide a solution to the challenges in implementing a computational efficient and accurate model that enables time-domain system-level simulation of dc-grids with 3p-DAB converters. The reduced complexity and computation time of the proposed GAM model allow the efficient validation of future system-level control schemes in dc-grids. By comparing the performance of the developed GAM model in terms of transient response, computation time and frequency response with alternative modeling techniques, it also provides a complete comprehensive understanding of the advantages and limitations of the GAM approach applied to the 3p-DAB. A large-scale test system is also implemented to validate the performance of the GAM model for fault current and transient recovery voltage analysis in a practical size system.

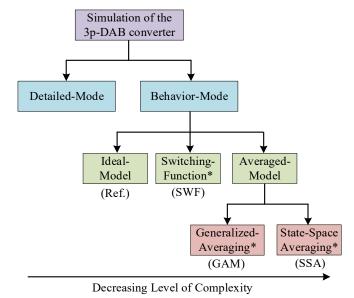


Figure 1.5 : Classification of approaches for time-domain simulation of the 3p-DAB

<u>Contribution #3</u>: Open-phase fault-tolerant operation of the three-phase dual active bridge converter [*paper conditionally accepted with major mandatory changes in IEEE Transactions on Power Electronics*]

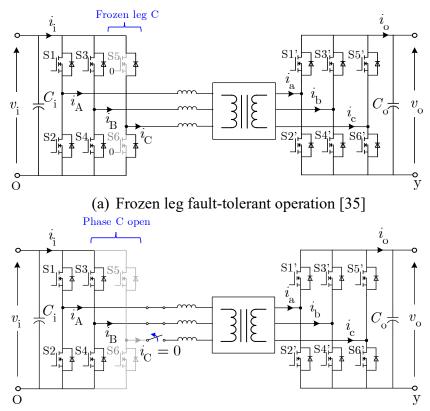
In ac-grids, ac transformers are well known for their high reliability even in the presence of temporary or permanent overload conditions [64]. In dc-grids, the overall grid reliability can be significantly affected by the reliability of individual power electronic converters. The reliability of power electronic systems is in fact negatively affected by the higher number of components, including more vulnerable parts such as semiconductors and electrolytic capacitors. Failure of one of the components or subsystems generally lead to shutdown of the whole converter unit because most converters are not built with redundancy [37], [65].

Other than thermal management [66], oversizing of components, redundancy of converters, and fault-tolerant (FT) methods are recognized methodologies to improve the reliability of converters. Among these approaches, oversizing of components is the most used in the industry. However, while oversizing components such as filter capacitors is generally not a problem other than a probable reduction in the power density, oversizing semiconductors can negatively affect the efficiency and increase the overall cost of the converter. Besides, while it may reduce the occurrence of failures, it does not mean that the converter can resume operation upon fault detection. Instead of oversizing components, the traditional redundancy approach consists in adding extra parallel converters or modules, and/or adding redundant cells inside the converter structure for rapid reconfiguration of the faulty converter section. The redundancy approach is an expensive solution. FT operation is recognized to be more cost-effective than the redundancy approach [36]. FT operation means that a fault in a component or subsystem does not cause the overall system to malfunction [67]. In other words, degraded operation under post-fault conditions implies that the converter can continue operating with reduced performance metrics [37].

Because of its higher number of switches, it would be fair to argue that the reliability of the 3p-DAB converter is reduced when compared to other IBDC topologies, including the 1p-DAB. However, it has been shown that the 3p-DAB converter can be operated in a frozen leg degraded-mode, i.e. with the two transistors of the same phase being deliberately opened by their gate driver internal protections, leaving their free-wheeling diodes self-commutated (Figure 1.6 a)) [35]. Because the free-wheeling diodes are left self-commutated, the analytical characterization of the

converter behavior in this degraded-mode is not trivial and leads to multiple cases to analyze which are functions of the converter voltage and loading conditions. The frozen leg operation of the 3p-DAB also leads to increased transformer current as well as a reduced power transfer capability for light and heavier load conditions. These loading conditions are characterized in [35] but are not generalized to all voltage and loading conditions. Moreover, due to the complexity in deriving an analytical model, the impacts on the ZVS capabilities, controller sizing, dynamic response, and stability are not validated.

In response to this challenge, the research contributions of this thesis are as follows. First, it is proposed to open the faulty-phase side such as it eliminates the interaction of the faulty-leg free-wheeling diodes (Figure 1.6 a)). As a result, it is possible to characterize the steady-state and transient operations of the 3p-DAB with one phase open for all voltage and loading conditions. It allows evaluating the major impacts of open-phase fault-tolerant operation as well as presenting key design considerations for a successful open-phase FT operation. Furthermore, the results also revealed that open-phase operation can provide increased power transfer capability and reduced circulating current as compared to frozen leg proposed in [35].



(b) Proposed open-phase fault-tolerant operation Figure 1.6 : Comparison of the fault-tolerant modes of the 3p-DAB

1.4 Methodology and limits

1.4.1 Methodology

The design and integration process of power electronic converters such as the 3p-DAB requires investigations with different abstraction levels [68]. The main objective of this thesis is to develop and validate accurate models for integration analyses of 3p-DAB converters in dc-grids. A hierarchical modeling, simulation, testing and analysis methodology is used. Time-domain simulation in EMTP and experimental results on a small-scale closed-loop EPC2022 gallium nitride (GaN)-based Y- Δ 3p-DAB converter are also provided. Four (4) different converter design parameter sets are used in this thesis. This is explained by the evolution of the project over time and the necessity to address comments in the publication process prior to the writing of this thesis. Their main parameters are summarized in Appendix A and additional details on the design and fabrication of the small-scale prototype are provided in Appendix B.

The methodology is summarized as follows:

- Steady-state analysis: A generalized phasor-domain method based on the first harmonic approximation (FHA) method [12] combined with Mordey's V-curve theory for synchronous machines [69] is first proposed for gaining understanding and physical insight. Then, the piecewise-linear modeling (PLM) approach [12], [24] is used for the determination of the parameters of the converter models, and for designing the small-scale prototype. PLM is also used for evaluating the converter operating point, the power transfer relationships, the ZVS boundaries, and for the sizing of the output capacitor. PLM is a necessary step for the development and implementation of small- and large-signal models.
- 2. Small-signal frequency-domain analysis: State-space averaging (SSA) and generalized state-space averaging (GSSA) small-signal models are derived for the calculation of all the open-loop transfer functions of the Y- Δ 3p-DAB converter (G_{vd} , G_{vg} , Z_D , Z_N , and Z_o). Both models are then compared and validated with time-domain simulations in EMTP. While GSSA is used to evaluate Z_D , a hybrid SSA and GSSA multi-input multi-output (MIMO) model is proposed to evaluate Z_N . The developed models are used for the design of the closed-loop controller, and for dynamic performance and stability assessments using MATLAB/Simulink.

- 3. Large-signal time-domain modeling and simulation: A generalized averaging model (GAM) of the Y-Δ 3p-DAB converter is developed, implemented and compared in closed-loop with the ideal, switching-function (SWF) and SSA methods applied to the 3p-DAB in EMTP. The performance of the GAM model is analyzed in terms of transient response, computation time and frequency response. A large-scale test system is also implemented to validate the performance of the GAM model for system-level transient fault current and transient recovery voltage analyses.
- 4. **Open-phase fault-tolerant analysis:** Steady-state analysis based on the PLM approach is performed to characterize the converter in open-phase operation and evaluate the impacts on the converter steady-state characteristics. Open-phase operation is also compared with frozen leg [35] and normal operations. Moreover, small-signal frequency-domain analysis using SSA and GSSA methodologies is performed to design a suitable closed-loop controller for both normal and open-phase operations as well as to assess the impacts on the converter stability and dynamic performance.

1.4.2 Limits

In this thesis, the converter under study is the Y- Δ 3p-DAB operated using single-phase-shift (SPS) control with a fixed switching frequency ω_s and a 180° (or 6-step) modulation scheme. The results are not extended to other emerging modulation methods. The converter is assumed lossless for steady-state, small-signal and large-signal analyses. Information on loss analysis for the 3p-DAB converter can be found in [68], [70]–[72]. The transformer magnetizing inductance and the presence of snubber capacitances are neglected. The switches are assumed ideal. The dead-time between the upper and lower switches is also neglected because it increases drastically the complexity in modeling the 3p-DAB converter [73]. The result is the assumption of an ideal transition of the current from the free-wheeling diodes to the transistors during the ZVS turn-on events. Furthermore, only forward operation and the Y- Δ transformer are considered but the generalization to other transformer connections is discussed. The load is assumed to be purely resistive.

1.5 Thesis outlines

The structure of this thesis is mainly oriented to follow the methodology and to expose the development specific to each of the contributions defined in section 1.3.

In Chapter 2 – Steady-State Analysis, steady-state modeling of the 3p-DAB converter is used for sizing and for providing a general understanding of the converter operating principles. It is a necessary step to small-signal (Chapter 3) and large-signal (Chapter 4) analyses.

In **Chapter 3** – **Small-Signal Frequency-Domain Analysis**, small-signal models of the 3p-DAB are developed, validated and implemented. They are used for closed-loop controller design, as well as small-signal stability assessment and dynamic performance analysis in MATLAB/Simulink.

In **Chapter 4 – Large-Signal Time-Domain Simulation**, a generalized average model (GAM) is developed, validated and implemented. It enables closed-loop system-level simulation of dc-grids with 3p-DAB converters in EMTP.

In Chapter 5 – Open-Phase Fault-Tolerant Operation, steady-state, small-signal and transient analyses are performed to enable the successful operation of 3p-DAB converters in open-phase fault-tolerant condition.

In Chapter 6 – Conclusions and Recommendations, the main conclusions of this thesis are presented along with recommendations for future area of research in modeling, simulation, and analysis of the 3p-DAB converter in both normal and fault-tolerant operations.

CHAPTER 2 STEADY-STATE ANALYSIS

In this chapter, steady-state modeling of the 3p-DAB converter is performed for supporting the selection of the parameters of the converter models as well as for assisting the selection of the components for the small-scale prototype. It also presents the main steps for the calculation of the converter operating point, the power transfer relationships, and the zero-voltage switching (ZVS) regions. This step is necessary for the development of small- and large-signal models as well as for the analysis of the converter in degraded-mode.

2.1 First harmonic approximation (FHA) in phasor-domain

2.1.1 Motivation

The steady-state analysis of the DAB is generally performed using the piecewise-linear modeling (PLM) approach. As it will be seen in section 2.2, this approach is a demanding task for 3p-DAB converters. To simplify the analysis, the 3p-DAB can also be analyzed using the first harmonic approximation (FHA) in the phasor-domain [12], [74]. It consists in replacing the input and output bridges by voltage sources at the switching frequency ω_s and the transformer by its equivalent leakage reactance X_s . Using this model, the power transfer relationship of the 3p-DAB is similar to the power-angle relationship of synchronous machines. This method can also be used to approximate the input and output bridge ZVS ranges.

In this thesis, a generalized FHA phasor-domain modeling method for steady-state analysis of 3p-DAB with single-phase-shift (SPS) control has been developed. A set of general and simplified equations has been developed and a useful diagram for first converter design has been developed in physical and per-unit quantities. The FHA method has been generalized for different transformer connections and validated using time-domain simulations.

The FHA method combined with the use of the developed diagram allows approximating the RMS and peak currents as well as the ZVS regions for given converter specifications. It also provides a means to understand the general operating principles of the 3p-DAB converter. Unlike PLM, the FHA method results in a single set of equations which does not depend on the transformer connection and the control phase-shift δ operating range.

2.1.2 Model development

The transformer is first replaced by its equivalent primary-referred leakage reactance $X_s = \omega_s L_s$. As seen in Figure 2.1, the voltages applied by the input and output bridges, v_{AN} and v'_{an} respectively, are modeled by equivalent phasor-domain voltage sources V_{AN} and V'_{an} at the switching frequency ω_s . The equivalent transformer current I_A obtained from the phasor-domain analysis is close to the time-domain current i_A which is almost sinusoidal owing to the three-phase structure. The resulting FHA phasor-domain circuit and phasor diagram are given in Figure 2.2.

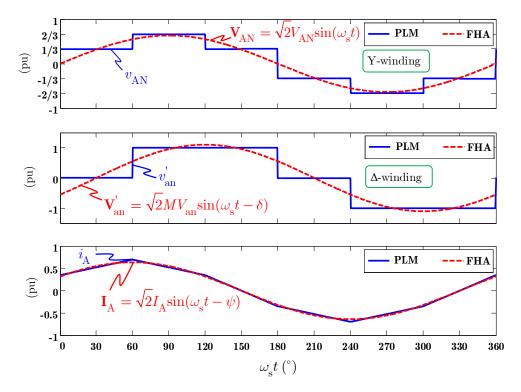
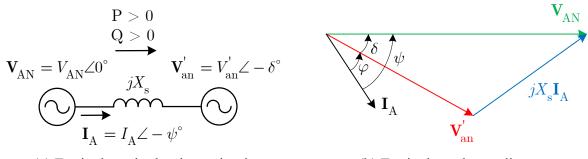


Figure 2.1 : Comparison of PLM and FHA waveforms for the Y- Δ transformer



(a) Equivalent single-phase circuit

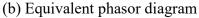


Figure 2.2 : FHA model in physical quantities

The phasor-diagram is defined in generating convention such that active and reactive powers are positive when flowing from \mathbf{V}_{AN} to \mathbf{V}_{an}' . The input voltage phasor \mathbf{V}_{AN} is taken as the reference, which implies that the modulator is synchronizing the output bridge with respect to the input bridge. \mathbf{V}_{AN} is comparable to the synchronous generator internal field excitation in the equivalent machine model. The output bridge voltage \mathbf{V}_{an}' is equivalent to the network voltage. The phase-shift δ is equivalent to the power angle of the synchronous machine such that, a positive value of δ means that the active power is transferred from \mathbf{V}_{AN} to \mathbf{V}_{an}' (forward operation). Conversely, a negative value of δ means that the active power is transferred from \mathbf{V}_{an}' to \mathbf{V}_{AN} (reverse operation). The angle φ is the angle between the transformer line current \mathbf{I}_A and the output bridge voltage \mathbf{V}_{an}' such that the value of $\cos(\varphi)$ is similar to the power factor of the synchronous machine with respect to the network. The angle ψ is the angle between the transformer line current \mathbf{I}_A and the input bridge voltage \mathbf{V}_{AN} . Based on these definitions, the active and reactive power equations for the input and output bridges are written as,

$$P_{\rm i} = 3V_{\rm AN}I_{\rm A}\cos(\psi), Q_{\rm i} = 3V_{\rm AN}I_{\rm A}\sin(\psi) \tag{2.1}$$

$$P_{\rm o} = 3V_{\rm an}^{'}I_{\rm A}\cos(\varphi), Q_{\rm o} = 3V_{\rm an}^{'}I_{\rm A}\sin(\varphi)$$
(2.2)

For the equivalent single-phase circuit of Figure 2.2 a), it is also well known that the power transfer equation (or power-angle equation) is given by,

$$P = P_{\rm i} = P_{\rm o} = \frac{3V_{\rm AN}V_{\rm an}}{X_{\rm s}}\sin(\delta)$$
(2.3)

Because both the voltage and the current are non-sinusoidal, it is expected that both the fundamental and harmonic components contribute to the active power flow. However, as the harmonic order increases, the equivalent reactance of the transformer also increases, which reduces the power transfer capability of the transformer at higher harmonics. This mainly explains why FHA can be used to evaluate the power transfer relationship of 3p-DAB converters in a similar way to the power-angle relationship of synchronous machines connected to an equivalent network at the fundamental frequency ω_s .

In the 3p-DAB, reactive current needs to circulate through the transistors free-wheeling diodes to provide ZVS at turn-on of the transistors. On the phasor diagram of Figure 2.2 b), it means that I_A

must lie between the input voltage \mathbf{V}_{AN} and the output voltage $\mathbf{V}_{an}^{'}$ [12]. In other words, \mathbf{I}_{A} must lag \mathbf{V}_{AN} for ZVS at the input bridge (see example in Figure 2.3), and similarly, \mathbf{I}_{A} must lead $\mathbf{V}_{an}^{'}$ for ZVS at the output bridge. Physically, it means that both bridges are providing reactive power to the transformer leakage reactance X_{s} . The equivalent phasor diagrams at the ZVS boundaries for the input and output bridges are given in Figure 2.4 a) and b) respectively. Mathematically, it means that $\psi = 0^{\circ}$ (i.e. $\cos(\psi) = 1$) at the ZVS boundary of the input bridge and $\varphi = 0^{\circ}$ (i.e. $\cos(\varphi) = 1$) at the ZVS boundary of the output bridge. Using the phasor diagrams of Figure 2.4 a) and b), the ZVS boundaries for both bridges are evaluated. For the input bridge it yields to,

$$I_{\rm A} = \frac{\sqrt{(V_{\rm an}^{'})^2 - (V_{\rm AN})^2}}{X_{\rm s}}$$
(2.4)

and for the output bridge it leads to,

$$I_{\rm A} = \frac{\sqrt{(V_{\rm AN})^2 - (V_{\rm an}')^2}}{X_{\rm s}}$$
(2.5)

As for the synchronous machine, the maximum power transfer in (2.3) occurs when $\delta = 90^{\circ}$. In this condition, the equivalent phasor diagram is shown in Figure 2.4 c), and the current I_A at the maximum power transfer boundary is evaluated as,

$$I_{\rm A} = \frac{\sqrt{(V_{\rm AN})^2 + (V_{\rm an}^{'})^2}}{X_{\rm s}}$$
(2.6)

Another useful relationship is the equation which relates the transformer current I_A to the input V_{AN} and output V_{an}' voltages for different phase-shift angles δ . It is given by,

$$I_{\rm A} = \frac{\sqrt{(V_{\rm AN} \cos(\delta) - V_{\rm an}^{'})^2 + (V_{\rm AN} \sin(\delta))^2}}{X_{\rm s}}$$
(2.7)

Finally, the equation which relates the transformer current I_A to the input V_{AN} and output V'_{an} voltages for different output power set-points P_o is established. This relationship is equivalent to Mordey's V-Curve for synchronous machines [69] which is widely used for design and testing of synchronous machines. It can be demonstrated that the resulting relationship for the 3p-DAB is given by,

$$I_{\rm A} = \frac{1}{3V_{\rm an}^{'}} \cdot \sqrt{\left[\frac{3V_{\rm an}^{'}}{X_{\rm s}} \left(\sqrt{(V_{\rm AN})^{2} - \left(\frac{P_{\rm o}X_{\rm s}}{3V_{\rm an}^{'}}\right)^{2} - V_{\rm an}^{'}}\right)\right]^{2} + (P_{\rm o})^{2}}$$
(2.8)

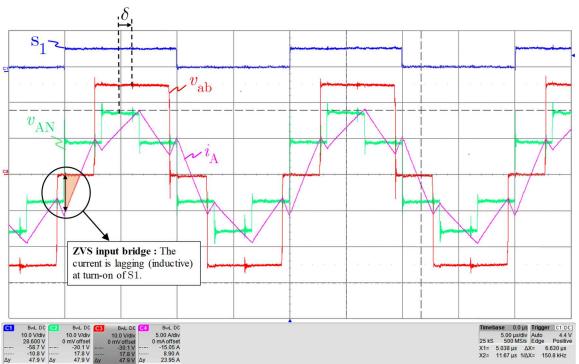


Figure 2.3 : Typical waveforms with ZVS in the input bridge (Y- Δ transformer)

Equations (2.4)–(2.8) need to be written in terms of the dc input and output voltages (V_i and V_o) to be useful for the analysis of 3p-DAB converters. Fourier decomposition of the waveforms for Y- and Δ -windings is used to obtain the amplitude of the phasors \mathbf{V}_{AN} and \mathbf{V}_{an}' in terms of V_i and V_o respectively,

$$V_{\rm AN} = \frac{2}{\sqrt{2\pi}} V_{\rm i} \tag{2.9}$$

$$V'_{\rm an} = M V_{\rm an} = M \frac{2}{\sqrt{2\pi}} V_{\rm o}$$
 (2.10)

The peak current can be approximated by,

$$I_{\rm A}^{\rm pk} = \sqrt{2}I_{\rm A} \tag{2.11}$$

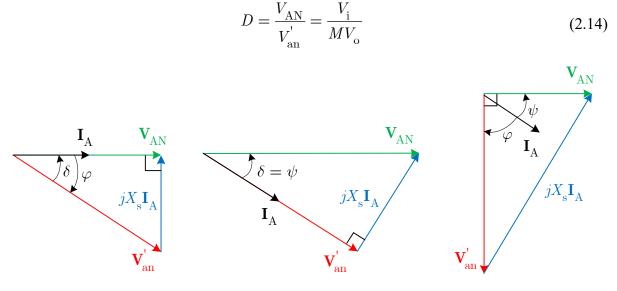
From the input bridge current, the output bridge current can also be obtained with,

$$I_{\rm a} = M I_{\rm A} \tag{2.12}$$

Moreover, the per-unit model is a generalization of the physical quantities model which allows more flexibility in the analysis of the 3p-DAB converter. The per-unit model is obtained from the physical quantities model by using the following base,

$$V_{\rm b} = V_{\rm an}', \quad I_{\rm b} = \frac{V_{\rm an}'}{X_{\rm s}}, \quad S_{\rm b} = V_{\rm b}I_{\rm b} = \frac{(V_{\rm an}')^2}{X_{\rm s}}, \quad Z_{\rm b} = X_{\rm s}$$
 (2.13)

For convenience, the ratio between the input and output voltages is also expressed as,



(a) At ZVS boundary input bridge

(b) At ZVS boundary output bridge

(c) At maximum power transfer

Figure 2.4 : Equivalent phasor diagrams

2.1.3 Model validation and analysis of the results

The equations of the generalized FHA model are validated for three different transformer connections (Y-Y, Δ - Δ , and Y- Δ) using time-domain simulations in EMTP. The results are shown in Figure 2.5 for the Y-Y and Δ - Δ transformers and Figure 2.6 for the Y- Δ transformer.

From the results obtained in Figure 2.5 and Figure 2.6, it is concluded that the FHA modeling method is accurate to evaluate the constant output power P_0 equation given in (2.8). It is also precise to determine the constant phase-shift δ equation given in (2.7) which also includes the maximum power transfer equation at $\delta = 90^{\circ}$ given in (2.6). The maximum error between the FHA model and time-domain simulations for the calculation of the RMS current is lower than 1%. For

(2.14)

the calculation of the peak current, the maximum error is 10%. This is explained by the fact that the current is not perfectly sinusoidal in the transformer.

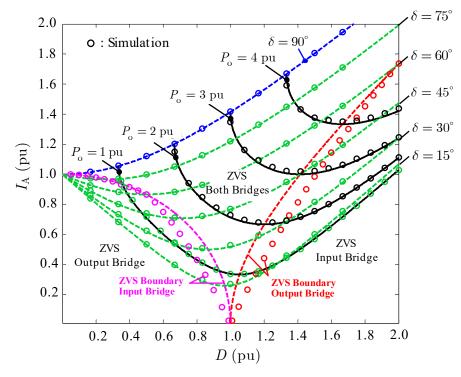


Figure 2.5 : Validation of the FHA design diagram for the Y-Y and Δ - Δ transformers

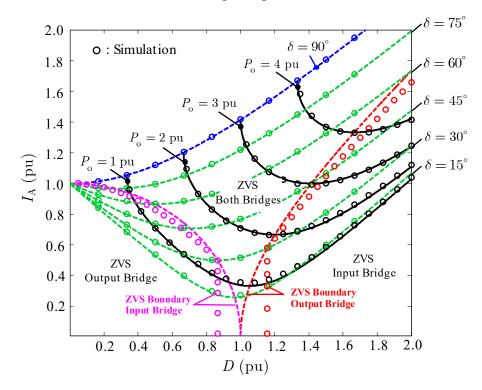


Figure 2.6 : Validation of the FHA design diagram for the Y-A transformer

It is also observed in Figure 2.5 and Figure 2.6 that the FHA modeling approach is less accurate to evaluate the input and output bridges ZVS boundaries at low output power. The maximum error between FHA and time-domain simulations is 15 % for the Y- Δ transformer, and 8 % for the Y-Y and Δ - Δ transformers. However, two important observations must be made. First, FHA leads to a conservative evaluation of the ZVS regions. This means that the ZVS regions evaluated using FHA are smaller than expected with time-domain simulations. Second, FHA gives a good estimation at high output power. The identification of the ZVS regions is generally more critical at high output power. The error between FHA and time-domain simulations is explained by the fact that harmonic distortion in the transformer current leads to errors in the evaluation of the current zero-crossing; ZVS occurs in the input bridge if $i_A < 0$ at turn-on of S1 as shown in Figure 2.3, and ZVS occurs in the output bridge if $i_A > 0$ at turn-on of S1'.

2.1.4 Application to the experimental prototype (model 4)

The FHA method combined with the use of the developed diagram allows approximating the RMS and peak currents as well as the ZVS regions for given converter specifications. It is an important step for the development of a hardware prototype and, to a lesser extent, to the implementation of simulation models. For this reason, the Y- Δ 3p-DAB prototype (model 4) is used in this section as a design example. For a constant output voltage $V_0 = 24$ V, the design diagrams for two different switching frequencies $f_s = 50$ kHz and $f_s = 100$ kHz are given in Figure 2.7. The testing zone represents the operating zone for which simulations and experiments have been conducted.

Important conclusions regarding the 3p-DAB operating principles can also be drawn from the results in Figure 2.7. First, it is possible to see that, for the same amount of active power transferred, minimum RMS current operation occurs at the output bridge ZVS boundary. High reactive circulating current increases the transformer losses and decreases the transformer utilization, such that, for the same output power, it is desirable to reduce the amount of reactive current while keeping ZVS at both bridges. Furthermore, from a design standpoint, the leakage inductance L_s should be selected such that maximum power transfer is possible for the entire voltage operating ranges. For instance, in Figure 2.7 a), for $V_o = 24$ V and $f_s = 50$ kHz, it is concluded that the converter can be operated at $P_o = 250$ W for the entire range of input voltage V_i (18 V to 30 V)

without reaching the maximum power transfer limit $\delta = 90^{\circ}$. It is not the case if $f_s = 100 \text{ kHz}$ as seen in Figure 2.7 b). This is because the power transfer capability of the 3p-DAB decreases as the switching frequency f_s increases and as the input voltage V_i decreases. This can be seen by rewriting (2.3) using (2.9) and (2.10),

$$P_{\rm o} = \frac{6\sqrt{3}}{\pi^2} \left(\frac{mV_{\rm i}V_{\rm o}}{\omega_{\rm s}L_{\rm s}} \right) \sin(\delta)$$
(2.15)

In Figure 2.7, it is also possible to perform a preliminary assessment of the ZVS boundaries for both bridges. First, it can be observed that, under low load conditions, ZVS is not guaranteed at the input bridge for low input voltage V_i , and at the output bridge for high input voltage V_i . At higher power set-points P_o , the ZVS range increases up to a point where the main limitation on the converter design becomes the maximum phase-shift angle $\delta = 90^\circ$. Also note that, for the same output power P_o , the region where both bridges are ZVS tends to increase at low power as the switching frequency f_s is increased.

The maximum RMS current expected for the hardware prototype is $I_A = 12.5 \text{ A} (I_A^{\text{pk}} = 17.7 \text{ A})$ in the input bridge, and $I_a = 10.8 \text{ A} (I_a^{\text{pk}} = 15.3 \text{ A})$ in the output bridge.

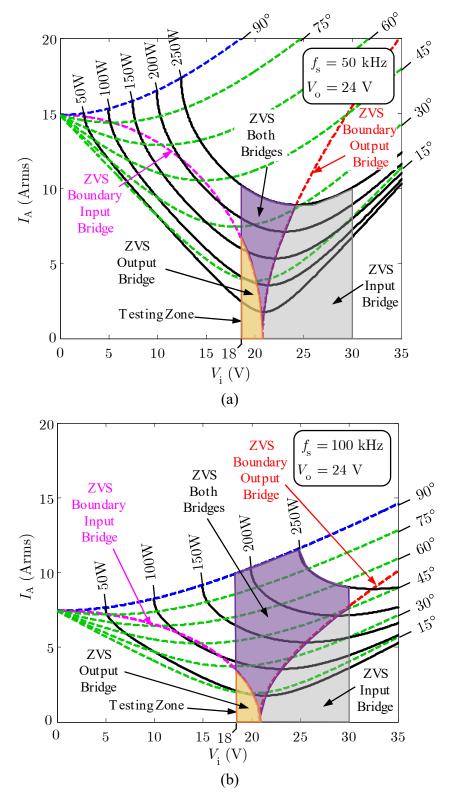


Figure 2.7 : Application of FHA to the Y- Δ 3p-DAB prototype (model 4)

2.2 Piecewise-linear modeling (PLM) in time-domain

2.2.1 Motivation

The steady-state analysis of DAB converters is often performed using the piecewise-linear (PLM) approach in time-domain. It consists in defining the transformer series inductor current equations for each operating mode over one half-cycle, i.e for the interval $\theta = [0, \pi]$. This approach is a demanding task for the 3p-DAB converter because it requires the derivation of the current equations for up to six modes, where the equations for each of the six modes also vary with the control phase-shift δ operating range, and the transformer connection (Y- Δ , Δ -Y or Y- Δ , and Δ - Δ).

The PLM approach allows evaluating with precision the ZVS boundaries, both the ripple currents and voltages of the filter capacitors, as well as the power transfer relationships. It also allows evaluating the RMS and peak currents through the transformer and the switches, but the resulting equations are more convenient using the FHA method presented in section 2.1.

2.2.2 Piecewise-linear current equations

The first step in applying the PLM approach for the 3p-DAB consists in solving the three-phase currents using the equivalent circuits shown in Figure 2.8.

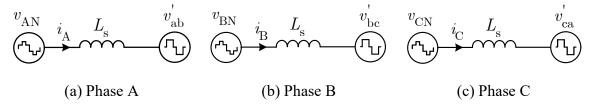


Figure 2.8 : Equivalent circuits for the application of PLM to the Y- Δ 3p-DAB

By making the assumption that constant dc voltages are applied on both sides of the series inductor for a given generic interval $\theta = [\theta_0, \theta_1]$, it is possible to calculate the current flowing through each phase using the following relationships,

$$i_{\rm A}(\theta) = \frac{1}{\omega_{\rm s}L_{\rm s}} \left(v_{\rm AN} - v_{\rm ab}' \right) \cdot \left(\theta - \theta_{\rm o} \right) + i_{\rm A}(\theta_{\rm o})$$
(2.16)

$$i_{\rm B}(\theta) = \frac{1}{\omega_{\rm s}L_{\rm s}} \left(v_{\rm BN} - v_{\rm bc}^{'} \right) \cdot \left(\theta - \theta_{\rm o} \right) + i_{\rm B}(\theta_{\rm o})$$
(2.17)

$$i_{\rm C}(\theta) = \frac{1}{\omega_{\rm s}L_{\rm s}} \left(v_{\rm CN} - v_{\rm ca}^{'} \right) \cdot \left(\theta - \theta_{\rm o} \right) + i_{\rm C}(\theta_{\rm o})$$
(2.18)

For the 3p-DAB, the derivation of the phase current equations must be done for two different ranges which depend on the control phase-shift δ and the transformer connection. For the specific case of the Y- Δ 3p-DAB, range 1 is defined as $0 \le \delta \le \pi/6$ and range 2 is defined as $\pi/6 \le \delta \le \pi/2$.

To apply (2.16)–(2.18), it is necessary to determine the primary voltages v_{AN} , v_{BN} and v_{CN} as functions of the dc input voltage v_i , and the primary-referred secondary voltages v'_{ab} , v'_{bc} and v'_{ca} in terms of the dc output voltage v_o . To do so, the equivalent circuit of Figure 2.9 is used.

The voltages applied by the input bridge on the primary-side are given by,

$$v_{\rm AN} = v_{\rm AO} - v_{\rm NO} \tag{2.19}$$

$$v_{\rm BN} = v_{\rm BO} - v_{\rm NO} \tag{2.20}$$

$$v_{\rm CN} = v_{\rm CO} - v_{\rm NO} \tag{2.21}$$

Assuming that the voltages v_{AN} , v_{BN} and v_{CN} are balanced, and adding (2.19) to (2.21) together, it leads to,

$$v_{\rm NO} = \frac{v_{\rm AO} + v_{\rm BO} + v_{\rm CO}}{3} \tag{2.22}$$

with,

$$v_{\rm AO} = s_1 v_{\rm i} \tag{2.23}$$

$$v_{\rm BO} = s_3 v_{\rm i} \tag{2.24}$$

$$v_{\rm CO} = s_5 v_{\rm i} \tag{2.25}$$

and s_1 , s_3 , and s_5 being the input bridge switching-functions which are defined in Appendix C. Inserting (2.23)–(2.25) into (2.22), it results that,

$$v_{\rm NO} = \frac{\left(s_1 + s_3 + s_5\right)v_{\rm i}}{3}$$
 (2.26)

From (2.26), it is concluded that the voltage $v_{\rm NO}$ on the primary-side only depends on the input bridge switching-functions and the converter input voltage. Furthermore, inserting (2.23)–(2.26) into (2.19)–(2.21) leads to,

$$v_{\rm AN} = \frac{\left(2s_1 - s_3 - s_5\right)v_{\rm i}}{3} \tag{2.27}$$

$$v_{\rm BN} = \frac{\left(2s_3 - s_1 - s_5\right)v_{\rm i}}{3}$$
 (2.28)

$$v_{\rm CN} = \frac{\left(2s_5 - s_1 - s_3\right)v_{\rm i}}{3} \tag{2.29}$$

A similar analysis can be carried out on the secondary-side which leads to,

$$v'_{ab} = mv_{ab} = m(s'_1 - s'_3)v_o$$
 (2.30)

$$v'_{bc} = mv_{bc} = m(s'_3 - s'_5)v_o$$
 (2.31)

$$v'_{ca} = mv_{ca} = m(s'_5 - s'_1)v_o$$
 (2.32)

with s'_1 , s'_3 and s'_5 being the output bridge switching-functions (Appendix C). Knowing the values of the switching-functions (either 0 or 1) for each mode over $\theta = [0, \pi]$, and making the assumption that the input and output voltages are both constant, i.e. that $v_i = V_i$ and $v_o = V_o$, the equations for the phase currents given by (2.16)–(2.18) can be solved using (2.27)–(2.32). The resulting current equations for each of the three phases are given in Appendix C. These equations are necessary to evaluate the power transfer relationships, the ZVS boundaries as well as the capacitor ripple current and voltage.

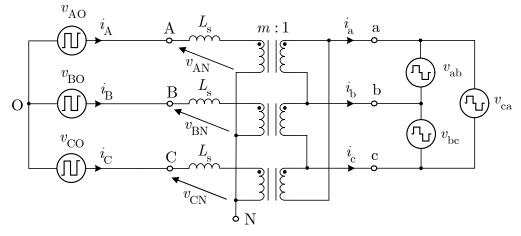


Figure 2.9 : Simplified representation of the Y- Δ 3p-DAB

2.2.3 Power transfer relationships

The procedure to derive the power transfer relationships of the 3p-DAB requires averaging the input or output currents which are expressed in terms of the primary transformer currents as well as the input and output bridge switching-functions given in Appendix C,

$$i_1 = s_1 i_A + s_3 i_B + s_5 i_C$$
 (2.33)

$$i_{o} = m \left[(\dot{s}_{1} - \dot{s}_{3})i_{A} + (\dot{s}_{3} - \dot{s}_{5})i_{B} + (\dot{s}_{5} - \dot{s}_{1})i_{C} \right]$$
(2.34)

Due to the symmetrical operation of the 3p-DAB, the analysis only needs to be performed over the interval $\theta = [0, \pi/3]$ i.e. for modes I and II. The analysis is performed here on the output current i_0 for the two different control phase-shift δ ranges (Figure 2.10). A similar analysis can be done on the input current i_1 .

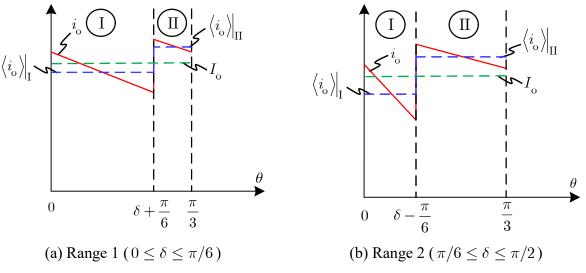


Figure 2.10 : Waveforms for the calculation of the average output current

The procedure to calculate the average output currents for mode I $\langle i_{o} \rangle |_{I}$ and mode II $\langle i_{o} \rangle |_{II}$ is given in Appendix C. Knowing the average output currents for these two modes, the total average output current I_{o} is calculated following the methodology presented in [54] and [75]. For range 1, I_{o} is calculated as follows,

$$I_{o} = \frac{3}{\pi} \left[\left(\delta + \frac{\pi}{6} \right) \cdot \left\langle i_{o} \right\rangle \right|_{\mathrm{I}} + \left(\frac{\pi}{6} - \delta \right) \cdot \left\langle i_{o} \right\rangle \right|_{\mathrm{II}} \right]$$
(2.35)

which, using (C.42) and (C.49), yields to,

$$I_{\rm o} = \frac{mV_{\rm i}}{\omega_{\rm s}L_{\rm s}}\delta \tag{2.36}$$

such that the output power is given by,

$$P_{\rm o} = V_{\rm o} I_{\rm o} = \frac{m V_{\rm i} V_{\rm o}}{\omega_{\rm s} L_{\rm s}} \delta$$
(2.37)

Applying a similar procedure in range 2, using (C.50) and (C.51), results in,

$$P_{\rm o} = \frac{mV_{\rm i}V_{\rm o}}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(\delta - \frac{\delta^2}{\pi} \right) - \frac{\pi}{24} \right]$$
(2.38)

Equations (2.37) and (2.38) are known to be the power transfer relationships of the Y- Δ 3p-DAB converter. They are necessary for the calculation of the operating point for the application of the state-space averaging (SSA) method (section 3.2) for the small-signal analysis of 3p-DAB converters. By comparing (2.37) and (2.38) with (2.15) which was obtained using FHA, it is concluded that, according to both the FHA and PLM models, the output power P_0 increases as mV_iV_0 increases and ω_sL_s decreases. It also shows that if m, V_i , V_0 , ω_s and L_s are all constants, then, the only way to regulate the output power of the 3p-DAB is by controlling the phase-shift δ . The power transfer relationships for the Y- Δ 3p-DAB prototype (model 4) are plotted in Figure 2.11. The FHA model is also compared with the PLM approach. It shows that FHA is also precise for determining the power transfer relationship.

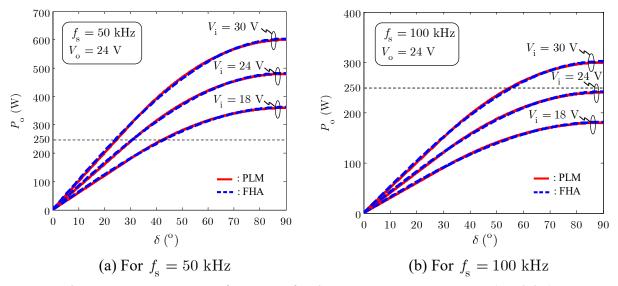


Figure 2.11 : Power transfer curves for the Y- Δ 3p-DAB prototype (model 4)

2.2.4 Zero-voltage switching (ZVS) boundaries

The 3p-DAB makes use of reactive circulating current through the transistor free-wheeling diodes of each bridge to provide ZVS. As mentioned before, ZVS occurs at the input bridge if $i_A < 0$ at turn-on of S1, and ZVS occurs in the output bridge if $i_a > 0$ at turn-on of S1'. The procedure to evaluate the theoretical ZVS boundaries for the 3p-DAB has been presented in [12] and [24]. In the case of the Y- Δ 3p-DAB, for the input bridge, it consists in solving the equation $i_A(0) = 0$ and, for the output bridge, it consists in solving the following equation,

$$i_{\rm a}(\delta + \frac{\pi}{6}) = m \left[i_{\rm A}(\delta + \frac{\pi}{6}) - i_{\rm C}(\delta + \frac{\pi}{6}) \right] = 0$$
 (2.39)

Using (C.6) and (C.24) for determining $i_A(0)$, the ZVS boundaries for the input bridge are obtained. For range 1 ($0 \le \delta \le \pi/6$), the input bridge is operated with ZVS when,

$$d > \frac{3}{2} \tag{2.40}$$

and, for range 2 ($\pi/6 \le \delta \le \pi/2$), the input bridge is operated with ZVS if,

$$d > \frac{9\pi - 18\delta}{4\pi} \tag{2.41}$$

with the voltage conversion ratio d defined as,

$$d = \frac{V_{\rm i}}{mV_{\rm o}} \tag{2.42}$$

Similarly, for range 1, it can be demonstrated that the output bridge is operated with ZVS for,

$$d < 2 \tag{2.43}$$

and for range 2, when,

$$d < \frac{4\pi}{3\pi - 6\delta} \tag{2.44}$$

From (2.40) and (2.43), it can be concluded that, in range 1, the ZVS regions do not depend on the control phase-shift δ , but do depend on the voltage conversion ratio *d*. In range 2, the ZVS regions are functions of both the control phase-shift δ , and the voltage conversion ratio *d*. The ZVS regions for the Y- Δ 3p-DAB are given in Figure 2.12. The testing zone for the prototype is also added for information. The results in Figure 2.12 show that it is advantageous from a soft-switching standpoint to operate the 3p-DAB with a voltage conversion ratio *d* ranging between 1.5 and 2. This is possible in applications where the tolerances on the input and output voltages are small.

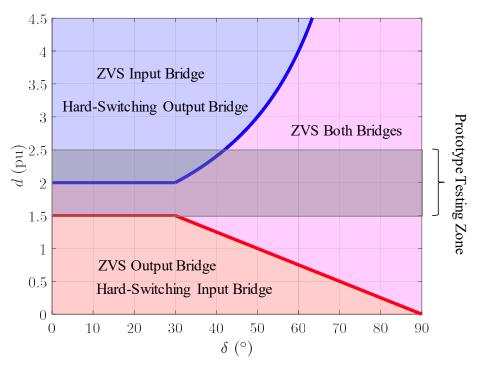


Figure 2.12 : ZVS regions for the Y-Δ 3p-DAB with the prototype (model 4) testing zone

2.2.5 Capacitor ripple current and voltage

In the previous sections, the ripples in the input and output voltages have been neglected, such that the ripples in the input and output currents were also neglected. In forward operation, the calculation of the ripple voltage Δv_o and the ripple current Δi_o is important for sizing the output filter capacitor C_o . The value of C_o is often selected based on the maximum ripple voltage Δv_o^{max} specified for a given application. The value of C_o also has an important impact on the dynamic performance as well as the stability of the converter such that Δv_o^{max} is not the only selection criterion. The influence of C_o on the converter dynamic will be discussed in Chapter 3.

The analysis of the ripple current is performed using the equivalent circuit of Figure 2.13 a). In the equivalent circuit of Figure 2.13 a), the 3p-DAB is modeled as a current source i_0 defined using the equations for i_0 provided in Appendix C. From Figure 2.13 b), it must also be noted that the ripples are theoretically at a frequency of $6f_s$. This is an important conclusion which will be used in the formulation of the small- and large-signal models in Chapter 3 and Chapter 4 respectively.

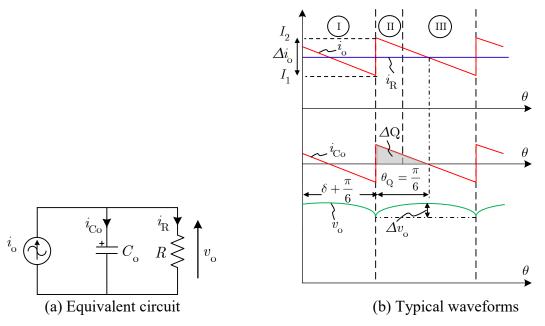


Figure 2.13 : Calculation of the output voltage and current ripples

The procedure will be presented here for range 1 ($0 \le \delta \le \pi/6$) because it leads to simpler analytical expressions. The procedure consists in first neglecting the ripple voltage Δv_0 for the evaluation of the ripple current Δi_0 . By analyzing the waveforms of Figure 2.13 b), the ripple current is defined as,

$$\Delta i_{\rm o} = \left| I_2 - I_1 \right| \tag{2.45}$$

From (C.40), it can be demonstrated that the current I_1 is given by,

$$I_1 = \frac{m}{9\omega_{\rm s}L_{\rm s}} \left[\left(9\delta + \frac{3\pi}{2}\right) V_{\rm i} - 3\pi m V_{\rm o} \right]$$
(2.46)

and, from the application of (C.43), it can be shown that the current I_2 is given by,

$$I_{2} = m \left(i_{\rm A} \left(\delta + \frac{\pi}{6} \right) - i_{\rm B} \left(\delta + \frac{\pi}{6} \right) \right)$$
(2.47)

where $i_A(\delta + \pi/6)$ is obtained using (C.1) and (C.6), and $i_B(\delta + \pi/6)$ is evaluated using (C.7) and (C.12). This leads to,

$$I_2 = \frac{m}{9\omega_{\rm s}L_{\rm s}} \left[\left(9\delta - \frac{3\pi}{2}\right) V_{\rm i} + 3\pi m V_{\rm o} \right]$$
(2.48)

Therefore, inserting (2.46) and (2.48) into (2.45), and simplifying, the ripple current is evaluated,

$$\Delta i_{\rm o} = \frac{m}{6f_{\rm s}L_{\rm s}} \Big| 2mV_{\rm o} - V_{\rm i} \Big|$$
(2.49)

From (2.49), it is concluded that the ripple current $\Delta i_{\rm o}$ is inversely proportional to the factor $6f_{\rm s}$ which corresponds, as mentioned before, to the ripple frequency. This is in fact one of the main advantages of the 3p-DAB over the 1p-DAB since it allows reducing the sizing of the output filter capacitor $C_{\rm o}$. Increasing the series inductance $L_{\rm s}$ can also reduce the ripple voltage but this is generally not used for this purpose since increasing $L_{\rm s}$ reduces the power transfer capability of the converter as seen in section 2.2.3.

The ripple current Δi_{o} is then used to evaluate the ripple voltage Δv_{o} . It requires analyzing the capacitor current i_{Co} waveform (Figure 2.13 b)). First, it is well known that,

$$\Delta \mathbf{Q} = i_{\mathbf{C}\mathbf{o}} \Delta t = C_{\mathbf{o}} \Delta v_{\mathbf{o}}$$
(2.50)

where ΔQ is the quantity of charge absorbed/released by the capacitor at each interval of duration $\theta_Q = 2\pi f_s T_Q = \pi/6$, as defined in Figure 2.13 b). This represents the area under the curve of i_{Co} , such that, Δv_o can be calculated by,

$$\Delta v_{\rm o} = \frac{\frac{\Delta i_{\rm o}}{2} \cdot T_{\rm Q}}{2C_{\rm o}} = \frac{\frac{\Delta i_{\rm o}}{2} \cdot \frac{1}{12f_{\rm s}}}{2C_{\rm o}} = \frac{\Delta i_{\rm o}}{48f_{\rm s}C_{\rm o}}$$
(2.51)

Inserting (2.49) into (2.51), it yields that,

$$\Delta v_{\rm o} = \frac{m}{288 f_{\rm s}^2 L_{\rm s} C_{\rm o}} \cdot \left| 2m V_{\rm o} - V_{\rm i} \right|$$
(2.52)

Knowing the maximum peak-to-peak ripple voltage specified Δv_o^{max} , the output capacitor C_o can be calculated as,

$$C_{\rm o} = \frac{m}{288 f_{\rm s}^2 L_{\rm s} \Delta v_{\rm o}^{\rm max}} \cdot \left| 2m V_{\rm o} - V_{\rm i} \right|$$

$$(2.53)$$

Equation (2.53) is important since it allows calculating the minimum capacitor value C_{o} required to meet the maximum peak-to-peak ripple voltage specified Δv_{o}^{\max} . In practice, Δv_{o}^{\max} is given in percentage of the output voltage V_{o} . A similar analysis can be performed for the 3p-DAB operated in range 2 ($\pi/6 \le \delta \le \pi/2$). The prototype (model 4) is used as a design example to demonstrate the use of the developed set of equations. For instance, for a maximum peak-to-peak ripple voltage specified $\Delta v_{\rm o}^{\rm max} = 0.2\%$, the results are shown in Figure 2.14 for $f_{\rm s} = 50$ kHz and $f_{\rm s} = 100$ kHz.

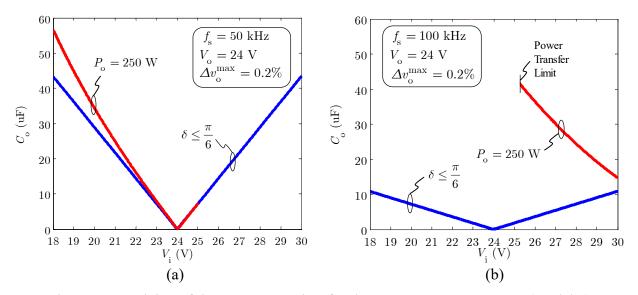


Figure 2.14 : Sizing of the output capacitor for the Y- Δ 3p-DAB prototype (model 4)

CHAPTER 3 SMALL-SIGNAL FREQUENCY-DOMAIN ANALYSIS

In this chapter, small-signal models are developed to determine all the transfer functions of the 3p-DAB converter. It allows designing a suitable controller for closed-loop operation as well as assessing dynamic performance and stability of the 3p-DAB within its environment. This step is particularly important to ensure that the addition of an input filter does not degrade the dynamic performance of the converter, or in worst-case lead to unstable conditions. This step is also a prerequisite for the implementation of the developed large-signal models in Chapter 4 since largesignal system-level analysis requires closed-loop modeling of converters.

3.1 Small-signal model of the 3p-DAB

Converter open-loop small-signal characteristics are generally defined by a set of standard transfer functions: the control-to-output transfer function G_{vd} , the input-to-output transfer function G_{vg} , the driving point Z_D and null driving point Z_N input impedances, and the output impedance Z_o [30]. The equivalent small-signal block diagram for output voltage regulation of the Y- Δ 3p-DAB converter in forward operation is shown in Figure 3.1.

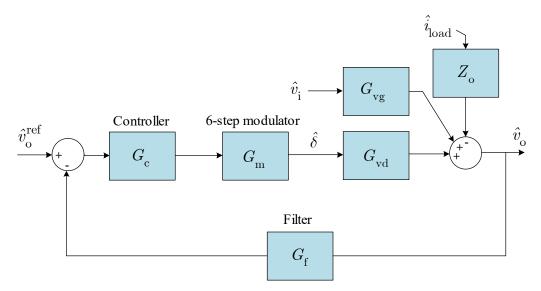


Figure 3.1 : Output voltage regulation small-signal block diagram (forward operation)

For the 3p-DAB, the open-loop transfer functions are defined as follows,

$$G_{\rm vd}(s) = \frac{\hat{v}_{\rm o}(s)}{\hat{\delta}(s)}\Big|_{\hat{v}_{\rm i}(s)=0}$$
(3.1)

$$G_{\rm vg}(s) = \frac{\hat{v}_{\rm o}(s)}{\hat{v}_{\rm i}(s)}\Big|_{\hat{\delta}(s)=0}$$
(3.2)

$$Z_{\rm D}(s) = \frac{\hat{v}_{\rm i}(s)}{\hat{i}_{\rm i}(s)} \bigg|_{\hat{\delta}(s)=0}$$
(3.3)

$$Z_{\rm N}(s) = \frac{\hat{v}_{\rm i}(s)}{\hat{i}_{\rm i}(s)} \bigg|_{\hat{v}_{\rm o}(s)=0}$$
(3.4)

$$Z_{o}(s) = -\frac{\hat{v}_{o}(s)}{\hat{i}_{load}(s)} \bigg|_{\hat{v}_{i}(s)=0, \ \hat{\delta}(s)=0}$$
(3.5)

In this chapter, the objective is to define each one of these transfer functions such that it allows evaluating the influence of the converter parameters on the small-signal characteristics of the 3p-DAB.

3.2 State-space averaging (SSA)

3.2.1 Model development

The general procedure to derive G_{vd} and G_{vg} for the Y-Y 3p-DAB using SSA has been presented in [54]. It assumes that the transformer currents are changing much faster than the dc voltages in addition to the assumption of balanced operation. This approach is used here to determine G_{vd} and G_{vg} for the Y- Δ 3p-DAB converter. It is also extended to derive Z_D , Z_N , and Z_o .

The idea behind the SSA modeling technique for 3p-DAB converters is to describe the converter behavior by the following dc-averaged and linearized state-space model,

$$\frac{d}{dt}\hat{\mathbf{x}}(t) = \hat{\mathbf{A}}\hat{\mathbf{x}}(t) + \hat{\mathbf{B}}\hat{\mathbf{u}}(t)$$
(3.6)

$$\hat{\mathbf{y}}(t) = \hat{\mathbf{C}}\hat{\mathbf{x}}(t) + \hat{\mathbf{D}}\hat{\mathbf{u}}(t)$$
(3.7)

where, to obtain all the desired transfer functions, it is proposed here to use more generalized set of vectors of state, inputs and outputs,

$$\hat{\mathbf{x}}(t) = \hat{v}_{o}(t), \quad \hat{\mathbf{u}}(t) = \begin{bmatrix} \hat{\delta}(t) \\ \hat{v}_{i}(t) \end{bmatrix}, \quad \hat{\mathbf{y}}(t) = \begin{bmatrix} \hat{v}_{o}(t) \\ \hat{i}_{i}(t) \end{bmatrix}$$
(3.8)

The objective is to determine the averaged and linearized state-space matrices in (3.6) and (3.7). For the 3p-DAB, there are two sets of state-space matrices depending on the operating range defined by the phase-shift δ . In both cases, the output capacitor $C_{\rm o}$ voltage equation is first given by,

$$C_{\rm o} \frac{\mathrm{d}v_{\rm o}(t)}{\mathrm{d}t} = i_{\rm o}(t) - \frac{v_{\rm o}(t)}{R}$$
(3.9)

where the output current i_0 is a function of the transformer currents i_A , i_B and i_C as defined in (2.34). By neglecting the input capacitor C_i , the input current i_i is also written in terms of the transformer currents i_A , i_B and i_C as defined in (2.33). It is simply written here as,

$$i_{\rm i}(t) = f(i_{\rm A}(t), i_{\rm B}(t), i_{\rm C}(t))$$
(3.10)

The input i_i and output i_o currents are then averaged as described in Appendix C. Note that, due to the symmetrical operation of the 3p-DAB, the procedure only needs to be performed for modes I and II. Then, as described in Appendix D, by inserting the average values of i_o and i_i obtained in Appendix C into (3.9) and (3.10) respectively, it results in the following set of intermediate statespace equations,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \mathbf{A}_{\mathrm{I}}v_{\mathrm{o}}(t) + \mathbf{B}_{\mathrm{I}}v_{\mathrm{i}}(t)$$
(3.11)

$$\frac{\mathrm{d}v_{o}(t)}{\mathrm{d}t} = \mathbf{A}_{\Pi}v_{o}(t) + \mathbf{B}_{\Pi}v_{i}(t)$$
(3.12)

$$i_{i}(t) = \mathbf{C}_{\mathrm{I}} v_{\mathrm{o}}(t) + \mathbf{D}_{\mathrm{I}} v_{i}(t)$$
(3.13)

$$\dot{u}_{i}(t) = \mathbf{C}_{II} v_{o}(t) + \mathbf{D}_{II} v_{i}(t)$$
(3.14)

with the intermediate matrices A_I , A_{II} , B_I , B_{II} , C_I , C_{II} , D_I and D_{II} being given for each range in Appendix D. As shown in Appendix D, the averaged state-space matrices A, B, C and D are then obtained by averaging their respective matrices for modes I and II. For range 1, it results in,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \frac{-1}{\underbrace{RC_{\mathrm{o}}}_{\mathbf{A}}} v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\delta(t)\right)}_{\mathbf{B}} v_{\mathrm{i}}(t)$$
(3.15)

$$i_{1}(t) = \underbrace{\frac{m\delta(t)}{\omega_{s}L_{s}}}_{\mathbf{C}} v_{o}(t)$$
(3.16)

and for range 2,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \frac{-1}{\underbrace{RC_{\mathrm{o}}}_{\mathbf{A}}} v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\left[\frac{3}{2}\left(\delta(t) - \frac{\delta(t)^{2}}{\pi}\right) - \frac{\pi}{24}\right]\right)}_{\mathbf{B}} v_{\mathrm{i}}(t)$$
(3.17)

$$i_{i}(t) = \underbrace{\frac{m}{\omega_{s}L_{s}} \left[\frac{3}{2} \left(\delta(t) - \frac{\delta^{2}(t)}{\pi} \right) - \frac{\pi}{24} \right]}_{\tilde{\mathbf{C}}} v_{o}(t)$$
(3.18)

Note that the matrix **D** is null in both cases. Taylor's linearization technique is used to linearize the non-linear terms in **B** and **C**. The operating point is calculated using (2.37) and (2.38). Then, changing into a small-signal model with the following perturbations,

$$\begin{split} \delta(t) &= \delta + \hat{\delta}(t) \ , \ v_{i}(t) = V_{i} + \hat{v}_{i}(t) \\ v_{o}(t) &= V_{o} + \hat{v}_{o}(t) \ , \ i_{i}(t) = I_{i} + \hat{i}_{i}(t) \end{split} \tag{3.19}$$

it results in the following small-signal state-space equations. For range 1,

$$\frac{\mathrm{d}\hat{v}_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{-1}{RC_{\mathrm{o}}}\right)}_{\hat{\mathbf{A}}} \hat{v}_{\mathrm{o}}(t) + \underbrace{\left[\frac{mV_{\mathrm{i}}}{\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\frac{m\delta}{\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right]}_{\hat{\mathbf{B}}} \begin{bmatrix}\hat{\delta}(t)\\\hat{v}_{i}(t)\end{bmatrix}$$
(3.20)

$$\begin{bmatrix} \hat{v}_{o}(t) \\ \hat{i}_{i}(t) \end{bmatrix} = \begin{bmatrix} 1 \\ \frac{m\delta}{\omega_{s}L_{s}} \end{bmatrix} \hat{v}_{o}(t) + \begin{bmatrix} 0 & 0 \\ \frac{mV_{o}}{\omega_{s}L_{s}} & 0 \\ \vdots \end{bmatrix} \begin{bmatrix} \hat{\delta}(t) \\ \hat{v}_{i}(t) \end{bmatrix}$$
(3.21)

and for range 2,

$$\frac{\mathrm{d}\hat{v}_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{-1}{RC_{\mathrm{o}}}\right)}_{\hat{\mathbf{A}}} \hat{v}_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right)}_{\hat{\mathbf{B}}} \left[\left[\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)\right] V_{\mathrm{i}} \left[\frac{3}{2} \left(\delta - \frac{\delta^{2}}{\pi}\right) - \frac{\pi}{24}\right]}_{\hat{\mathbf{B}}} \left[\hat{\delta}(t)\right] (3.22)$$

$$\begin{bmatrix} \hat{v}_{o}(t) \\ \hat{i}_{i}(t) \end{bmatrix} = \underbrace{\begin{bmatrix} 1 \\ \frac{m}{\omega_{s}L_{s}} \begin{bmatrix} 3 \\ 2 \end{bmatrix} \left(\delta - \frac{\delta^{2}}{\pi} \right) - \frac{\pi}{24} \end{bmatrix}}_{\hat{\mathbf{C}}} \hat{v}_{o}(t) + \underbrace{\begin{bmatrix} 0 & 0 \\ \frac{mV_{o}}{\omega_{s}L_{s}} \begin{bmatrix} 3 \\ 2 \end{bmatrix} \left(1 - \frac{2\delta}{\pi} \right) \end{bmatrix}}_{\hat{\mathbf{D}}} \begin{bmatrix} \hat{\delta}(t) \\ \hat{v}_{i}(t) \end{bmatrix}$$
(3.23)

3.2.2 Derivation of the converter transfer functions

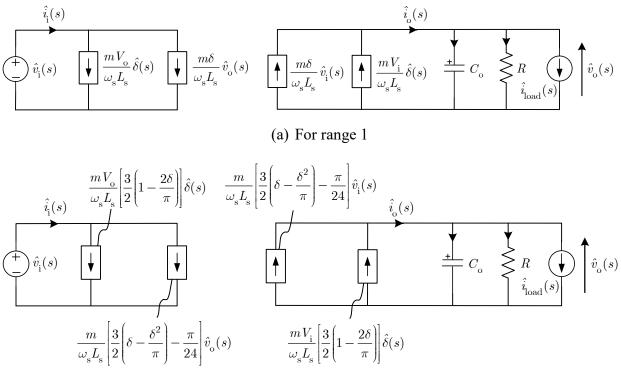
The SSA model allows drawing equivalent small-signal circuits for the 3p-DAB in an analogous way to simpler topologies. By transferring (3.20)–(3.23) into the frequency-domain, two equivalent circuits are defined in Figure 3.2. These equivalent circuits are then used to derive the converter transfer functions $G_{\rm vd}$, $G_{\rm vg}$, $Z_{\rm D}$, $Z_{\rm N}$ and $Z_{\rm o}$. The resulting transfer functions are given in Table 3.1. Furthermore, by posing that $\hat{v}_{\rm o}(s) \rightarrow 0$ in the equivalent circuits of Figure 3.2, the perturbation $\hat{\delta}(s)$ that needs to be injected for the evaluation of $Z_{\rm N}$ can be defined as follows. For range 1, it results in,

$$G_{\delta i}(s) = \frac{\hat{\delta}(s)}{\hat{v}_i(s)}\Big|_{\hat{v}_o(s)=0} = -\frac{\delta}{V_i}$$
(3.24)

and for range 2, it leads to,

$$G_{\delta i}(s) = \frac{\hat{\delta}(s)}{\hat{v}_{i}(s)}\Big|_{\hat{v}_{o}(s)=0} = -\frac{\frac{3}{2}\left[\delta - \frac{\delta^{2}}{\pi}\right] - \frac{\pi}{24}}{\left[\frac{3}{2}\left[1 - \frac{2\delta}{\pi}\right]\right]V_{i}}$$
(3.25)

Equations (3.24) and (3.25) will be used in section 3.4 for the application of the proposed hybrid averaging methodology. However, an important remark here is that the required perturbation $\hat{\delta}(s)$ on the command must be 180° phase-shifted from the injected perturbation \hat{v}_i on the input voltage in order to have $\hat{v}_0(s) \rightarrow 0$.



(b) For range 2

Figure 3.2 : Equivalent SSA small-signal circuits of the Y- Δ 3p-DAB converter

Range 1 ($0 \le \delta \le \pi/6$)		Range 2 ($\pi/6 \le \delta \le \pi/2$)	
$G_{\rm vd}(s) {=} \left(\frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}} \right) \frac{1}{sRC_{\rm o} + 1}$	(3.26)	$G_{\rm vd}(s) = \left(\frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)\right]\right) \frac{1}{sRC_{\rm o} + 1}$	(3.27)
$G_{\rm vg}(s) = \left(\frac{mR\delta}{\omega_{\rm s}L_{\rm s}}\right) \frac{1}{sRC_{\rm o}+1}$	(3.28)	$G_{\rm vg}(s) = \left(\frac{mR}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]\right) \frac{1}{sRC_{\rm o} + 1}$	(3.29)
$Z_{\rm D}(s) = \left(\frac{\omega_{\rm s}L_{\rm s}}{m\delta}\right)^2 \frac{sRC_{\rm o} + 1}{R}$	(3.30)	$Z_{\rm D}(s) = \frac{sRC_{\rm o} + 1}{R \bigg[\frac{m}{\omega_{\rm s}L_{\rm s}} \bigg[\frac{3}{2} \bigg[\delta - \frac{\delta^2}{\pi} \bigg] - \frac{\pi}{24} \bigg] \bigg]^2}$	(3.31)
$Z_{\rm N}(s) = -\frac{\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}\delta}$	(3.32)	$Z_{\rm N}(s) = -\left(\frac{\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}}\right) \frac{1}{\left[\frac{3}{2}\left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]}$	(3.33)
$Z_{\rm o}(s) = -\frac{R}{sRC_{\rm o}+1}$			

Table 3.1: Transfer functions of the Y- Δ 3p-DAB converter obtained with SSA

3.3 Generalized state-space averaging (GSSA)

3.3.1 Model development

The generalized state-space averaging (GSSA) [56] methodology is widely used for the smallsignal analysis of dc-dc converters. It is based on the concept of dynamic phasors which is well known for the analysis of power electronic converters and electric machines in power systems [76]–[80]. The concept of GSSA is to expand the converter state-variables and its switchingfunctions using a k^{th} coefficient Fourier series decomposition. The general procedure consists in 1) writing the converter differential equations using the state-space representation and the definition of the switching-functions, 2) deriving the index- k^{th} state-space equations by averaging the switching-functions and the state-space equations using Fourier series properties [53], [56], and 3) deriving the small-signal model by linearizing the equations around the operating point. Unlike SSA, it results in a single model that does not depend on the phase-shift δ operating range. The transformer connection is considered through the definition of the switching-functions, the calculation of the winding ratio, and the definition of the input and output currents.

3.3.1.1 Converter differential equations using state-space representation and the definition of the switching-functions

For the 3p-DAB, the output capacitor voltage v_0 and the transformer primary currents i_A , i_B , and i_C are taken here as state-variables. Unlike SSA which is only valid for the balanced case, GSSA is valid for the unbalanced case, i.e. that L_A , L_B and L_C are not necessarily identical. First, inserting (2.34) into (3.9), the output capacitor voltage equation is obtained,

$$\frac{\mathrm{d}v_{o}}{\mathrm{d}t} = \frac{m}{C_{o}} \Big[(\dot{s_{1}} - \dot{s_{3}})i_{A} + (\dot{s_{3}} - \dot{s_{5}})i_{B} + (\dot{s_{5}} - \dot{s_{1}})i_{C} \Big] - \frac{v_{o}}{RC_{o}}$$
(3.35)

Then, the differential equations for the three-phase currents are derived. For example, for phase A, the current flowing through inductor L_A is expressed as,

$$L_{\rm A} \frac{\mathrm{d}i_{\rm A}}{\mathrm{d}t} = v_{\rm AN} - v_{\rm ab}^{'} \tag{3.36}$$

with v_{AN} being dependent on the input bridge switching-functions as defined in (2.27) and v'_{ab} being dependent on the output bridge switching-functions as defined in (2.30). It yields to,

$$\frac{\mathrm{d}i_{\mathrm{A}}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{A}}} \left[\left(2\mathbf{s}_{1} - \mathbf{s}_{3} - \mathbf{s}_{5} \right) v_{\mathrm{i}} - 3m \left(\mathbf{s}_{1}^{'} - \mathbf{s}_{3}^{'} \right) v_{\mathrm{o}} \right]$$
(3.37)

Similarly, for phases B and C, it results in,

$$\frac{\mathrm{d}i_{\mathrm{B}}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{B}}} \left[\left(2s_{3} - s_{1} - s_{5} \right) v_{\mathrm{i}} - 3m \left(s_{3}^{'} - s_{5}^{'} \right) v_{\mathrm{o}} \right]$$
(3.38)

$$\frac{\mathrm{d}i_{\mathrm{C}}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{C}}} \left[\left(2\mathbf{s}_{5} - \mathbf{s}_{1} - \mathbf{s}_{3} \right) v_{\mathrm{i}} - 3m \left(\mathbf{s}_{5}^{'} - \mathbf{s}_{1}^{'} \right) v_{\mathrm{o}} \right]$$
(3.39)

Finally, it is also convenient to add the input current i_i equation given in (2.33) in the GSSA model formulation.

3.3.1.2 Deriving the index kth state-space equations by averaging the switching-functions and the state-space equations using Fourier series properties.

As previously mentioned, the main idea behind GSSA is to expand the converter state-variables and its switching-functions using a k^{th} coefficient Fourier series decomposition. It is assumed that only a few Fourier coefficients $X_k(t)$ provides an acceptable approximation of the original waveform, and that the coefficients vary slowly with time [33]. The k^{th} coefficients (or index-k phasors) of the generic variable x at time t are determined by the application of the Fourier series definition to continuous-time periodic signals with a sliding averaging window defined by the interval $[t - T_s, T_s]$. It results in the following definition,

$$X_{\mathbf{k}}(t) = \left\langle x \right\rangle_{\mathbf{k}}(t) = \frac{1}{T_{\mathbf{s}}} \int_{t-T_{\mathbf{s}}}^{t} x(\tau) \cdot e^{-jk\omega_{\mathbf{s}}\tau} d\tau$$
(3.40)

with $\langle x \rangle_k(t)$ being the complex Fourier coefficients which are functions of time t since the interval under consideration slides with respect to time. It results in a dynamic model of the dominant Fourier series coefficients in which the coefficients $\langle x \rangle_k(t)$ are the state-variables. The index k = 0 component is the dc-component and is expressed as $\langle x \rangle_0$. The other coefficients are the dynamic phasors $\langle x \rangle_k$ (for $k \neq 0$) and are complex values which are expressed as follows,

$$\langle x \rangle_{\mathbf{k}} = \langle x \rangle_{\mathbf{k}}^{\mathbf{R}} + j \langle x \rangle_{\mathbf{k}}^{\mathbf{I}} = \langle x \rangle_{\mathbf{k}}^{*}$$
 (3.41)

with $\langle x \rangle_k$, $\langle x \rangle_k^R$ and $\langle x \rangle_k^I$ being all functions of time and $\langle x \rangle_{-k}^*$ being the complex conjugate of $\langle x \rangle_k$. Similarly, the converter switching-functions are also expanded. For the generic switching-

function q(t), the index k = 0 component is expressed as $\langle q \rangle_0$, and the dynamic phasors $\langle q \rangle_k$ (for $k \neq 0$) are also complex values which are expressed as follows,

$$\langle q \rangle_{\mathbf{k}} = \langle q \rangle_{\mathbf{k}}^{\mathbf{R}} + j \langle q \rangle_{\mathbf{k}}^{\mathbf{I}} = \langle q \rangle_{\mathbf{k}}^{*}$$
 (3.42)

If only the index k = 0 and $k = \pm 1$ terms are considered, the resulting Fourier series decomposition for the generic variable x and the generic switching-function q are given by,

$$x(t) \approx \left\langle x \right\rangle_{0} + \left\langle x \right\rangle_{1} e^{-j\omega_{s}t} + \left\langle x \right\rangle_{1} e^{j\omega_{s}t}$$
(3.43)

$$q(t) \approx \left\langle q \right\rangle_{0} + \left\langle q \right\rangle_{-1} e^{-j\omega_{s}t} + \left\langle q \right\rangle_{1} e^{j\omega_{s}t}$$
(3.44)

To average the converter differential equations using the Fourier series decomposition, two important Fourier series properties are used [56].

Property 1 – *Differentiation with respect to time*

This property is formulated as follows,

$$\frac{d\langle x\rangle_{k}(t)}{dt} = \langle dx/dt\rangle_{k}(t) - jk\omega_{s}\langle x\rangle_{k}(t)$$
(3.45)

where $\langle dx/dt \rangle_k(t)$ represents the average of the differential of the state-variable x in the converter differential equations.

Property 2 – Average of product

This property is formulated as follows,

$$\left\langle qx\right\rangle_{\mathbf{k}} = \sum_{\mathbf{i}=-\infty}^{+\infty} \left\langle q\right\rangle_{\mathbf{k}\cdot\mathbf{i}} \left\langle x\right\rangle_{\mathbf{k}\cdot\mathbf{i}}$$
(3.46)

For k = 0, it results in,

$$\left\langle qx\right\rangle_{0} = \left\langle q\right\rangle_{0} \left\langle x\right\rangle_{0} + 2\left[\left\langle x\right\rangle_{1}^{\mathrm{R}} \left\langle q\right\rangle_{1}^{\mathrm{R}} + \left\langle x\right\rangle_{1}^{\mathrm{I}} \left\langle q\right\rangle_{1}^{\mathrm{I}}\right]$$
(3.47)

and, for k = 1, it results in,

$$\langle qx \rangle_{1}^{\mathrm{R}} = \langle q \rangle_{0} \langle x \rangle_{1}^{\mathrm{R}} + \langle x \rangle_{0} \langle q \rangle_{1}^{\mathrm{R}}$$
 (3.48)

$$\left\langle qx\right\rangle_{1}^{\mathbf{I}} = \left\langle q\right\rangle_{0} \left\langle x\right\rangle_{1}^{\mathbf{I}} + \left\langle x\right\rangle_{0} \left\langle q\right\rangle_{1}^{\mathbf{I}}$$
(3.49)

In the case of the 3p-DAB converter, the index $k = \pm 1$ terms of v_i and v_o are neglected here because it is assumed that the dynamic of v_i and v_o is much slower than the dynamic of the transformer currents. Mathematically, it means that,

$$\left\langle v_{i}\right\rangle_{1}^{R} = \left\langle v_{i}\right\rangle_{1}^{I} = \left\langle v_{o}\right\rangle_{1}^{R} = \left\langle v_{o}\right\rangle_{1}^{R} = \left\langle v_{o}\right\rangle_{1}^{I} = 0$$
(3.50)

The model only includes the index k=0 terms of $i_{\rm i}, v_{\rm i}$ and $v_{\rm o}$ such that,

$$v_{i} = \langle v_{i} \rangle_{0}, i_{i} = \langle i_{i} \rangle_{0}, v_{o} = \langle v_{o} \rangle_{0}$$
(3.51)

Moreover, the transformer currents are assumed to be purely ac, such that the model considers that the dc-components of i_A , i_B and i_C are zero,

$$\left\langle i_{\rm A} \right\rangle_0 = \left\langle i_{\rm B} \right\rangle_0 = \left\langle i_{\rm C} \right\rangle_0 = 0$$
 (3.52)

Since the three-phase structure leads to a ripple voltage at k = 6, which, by design, should be adequately filtered by C_0 as discussed in section 2.2.5, harmonics at k > 1 are neglected such that only the index $k = \pm 1$ terms (fundamental dynamic phasors) of i_A , i_B and i_C are considered here,

$$i_{\rm A} = \langle i_{\rm A} \rangle_{1} = \langle i_{\rm A} \rangle_{1}^{\rm R} + j \langle i_{\rm A} \rangle_{1}^{\rm I} = \langle i_{\rm A} \rangle_{-1}^{*}$$

$$i_{\rm B} = \langle i_{\rm B} \rangle_{1} = \langle i_{\rm B} \rangle_{1}^{\rm R} + j \langle i_{\rm B} \rangle_{1}^{\rm I} = \langle i_{\rm B} \rangle_{-1}^{*}$$

$$i_{\rm C} = \langle i_{\rm C} \rangle_{1} = \langle i_{\rm C} \rangle_{1}^{\rm R} + j \langle i_{\rm C} \rangle_{1}^{\rm I} = \langle i_{\rm C} \rangle_{-1}^{*}$$
(3.53)

Furthermore, for the input bridge, the index k = 0 component of the switching-function s_x is expressed as $\langle s_x \rangle_0$, and the $k = \pm 1$ terms of s_x are expressed as follows,

$$\langle \mathbf{s}_{\mathbf{x}} \rangle_{1} = \langle \mathbf{s}_{\mathbf{x}} \rangle_{1}^{\mathrm{R}} + j \langle \mathbf{s}_{\mathbf{x}} \rangle_{1}^{\mathrm{I}} = \langle \mathbf{s}_{\mathbf{x}} \rangle_{-1}^{*}$$
 (3.54)

with x = 1, 3, or 5 as defined in Appendix C.

Similarly, for the output bridge, the index k = 0 component of the switching-function s'_x is expressed as $\langle s'_x \rangle_0$, and the $k = \pm 1$ terms of s'_x are expressed as follows,

$$\left\langle \mathbf{s}_{\mathbf{x}}^{'}\right\rangle_{1} = \left\langle \mathbf{s}_{\mathbf{x}}^{'}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{\mathbf{x}}^{'}\right\rangle_{1}^{\mathrm{I}} = \left\langle \mathbf{s}_{\mathbf{x}}^{'}\right\rangle_{-1}^{*}$$
(3.55)

By taking the average on both sides of (3.35) and (2.33), and using property 1 (*differentiation with respect to time*), the index-0 averaged model equations are obtained,

$$\frac{\mathrm{d}\langle v_{\mathrm{o}}\rangle_{0}}{\mathrm{d}t} = \frac{m}{C_{\mathrm{o}}} \Big[\langle \mathbf{s}_{1}^{'}i_{\mathrm{A}}\rangle_{0} - \langle \mathbf{s}_{3}^{'}i_{\mathrm{A}}\rangle_{0} + \langle \mathbf{s}_{3}^{'}i_{\mathrm{B}}\rangle_{0} - \langle \mathbf{s}_{5}^{'}i_{\mathrm{B}}\rangle_{0} + \langle \mathbf{s}_{5}^{'}i_{\mathrm{C}}\rangle_{0} - \langle \mathbf{s}_{1}^{'}i_{\mathrm{C}}\rangle_{0} \Big] - \frac{\langle v_{\mathrm{o}}\rangle_{0}}{RC_{\mathrm{o}}} \quad (3.56)$$
$$\langle i_{1}\rangle_{0} = \langle \mathbf{s}_{1}i_{\mathrm{A}}\rangle_{0} + \langle \mathbf{s}_{3}i_{\mathrm{B}}\rangle_{0} + \langle \mathbf{s}_{5}i_{\mathrm{C}}\rangle_{0} \quad (3.57)$$

Similarly, by taking the average on both sides of (3.37)–(3.39), and using property 1 (*differentiation with respect to time*), the index-1 averaged model equations are derived,

$$\frac{\mathrm{d}\langle i_{\mathrm{A}}\rangle_{1}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{A}}} \left[2\langle \mathbf{s}_{1}v_{\mathrm{i}}\rangle_{1} - \langle \mathbf{s}_{3}v_{\mathrm{i}}\rangle_{1} - \langle \mathbf{s}_{5}v_{\mathrm{i}}\rangle_{1} - 3m\left(\langle \mathbf{s}_{1}^{'}v_{\mathrm{o}}\rangle_{1} - \langle \mathbf{s}_{3}^{'}v_{\mathrm{o}}\rangle_{1}\right) \right] - j\omega_{\mathrm{s}}\langle i_{\mathrm{A}}\rangle_{1} \quad (3.58)$$

$$\frac{\mathrm{d}\langle i_{\mathrm{B}}\rangle_{1}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{B}}} \left[2\left\langle \mathbf{s}_{3}v_{\mathrm{i}}\right\rangle_{1} - \left\langle \mathbf{s}_{1}v_{\mathrm{i}}\right\rangle_{1} - \left\langle \mathbf{s}_{5}v_{\mathrm{i}}\right\rangle_{1} - 3m\left(\left\langle \mathbf{s}_{3}v_{\mathrm{o}}\right\rangle_{1} - \left\langle \mathbf{s}_{5}v_{\mathrm{o}}\right\rangle_{1}\right) \right] - j\omega_{\mathrm{s}}\left\langle i_{\mathrm{B}}\right\rangle_{1} \quad (3.59)$$

$$\frac{\mathrm{d}\langle \mathbf{i}_{\mathrm{C}}\rangle_{1}}{\mathrm{d}t} = \frac{1}{3L_{\mathrm{C}}} \left[2\left\langle \mathbf{s}_{5}v_{\mathrm{i}}\right\rangle_{1} - \left\langle \mathbf{s}_{1}v_{\mathrm{i}}\right\rangle_{1} - \left\langle \mathbf{s}_{3}v_{\mathrm{i}}\right\rangle_{1} - 3m\left(\left\langle \mathbf{s}_{5}'v_{\mathrm{o}}\right\rangle_{1} - \left\langle \mathbf{s}_{1}'v_{\mathrm{o}}\right\rangle_{1}\right) \right] - j\omega_{\mathrm{s}}\left\langle i_{\mathrm{C}}\right\rangle_{1} \quad (3.60)$$

Applying property 2 (*average of product*) to (3.56)–(3.60) and writing the system of equations using the state-space formulation, it leads to the following GSSA model,

$$\frac{d\langle \mathbf{x}(t) \rangle}{dt} = \mathbf{A} \langle \mathbf{x}(t) \rangle + \mathbf{B} \langle \mathbf{u}(t) \rangle$$
(3.61)

$$\langle \mathbf{y}(t) \rangle = \mathbf{C} \langle \mathbf{x}(t) \rangle + \mathbf{D} \langle \mathbf{u}(t) \rangle$$
 (3.62)

with,

$$\left\langle \mathbf{x}(t) \right\rangle = \left[\left\langle v_{\mathrm{o}} \right\rangle_{0} \left\langle i_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{A}} \right\rangle_{1}^{\mathrm{I}} \left\langle i_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{B}} \right\rangle_{1}^{\mathrm{I}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{I}} \right]^{T}$$
(3.63)

$$\langle \mathbf{y}(t) \rangle = \left[\left\langle v_{o} \right\rangle_{0} \left\langle i_{i} \right\rangle_{0} \right]^{\mathrm{T}}, \left\langle \mathbf{u}(t) \right\rangle = \left[\left\langle \delta \right\rangle_{0} \left\langle v_{i} \right\rangle_{0} \right]^{\mathrm{T}}$$
 (3.64)

and,

To solve (3.61)–(3.69), the index k = 1 average of the input and output bridge switching-functions must be derived. From (3.40), it can be written that [53],

$$\left\langle \mathbf{s}_{\mathbf{x}} \right\rangle_{1}(t) = \frac{1}{T_{\mathbf{s}}} \int_{t-T_{\mathbf{s}}}^{t} \mathbf{s}_{\mathbf{x}}(\tau) \cdot \cos(\omega_{\mathbf{s}}\tau) d\tau - j \frac{1}{T_{\mathbf{s}}} \int_{t-T_{\mathbf{s}}}^{t} \mathbf{s}_{\mathbf{x}}(\tau) \cdot \sin(\omega_{\mathbf{s}}\tau) d\tau$$
(3.70)

$$\left\langle \mathbf{s}_{\mathbf{x}}^{'}\right\rangle_{1}(t) = \frac{1}{T_{\mathbf{s}}} \int_{t-T_{\mathbf{s}}}^{t} \mathbf{s}_{\mathbf{x}}^{'}(\tau) \cdot \cos(\omega_{\mathbf{s}}\tau) d\tau - j \frac{1}{T_{\mathbf{s}}} \int_{t-T_{\mathbf{s}}}^{t} \mathbf{s}_{\mathbf{x}}^{'}(\tau) \cdot \sin(\omega_{\mathbf{s}}\tau) d\tau$$
(3.71)

with x = 1, 3, or 5 as defined in Appendix C. For phase A, the switching-functions $s_1(\tau)$ and $s'_1(\tau)$ are defined as follows.

Using (3.72) to solve (3.70), it yields that,

$$\left\langle \mathbf{s}_{1}\right\rangle_{1} = \left\langle \mathbf{s}_{1}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{1}\right\rangle_{1}^{\mathrm{I}} = -j\frac{1}{\pi}$$
(3.74)

Similarly, using (3.73) to solve (3.71), it results in,

$$\left\langle \mathbf{s}_{1}^{'}\right\rangle_{1} = \left\langle \mathbf{s}_{1}^{'}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{1}^{'}\right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi}\sin\left(\left\langle \delta\right\rangle_{0} + \frac{7\pi}{6}\right) + j\frac{1}{\pi}\cos\left(\left\langle \delta\right\rangle_{0} + \frac{7\pi}{6}\right)$$
(3.75)

Note here that the average of the input bridge switching-functions does not depend on the control phase-shift $\langle \delta \rangle_0$ but it is the case for the output bridge. Similarly, for phase B, it is results in,

$$\left\langle \mathbf{s}_{3}\right\rangle_{1} = \left\langle \mathbf{s}_{3}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{3}\right\rangle_{1}^{\mathrm{I}} = -\frac{\sqrt{3}}{2\pi} + j\frac{1}{2\pi}$$
(3.76)

$$\left\langle \mathbf{\dot{s}}_{3}\right\rangle_{1} = \left\langle \mathbf{\dot{s}}_{3}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{\dot{s}}_{3}\right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi}\sin\left(\left\langle \delta\right\rangle_{0} + \frac{11\pi}{6}\right) + j\frac{1}{\pi}\cos\left(\left\langle \delta\right\rangle_{0} + \frac{11\pi}{6}\right)$$
(3.77)

and for phase C, it results in,

$$\left\langle \mathbf{s}_{5}\right\rangle_{1} = \left\langle \mathbf{s}_{5}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{5}\right\rangle_{1}^{\mathrm{I}} = \frac{\sqrt{3}}{2\pi} + j\frac{1}{2\pi}$$
(3.78)

$$\left\langle \mathbf{s}_{5}^{'}\right\rangle_{1} = \left\langle \mathbf{s}_{5}^{'}\right\rangle_{1}^{\mathrm{R}} + j\left\langle \mathbf{s}_{5}^{'}\right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi}\sin\left(\left\langle\delta\right\rangle_{0} + \frac{\pi}{2}\right) + j\frac{1}{\pi}\cos\left(\left\langle\delta\right\rangle_{0} + \frac{\pi}{2}\right)$$
(3.79)

Figure 3.3 shows the averaged switching-function dynamic phasors for the Y- Δ 3p-DAB. All the phasors are equal in magnitude (1/ π), but the output bridge switching-function phasors (red) are $\langle \delta \rangle_0 + \pi/6$ lagging the input bridge switching-function phasors (blue).

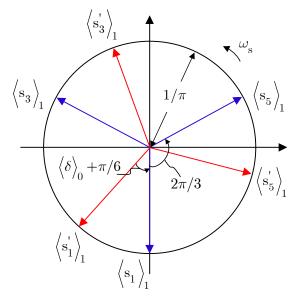


Figure 3.3 : Averaged switching-function dynamic phasors

3.3.1.3 Linearized small-signal model and operating point

To derive the small-signal model, it is necessary to linearize the system of equations defined by (3.61)-(3.69) and (3.74)-(3.79) by applying the following perturbations around the operating point,

$$\begin{split} \left\langle \delta \right\rangle_{0} &= \delta + \left\langle \hat{\delta} \right\rangle_{0}, \left\langle v_{o} \right\rangle_{0} = V_{o} + \left\langle \hat{v}_{o} \right\rangle_{0}, \\ \left\langle v_{i} \right\rangle_{0} &= V_{i} + \left\langle \hat{v}_{i} \right\rangle_{0}, \left\langle i_{i} \right\rangle_{0} = I_{i} + \left\langle \hat{i}_{i} \right\rangle_{0}, \\ \left\langle i_{A} \right\rangle_{1}^{R} &= \left\langle I_{A} \right\rangle_{1}^{R} + \left\langle \hat{i}_{A} \right\rangle_{1}^{R}, \left\langle i_{A} \right\rangle_{1}^{I} = \left\langle I_{A} \right\rangle_{1}^{I} + \left\langle \hat{i}_{A} \right\rangle_{1}^{I}, \\ \left\langle i_{B} \right\rangle_{1}^{R} &= \left\langle I_{B} \right\rangle_{1}^{R} + \left\langle \hat{i}_{B} \right\rangle_{1}^{R}, \left\langle i_{B} \right\rangle_{1}^{I} = \left\langle I_{B} \right\rangle_{1}^{I} + \left\langle \hat{i}_{B} \right\rangle_{1}^{I}, \\ \left\langle i_{C} \right\rangle_{1}^{R} &= \left\langle I_{C} \right\rangle_{1}^{R} + \left\langle \hat{i}_{C} \right\rangle_{1}^{R}, \left\langle i_{C} \right\rangle_{1}^{I} = \left\langle I_{C} \right\rangle_{1}^{I} + \left\langle \hat{i}_{C} \right\rangle_{1}^{I} \end{split}$$
(3.80)

$$\mathbf{X} = \begin{bmatrix} V_{\mathrm{o}} & \left\langle I_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} & \left\langle I_{\mathrm{A}} \right\rangle_{1}^{\mathrm{I}} & \left\langle I_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} & \left\langle I_{\mathrm{B}} \right\rangle_{1}^{\mathrm{I}} & \left\langle I_{\mathrm{C}} \right\rangle_{1}^{\mathrm{R}} & \left\langle I_{\mathrm{C}} \right\rangle_{1}^{\mathrm{I}} \end{bmatrix}^{\mathrm{T}}$$
(3.81)

and it is obtained by solving the following equation,

$$\mathbf{X} = -\mathbf{A}^{-1}\mathbf{B}\mathbf{U} \tag{3.82}$$

with **A** and **B** being given in (3.65) and (3.67) respectively, and $\mathbf{U} = \begin{bmatrix} 0 & V_i \end{bmatrix}^T$.

It is also proposed here to calculate the operating point δ of the control phase-shift by solving (2.37) or (2.38).

The main steps to linearize the system of equations are summarized in Appendix E. The result is a *k*-dependent averaged small-signal model written as,

$$\frac{d\left\langle \hat{\mathbf{x}}(t)\right\rangle}{dt} = \hat{\mathbf{A}}\left\langle \hat{\mathbf{x}}(t)\right\rangle + \hat{\mathbf{B}}\left\langle \hat{\mathbf{u}}(t)\right\rangle$$
(3.83)

$$\left\langle \hat{\mathbf{y}}(t) \right\rangle = \hat{\mathbf{C}} \left\langle \hat{\mathbf{x}}(t) \right\rangle + \hat{\mathbf{D}} \left\langle \hat{\mathbf{u}}(t) \right\rangle$$
 (3.84)

with the following averaged state-variable vector,

$$\left\langle \hat{\mathbf{x}} \right\rangle = \left[\left\langle \hat{v}_{o} \right\rangle_{0} \left\langle \hat{i}_{A} \right\rangle_{1}^{R} \left\langle \hat{i}_{A} \right\rangle_{1}^{I} \left\langle \hat{i}_{B} \right\rangle_{1}^{R} \left\langle \hat{i}_{B} \right\rangle_{1}^{R} \left\langle \hat{i}_{C} \right\rangle_{1}^{R} \left\langle \hat{i}_{C} \right\rangle_{1}^{R} \left\langle \hat{i}_{C} \right\rangle_{1}^{I} \right]^{T}$$
(3.85)

and the following vectors of input and output,

$$\left\langle \hat{\mathbf{u}} \right\rangle = \left[\left\langle \hat{\delta} \right\rangle_0 \left\langle \hat{v}_i \right\rangle_0 \right]^T, \ \left\langle \hat{\mathbf{y}} \right\rangle = \left[\left\langle \hat{v}_o \right\rangle_0 \left\langle \hat{i}_i \right\rangle_0 \right]^T$$
(3.86)

The averaged state-space matrices are given by,

$$\hat{\mathbf{A}} = \begin{bmatrix} \frac{-1}{RC_{o}} & A_{12} & A_{13} & A_{14} & A_{15} & A_{16} & A_{17} \\ A_{21} & 0 & \omega_{s} & 0 & 0 & 0 & 0 \\ A_{31} & -\omega_{s} & 0 & 0 & 0 & 0 & 0 \\ A_{41} & 0 & 0 & 0 & \omega_{s} & 0 & 0 \\ A_{51} & 0 & 0 & -\omega_{s} & 0 & 0 & 0 \\ A_{61} & 0 & 0 & 0 & 0 & \omega_{s} \\ A_{71} & 0 & 0 & 0 & 0 & -\omega_{s} & 0 \end{bmatrix}$$
(3.87)

$$\hat{\mathbf{B}} = \begin{bmatrix} B_{11} & B_{12} & B_{13} & B_{14} & B_{15} & B_{16} & B_{17} \\ 0 & 0 & \frac{-1}{\pi L_{\rm A}} & \frac{-\sqrt{3}}{2\pi L_{\rm B}} & \frac{1}{2\pi L_{\rm B}} & \frac{\sqrt{3}}{2\pi L_{\rm C}} & \frac{1}{2\pi L_{\rm C}} \end{bmatrix}^{T}$$
(3.88)

$$\hat{\mathbf{C}} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & -\frac{2}{\pi} & -\frac{\sqrt{3}}{\pi} & \frac{1}{\pi} & \frac{\sqrt{3}}{\pi} & \frac{1}{\pi} \end{bmatrix}$$
(3.89)
$$\hat{\mathbf{D}} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix}$$
(3.90)

with,

$$\begin{split} A_{12} &= \frac{2}{C_{o}} S_{1}, A_{13} = \frac{2}{C_{o}} S_{2}, A_{14} = \frac{2}{C_{o}} S_{3}, \\ A_{15} &= \frac{2}{C_{o}} S_{4}, A_{16} = \frac{2}{C_{o}} S_{5}, A_{17} = \frac{2}{C_{o}} S_{6} \\ A_{21} &= -\frac{1}{L_{A}} S_{1}, A_{31} = -\frac{1}{L_{A}} S_{2}, A_{41} = -\frac{1}{L_{B}} S_{3} \\ A_{51} &= -\frac{1}{L_{B}} S_{4}, A_{61} = -\frac{1}{L_{C}} S_{5}, A_{71} = -\frac{1}{L_{C}} S_{6} \\ B_{12} &= -\frac{V_{o}}{L_{A}} S_{2}, B_{13} = \frac{V_{o}}{L_{A}} S_{1}, B_{14} = -\frac{V_{o}}{L_{B}} S_{4} \\ B_{15} &= \frac{V_{o}}{L_{B}} S_{3}, B_{16} = -\frac{V_{o}}{L_{C}} S_{6}, B_{17} = \frac{V_{o}}{L_{C}} S_{5} \\ \end{split}$$
(3.91)

$$B_{11} = \frac{2}{C_0} \sum_{n=1}^{0} B_{11}(n)$$
(3.94)

where the $B_{11}(n)$ terms are defined by,

$$\begin{split} B_{11}(1) &= S_2 \left\langle I_{\rm A} \right\rangle_1^{\rm R}, B_{11}(2) = -S_1 \left\langle I_{\rm A} \right\rangle_1^{\rm I}, \\ B_{11}(3) &= S_4 \left\langle I_{\rm B} \right\rangle_1^{\rm R}, B_{11}(4) = -S_3 \left\langle I_{\rm B} \right\rangle_1^{\rm I} \\ B_{11}(5) &= S_6 \left\langle I_{\rm C} \right\rangle_1^{\rm R}, B_{11}(6) = -S_5 \left\langle I_{\rm C} \right\rangle_1^{\rm I} \end{split}$$
(3.95)

and the S_1 to S_6 terms are defined by,

$$\begin{split} S_1 &= \frac{m}{\pi} \left[\sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right) \right] \\ S_2 &= \frac{m}{\pi} \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right) \right] \\ S_3 &= \frac{m}{\pi} \left[\sin\left(\delta + \frac{11\pi}{6}\right) - \sin\left(\delta + \frac{\pi}{2}\right) \right] \\ S_4 &= \frac{m}{\pi} \left[\cos\left(\delta + \frac{11\pi}{6}\right) - \cos\left(\delta + \frac{\pi}{2}\right) \right] \\ S_5 &= \frac{m}{\pi} \left[\sin\left(\delta + \frac{\pi}{2}\right) - \sin\left(\delta + \frac{7\pi}{6}\right) \right] \\ S_6 &= \frac{m}{\pi} \left[\cos\left(\delta + \frac{\pi}{2}\right) - \cos\left(\delta + \frac{7\pi}{6}\right) \right] \end{split}$$
(3.96)

3.4 Hybrid averaging and transfer functions evaluation

The system of equations defined by the GSSA model (3.83)–(3.96) is proposed to be combined with (3.24) and (3.25) from the SSA model, such that, the system is represented as a coupled multiinput multi-output (MIMO) system in the frequency-domain (Figure 3.4). This representation is called "hybrid" because it combines both the SSA and GSSA models. For the evaluation of the transfer function Z_N , the SSA model is used to approximate the required perturbation $\langle \hat{\delta}(s) \rangle_0$ to be injected in the GSSA model to impose $\langle \hat{v}_0(s) \rangle_0 \to 0$.

The general procedure to obtain all the transfer functions is decomposed into four (4) steps:

 The GSSA model (3.83)–(3.96) is solved using MATLAB to obtain the transfer functions defined in (3.97), i.e. G_{vd}, G_{vg}, G_{id}, and Z_D.

$$\begin{bmatrix} \left\langle \hat{v}_{o}(s) \right\rangle_{0} \\ \left\langle \hat{i}_{i}(s) \right\rangle_{0} \end{bmatrix} = \begin{bmatrix} G_{vd}(s) & G_{vg}(s) \\ G_{id}(s) & 1 \neq Z_{D}(s) \end{bmatrix} \begin{bmatrix} \left\langle \hat{\delta}(s) \right\rangle_{0} \\ \left\langle \hat{v}_{i}(s) \right\rangle_{0} \end{bmatrix}$$
(3.97)

- 2) The SSA model is solved to obtain the transfer functions $G_{\delta i}$ defined by (3.24) or (3.25), depending on the operating range of the control phase-shift δ . Note that, the SSA model is also used to obtain Z_0 directly.
- Knowing all the transfer functions in Figure 3.4, the MIMO system is implemented in MATLAB/Simulink. The GSSA part is implemented as defined in (3.97).
- 4) The transfer function Z_N is evaluated by solving the hybrid system represented in Figure
 3.4 using the Linear Analysis tool of MATLAB/Simulink.

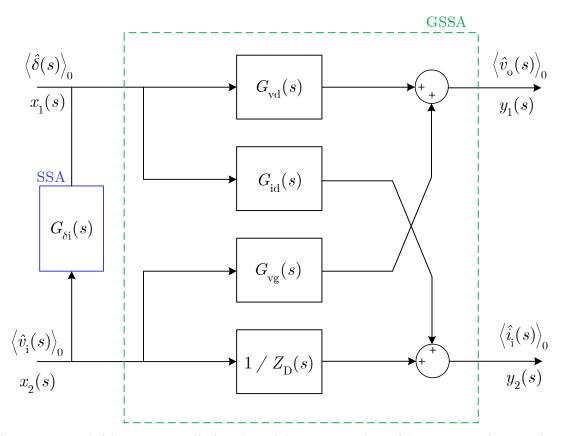


Figure 3.4 : Hybrid MIMO small-signal model representation of the 3p-DAB in open-loop

3.5 EMTP validation of the SSA, GSSA, and hybrid models

3.5.1 General procedure

The SSA, GSSA and hybrid models are validated with time-domain simulations in EMTP. The equivalent circuits for the validation of all the transfer functions are shown in Figure 3.5. The injected perturbations are defined as follows,

$$\hat{v}_{i}(t) = v_{\text{pert}} \cos(2\pi f_{\text{pert}} t)$$
(3.98)

$$\hat{\delta}(t) = \delta_{\text{pert}} \cos(2\pi f_{\text{pert}} t + \theta_{\text{pert}})$$
(3.99)

$$\hat{i}_{\text{load}}(t) = i_{\text{pert}} \cos(2\pi f_{\text{pert}} t)$$
(3.100)

The amplitude of the perturbations is kept small around the operating point (< 5%). The frequency of the perturbations is varied between 50 Hz to 48 kHz, for a switching frequency $f_s = 50$ kHz. The simulation time-step is fixed to $\Delta t = 0.01 \,\mu s$. The simulation parameters for model 1 (Appendix A) are used, and the results are shown in Figure 3.6.

For the validation of $G_{\rm vd}$ in Figure 3.5 a), a perturbation $\hat{\delta}(t)$ of amplitude $\delta_{\rm pert}$ on the control phase-shift $\delta(t)$ is applied with $\theta_{\rm pert} = 0^{\circ}$. The output voltage $v_{\rm o}(t)$ is measured, and its magnitude at the perturbation frequency $f_{\rm pert}$, noted here $v_{\rm mes}$, is extracted by Fast Fourier Transform (FFT) analysis using the ScopeView tool in EMTP. The gain of the transfer function $G_{\rm vd}$ is obtained by calculating,

$$\left|G_{\rm vd}\right| = 20\log\left(\frac{v_{\rm mes}}{\delta_{\rm pert}}\right)$$
 (3.101)

For the validation of $G_{\rm vg}$ and $Z_{\rm D}$ in Figure 3.5 b), a perturbation $\hat{v}_{\rm i}(t)$ of amplitude $v_{\rm pert}$ on the input voltage $v_{\rm i}(t)$ is applied, and the output voltage $v_{\rm o}(t)$ and the input current $i_{\rm i}(t)$ are both measured. The magnitude of both $v_{\rm o}(t)$ and $i_{\rm i}(t)$ at the perturbation frequency $f_{\rm pert}$, noted here $v_{\rm mes}$ and $i_{\rm mes}$ respectively, are also extracted by FFT analysis. The gain of the transfer functions $G_{\rm vg}$ and $Z_{\rm D}$ is obtained by calculating,

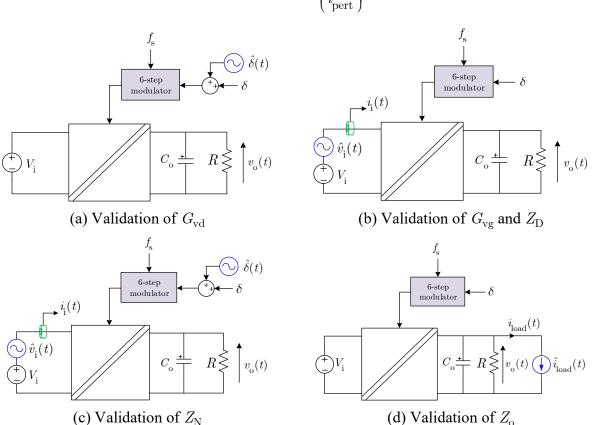
$$\left|G_{\rm vg}\right| = 20\log\left(\frac{v_{\rm mes}}{v_{\rm pert}}\right)$$
(3.102)

$$\left|Z_{\rm D}\right| = 20\log\left(\frac{v_{\rm pert}}{i_{\rm mes}}\right)$$
(3.103)

For the validation of $Z_{\rm N}$ in Figure 3.5 c), a perturbation $\hat{v}_{\rm i}(t)$ of amplitude $v_{\rm pert}$ on the input voltage $v_{\rm i}(t)$ is applied while a perturbation $\hat{\delta}(t)$ of amplitude $\delta_{\rm pert}$ on the control phase-shift $\delta(t)$ is applied with $\theta_{\rm pert} = 180^{\circ}$ as required by (3.24) and (3.25). The goal of the perturbation $\hat{\delta}(t)$ is to maintain $\hat{v}_{\rm o} = 0$. The input current $i_{\rm i}(t)$ is measured, and its magnitude at the perturbation frequency $f_{\rm pert}$, noted here $i_{\rm mes}$, is also obtained by FFT analysis. The gain of the transfer function $Z_{\rm N}$ is obtained by calculating,

$$\left|Z_{\rm N}\right| = 20 \log\left(\frac{v_{\rm pert}}{i_{\rm mes}}\right)$$
(3.104)

Finally, for the validation of $Z_{\rm o}$ in Figure 3.5 d), a perturbation $\hat{i}_{\rm load}(t)$ of amplitude $i_{\rm pert}$ on the load current $i_{\rm load}(t)$ is applied. The output voltage $v_{\rm o}(t)$ is measured, and its magnitude at the perturbation frequency $f_{\rm pert}$, noted here $v_{\rm mes}$, is also extracted by FFT analysis. The gain of the transfer function $Z_{\rm o}$ is obtained by calculating,



 $\left|Z_{\rm o}\right| = 20\log\left(\frac{v_{\rm mes}}{i_{\rm pert}}\right)$ (3.105)

Figure 3.5 : Equivalent circuits for the validation of the open-loop transfer functions

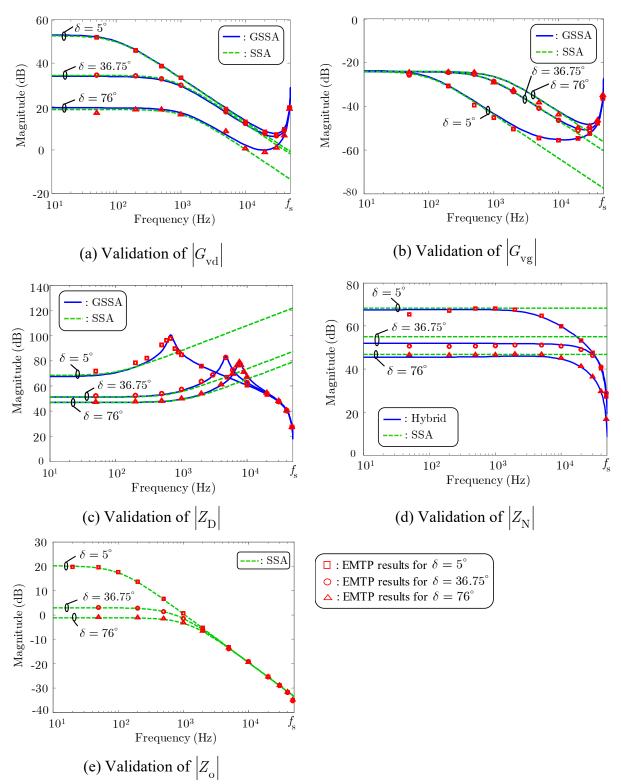


Figure 3.6 : Validation of the transfer functions with time-domain simulations (model 1)

3.5.2 Comparison of SSA, GSSA, and hybrid models

From Figure 3.6 a) and b), it is first observed that both the GSSA and SSA models are accurate to evaluate $G_{\rm vd}$, and $G_{\rm vg}$ for frequencies up to around 1/5th of the switching frequency $f_{\rm s}$. The precision of the SSA model decreases as the frequency of the perturbation is increased toward $f_{\rm s}$. This is because SSA does not consider the dynamic of both the transformer currents and the switching-functions. By analyzing Figure 3.6. c) and d), it is also concluded that the SSA model does not correctly model $Z_{\rm D}$ and $Z_{\rm N}$, but the proposed hybrid model provides a mean to evaluate $Z_{\rm N}$ which is not possible by using GSSA only.

Overall, from the simulation results in Figure 3.6, it is also concluded that the GSSA and hybrid models, together, are accurate to determine $G_{\rm vd}$, $G_{\rm vg}$, $Z_{\rm N}$, and $Z_{\rm D}$ over the validation frequency range. However, for $G_{\rm vd}$ and $G_{\rm vg}$, the GSSA model tends to slightly lose accuracy at high-power while, for $Z_{\rm D}$, the accuracy is slightly reduced at low power. For $Z_{\rm N}$, the hybrid model is also a little less accurate at low frequencies. This is mainly explained by errors on the calculation of the operating point with GSSA. From Figure 3.6 e), it is also concluded that the SSA model is accurate for the evaluation of the output impedance $Z_{\rm o}$. Being able to model the 3p-DAB from low frequencies up to the switching frequency $f_{\rm s}$ is important because it allows to 1) design a controller with an increased bandwidth, and 2) predict stability as well as the dynamic behavior of the converter over a broad range of frequencies (DC to $f_{\rm s}$).

Although SSA is less precise, it allows representing the converter equivalent model using standard circuit theory. These circuits are easier to manipulate to determine analytical expressions of the converter transfer functions. Thus, SSA gives a better insight of system parameters sensitivity. However, SSA also results in two sets of state-space matrices depending on the control phase-shift δ operating range.

3.6 Controller sizing and closed-loop operation

3.6.1 Simplified closed-loop model for parameter sensitivity analysis with SSA

As mentioned before, SSA provides a better intuition than GSSA of system parameters sensitivity. The closed-loop system of Figure 3.1 is simplified here for the analysis of the converter parameter sensitivity with SSA. It neglects the modulator gain $K_{\rm m}$ and it assumes that the converter dynamic is dominant over the dynamic of the measurement filter $G_{\rm f}$. The simplified closed-loop diagram is shown in Figure 3.7.

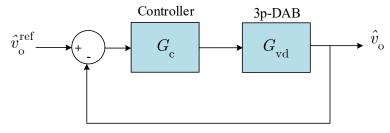


Figure 3.7 : Simplified closed-loop diagram for parameters sensitivity analysis with SSA The controller G_c used here is a simple PI-type with the transfer function given as,

$$G_{\rm c}(s) = \frac{sK_{\rm p} + K_{\rm i}}{s}$$
 (3.106)

The general form of the transfer function $G_{\rm vd}$ with the SSA model is also written as,

$$G_{\rm vd}(s) = \frac{K_{\rm vd}}{sRC_{\rm o} + 1} \tag{3.107}$$

The closed-loop transfer function from the reference to the output is given by,

$$H(s) = \frac{\hat{v}_{o}}{\hat{v}_{o}^{\text{ref}}} = \frac{1}{1 + G_{c}G_{vd}}$$
(3.108)

which yields to,

$$H(s) = \frac{\hat{v}_{o}}{\hat{v}_{o}^{\text{ref}}} = \frac{s(sRC_{o} + 1)(1/K_{vd}K_{i})}{\left(\frac{RC_{o}}{K_{vd}K_{i}}\right)s^{2} + \left(\frac{1 + K_{vd}K_{p}}{K_{vd}K_{i}}\right)s + 1}$$
(3.109)

By making the assumption that $K_{\rm vd}K_{\rm p} >> 1$, the analysis of the 2nd order denominator, which has the general form $\tau^2 s^2 + 2\zeta \tau s + 1$, leads to an approximate damping coefficient ζ of,

$$\zeta \approx \frac{K_{\rm p}}{\sqrt{K_{\rm i}}} \frac{\sqrt{K_{\rm vd}}}{2\sqrt{RC_{\rm o}}}$$
(3.110)

and an approximate time-constant τ of,

$$\tau \approx \frac{1}{\sqrt{K_{\rm i}}} \sqrt{\frac{RC_{\rm o}}{K_{\rm vd}}}$$
(3.111)

with $K_{\rm vd}$ given for range 1 by,

$$K_{\rm vd} = \frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}} \tag{3.112}$$

and for range 2 by,

$$K_{\rm vd} = \frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(1 - \frac{2\delta}{\pi} \right) \right]$$
(3.113)

Inserting (3.112) and (3.113) into (3.110) and (3.111), it yields to the damping coefficients ζ and the time-constants τ provided in Table 3.2. Knowing that the overshoot (OS) and the time-to-first-peak t_p of the time-domain response are both related to the damping coefficient ζ and the time-constant τ , it is possible to assess the impact of the parameters on the closed-loop response using basic control theory,

$$t_{\rm p} = \frac{\pi\tau}{\sqrt{1-\zeta^2}} \tag{3.114}$$

$$OS = e^{\frac{-\pi\zeta}{\sqrt{1-\zeta^2}}}$$
(3.115)

Theoretically, the closed-loop dynamic can be controlled through the controller parameters $K_{\rm p}$, $K_{\rm i}$ as well as the converter design parameters m, $V_{\rm i}$, $\omega_{\rm s}$, $L_{\rm s}$, and $C_{\rm o}$. In practice, it is more convenient to use only $K_{\rm p}$, $K_{\rm i}$ and $C_{\rm o}$ since m, $V_{\rm i}$, $\omega_{\rm s}$ and $L_{\rm s}$ all affect the steady-state operating point. For example, as shown in Figure 3.8, for a given PI-controller, increasing the output capacitor $C_{\rm o}$ increases the response time $t_{\rm p}$ as well as the overshoot (OS). This can be explained by the fact that increasing $C_{\rm o}$ decreases ζ and increases τ with all other parameters being held constant (Table 3.2).

Table 3.2: Closed-loop dynamic response analysis with SSA

Range 1 ($0 \le \delta \le \pi/6$)		Range 2 ($\pi/6 \le \delta \le \pi/2$)	
$\zeta \approx \frac{K_{\rm p}}{2\sqrt{K_{\rm i}}} \sqrt{\frac{mV_{\rm i}}{\omega_{\rm s}L_{\rm s}C_{\rm o}}}$	(3.116)	$\zeta \approx \frac{K_{\rm p}}{2\sqrt{K_{\rm i}}} \sqrt{\frac{mV_{\rm i}}{\omega_{\rm s}L_{\rm s}C_{\rm o}}} \sqrt{\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)}$	(3.117)
$\tau \approx \frac{1}{\sqrt{K_{\rm i}}} \sqrt{\frac{\omega_{\rm s} L_{\rm s} C_{\rm o}}{m V_{\rm i}}}$	(3.118)	$\tau \approx \frac{1}{\sqrt{K_{\rm i}}} \sqrt{\frac{\omega_{\rm s} L_{\rm s} C_{\rm o}}{m V_{\rm i}}} \frac{1}{\sqrt{\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)}}$	(3.119)

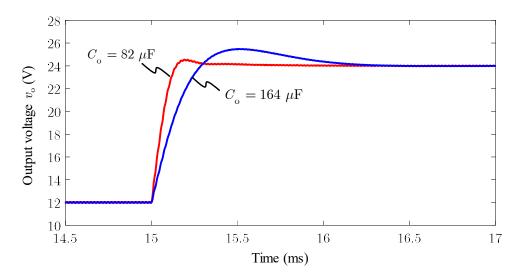


Figure 3.8 : Response to a step in the reference voltage from 12 V to 24 V (model 4)

3.6.2 Complete closed-loop analysis for controller sizing

For controller sizing, the complete closed-loop small-signal diagram of Figure 3.9 must be used because the modulator introduces a gain $K_{\rm m}$ and the dynamic of the feedback measurement filter $G_{\rm f}$ may not be negligible.

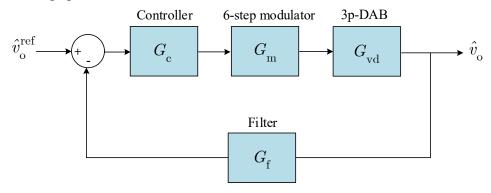


Figure 3.9 : Complete closed-loop diagram for controller sizing

Using Nyquist's theorem, the analysis is performed on the loop gain $T = G \cdot G_c$ with G defined as,

$$G = G_{\rm vd} G_{\rm m} G_{\rm f} \tag{3.120}$$

For the prototype (model 4), the bode diagram of the system transfer functions is shown in Figure 3.10. The objective is to calculate the PI-controller parameters $K_{\rm p}$ and $K_{\rm i}$ to meet desired dynamic performance defined by a cross-over frequency $\omega_{\varphi m} = 2\pi f_{\varphi m}$ and a phase-margin $\varphi_{\rm m}$. The general transfer function of the PI-controller is given in (3.106). The bode diagram of the controller

 $G_{\rm c}$ and the open-loop transfer function $G \cdot G_{\rm c}$ is shown in Figure 3.11. The resulting transient response to a step in the output voltage reference $v_{\rm o}^{\rm ref}$ from 0 to 24 V is shown in Figure 3.12. The resulting time-to-peak $t_{\rm p}$ is equal to 0.5 ms, and the resulting overshoot is equal to 9 %.

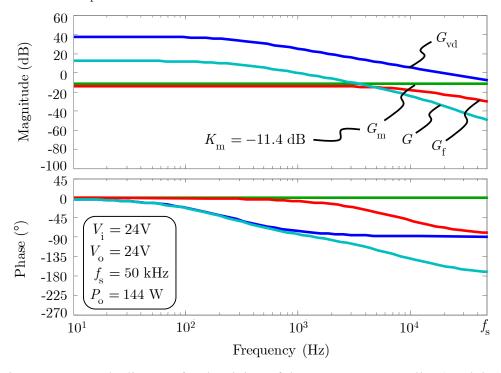


Figure 3.10 : Bode diagram for the sizing of the prototype controller (model 4)

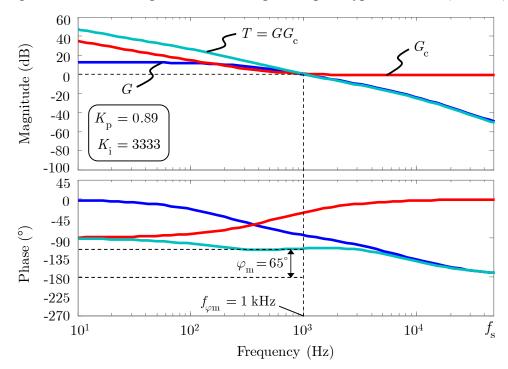


Figure 3.11 : Bode diagram of the prototype controller (model 4)

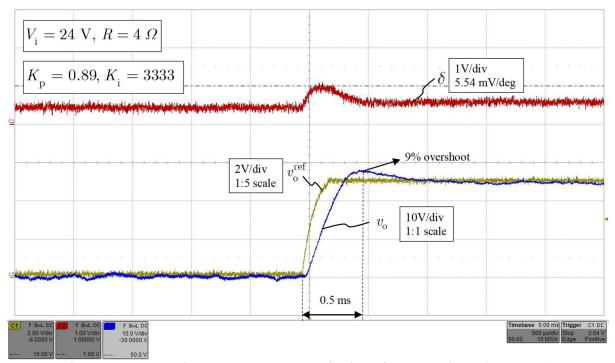


Figure 3.12 : Transient response to a step in the reference voltage (prototype)

3.6.3 Closed-loop transfer functions

The analysis of the converter closed-loop transfer functions is important for systems integration. One of the advantages of being able to evaluate Z_D and Z_N is the fact that it allows calculating the closed-loop input impedance of the 3p-DAB knowing its loop gain T for a given controller [30],

$$\frac{1}{Z_{\text{in}}^{\text{c}}(s)} = \frac{\hat{i}_{1}(s)}{\hat{v}_{i}(s)} \bigg|_{\substack{\hat{v}_{\text{o}}^{\text{ref}}(s)=0\\\hat{i}_{\text{load}}(s)=0}} = \frac{1}{Z_{\text{N}}(s)} \bigg(\frac{T(s)}{1+T(s)} \bigg) + \frac{1}{Z_{\text{D}}(s)} \bigg(\frac{1}{1+T(s)} \bigg)$$
(3.121)

Furthermore, knowing the open-loop transfer functions G_{vd} , G_{vg} and Z_o , and the loop gain T, the remaining closed-loop transfer functions can also be evaluated with [30],

$$G_{\rm vd}^{\rm c}(s) = \frac{\hat{v}_{\rm o}(s)}{\hat{v}_{\rm o}^{\rm ref}(s)} \bigg|_{\substack{\hat{v}_{\rm s}(s)=0\\i_{\rm load}(s)=0}} = \frac{1}{G_{\rm f}(s)} \frac{T(s)}{1+T(s)}$$
(3.122)

$$Z_{o}^{c}(s) = -\frac{\hat{v}_{o}(s)}{\hat{i}_{load}(s)} \bigg|_{\substack{\hat{v}_{o}^{ref}(s)=0\\\hat{v}_{i}(s)=0}} = \frac{Z_{o}(s)}{1+T(s)}$$
(3.123)

$$G_{\rm vg}^{\rm c}(s) = \frac{\hat{v}_{\rm o}(s)}{\hat{v}_{\rm i}(s)} \Big|_{\substack{\hat{v}_{\rm o}^{\rm ref}(s)=0\\\hat{i}_{\rm load}(s)=0}} = \frac{G_{\rm vg}(s)}{1+T(s)}$$
(3.124)

Analyzing (3.121)–(3.124), it must be concluded that it is important to model accurately the openloop transfer functions G_{vd} , G_{vg} , Z_D , Z_N and Z_o to evaluate the closed-loop characteristics of the converter. This is because even slight errors in determining G_{vd} , G_{vg} , Z_D , Z_N and Z_o are multiplied together in (3.121)–(3.124) through the loop gain *T*. As an application example, the closed-loop transfer functions for the prototype (model 4) are compared with the open-loop transfer functions in Figure 3.13.

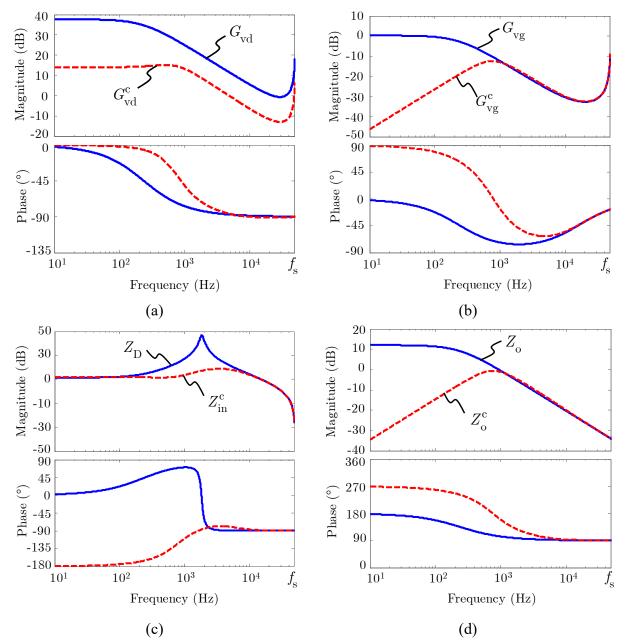


Figure 3.13 : Open-loop vs closed-loop transfer functions (model 4)

3.7 Input filter sizing

3.7.1 Input filter transfer functions

In practice, a low-pass filter is generally added at the input of dc-dc converters to meet electromagnetic compatibility (EMC) requirements. The common practice is to use a 2^{nd} order LC-filter as shown in Figure 3.14. Four (4) transfer functions are considered from a systems integration standpoint. They are given as follows. The input impedance Z_f is given by,

$$Z_{\rm f}(s) = \frac{\hat{v}_{\rm s}(s)}{\hat{i}_{\rm s}(s)} = \frac{s^2 L_{\rm g} C_{\rm i} + s \left(R_{\rm g} + R_{\rm i}\right) C_{\rm i} + 1}{s C_{\rm i}}$$
(3.125)

The output impedance $Z_{\rm g}$ is given by,

$$Z_{\rm g}(s) = -\frac{\hat{v}_{\rm i}(s)}{\hat{i}_{\rm i}(s)} = -\frac{s^2 R_{\rm i} L_{\rm g} C_{\rm i} + s \left(R_{\rm g} R_{\rm i} C_{\rm i} + L_{\rm g}\right) + R_{\rm g}}{s^2 L_{\rm g} C_{\rm i} + s \left(R_{\rm g} + R_{\rm i}\right) C_{\rm i} + 1}$$
(3.126)

The input-to-output voltage transfer function G_0 is given by,

$$G_{\rm o}(s) = \frac{\hat{v}_{\rm i}(s)}{\hat{v}_{\rm s}(s)} = \frac{sR_{\rm i}C_{\rm i} + 1}{s^2L_{\rm g}C_{\rm i} + s\left(R_{\rm g} + R_{\rm i}\right)C_{\rm i} + 1}$$
(3.127)

The back-current transfer function H_{i} is given by,

$$H_{i}(s) = \frac{\hat{i}_{s}(s)}{\hat{i}_{i}(s)} = \frac{Z_{g}(s)}{R_{g} + sL_{g}}$$
(3.128)

The input filter is generally selected based on desired characteristics of G_0 , H_i and Z_f . The inputto-output voltage transfer function G_0 is important because it shows how voltage noise coming from the source will propagates at the converter input, which can further propagate at the output of the converter through its closed-loop input-to-output transfer function G_{vg}^c . The transfer function H_i is also of interest because it allows to design a filter which reduces the propagation of noise current generated by the converter back to the source. In some applications, it may also be desirable to have a minimum input impedance at given frequencies to avoid circulating currents at these frequencies which could further impact other systems in the network.

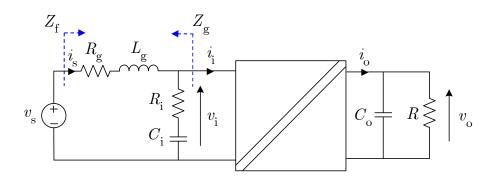


Figure 3.14 : Converter with input filter

3.7.2 Modified transfer functions and stability analysis

The output impedance $Z_{\rm g}$ is generally the consequence of design choices on $Z_{\rm f}$, $G_{\rm o}$ and $H_{\rm i}$. However, $Z_{\rm g}$ affects the dynamic performance of the converter and the overall system may even become unstable if it is not properly considered during design. In fact, the input filter affects the open-loop transfer functions of the converter, mainly the control-to-output transfer function $G_{\rm vd}$, the input-to-output transfer function $G_{\rm vg}$, and the output impedance $Z_{\rm o}$ [30]. Consequently, the closed-loop behavior of the converter is also impacted.

According to Middlebrook's EET [30], [55], the magnitude of the output impedance of the input filter Z_g should meet (3.129) to both ensure stability and that the input filter does not substantially alter the converter open-loop transfer functions,

$$\left|Z_{\rm g}(s)\right| \ll \left|Z_{\rm N}(s)\right|, \ \left|Z_{\rm g}(s)\right| \ll \left|Z_{\rm D}(s)\right|$$

$$(3.129)$$

Middlebrook's EET is a widely used stability criterion. It is used not only to guarantee stability, but also to ensure that the introduction of an input filter does not substantially alter the converter open-loop transfer functions $G_{\rm vd}$, $G_{\rm vg}$ and $Z_{\rm o}$. Its application greatly reduces the required number of both time-domain simulations and experiments to 1) predict stability, and 2) design a system with an acceptable and predictable transient response. It is known to be a conservative approach which means that, even if (3.129) is violated, the system still can be stable [60]. Knowing all the converter transfer functions, it is frequent practice to analyze the modified converter loop gain. This is a key step to ensure the predictability of the converter dynamic. Under the presence of an additional input filter, the converter control-to-output transfer function is modified following [30],

$$G_{\rm vd}^{\rm new}(s) = G_{\rm vd}(s) \left(\frac{1 + Z_{\rm g}(s) / Z_{\rm N}(s)}{1 + Z_{\rm g}(s) / Z_{\rm D}(s)} \right)$$
(3.130)

which leads to the modified loop gain calculated as,

$$T^{\text{new}}(s) = T(s) \left(\frac{1 + Z_{\text{g}}(s) / Z_{\text{N}}(s)}{1 + Z_{\text{g}}(s) / Z_{\text{D}}(s)} \right)$$
(3.131)

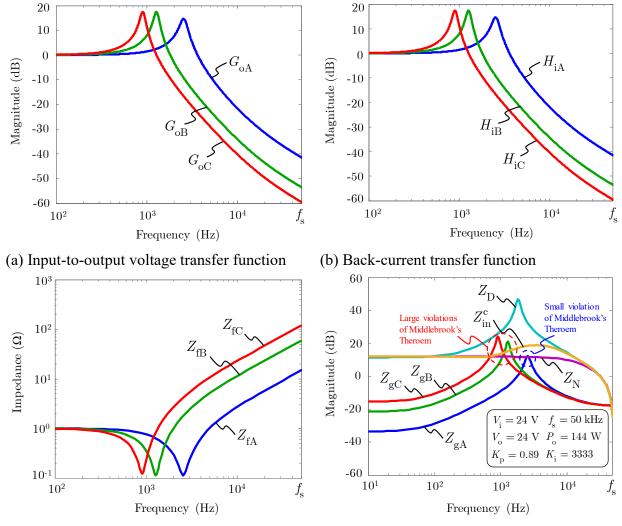
The correction factor K_{corr} is also defined as [30],

$$K_{\rm corr}(s) = \left(\frac{1 + Z_{\rm g}(s) / Z_{\rm N}(s)}{1 + Z_{\rm g}(s) / Z_{\rm D}(s)}\right)$$
(3.132)

From (3.129)–(3.132), it is again concluded that, being able to evaluate $Z_{\rm D}$ and $Z_{\rm N}$, allows evaluating the impacts of the input filter on the converter dynamic. The utilization of the developed models is demonstrated through the application of Middlebrook's extra element theorem for additional input filter. Three filters are designed. The magnitude of all their transfer functions is shown in Figure 3.15. The filter parameters are shown in Table 3.3. The value of the input capacitor $C_{\rm i}$ is kept constant and the inductor $L_{\rm g}$ is increased such that the filter cut-off frequency $f_{\rm g}$ is decreased.

According to (3.129) and analyzing Figure 3.15 d), Filter A is not expected to largely deteriorate the dynamic response, but Filter B and Filter C are expected to substantially impact the dynamic performance. By analyzing the results in Figure 3.16 and Figure 3.17, it is concluded that Filter B and Filter C have an important impact on the transient performance of the converter. Filter C even lead to instability, that is, an oscillating behavior. In dc systems, unstable conditions are often characterized by a constant AC component superimposed on the DC value [81]. In practical applications, saturation of the controller introduces non-linear behavior which often leads to oscillating waveforms exhibiting saturation plateau. The results in Figure 3.18 show an example of waveforms on a 3p-DAB converter with an input filter which does not meet Middlebrook's theorem. The phase-shift δ (purple) oscillates and saturates at 90°. The output voltage v_0 (cyan) is oscillating around its reference v_0^{ref} (yellow). The input voltage v_1 (green) also exhibits unwanted oscillations due to the constant power (i.e. negative incremental resistance) behavior of the converter input impedance.

Parameter	Symbol	Filter A	Filter B	Filter C
Input Inductor	$L_{ m g}$	47 μH	188 µH	376 µH
Inductor ESR	$R_{ m g}$	$20.8 \text{ m}\Omega$	83.2 mΩ	$166 \text{ m}\Omega$
Input Capacitor	C_{i}	82 µF	82 µF	82 µF
Capacitor ESR	$R_{\rm i}$	120 mΩ	$120 \text{ m}\Omega$	120 mΩ
Cut-off frequency	$f_{ m g}$	2.56 kHz	1.28 kHz	905 Hz



(c) Filter input impedance

(d) Filter output impedance vs converter input impedance (Middlebrook's theorem)

Figure 3.15 : Input filter transfer functions magnitude

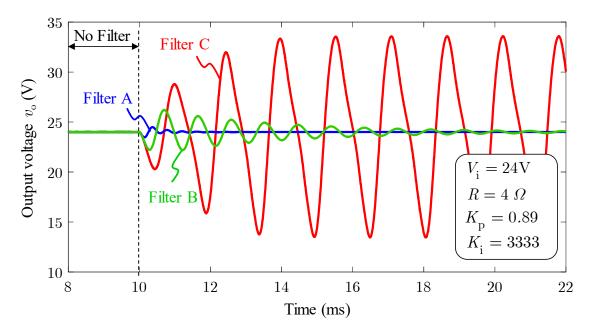


Figure 3.16 : Transient response after introduction of filters (model 4)

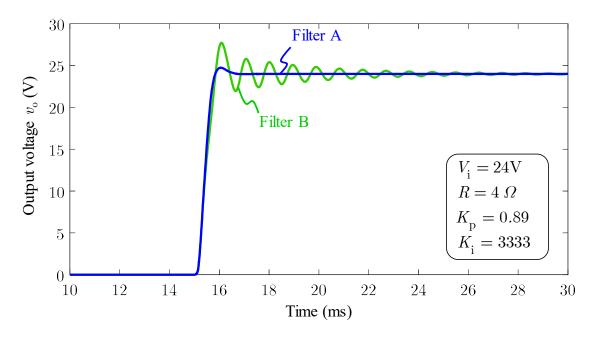


Figure 3.17 : Response to a step in the reference voltage from 0 V to 24 V (model 4)

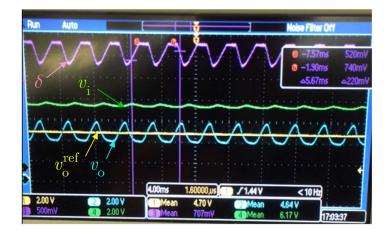
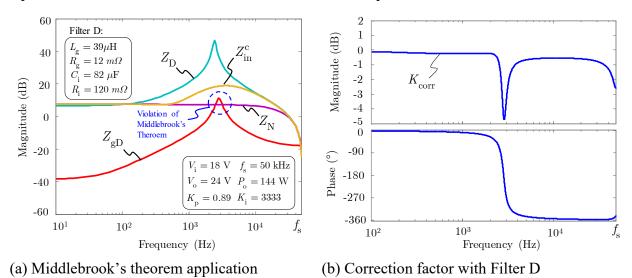
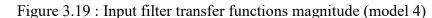


Figure 3.18 : Unstable condition due to an additional input filter

3.7.3 Experimental results

The impact of an additional input filter on the transient response is verified with the experimental prototype. An input filter (Filter D) with the parameters given in Figure 3.19 a) is designed. It is added at the input of the closed-loop 3p-DAB prototype operated at the operating point given in Figure 3.19 a). As seen in Figure 3.19 a), the magnitude of the filter output impedance Z_{gD} is violating Middlebrook's EET (3.129) with respect to the null driving point impedance Z_N . This means that the correction factor (Figure 3.19 b)) will impact both the magnitude and the phase of the converter transfer functions according to (3.130)–(3.132), thus, its transient response. The experimental results in Figure 3.20 and Figure 3.21 confirm the degradation in the transient response. The overshoot OS increases from 8% without input filter to 16% with Filter D.





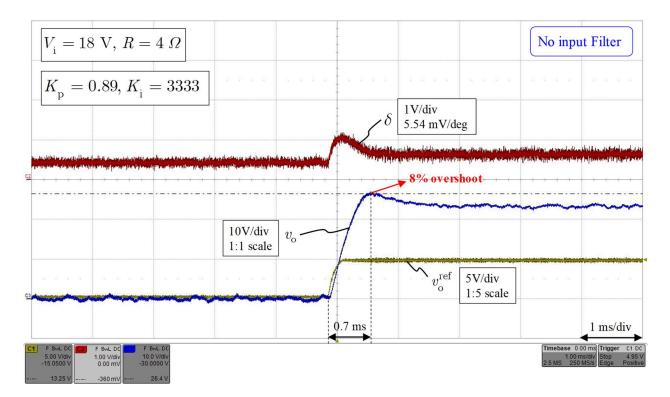


Figure 3.20 : Response to a step in the reference voltage without input filter (prototype)

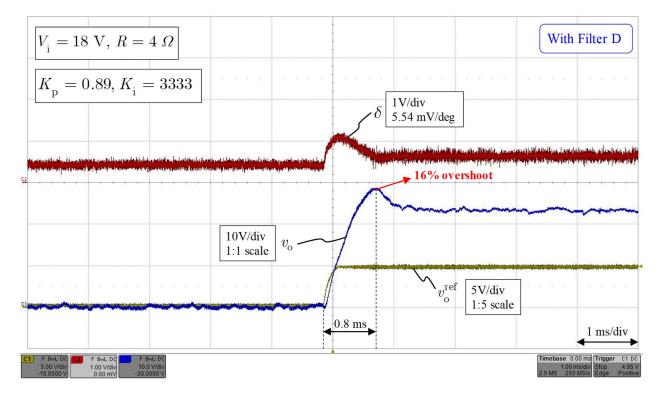


Figure 3.21 : Response to a step in the reference voltage with Filter D (prototype)

CHAPTER 4 LARGE-SIGNAL TIME-DOMAIN SIMULATION

In this chapter, large-signal models are developed to enable system-level simulation of dc-grids with 3p-DAB converters in EMT-type programs. Switching-functions (SWF), state-space averaging (SSA) and generalized averaging (GAM) models of the Y- Δ 3p-DAB converter are developed, implemented and compared with the ideal-model approach in EMTP. While the implementation of the SWF and SSA models is presented for comparison purpose, the main contribution of this chapter is the development and the implementation of the GAM model.

4.1 Switching-function (SWF) model

The concept of switching-functions has been first introduced in [63] for the simulation of threephase inverters in PSpice. For the 3p-DAB, it consists in replacing each leg of each bridge by an equivalent controlled current source seen from the dc-side (Figure 4.1 a)), and an equivalent controlled voltage source seen from the ac-side (Figure 4.1 b)). The three-phase transformer is directly modeled using EMTP ideal transformer model, which allows to easily consider the winding-connection, the ratio m and the series inductances L_A , L_B and L_C . The input and output diodes D_i and D_o are used to model the effect of the input and output bridges anti-parallel diodes on negative voltage. These diodes prevent the voltage from becoming negative at either one of the two dc ports. Their importance is discussed in section 4.4.3.

The switching-functions are defined by the gating signals coming from the output of the modulator as defined in Appendix C. In Figure 4.1 c), the switching-functions for phase A are provided as an example. Phases B and C are $\pm 120^{\circ}$ phase-shifted from phase A. For any transformer connection, the input current source is calculated using (2.33) and the output current source is calculated as follows.

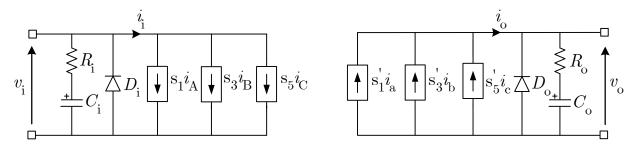
$$i_{o} = s'_{1}i_{a} + s'_{3}i_{b} + s'_{5}i_{c}$$
 (4.1)

The transformer primary and secondary voltage sources are defined as,

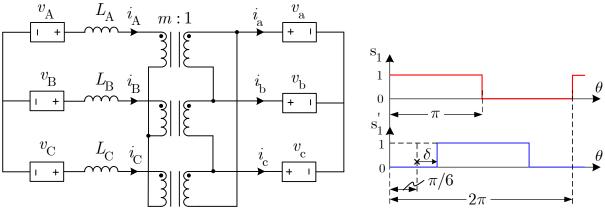
$$v_{\rm A} = \left(2s_1 - 1\right)\frac{v_{\rm i}}{2}, v_{\rm B} = \left(2s_3 - 1\right)\frac{v_{\rm i}}{2}, v_{\rm C} = \left(2s_5 - 1\right)\frac{v_{\rm i}}{2}$$
(4.2)

$$v_{\rm a} = \left(2s_1' - 1\right)\frac{v_{\rm o}}{2}, v_{\rm b} = \left(2s_3' - 1\right)\frac{v_{\rm o}}{2}, v_{\rm c} = \left(2s_5' - 1\right)\frac{v_{\rm o}}{2}$$
(4.3)

All the switching-functions, currents and voltages in (4.1)–(4.3) are updated at each time-step Δt . The dependence to time has been removed for the sake of clarity of the presented material. The control phase-shift δ is hidden in (4.3) though the calculation of the output bridge switching-functions s'_1 , s'_3 and s'_5 . In open-loop, the phase-shift δ is fixed while in closed-loop it is calculated by implementing the controller structure shown in Figure 1.4 b). The implementation of the SWF model also requires to completely model the modulator.



(a) Equivalent switching-function model of the 3p-DAB seen from the dc-grids



(b) Equivalent Y- Δ transformer model (c) Switching-functions for phase A Figure 4.1 : SWF model of the Y- Δ 3p-DAB

4.2 State-space averaging (SSA) model

The state-space averaging (SSA) method [51] has been first applied in [54] to approximate the control-to-output $G_{\rm vd}$ and the input-to-output $G_{\rm vg}$ transfer functions of the Y-Y 3p-DAB converter. Since there is no resource in the literature regarding the use of SSA for large-signal analysis of 3p-DAB converters in EMT-type programs, the approach used in this thesis is presented in the next paragraphs.

Following the averaging steps described in section 3.2, the index-0 (dc) model of the converter is obtained. As it is derived using the piecewise-linear modeling (PLM) approach, it results in two sets of equations which depend on the phase-shift δ operating range. Using (2.37) and (2.38) which assumes a lossless converter, the resulting averaged input current $\langle i_i \rangle_0$ and output current $\langle i_o \rangle_0$ are calculated at each time-step Δt . They are used to control the current sources of Figure 4.2. For range 1 ($0 \le \delta \le \pi/6$), the input and output currents are given by,

$$\left\langle i_{i}\right\rangle_{0} = \left(\frac{m}{\omega_{s}L_{s}}\right) \cdot \left\langle \delta \right\rangle_{0} \cdot \left\langle v_{o}\right\rangle_{0}$$

$$(4.4)$$

$$\left\langle i_{o}\right\rangle_{0} = \left(\frac{m}{\omega_{s}L_{s}}\right) \cdot \left\langle \delta\right\rangle_{0} \cdot \left\langle v_{i}\right\rangle_{0}$$

$$(4.5)$$

and for range 2 ($\pi/6 \le \delta \le \pi/2$), they are calculated by,

$$\left\langle i_{i}\right\rangle_{0} = \left(\frac{m}{\omega_{s}L_{s}}\right) \left[\frac{3}{2} \left(\left\langle \delta \right\rangle_{0} - \frac{\left(\left\langle \delta \right\rangle_{0}\right)^{2}}{\pi}\right) - \frac{\pi}{24}\right] \left\langle v_{o}\right\rangle_{0}$$
(4.6)

$$\left\langle i_{\rm o} \right\rangle_{0} = \left(\frac{m}{\omega_{\rm s} L_{\rm s}}\right) \left[\frac{3}{2} \left(\left\langle \delta \right\rangle_{0} - \frac{\left(\left\langle \delta \right\rangle_{0}\right)^{2}}{\pi}\right) - \frac{\pi}{24} \right] \left\langle v_{\rm i} \right\rangle_{0}$$
(4.7)

As for the SWF model, in open-loop, the phase-shift $\langle \delta \rangle_0$ is fixed while, in closed-loop, it is calculated by implementing the controller structure presented in Figure 1.4 b).

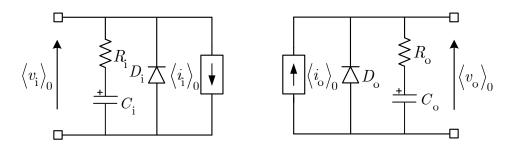
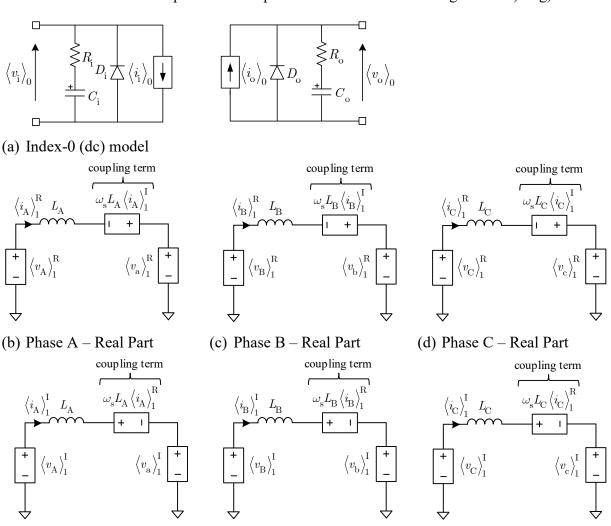


Figure 4.2 : SSA model of the Y- Δ 3p-DAB

4.3 Generalized averaging model (GAM)

The generalized averaging model (GAM) is based on the averaged index-0 (dc) and the index-1 (fundamental frequency) system of equations (3.61)–(3.69) developed in section 3.3.1.2. As for the SSA model, seen from the dc-grids, the GAM approach leads to the equivalent index-0 model shown in Figure 4.3 a). The index-0 model is governed by a system of equations which is function of the index-1 model (Figure 4.3 b) to g)). The index-1 model includes the ac dynamic of both the transformer currents and the switching-functions. The index-1 model can be solved in EMTP by either using control system equations or the network solver with controlled voltage sources and inductors to implement the equivalent circuits shown in Figure 4.3 b) to g).



(e) Phase A – Imaginary Part (f) Phase B – Imaginary Part (g) Phase C – Imaginary Part Figure 4.3 : GAM model of the Y-Δ 3p-DAB

In this thesis, the index-1 model has been implemented using the control system equations. The large-signal model of the 3p-DAB converter is given here using the following state-space notation,

$$\frac{d\langle \mathbf{x} \rangle}{dt} = \mathbf{A} \langle \mathbf{x} \rangle + \mathbf{B} \langle \mathbf{u} \rangle$$
(4.8)

$$\langle \mathbf{y} \rangle = \mathbf{C} \langle \mathbf{x} \rangle + \mathbf{D} \langle \mathbf{u} \rangle$$
 (4.9)

with,

$$\left\langle \mathbf{x} \right\rangle = \left[\left\langle i_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{A}} \right\rangle_{1}^{\mathrm{I}} \left\langle i_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{R}} \left\langle i_{\mathrm{C}} \right\rangle_{1}^{\mathrm{I}} \right]^{T}$$
(4.10)

$$\langle \mathbf{u} \rangle = \left[\langle v_{i} \rangle_{0} \langle v_{o} \rangle_{0} \right]^{\mathrm{T}}, \langle \mathbf{y} \rangle = \left[\langle i_{i} \rangle_{0} \langle i_{o} \rangle_{0} \right]^{\mathrm{T}}$$
 (4.11)

The resulting system of equations defined by (4.8)–(4.18) is implemented in EMTP by using the control blocks and solved based on [82]. The calculated values of the input current $\langle i_i \rangle_0$ and the output current $\langle i_o \rangle_0$ are used to control the current sources of Figure 4.3 a). The system matrices **A**, **B**, **C** and **D** are time-dependent and given by,

$$\mathbf{A} = \begin{bmatrix} 0 & \omega_{s} & 0 & 0 & 0 & 0 \\ -\omega_{s} & 0 & 0 & 0 & 0 & 0 \\ 0 & 0 & 0 & \omega_{s} & 0 & 0 \\ 0 & 0 & -\omega_{s} & 0 & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & \omega_{s} \\ 0 & 0 & 0 & 0 & -\omega_{s} & 0 \end{bmatrix}, \mathbf{B} = \begin{bmatrix} B_{11} & B_{12} \\ B_{21} & B_{22} \\ B_{31} & B_{32} \\ B_{41} & B_{42} \\ B_{51} & B_{52} \\ B_{61} & B_{62} \end{bmatrix}$$
(4.12)
$$\mathbf{C} = \begin{bmatrix} C_{11} & C_{12} & C_{13} & C_{14} & C_{15} & C_{16} \\ C_{21} & C_{22} & C_{23} & C_{24} & C_{25} & C_{26} \end{bmatrix}, \mathbf{D} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix}$$

with,

$$B_{12} = \frac{m}{L_{A}} \left[\left\langle \mathbf{s}_{3}^{'} \right\rangle_{1}^{R} - \left\langle \mathbf{s}_{1}^{'} \right\rangle_{1}^{R} \right], B_{22} = \frac{m}{L_{A}} \left[\left\langle \mathbf{s}_{3}^{'} \right\rangle_{1}^{I} - \left\langle \mathbf{s}_{1}^{'} \right\rangle_{1}^{I} \right]$$

$$B_{32} = \frac{m}{L_{B}} \left[\left\langle \mathbf{s}_{5}^{'} \right\rangle_{1}^{R} - \left\langle \mathbf{s}_{3}^{'} \right\rangle_{1}^{R} \right], B_{42} = \frac{m}{L_{B}} \left[\left\langle \mathbf{s}_{5}^{'} \right\rangle_{1}^{I} - \left\langle \mathbf{s}_{3}^{'} \right\rangle_{1}^{I} \right]$$

$$B_{52} = \frac{m}{L_{C}} \left[\left\langle \mathbf{s}_{1}^{'} \right\rangle_{1}^{R} - \left\langle \mathbf{s}_{5}^{'} \right\rangle_{1}^{R} \right], B_{62} = \frac{m}{L_{C}} \left[\left\langle \mathbf{s}_{1}^{'} \right\rangle_{1}^{I} - \left\langle \mathbf{s}_{5}^{'} \right\rangle_{1}^{I} \right]$$

$$(4.13)$$

$$\begin{split} B_{11} &= \frac{1}{3L_{\rm A}} \left[2 \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm R} \right], B_{21} = \frac{1}{3L_{\rm A}} \left[2 \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm I} \right] \\ B_{31} &= \frac{1}{3L_{\rm B}} \left[2 \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm R} \right], B_{41} = \frac{1}{3L_{\rm B}} \left[2 \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm I} \right] \\ B_{51} &= \frac{1}{3L_{\rm C}} \left[2 \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} \right], B_{61} = \frac{1}{3L_{\rm C}} \left[2 \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} \right] \\ C_{21} &= 2m \left[\left\langle {\rm s}_{1} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} \right], C_{22} = 2m \left[\left\langle {\rm s}_{1} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} \right] \\ C_{23} &= 2m \left[\left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} \right], C_{24} = 2m \left[\left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} \right] \\ C_{25} &= 2m \left[\left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} \right], C_{26} = 2m \left[\left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} - \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I} \right] \\ C_{11} &= 2 \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm R}, C_{12} = 2 \left\langle {\rm s}_{1} \right\rangle_{1}^{\rm I}, C_{13} &= 2 \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm R} \\ C_{14} &= 2 \left\langle {\rm s}_{3} \right\rangle_{1}^{\rm I}, C_{15} = 2 \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm R}, C_{16} &= 2 \left\langle {\rm s}_{5} \right\rangle_{1}^{\rm I} \end{aligned}$$

$$(4.16)$$

As shown in section 3.3.1.2, for the input bridge, the averaged switching-functions are constant because they do not depend on the control phase-shift. They are expressed here as,

$$\left\langle \mathbf{s}_{1}\right\rangle_{1} = \frac{1}{\pi}e^{-j\frac{\pi}{2}}, \ \left\langle \mathbf{s}_{3}\right\rangle_{1} = \frac{1}{\pi}e^{j\frac{\pi}{6}}, \ \left\langle \mathbf{s}_{5}\right\rangle_{1} = \frac{1}{\pi}e^{j\frac{5\pi}{6}}$$
 (4.17)

For the output bridge, they depend on the instantaneous value of the control phase-shift $\langle \delta \rangle_0$ and are referenced with respect to the input bridge. They are expressed here using Euler's formula,

$$\left\langle \mathbf{s}_{1}^{'}\right\rangle_{1} = \left\langle \mathbf{s}_{1}\right\rangle_{1} e^{-j\left(\frac{\pi}{6} + \left\langle\delta\right\rangle_{0}\right)}, \left\langle \mathbf{s}_{3}^{'}\right\rangle_{1} = \left\langle\mathbf{s}_{3}\right\rangle_{1} e^{-j\left(\frac{\pi}{6} + \left\langle\delta\right\rangle_{0}\right)}, \left\langle \mathbf{s}_{5}^{'}\right\rangle_{1} = \left\langle\mathbf{s}_{5}\right\rangle_{1} e^{-j\left(\frac{\pi}{6} + \left\langle\delta\right\rangle_{0}\right)}$$
(4.18)

For solving the index-1 model using the network solver (instead of the control equations solver) as in Figure 4.3 b) to g), the voltage sources are the following,

$$\left\langle v_{\mathrm{A}}\right\rangle_{1}^{\mathrm{R}} = \frac{1}{3} \left[2\left\langle \mathbf{s}_{1}\right\rangle_{1}^{\mathrm{R}} - \left\langle \mathbf{s}_{3}\right\rangle_{1}^{\mathrm{R}} - \left\langle \mathbf{s}_{5}\right\rangle_{1}^{\mathrm{R}} \right] \cdot \left\langle v_{\mathrm{i}}\right\rangle_{0}, \quad \left\langle v_{\mathrm{a}}\right\rangle_{1}^{\mathrm{R}} = m \left[\left\langle \mathbf{s}_{1}^{\mathrm{i}}\right\rangle_{1}^{\mathrm{R}} - \left\langle \mathbf{s}_{3}^{\mathrm{i}}\right\rangle_{1}^{\mathrm{R}} \right] \cdot \left\langle v_{\mathrm{o}}\right\rangle_{0} \quad (4.19)$$

$$\left\langle v_{\mathrm{A}}\right\rangle_{1}^{\mathrm{I}} = \frac{1}{3} \left[2\left\langle \mathbf{s}_{1}\right\rangle_{1}^{\mathrm{I}} - \left\langle \mathbf{s}_{3}\right\rangle_{1}^{\mathrm{I}} - \left\langle \mathbf{s}_{5}\right\rangle_{1}^{\mathrm{I}} \right] \cdot \left\langle v_{i}\right\rangle_{0}, \quad \left\langle v_{\mathrm{a}}\right\rangle_{1}^{\mathrm{I}} = m \left[\left\langle \mathbf{s}_{1}^{'}\right\rangle_{1}^{\mathrm{I}} - \left\langle \mathbf{s}_{3}^{'}\right\rangle_{1}^{\mathrm{I}} \right] \cdot \left\langle v_{\mathrm{o}}\right\rangle_{0}$$
(4.20)

$$\left\langle v_{\rm B} \right\rangle_{1}^{\rm R} = \frac{1}{3} \left[2 \left\langle s_{3} \right\rangle_{1}^{\rm R} - \left\langle s_{1} \right\rangle_{1}^{\rm R} - \left\langle s_{5} \right\rangle_{1}^{\rm R} \right] \cdot \left\langle v_{i} \right\rangle_{0}, \quad \left\langle v_{\rm b} \right\rangle_{1}^{\rm R} = m \left[\left\langle s_{3}^{\prime} \right\rangle_{1}^{\rm R} - \left\langle s_{5}^{\prime} \right\rangle_{1}^{\rm R} \right] \cdot \left\langle v_{\rm o} \right\rangle_{0} \quad (4.21)$$

$$\left\langle v_{\rm B} \right\rangle_{1}^{\rm I} = \frac{1}{3} \left[2 \left\langle \mathbf{s}_{3} \right\rangle_{1}^{\rm I} - \left\langle \mathbf{s}_{1} \right\rangle_{1}^{\rm I} - \left\langle \mathbf{s}_{5} \right\rangle_{1}^{\rm I} \right] \cdot \left\langle v_{i} \right\rangle_{0}, \quad \left\langle v_{\rm b} \right\rangle_{1}^{\rm I} = m \left[\left\langle \mathbf{s}_{3}^{\rm '} \right\rangle_{1}^{\rm I} - \left\langle \mathbf{s}_{5}^{\rm '} \right\rangle_{1}^{\rm I} \right] \cdot \left\langle v_{o} \right\rangle_{0}$$
(4.22)

$$\left\langle v_{\rm C} \right\rangle_{1}^{\rm R} = \frac{1}{3} \left[2 \left\langle \mathbf{s}_{5} \right\rangle_{1}^{\rm R} - \left\langle \mathbf{s}_{1} \right\rangle_{1}^{\rm R} - \left\langle \mathbf{s}_{3} \right\rangle_{1}^{\rm R} \right] \cdot \left\langle v_{\rm i} \right\rangle_{0}, \quad \left\langle v_{\rm c} \right\rangle_{1}^{\rm R} = m \left[\left\langle \mathbf{s}_{5}^{\prime} \right\rangle_{1}^{\rm R} - \left\langle \mathbf{s}_{1}^{\prime} \right\rangle_{1}^{\rm R} \right] \cdot \left\langle v_{\rm o} \right\rangle_{0} \quad (4.23)$$

$$\left\langle v_{\rm C} \right\rangle_{\rm I}^{\rm I} = \frac{1}{3} \left[2 \left\langle {\rm s}_5 \right\rangle_{\rm I}^{\rm I} - \left\langle {\rm s}_1 \right\rangle_{\rm I}^{\rm I} - \left\langle {\rm s}_3 \right\rangle_{\rm I}^{\rm I} \right] \cdot \left\langle v_{\rm i} \right\rangle_0, \quad \left\langle v_{\rm c} \right\rangle_{\rm I}^{\rm I} = m \left[\left\langle {\rm s}_5^{\rm '} \right\rangle_{\rm I}^{\rm I} - \left\langle {\rm s}_1^{\rm '} \right\rangle_{\rm I}^{\rm I} \right] \cdot \left\langle v_{\rm o} \right\rangle_0 \tag{4.24}$$

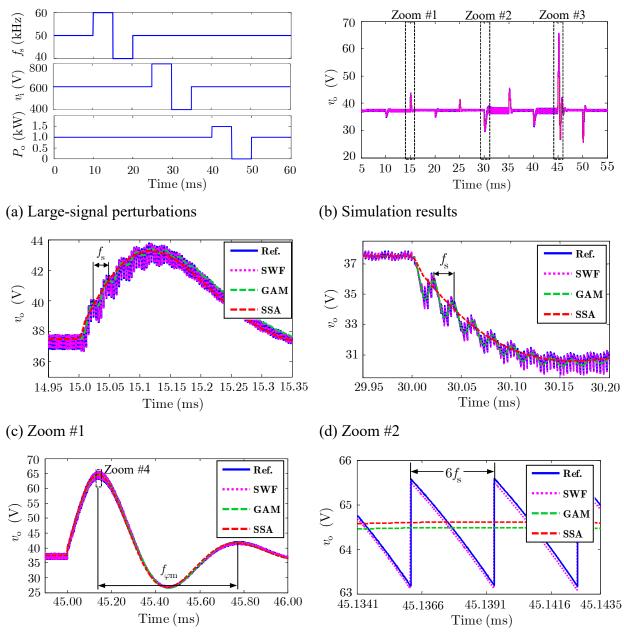
4.4 Performance comparison

In this section, the performance of the GAM model is compared with the SWF, SSA and idealmodel approaches. The main results are provided in sections 4.4.1 to 4.4.5. They are analyzed together in section 4.4.6.

4.4.1 Closed-loop time-domain transient response

The transient response of the GAM model is first compared with equivalent closed-loop ideal, SWF and SSA models in EMTP. As for the validation of the small-signal models in section 3.5.1, model 1 parameters given in Appendix A are used. The integration time-step Δt is equal to 0.01 µs for both the reference (Ref.) and SWF models, and is equal to 0.1 µs for both the GAM and SSA models.

To compare the models, large-signal step-function perturbations are applied as shown in Figure 4.4 a). The resulting output voltages v_o for each model are compared in Figure 4.4 b). Figure 4.4 c) and Figure 4.4 d) demonstrate that the SWF and GAM models consider the transient oscillations at the switching frequency f_s but not the SSA model. Figure 4.4 e) shows that all the models consider the transient oscillations at the cross-over frequency $f_{\varphi m}$ while Figure 4.4 f) indicates that only the SWF model includes the ripple at $6f_s$. The component at $6f_s$ is generally negligible in large signal studies because it is generally significantly attenuated by the output capacitor C_o . Cases where the ripple at $6f_s$ is not negligible are not investigated in this thesis.



(e) Zoom #3

(f) Zoom #4

Figure 4.4 : Simulation results for the validation of the large-signal models

4.4.2 Simulation timing analysis

In system-level analysis, the simulation time-step Δt is generally selected based on the frequency spectra of the targeted power system studies. In the case of many power electronic converters, even for the analysis of low-frequency phenomena, if the switching-effect is modeled, it is necessary to reduce the time-step Δt to typically 1/100th of the switching period $T_s = 1/f_s$ (rule of thumb). In the case of the 3p-DAB, since the ripple is at $6f_s$, it requires having a time-step smaller than around 1/600th of the switching period T_s . This problem has been highlighted in [62] for the specific case of the 3p-DAB converter.

Since the computation time t_{cpu} highly depends on the integration time-step Δt , multiple simulation runs as in Figure 4.4 have been performed to thoroughly compare the models. The results are shown in Figure 4.5. An Intel® CoreTM processor i7-4700HQ (2.4 GHz) with 12 GB of RAM, and EMTP-RV software version 3.4 is used. The total simulation time $t_{sim} = 60$ ms.

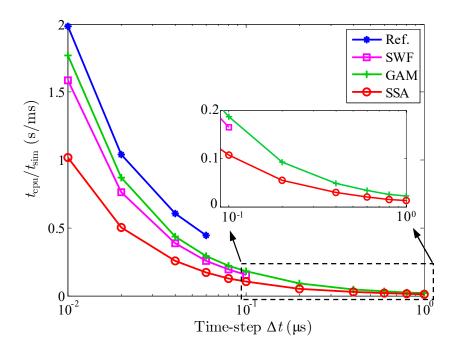


Figure 4.5 : Computation time for different integration time-steps

4.4.3 Transient short-circuit with multiple 3p-DAB

A bolted fault of 20 ms duration is applied at the output of ten (10) 3p-DAB converters connected in parallel. An output VI-characteristic with the parameters of model 1 given in Appendix A is implemented to consider a common converter fault protection scheme [4]. It is implemented as shown in Figure 4.6. The resulting transient short-circuit current for each model are compared in Figure 4.7. The maximum integration time-step obtained in Figure 4.5 is used for each model, i.e., $\Delta t = 0.06 \ \mu s$ for the reference (Ref.) model, $\Delta t = 0.1 \ \mu s$ for the SWF model, and $\Delta t = 1 \ \mu s$ for both the GAM and SSA models. A breakdown analysis of the CPU effort is also shown in Figure 4.8.

Moreover, as mentioned before, diodes D_i and D_o are used to model the clamping effect of the freewheeling diodes on prospective negative voltage at the dc ports. These diodes are always present in practical applications. As an example, the importance of D_o in fault analysis is shown in Figure 4.9 by comparing the ideal-model results with the SWF, GAM and SSA results without D_o . Due to oscillations caused by interaction between C_o and the fault path inductance, the voltage v_o can become negative during transient events such that the transistor free-wheeling diodes are activated, therefore influencing the transient response. An example of the importance of D_i is shown experimentally in [83].

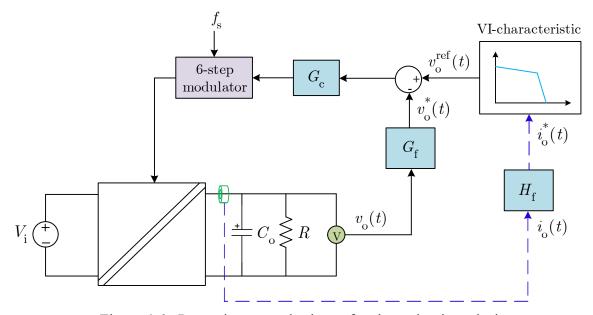


Figure 4.6 : Protection control scheme for short-circuit analysis

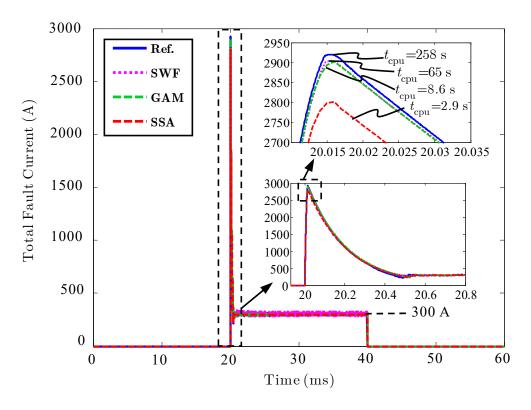


Figure 4.7 : Simulation results for a short-circuit at the output of ten (10) 3p-DAB in parallel.

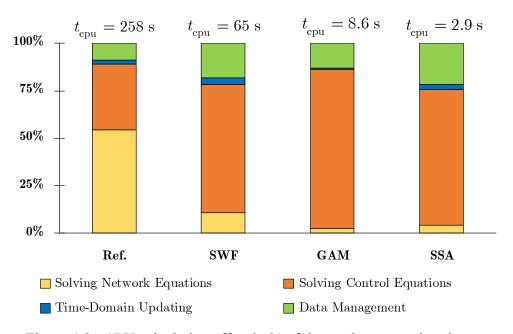


Figure 4.8 : CPU calculation effort in % of the total computation time

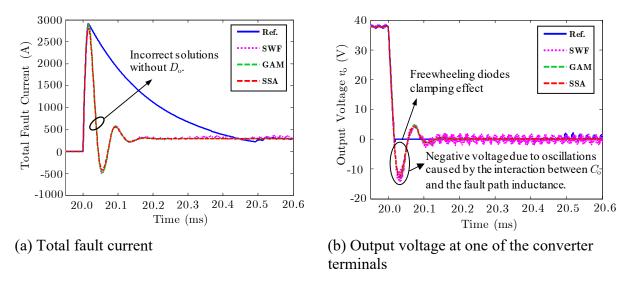


Figure 4.9 : Analysis of the effect of D_0 in fault analysis

4.4.4 Closed-loop frequency-domain response

In Figure 4.4 e), the main frequency component of the oscillations in the closed-loop response is close to the cross-over frequency of the designed PI-controller, i.e. $f_{\varphi m} = 1.5$ kHz. However, as mentioned before, for system-level studies, the frequency content is a function of the aimed power system analysis. This means that, instead of being step-functions, the large-signal perturbations may contain a large spectrum of frequencies.

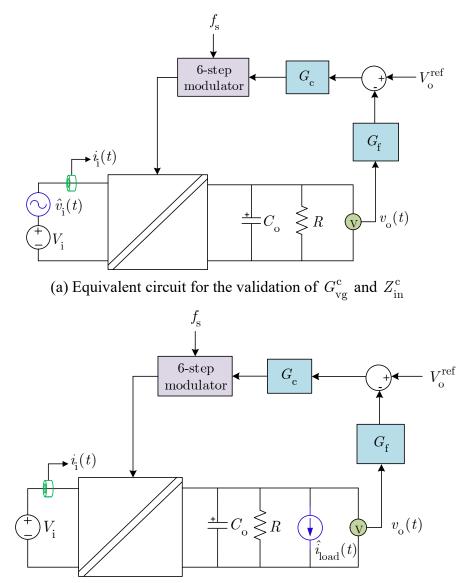
Therefore, the precision of the models at different frequencies is assessed here. For system-level analysis, the input impedance Z_{in}^{c} , the output impedance Z_{o}^{c} , the input-to-output voltage relationship G_{vg}^{c} , and the back-current gain G_{load}^{c} are the main closed-loop transfer functions of interest. The closed-loop input impedance Z_{in}^{c} is defined in (3.121), the closed-loop output impedance Z_{o}^{c} is defined in (3.123), and the closed-loop input-to-output voltage relationship G_{vg}^{c} is defined in (3.124). The back-current gain G_{load}^{c} is defined as follows,

$$G_{\text{load}}^{c}(s) = \frac{\hat{i}_{1}(s)}{\hat{i}_{\text{load}}(s)} \bigg|_{\substack{\hat{v}_{0}^{\text{ref}}(s) = 0\\v_{1}(s) = 0}}$$
(4.25)

The transfer functions G_{vg}^{c} and G_{load}^{c} are particularly interesting because they show how perturbations at the input of the converter propagate at the output and vice versa. The input and

output impedances, defined by Z_{in}^{c} and Z_{o}^{c} respectively, are also relevant because, by definition, they show the local reaction of the converter to perturbations at either one of the ports.

The models are validated by applying sine wave perturbations at different frequencies in the simulation models. The magnitude of the output signals is extracted by FFT analysis using the ScopeView tool in EMTP. The equivalent circuits for the validation of the closed-loop frequency characteristics of the large-signal models are shown in Figure 4.10. The results are shown in Figure 4.11.



(b) Equivalent circuit for the validation of G_{load}^{c} and Z_{o}^{c} Figure 4.10 : Equivalent circuits for the validation of the closed-loop frequency characteristics

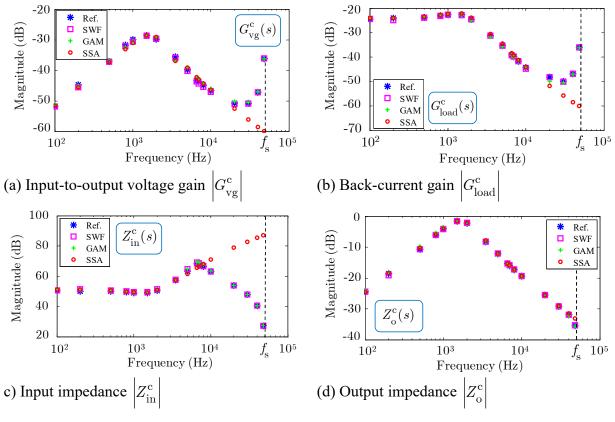


Figure 4.11 : Closed-loop frequency response

4.4.5 Transient analysis with a large-scale test system

The effectiveness and accuracy of the GAM model to study a large-scale system are now validated through a short-circuit test case in which the transient fault current as well as the transient recovery voltage at fault clearing are analyzed. A single line diagram of the test system is shown in Figure 4.12. It is composed of a primary voltage ring bus at 750 V, two intermediate voltage buses at 380 V, and a low voltage ring bus at 110 V. The system is divided into ten (10) identical regions with a total of twenty (20) 3p-DAB converters. All the converters are designed with an LC input filter using the methodology presented in section 3.7. The parameters of the 3p-DAB converters used in this test case are provided in Appendix A (DABXA = model 2, and DABXB = model 3, with X = 1 to 10). Cables are modeled with equivalent RL circuits (not shown in Figure 4.12). Detailed models of battery, circuit breaker detection and arcing, fuse detection and arcing, as well as metal oxide varistors (MOV) are used. Detailed information on these models can be found in [4], [83]–[85].

At t = 20 ms, a 1 m Ω fault is applied in region 6 of the 110 V bus as shown in Figure 4.12. The short-circuit current and the sequence of events are shown in Figure 4.13 while the analysis of the transient voltage (V5 in Figure 4.12) is provided in Figure 4.14. These results confirm that the developed GAM model can be used for accurate grid-level analyses such as fault current evaluation and transient recovery voltage assessment.

Furthermore, for the reference (ref.) model, the integration time-step is $\Delta t = 0.06 \,\mu\text{s}$ and the total computation time is $t_{cpu} = 4565 \,\text{s}$ for a total simulation time $t_{sim} = 50 \,\text{ms}$. This is the largest time-step that can be used to maintain numerical stability. For the GAM model, the integration time-step Δt is increased to 1 μ s such that the total computation time is significantly reduced to $t_{cpu} = 45 \,\text{s}$.

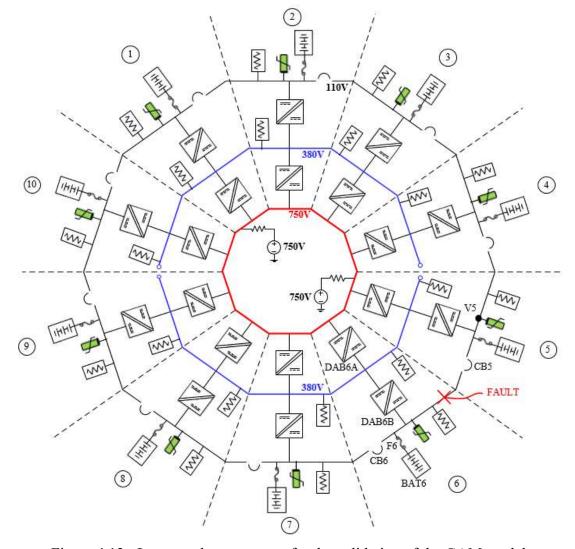
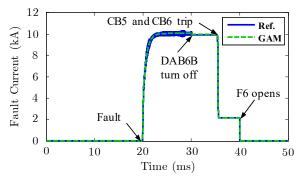
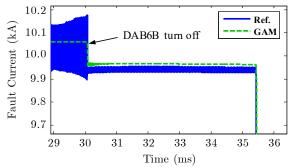


Figure 4.12 : Large-scale test system for the validation of the GAM model

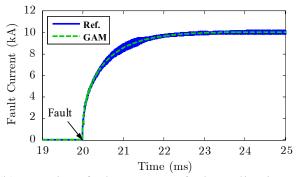




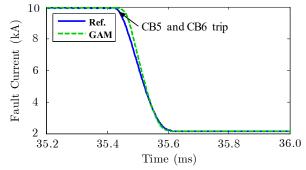
(a) Fault current for the complete simulation run



(c) Transient fault current after DAB6B turn off

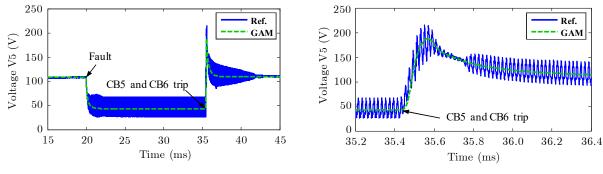


(b) Transient fault current at fault application



(d) Transient fault current during CB5 and CB6 arcing

Figure 4.13 : Analysis of the fault current at fault application and during fault clearing



(a) Voltage at fault application and fault clearing

(b) Transient recovery voltage during arcing of CB5 and CB6

Figure 4.14 : Analysis of the voltage V5 during fault application and fault clearing

4.4.6 Performance comparison

Based on the results obtained in sections 4.4.1 to 4.4.5, the performance of the GAM model is now compared with the implemented ideal, SWF, and SSA models. The analysis mainly focuses on accuracy, frequency response as well as computational speed. The main outcomes regarding these three criteria are summarized in Table 4.1. It leads to the conclusion that the proposed GAM model offers the best compromise in terms of accuracy, speed and wideband response for system-level analysis. Further details on models' implementation are summarized in Table 4.2, while additional justifications are given below.

By first analyzing the time-domain simulation results, it is concluded that all the models accurately represent the averaged dynamic of the converter at the cross-over frequency $f_{\varphi m}$ (Figure 4.4 e)). As for the SWF model, the GAM method also considers the transient component at the switching frequency f_s but it is not the case with SSA (Figure 4.4 c) and d)). The SWF model also considers the output ripple voltage at $6f_s$ which is characteristic of the 3p-DAB while both GAM and SSA neglect this component (Figure 4.4 f)). Even if the transient responses of all the models are very similar for step-function perturbations (Figure 4.4 c) to e)), it can be seen from the frequency-domain analysis in Figure 4.11 that, unlike the SWF and GAM models, the SSA model is not accurate to reproduce the converter behavior for perturbations beyond $1/5^{\text{th}}$ of f_s . These conclusions are summarized in Table 4.1.

Furthermore, from Figure 4.5 and Figure 4.7, it is concluded that both GAM and SSA provide a significant increase in computation speed when compared to the reference and SWF models. This is mainly explained by three fundamental differences. The primary reason is the fact that they both allow the use of larger simulation time-steps Δt which significantly increases the computation speed. It is also explained, to a lesser extent, by the reduction in the size of the network main system of equations (see Table 4.2), which follows the modified-augmented-nodal analysis (MANA) formulation approach [59], [86]. Finally, it is also attributed to the fact that, with the reference and SWF models, the modulator must be completely modeled to obtain the switching-functions, while, for GAM and SSA, it is simply modeled as a gain.

Model	Accuracy ¹	Speed ²	Frequency Response (up to)
Ideal-Model (Ref.)	1	4	$6f_{\rm s}$
Switching-Function (SWF)	1	3	$6f_{s}$
Generalized Averaging (GAM)	2	2	$f_{\rm s}$
State-Space Averaging (SSA)	3	1	$\sim 0.2 f_{\rm s}$

Table 4.1: Large-signal models comparison summary

¹Ranked from 1 (Most accurate) to 3 (Less accurate) ²Ranked from 1 (Fastest) to 4 (Slowest)

Model	Network Nodes	Size of the network main system of equations	Number of non-zeros	Number of solution points in time- domain (n_{iter})	Size of the equivalent control system equations using state-space formulation
Ref.	16 ¹	43 ¹	162 ¹	Eq. (4.27)	-
SWF	18	29	99	Eq. (4.26)	-
GAM	6	8	24	Eq. (4.26)	A :6x6, B :6x2
GAM	6	0	24		C :2x6, D =[0]
SSA	6 8	_	24	Eq. (4.26)	A =[0], B =[0]
		8			C =[0], D =2x2

Table 4.2: Summary of 3p-DAB models' implementation in EMTP

¹ This assumes that each transistor is modeled with an ideal switch in parallel with an ideal free-wheeling diode.

However, as seen in Figure 4.5, for the same Δt , the benefit in computation speed of both the SSA and GAM approaches compared to the reference model is less significant than the reduction in the size of the network main system of equations given in Table 4.2. This is because the size of the network main system of equations cannot be used alone to conclude on the relative simulation speed between models since control system equations are also solved separately from network equations [82], [86]. For the GAM method, the computation burden of solving the larger non-linear and coupled set of control equations defined by (4.8)–(4.18) (as compared to (4.4)–(4.7) for SSA) also explains why, for the same Δt , the computation time in Figure 4.5 is higher with GAM as compared to SSA.

Finally, it is also worth mentioning that the SWF, GAM and SSA models all lead to a reduced number of solution points n_{iter} in time-domain as compared to the reference model (Table 4.2). In fact, for the simplest 3p-DAB model representation, the number of solution points for the SWF, GAM and SSA models is given by,

$$n_{\rm iter} = t_{\rm sim} / \Delta t \tag{4.26}$$

while, for the reference model, it is fundamentally higher by an additional term $12t_{sim}f_{s}$,

$$n_{\rm iter} = t_{\rm sim} \big/ \Delta t + 12 t_{\rm sim} f_{\rm s} \tag{4.27}$$

This difference is explained by the fact that, at each switching event, EMTP switches from a fixed time-step trapezoidal integration method to backward Euler with two half-step $\Delta t/2$ [87]. This feature helps reduce numerical oscillations when ideal switches are used. It is not required to solve the SWF, GAM, and SSA models because the circuit topology is time-invariant. Furthermore, the simultaneous-switching control algorithm [88], which allows a simultaneous solution of network and control system equations at switching events, also explains why, for the same Δt , the computation time is higher with the reference model.

Finally, the results of the short-circuit test cases (Figure 4.7, Figure 4.13 and Figure 4.14) also shows that the developed GAM model can effectively be used for accelerated grid-level analyses. The GAM model reduces the computation time by a factor of 1/30 in the test case of Figure 4.7, while still providing accurate results (error of less than 1% on the peak fault current). As seen in Figure 4.13 and Figure 4.14, the GAM model also provides accurate results for typical power system transient analyses such as the evaluation of transient fault current and transient recovery voltage during arcing of circuit breakers. It also provides a significant reduction in computation time for the large-scale test case (factor of 1/100: 45 s for the GAM model vs 4565 s for the reference model).

CHAPTER 5 OPEN-PHASE FAULT-TOLERANT OPERATION

In this chapter, a new mode for open-phase fault-tolerant operation of the 3p-DAB converter is proposed and confirmed experimentally on the closed-loop GaN-based small-scale prototype. Steady-state analysis is performed to evaluate the impact of open-phase operation on the converter voltage and current waveforms, and its power transfer and soft-switching capabilities. Small-signal analysis along with time-domain simulations and experimental results are also provided to assess the impacts on the converter stability and its transient dynamic performance. The adaptation of the steady-state (Chapter 2) and small-signal (Chapter 3) modeling strategies to open-phase operation is also highlighted.

5.1 Open-phase operation

Upon detection of a fault in a transistor module, intelligent gate drivers can interrupt the gating signals going to the faulty-leg and provide a feedback signal of the fault condition. In frozen leg operation [35] (Figure 5.1), following the detection of a fault in a transistor module, the two transistors of the same phase are open, leaving their free-wheeling diode self-commutated into the circuit. As shown in Figure 5.1, because the diodes are left into the circuit, reactive current can still circulate in the faulty-phase. As shown in Figure 5.2, it is proposed here to use the same fault signal to open the faulty-phase such that it eliminates the interaction of the faulty-leg free-wheeling diodes. As it will be shown in this chapter, due to the circulation of reactive current in the faulty-phase free-wheeling diodes in frozen leg operation, the power transfer capability is reduced, and the current stress in the faulty-bridge may be increased as compared to open-phase. The equivalent circuits for the 3p-DAB in frozen leg and open-phase operations (with phase C open) are shown in Figure 1.6 a) and b) respectively.

Since the prototype is fabricated with GaN FETs, it is important to note here that GaN FETs do not have a body diode, but the channel itself is bidirectional and behaves like a body diode when $V_{GS} = 0$ V, which is the prerequisite to frozen leg operation. However, this "equivalent body diode" can exhibit an important voltage drop. For example, as seen in Figure 5.3 for the EPC2022 (100 V, 90 A) GaN FET, the minimum forward voltage in reverse conduction is 2 V. If GaN FETs are expected to conduct large currents in the reverse direction when $V_{GS} = 0$ V, it may be difficult to ensure that adequate cooling is provided due to their small size (see Appendix B for EPC2022 footprint). The open-phase operation is therefore even more advantageous with GaN FETs since it reduces the circulation of reactive current through the "equivalent body diodes".

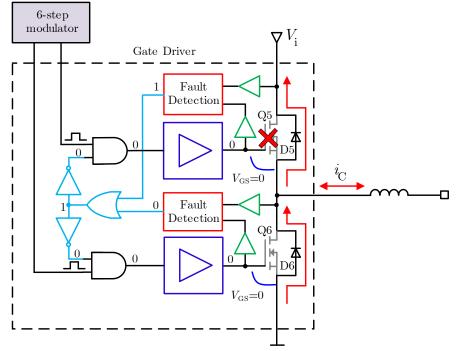


Figure 5.1 : Protection scheme concept for frozen-leg operation (e.g. Fault on Q5, phase C)

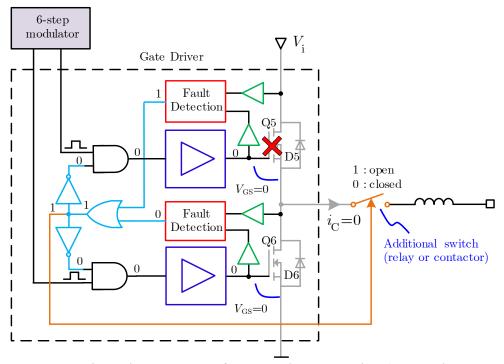


Figure 5.2 : Protection scheme concept for open-phase operation (e.g. Fault on Q5, phase C)

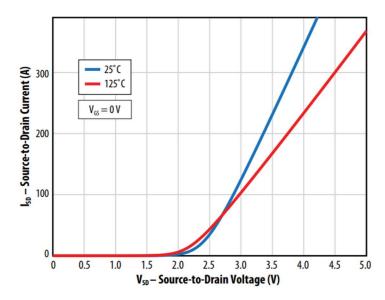


Figure 5.3 : Reverse drain-source characteristic of the EPC2022 GaN FET (100V, 90A) [89]

5.2 Steady-state analysis

5.2.1 Transformer voltage and current waveforms

The steady-state analysis of the 3p-DAB converter in normal operation using the piecewise-linear modeling (PLM) approach has been presented in section 2.2. In open-phase operation, the voltage waveforms are affected and differs from the normal case. As shown in Figure 5.4, if phase C is open, the current in phase C is zero ($i_{\rm C} = 0$). Knowing that the voltage across $L_{\rm s}$ on phase C must also be zero, and assuming that the output bridge is kept in operation, it can be concluded that the voltage $v_{\rm CN}$ on the primary side becomes a function of the output voltage $v_{\rm o}$ as well as the output bridge switching-functions. Following (2.32) given for the normal operation case, the new voltage $v_{\rm CN}$ in open-phase mode is defined as,

$$v_{\rm CN} = m v_{\rm ca} = m \left({\rm s}_5' - {\rm s}_1' \right) v_{\rm o}$$
 (5.1)

In open-phase operation, the output bridge is proposed to be kept operated with the same balanced set of switching-functions such that the following relationship still applies,

$$v_{\rm ab} + v_{\rm bc} + v_{\rm ca} = 0$$
 (5.2)

Therefore, v_{AN} , v_{BN} and v_{CN} remain balanced, and the definition of v_{NO} given in (2.22) for the normal operation case still stands in open-phase operation. While the voltages applied by the input

bridge on phases A and B (v_{AO} and v_{BO}) are still given by (2.23) and (2.24) respectively, the voltage v_{CO} is no longer applied by the input bridge since phase C is open. Nevertheless, the application of KVL on phase C still requires that,

$$v_{\rm CO} = v_{\rm CN} + v_{\rm NO} \tag{5.3}$$

Inserting (2.23), (2.24) and (5.3) into the original definition of $v_{\rm NO}$ given in (2.22), with $v_{\rm CN}$ given by (5.1), the voltage $v_{\rm NO}$ for the open-phase operation is reduced to,

$$v_{\rm NO} = \frac{\left(s_1 + s_3\right)v_{\rm i} + m\left(s_5' - s_1'\right)v_{\rm o}}{2}$$
(5.4)

Furthermore, inserting (5.4) into the definitions of v_{AN} and v_{BN} given by (2.19) and (2.20) respectively, the new voltages v_{AN} and v_{BN} are defined by,

$$v_{\rm AN} = \frac{\left(s_1 - s_3\right)v_{\rm i} + m\left(s_1^{\rm i} - s_5^{\rm i}\right)v_{\rm o}}{2}$$
(5.5)

$$v_{\rm BN} = \frac{\left(s_3 - s_1\right)v_{\rm i} + m\left(s_1^{'} - s_5^{'}\right)v_{\rm o}}{2}$$
(5.6)

The results obtained in (5.5) and (5.6) lead to an important conclusion: in open-phase operation, the voltages v_{AN} and v_{BN} are now dependent on the output bridge switching-functions and the output voltage v_o . This is a fundamental difference with normal operation for which v_{AN} and v_{BN} are only dependent on the input bridge switching-functions and the input voltage v_i .

Moreover, in open-phase operation, since the transformer primary winding is floating Y, the following relationship apply for both the normal and open-phase operation cases,

$$i_{\rm A} + i_{\rm B} + i_{\rm C} = 0$$
 (5.7)

Since $i_{\rm C} = 0$, it leads to the other important conclusion that the phase A and B currents are now out of phase,

$$i_{\rm A} = -i_{\rm B} \tag{5.8}$$

In Figure 5.5, the resulting theoretical waveforms for phase A in range 1 and range 2 are compared with the normal operation case. Phase B can be obtained by applying a similar procedure (not shown here). For the same voltage and loading conditions, experimental waveforms for phase A are provided to compare the normal (Figure 5.6) and open-phase (Figure 5.7) operations. The

results are in accordance with Figure 5.5 a) for the normal case and Figure 5.5 d) for the openphase case. The operating points in Figure 5.6 and Figure 5.7 are referred in the next sections as the "main test cases".

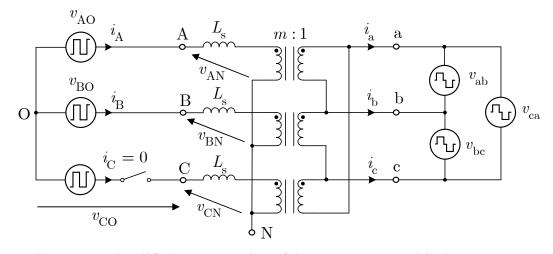
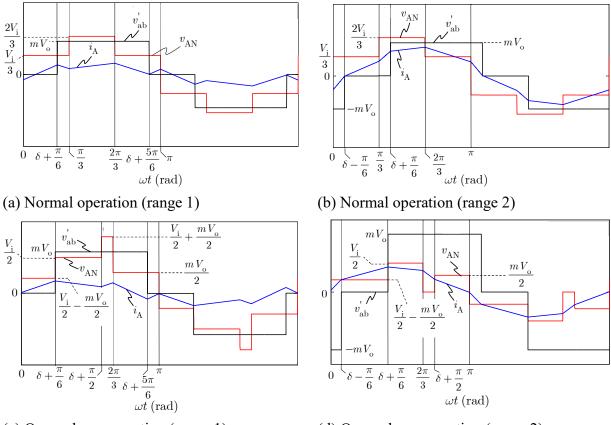


Figure 5.4 : Simplified representation of the Y- Δ 3p-DAB with phase C open



(c) Open-phase operation (range 1)(d) Open-phase operation (range 2)Figure 5.5 : Theoretical waveforms for phase A (normal vs open-phase)

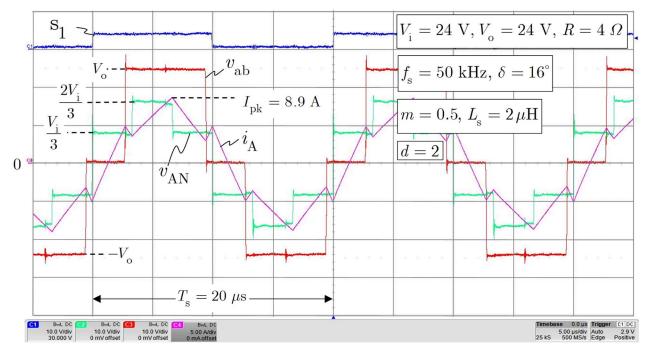
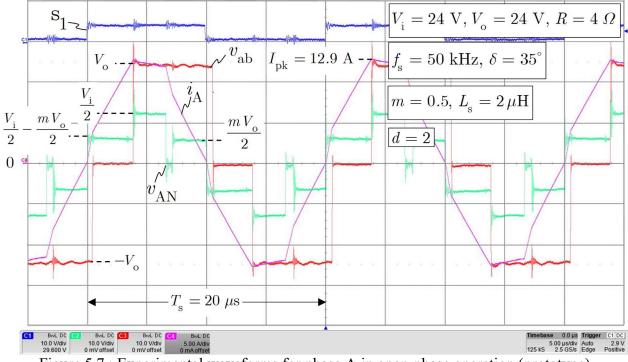
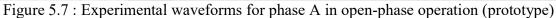


Figure 5.6 : Experimental waveforms for phase A in normal operation (prototype)





Knowing the values of the switching-functions (either 0 or 1) for each mode defined in Appendix F over $\theta = [0, \pi]$, and taking the assumption that the input and output voltages are constant, i.e. $v_i = V_i$ and $v_o = V_o$, it is possible to evaluate v_{AN} , v_{BN} and v_{CN} using (5.5), (5.6) and (5.1)

respectively. It then allows solving the current equations for phase A $i_A(\theta)$ and phase B $i_B(\theta)$ for each mode using (2.16) and (2.17). The resulting current equations for each mode, and for the two phase-shift δ operating ranges are given in Appendix F. From the knowledge of $i_A(\theta)$, the output bridge currents can be derived. It can be demonstrated that,

$$i_{\rm a} = i_{\rm c} = m i_{\rm A} \tag{5.9}$$

$$i_{\rm b} = -2mi_{\rm A} \tag{5.10}$$

A similar steady-state analysis can be performed if one phase is open on the secondary side. In this case, it is the secondary voltage waveforms v'_{ab} , v'_{bc} and v'_{ca} that are modified instead of v_{AN} , v_{BN} and v_{CN} . For example, if phase c is open on the secondary side, the voltage v'_{ab} remains the same as in normal operation but v'_{bc} and v'_{ca} will depend on the input voltage v_i due to coupling with the voltages applied by the input bridge on the primary side of the three-phase transformer.

5.2.2 Power transfer relationship

The procedure to derive the power transfer relationship of the 3p-DAB in open-phase operation is similar to normal operation in section 2.2.3. It requires averaging the output current which is function of the transformer currents as well as the output bridge switching-functions given in Appendix F. In open-phase operation, because $i_{\rm C} = 0$, equation (2.34) is reduced to,

$$i_{o} = m \left[(s'_{1} - s'_{3})i_{A} + (s'_{3} - s'_{5})i_{B} \right]$$
 (5.11)

Due to the symmetrical operation of the 3p-DAB in normal operation, the analysis in section 2.2.3 has been only performed over the interval $\theta = [0, \pi/3]$, i.e. for modes I and II. Because of the unbalanced operation in open-phase, it requires analyzing the transformer currents over the interval $\theta = [0, \pi]$, i.e. for modes I to V.

The procedure to calculate the average output current for each mode is similar to normal operation. The results are given in Appendix F. Knowing the average currents for modes I to V, the average output current I_{o} is calculated. For range 1, I_{o} is calculated by applying the following averaging operation,

$$I_{o} = \frac{1}{\pi} \begin{bmatrix} \left(\delta + \frac{\pi}{6}\right) \cdot \left\langle i_{o}\right\rangle \Big|_{\mathrm{I}} + \frac{\pi}{3} \cdot \left\langle i_{o}\right\rangle \Big|_{\mathrm{II}} + \left(\frac{\pi}{6} - \delta\right) \cdot \left\langle i_{o}\right\rangle \Big|_{\mathrm{III}} \\ + \left(\delta + \frac{\pi}{6}\right) \cdot \left\langle i_{o}\right\rangle \Big|_{\mathrm{IV}} + \left(\frac{\pi}{6} - \delta\right) \cdot \left\langle i_{o}\right\rangle \Big|_{\mathrm{V}} \end{bmatrix}$$
(5.12)

which, using (F.25) to (F.29) yields to,

$$I_{o} = \frac{mV_{i}}{2\omega_{s}L_{s}}\delta$$
(5.13)

such that,

$$P_{\rm o} = V_{\rm o} I_{\rm o} = \frac{m V_{\rm i} V_{\rm o}}{2\omega_{\rm s} L_{\rm s}} \delta$$
(5.14)

Applying a similar procedure in range 2 with the use of (F.30) to (F.34), it leads to,

$$P_{\rm o} = \frac{mV_{\rm i}V_{\rm o}}{2\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(\delta - \frac{\delta^2}{\pi} \right) - \frac{\pi}{24} \right]$$
(5.15)

By comparing (5.14) with (2.37), and (5.15) with (2.38), it is concluded that the power transfer capability of the 3p-DAB in open-phase operation is theoretically half the one in normal operation. For the experimental prototype parameters (model 4), the power transfer relationships in normal, frozen leg and open-phase operations are compared in Figure 5.8. Experimental results are included to validate the theoretical models. For the frozen leg mode, the power transfer capability is evaluated using time-domain simulations. For the normal and open-phase cases, the errors between the theoretical and the experimental results are mainly due to losses (5-10%) and the dead-time (0.1 μ s) which are both not considered in the theoretical model. As the power increases, losses reduce the accuracy of the theoretical models for the open-phase and frozen leg modes. The power transfer capability is also smaller than expected by the theoretical prediction.

As shown in Figure 5.8, when the converter is operated in closed-loop with regulated output voltage $v_0 = V_0$ and a resistive load R, the output power P_0 must remain constant, such that, in frozen leg and open-phase conditions, the control phase-shift δ will naturally be increased to meet the new operating conditions. In frozen leg operation, due to the circulation of reactive current in the faulty-phase free-wheeling diodes, the power transfer capability is reduced as compared to open-phase.

However, for conditions where there is no reactive current circulating in the faulty-phase, it can be demonstrated that frozen leg operation is virtually equivalent to open-phase operation, such that, in these conditions, the power transfer capabilities are the same.

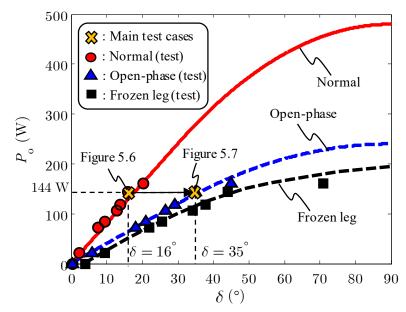


Figure 5.8 : Comparison of the power transfer capabilities (model 4/prototype)

5.2.3 Current stress in the faulty-bridge

The results in Figure 5.9 show the rms current stress for each phase in the faulty-bridge for different voltage and loading conditions. These results are obtained from time-domain simulations in EMTP. Experimental results are also added.

By analyzing the results for the currents in phase A and phase B, it is concluded that the current stress on the switches is higher in frozen leg operation as compared to the open-phase case due to reactive current circulating in the faulty phase C in frozen leg operation. It is also concluded that this advantage of open-phase over frozen leg increases as d decreases.

Moreover, it is remarked that the rms current in phase A and phase B is generally higher in frozen leg and open-phase operations as compared to normal operation. This is an intuitive result since the total active power is transmitted through the two remaining phases in frozen leg and open-phase operations. For low power, it can however be noted that the rms current in phase A and phase B can be lower in frozen leg and open-phase operations as compared to normal operation. This is a counterintuitive result, but this is due to modifications in the voltage and current waveforms.

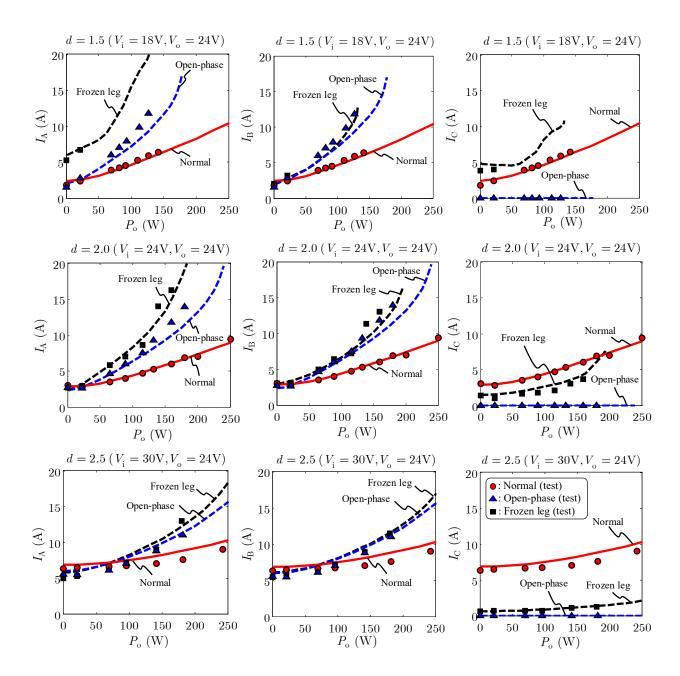


Figure 5.9 : RMS current for different voltage and loading conditions (model 4/prototype)

Note that, for d = 1.5, it was not possible to test the prototype for output power higher than 25 W because of the high reactive component circulating through the "equivalent body diodes" of the GaN transistors Q5 and Q6 in phase C. At this operating point, the conduction losses in Q5 and Q6 of phase C are estimated to 3.75 W/transistor as compared to 13 mW/transistor in normal operation. In frozen leg, this led to rapid failure of the GaN transistors because of the very high-power loss concentration (approximately 0.27 W/mm² for the EPC2022).

5.2.4 ZVS soft-switching regions

The procedure to evaluate the theoretical ZVS regions for the 3p-DAB in normal operation has been presented in section 2.2.4. The results are recalled here in Figure 5.10 a). A similar procedure can be applied in open-phase operation, but, since the currents are unbalanced, each phase must be studied independently. For phase A of the input bridge, it leads to the following ZVS boundaries. For range 1 ($0 \le \delta \le \pi/6$), it results in,

$$d > \frac{3\pi + 6\delta}{2\pi} \tag{5.16}$$

and for range 2 ($\pi/6 \le \delta \le \pi/2$), it yields to,

$$d > \frac{5\pi - 6\delta}{2\pi} \tag{5.17}$$

For phase a of the output bridge, it leads to the following ZVS boundary in range 1,

$$d < \frac{2\pi}{\pi - 6\delta} \tag{5.18}$$

For range 2, there is no ZVS boundary which means that ZVS is always provided in phase a of the output bridge for loading conditions where $\delta \ge \pi/6$. The results for phase A (input bridge) and phase a (output bridge) in open-phase are shown in Figure 5.10 b).

The theoretical results in Figure 5.10 are also compared with the main test case experimental results. As expected for the normal operation case in Figure 5.6, i.e. that $i_A < 0$ at turn-on of S1, the input bridge is soft-switched in Figure 5.10 a). Since it is operated far from its hard-switching boundary, the reactive component of i_A is significant in Figure 5.6. In open-phase operation, according to Figure 5.10. b), the phase A of the input bridge should be soft-switched but operated very close its hard-switching boundary. According to Figure 5.7, i_A is close to zero at turn-on of S1 which means that it is indeed operated close to its ZVS boundary.

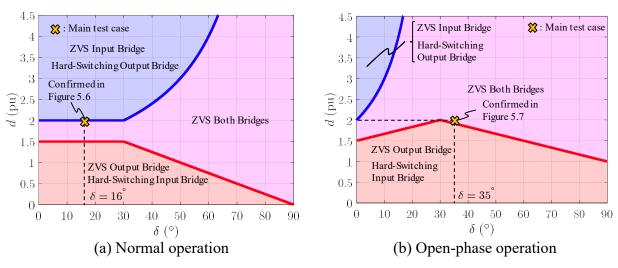


Figure 5.10 : ZVS regions (normal vs open-phase)

The results in Figure 5.10 a) show that it is advantageous from a soft-switching standpoint to operate the 3p-DAB in normal operation with a voltage conversion ratio d ranging between 1.5 and 2. It is possible in applications where the input voltage V_i is tightly regulated by a pre-regulating stage and the output voltage V_0 is held constant by the DAB converter.

In open-phase, a similar analysis can be performed for the other phases. In fact, ZVS happens in phase B of the input bridge if $i_{\rm B} < 0$ at turn-on of S3, and ZVS take place in phase b of the output bridge if $i_{\rm b} > 0$ at turn-on of S3'. Since phase C is open, the ZVS analysis is not applicable for phase C of the input bridge but ZVS occurs in phase c of the output bridge if $i_{\rm c} > 0$ at turn-on of S5'. Since $i_{\rm A} = -i_{\rm B}$ and $i_{\rm C} = 0$, it has been shown previously that the output bridge currents $i_{\rm a}$, $i_{\rm b}$ and $i_{\rm c}$ can all be derived from the knowledge of $i_{\rm A}$. This simplifies the ZVS analysis for the other phases. The results for all the phases are compared in Figure 5.11; the darker the region, the more phases are soft-switched. A detailed analysis of the relationship between the output power $P_{\rm o}$, the conversion ratio d and the ZVS boundaries is also provided for all phases in Figure 5.12. For the per-unit conversion, the following bases are taken,

$$V_{\rm b} = mV_{\rm o}, \ P_{\rm b} = \frac{\left(mV_{\rm o}\right)^2}{\omega_{\rm s}L_{\rm s}}$$
 (5.19)

For the normal case, the results in Figure 5.12 a) are valid for all phases. In open-phase operation, the results are given in Figure 5.12 b) to d). From these results, it is concluded that, in open-phase

operation, for d = 2, the input bridge is always ZVS, but only phase a and phase b in the output bridge are ZVS. The whole output bridge is also always ZVS for a voltage conversion ratio d < 1.

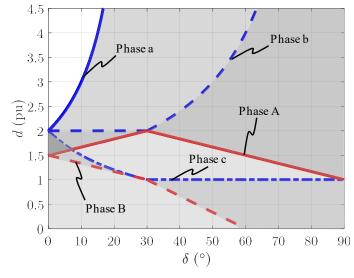


Figure 5.11 : ZVS regions for all phases in open-phase

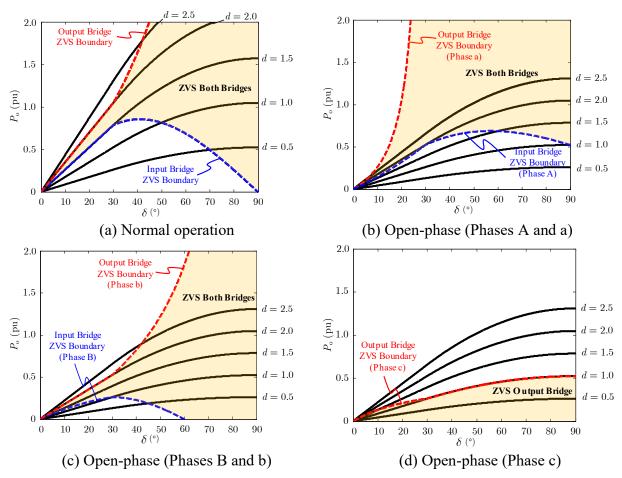


Figure 5.12 : Output power versus the control phase-shift with d as a parameter

5.3 Small-signal analysis

5.3.1 State-space averaging (SSA) model in open-phase

In this section, the state-space averaging (SSA) methodology presented in section 3.2 is extended for the determination of the transfer functions in open-phase operation. The model is still defined using the same general state-space formulation given in (3.6)–(3.8). The average input and output currents are still necessary to solve the output capacitor equation (3.9) as well as the input current equation (3.10). As for the PLM approach, due to the unbalanced operation of the 3p-DAB in openphase operation, the procedure needs to be applied for modes I to V instead of modes I and II only. The average currents for modes I to V in open-phase operation are given in Appendix F. The averaged matrices **A**, **B**, **C**, and **D** are then obtained by averaging their respective state-space matrices for modes I to V. Taylor's linearization technique is used afterward to linearize the nonlinear terms around the operating point calculated using (5.14) or (5.15) depending on the control phase-shift range. It results in the following small-signal state-space equations. For range 1,

$$\frac{\mathrm{d}\hat{v}_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{-1}{RC_{\mathrm{o}}}\right)}_{\hat{i}}\hat{v}_{\mathrm{o}}(t) + \underbrace{\left[\frac{mV_{\mathrm{i}}}{2\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\frac{m\delta}{2\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right]}_{\hat{i}}\begin{vmatrix}\hat{\delta}(t)\\\hat{v}_{i}(t)\end{vmatrix}$$
(5.20)

$$\begin{bmatrix} \hat{v}_{o}(t) \\ \hat{i}_{i}(t) \end{bmatrix} = \begin{bmatrix} 1 \\ \frac{m\delta}{2\omega_{s}L_{s}} \end{bmatrix} \hat{v}_{o}(t) + \begin{bmatrix} 0 & 0 \\ \frac{mV_{o}}{2\omega_{s}L_{s}} & 0 \\ \frac{mV_{o}}{2\omega_{s}L_{s}} & 0 \end{bmatrix} \begin{bmatrix} \hat{\delta}(t) \\ \hat{v}_{i}(t) \end{bmatrix}$$
(5.21)

and range 2,

$$\frac{\mathrm{d}\hat{v}_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{-1}{RC_{\mathrm{o}}}\right)}_{\hat{\mathbf{A}}}\hat{v}_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{2\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right)}_{\hat{\mathbf{A}}}\left[\left[\frac{3}{2}\left(1-\frac{2\delta}{\pi}\right)\right]V_{\mathrm{i}}\left[\frac{3}{2}\left(\delta-\frac{\delta^{2}}{\pi}\right)-\frac{\pi}{24}\right]\right]\left[\hat{\delta}(t)\right]}_{\hat{\mathbf{B}}} \quad (5.22)$$

$$\frac{\hat{\mathbf{B}}}{\hat{\mathbf{B}}}$$

$$\begin{bmatrix}\hat{v}_{\mathrm{o}}(t)\\\hat{i}_{\mathrm{i}}(t)\end{bmatrix} = \underbrace{\left[\frac{m}{2\omega_{\mathrm{s}}L_{\mathrm{s}}}\left[\frac{3}{2}\left(\delta-\frac{\delta^{2}}{\pi}\right)-\frac{\pi}{24}\right]}_{\hat{\mathbf{C}}}\hat{v}_{\mathrm{o}}(t) + \underbrace{\left[\frac{0}{2\omega_{\mathrm{s}}L_{\mathrm{s}}}\left[\frac{3}{2}\left(1-\frac{2\delta}{\pi}\right)\right]0\right]}_{\hat{\mathbf{D}}}\left[\hat{\delta}(t)\right]}_{\hat{\mathbf{U}}_{\mathrm{i}}(t)\end{bmatrix} \quad (5.23)$$

By transferring (5.20)–(5.23) into the frequency-domain, the SSA model allows deriving the equivalent small-signal circuits for the 3p-DAB in open-phase operation (Figure 5.13). As for the normal operation case, these equivalent circuits are then used to derive the converter transfer functions $G_{\rm vd}$, $G_{\rm vg}$, $Z_{\rm D}$, $Z_{\rm N}$ and $Z_{\rm o}$ in open-phase operation. The resulting transfer functions are compared with the normal operation case in Table 5.1.

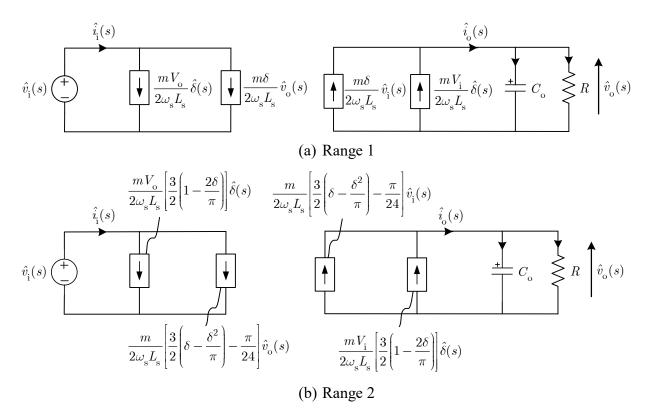


Figure 5.13 : Equivalent SSA small-signal circuits in open-phase operation

The analysis of the results given in Table 5.1 first shows that, in open-phase operation, the gain of the control-to-output transfer function $G_{\rm vd}$ is half the gain in normal operation. This means that, according to SSA, the gain of $G_{\rm vd}$ in open-phase is expected to be -6 dB inferior to the normal case. While it may look that it is also the case in range 2, the gain of $G_{\rm vd}$ depends on the control phase-shift δ which, as previously mentioned, for the same input and output voltages as well as loading conditions, must increase in open-phase condition as shown in Figure 5.8. Nevertheless, it is also concluded that the phase of $G_{\rm vd}$ is not affected in open-phase operation.

Moreover, the gain of the input-to-output transfer function G_{vg} in open-phase operation is also expected to be half (or -6 dB) the gain in normal operation. This suggests that perturbations on the

Furthermore, according to the results in Table 5.1, the gain of the driving point input impedance Z_D is expected to increase by a factor 4 (+12 dB), and the gain of the null driving point input impedance Z_N is expected to increase by a factor 2 (+6 dB). As discussed in section 3.7, according to Middlebrook's theorem [55], for a given input filter, it suggests that the converter should be more stable in open-phase operation. In other words, this indicates that an input filter designed considering the normal conditions should not lead to instability in open-phase operation. However, it will also be shown in section 5.3.3 that the conclusions may be different if the increase in the control phase-shift δ in open-phase condition is considered.

condition such that the conclusions may be different. This will be demonstrated in section 5.3.3.

Finally, according to the SSA model, the perturbation $\hat{\delta}$ that needs to be injected for the evaluation of $Z_{\rm N}$ can be calculated using the same relationships as for the normal operation case given in (3.24) and (3.25). Moreover, since the output impedance $Z_{\rm o}$ only depends on the load R and the output capacitor $C_{\rm o}$, it is concluded that it is not affected in open-phase operation.

Normal operation		Open-phase operation	
For range 1 ($0 \le \delta \le \pi/6$):			
$G_{\rm vd}(s) = \left(\frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}}\right) \frac{1}{sRC_{\rm o} + 1}$	(5.24)	$G_{\rm vd}(s) = \left(\frac{mRV_{\rm i}}{2\omega_{\rm s}L_{\rm s}}\right) \frac{1}{sRC_{\rm o} + 1}$	(5.25)
$G_{\rm vg}(s) = \left(\frac{mR\delta}{\omega_{\rm s}L_{\rm s}}\right) \frac{1}{sRC_{\rm o} + 1}$	(5.26)	$G_{\rm vg}(s) = \left(\frac{mR\delta}{2\omega_{\rm s}L_{\rm s}}\right) \frac{1}{sRC_{\rm o}+1}$	(5.27)
$Z_{\rm D}(s) = \left(\frac{\omega_{\rm s} L_{\rm s}}{m\delta}\right)^2 \frac{sRC_{\rm o} + 1}{R}$	(5.28)	$Z_{\rm D}(s) = 4 \left(\frac{\omega_{\rm s} L_{\rm s}}{m \delta}\right)^2 \frac{s R C_{\rm o} + 1}{R}$	(5.29)
$Z_{\rm N}(s) = -\frac{\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}\delta}$	(5.30)	$Z_{\rm N}(s) = -\frac{2\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}\delta}$	(5.31)
For range 2 ($\pi/6 \le \delta \le \pi/2$):			
$G_{\rm vd}(s) = \frac{\left(\frac{mRV_{\rm i}}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)\right]\right)}{sRC_{\rm o} + 1}$	(5.32)	$G_{\rm vd}(s) = \frac{\left(\frac{mRV_{\rm i}}{2\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(1 - \frac{2\delta}{\pi}\right)\right]\right)}{sRC_{\rm o} + 1}$	(5.33)
$G_{\rm vg}(s) = \frac{\left(\frac{mR}{\omega_{\rm s}L_{\rm s}} \left[\frac{3}{2} \left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]\right)}{sRC_{\rm o} + 1}$	(5.34)	$G_{\rm vg}(s) = \frac{\left(\frac{mR}{2\omega_{\rm s}L_{\rm s}}\left[\frac{3}{2}\left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]\right)}{sRC_{\rm o} + 1}$	(5.35)
$\begin{split} Z_{\mathrm{D}}(s) = \frac{sRC_{\mathrm{o}} + 1}{R \! \left(\! \frac{m}{\omega_{\mathrm{s}} L_{\mathrm{s}}} \! \left[\! \frac{3}{2} \! \left(\! \delta - \! \frac{\delta^2}{\pi} \right) \! - \! \frac{\pi}{24} \right]\! \right)^2} \end{split}$	(5.36)	$Z_{\rm D}(s) = \frac{4\left(sRC_{\rm o}+1\right)}{R\left(\frac{m}{\omega_{\rm s}L_{\rm s}}\left[\frac{3}{2}\left(\delta-\frac{\delta^2}{\pi}\right)-\frac{\pi}{24}\right]\right)^2}$	(5.37)
$Z_{\rm N}(s) = \frac{\left(-\frac{\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}}\right)}{\left[\frac{3}{2}\left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]}$ Note: the differences between the normal		$Z_{\rm N}(s) = \frac{\left(-\frac{2\omega_{\rm s}L_{\rm s}V_{\rm i}}{mV_{\rm o}}\right)}{\left[\frac{3}{2}\left(\delta - \frac{\delta^2}{\pi}\right) - \frac{\pi}{24}\right]}$	(5.39)

Table 5.1: Comparison of the transfer functions in normal and open-phase operations

5.3.2 Generalized state-space averaging (GSSA) model in open-phase

While SSA is derived in the previous section because it provides a better mathematical insight of the impacts of open-phase on the transfer functions, it has been concluded in section 3.5.2 that it is of limited use for small-signal modeling of the 3p-DAB at frequencies higher than $1/5^{\text{th}}$ of f_{s} . SSA is generally precise enough for controller sizing with G_{vd} and for the evaluation of Z_{o} , but it lacks precision for the evaluation of G_{vg} , Z_{D} , and Z_{N} .

As discussed before, to overcome the limitations of SSA for DAB converters, the generalized statespace averaging (GSSA) approach [56] is used. Since GSSA is valid for the unbalanced case, it may be tempted to think that the open-phase case simply consists in replacing L_A , L_B , or L_C by a large value and recalculate the operating point using (5.14) and (5.15) to consider the fact that one phase is open. This has been proved not to be the case.

In fact, in open-phase operation, since the primary voltages v_{AN} and v_{BN} both depend on the output bridge switching-functions as well as the output voltage v_o the derivation of the GSSA model for the open-phase case requires to rewrite the converter differential equations. Using (2.30), (2.31), (5.5), and (5.6) for defining the voltages applied to L_A and L_B the following current differential equations are obtained,

$$\frac{di_{\rm A}}{dt} = \frac{1}{2L_{\rm s}} \left(\left[{\rm s}_1 - {\rm s}_3 \right] v_{\rm i} - m \left[{\rm s}_1^{'} - 2 {\rm s}_3^{'} + {\rm s}_5^{'} \right] v_{\rm o} \right)$$
(5.40)

$$\frac{di_{\rm B}}{dt} = \frac{1}{2L_{\rm s}} \left(\left[{{\rm s}_3 - {\rm s}_1 } \right]v_{\rm i} + m \left[{{\rm s}_1^{'} - 2{\rm s}_3^{'} + {\rm s}_5^{'} } \right]v_{\rm o} \right)$$
(5.41)

It must be noted in (5.40) and (5.41) that it is assumed that the series inductance is the same for phases A and B, i.e. that, $L_{\rm A} = L_{\rm B} = L_{\rm s}$. Then, using (5.11), the output capacitor voltage differential equation given in (3.9) is obtained,

$$\frac{dv_{o}}{dt} = \frac{m}{C_{o}} \left(\left[s_{1}' - s_{3}' \right] i_{A} + \left[s_{3}' - s_{5}' \right] i_{B} \right) - \frac{v_{o}}{RC_{o}}$$
(5.42)

As for the normal operation case, it is also convenient to add the input current equation into the GSSA model. In open-phase operation, it is reduced to,

$$i_1 = s_1 i_A + s_3 i_B \tag{5.43}$$

By making similar assumptions as for the normal case, the model considers the index k = 0 (dc) term of v_i , i_i , and v_o , and the index $k = \pm 1$ (fundamental frequency) terms of i_A and i_B . Therefore, in open-phase operation, the following assumptions are made for the GSSA model,

$$v_{i} = \left\langle v_{i} \right\rangle_{0}, i_{i} = \left\langle i_{i} \right\rangle_{0}, v_{o} = \left\langle v_{o} \right\rangle_{0}$$

$$(5.44)$$

$$i_{A} = \langle i_{A} \rangle_{1} = \langle i_{A} \rangle_{1}^{R} + j \langle i_{A} \rangle_{1}^{I} = \langle i_{A} \rangle_{-1}^{I}$$

$$i_{B} = \langle i_{B} \rangle_{1} = \langle i_{B} \rangle_{1}^{R} + j \langle i_{B} \rangle_{1}^{I} = \langle i_{B} \rangle_{-1}^{*}$$
(5.45)

Since the there is no change to the switching-pattern, there is also no change in the switching-functions definition such that (3.54) and (3.55) still apply. Consequently, their average values are still given by (3.74)–(3.79).

By taking the average on both sides of (5.42) and (5.43), and using property 1 (*differentiation with respect to time*), the index-0 averaged model equations are obtained,

$$\frac{\mathrm{d}\langle v_{\mathrm{o}}\rangle_{0}}{\mathrm{d}t} = \frac{m}{C_{\mathrm{o}}} \left[\left\langle \mathbf{s}_{1}^{'}i_{\mathrm{A}} \right\rangle_{0} - \left\langle \mathbf{s}_{3}^{'}i_{\mathrm{A}} \right\rangle_{0} + \left\langle \mathbf{s}_{3}^{'}i_{\mathrm{B}} \right\rangle_{0} - \left\langle \mathbf{s}_{5}^{'}i_{\mathrm{B}} \right\rangle_{0} \right] - \frac{\left\langle v_{\mathrm{o}} \right\rangle_{0}}{RC_{\mathrm{o}}}$$
(5.46)

$$\left\langle i_{i}\right\rangle_{0} = \left\langle s_{1}i_{A}\right\rangle_{0} + \left\langle s_{3}i_{B}\right\rangle_{0}$$
(5.47)

Similarly, by taking the average on both sides of (5.40) and (5.41), and using property 1 (*differentiation with respect to time*), the index-1 averaged model equations are given by,

$$\frac{\mathrm{d}\langle i_{\mathrm{A}}\rangle_{1}}{\mathrm{d}t} = \frac{1}{2L_{\mathrm{s}}} \Big[\langle \mathbf{s}_{1}v_{\mathrm{i}}\rangle_{1} - \langle \mathbf{s}_{3}v_{\mathrm{i}}\rangle_{1} - m \Big[\langle \mathbf{s}_{1}^{'}v_{\mathrm{o}}\rangle_{1} - 2 \langle \mathbf{s}_{3}^{'}v_{\mathrm{o}}\rangle_{1} + \langle \mathbf{s}_{5}^{'}v_{\mathrm{o}}\rangle_{1} \Big] - j\omega_{\mathrm{s}} \langle i_{\mathrm{A}}\rangle_{1}$$
(5.48)

$$\frac{\mathrm{d}\langle i_{\mathrm{B}}\rangle_{1}}{\mathrm{d}t} = \frac{1}{2L_{\mathrm{s}}} \left[\left\langle \mathbf{s}_{3}v_{\mathrm{i}} \right\rangle_{1} - \left\langle \mathbf{s}_{1}v_{\mathrm{i}} \right\rangle_{1} + m \left(\left\langle \mathbf{s}_{1}'v_{\mathrm{o}} \right\rangle_{1} - 2\left\langle \mathbf{s}_{3}'v_{\mathrm{o}} \right\rangle_{1} + \left\langle \mathbf{s}_{5}'v_{\mathrm{o}} \right\rangle_{1} \right) \right] - j\omega_{\mathrm{s}} \left\langle i_{\mathrm{B}} \right\rangle_{1}$$
(5.49)

Applying property 2 (*average of product*) to (5.46)–(5.49), writing the system of equation using the state-space formulation, and linearizing the resulting set of equations, it results in the following averaged small-signal state-space matrices,

$$\hat{\mathbf{A}} = \begin{bmatrix} \frac{-1}{RC_{o}} & A_{12} & A_{13} & A_{14} & A_{15} \\ A_{21} & 0 & \omega_{s} & 0 & 0 \\ A_{31} & -\omega_{s} & 0 & 0 & 0 \\ A_{41} & 0 & 0 & 0 & \omega_{s} \\ A_{51} & 0 & 0 & -\omega_{s} & 0 \end{bmatrix}, \quad \hat{\mathbf{B}} = \begin{bmatrix} B_{11} & 0 \\ B_{21} & \frac{\sqrt{3}}{4\pi L_{s}} \\ B_{31} & \frac{-3}{4\pi L_{s}} \\ B_{41} & \frac{-\sqrt{3}}{4\pi L_{s}} \\ B_{41} & \frac{-\sqrt{3}}{4\pi L_{s}} \\ B_{51} & \frac{3}{4\pi L_{s}} \end{bmatrix}, \quad \hat{\mathbf{C}} = \begin{bmatrix} 1 & 0 & 0 & 0 & 0 \\ 0 & 0 & -\frac{2}{\pi} & -\frac{\sqrt{3}}{\pi} & \frac{1}{\pi} \end{bmatrix}, \quad \hat{\mathbf{D}} = \begin{bmatrix} 0 & 0 \\ 0 & 0 \end{bmatrix} \quad (5.50)$$

The $\hat{\mathbf{A}}$ matrix terms are given as follows,

$$A_{12} = \frac{2S_{12}}{C_{o}}, A_{13} = \frac{2S_{13}}{C_{o}}, A_{14} = \frac{2S_{14}}{C_{o}}, A_{15} = \frac{2S_{15}}{C_{o}}$$
(5.51)

with,

$$S_{12} = \frac{m}{\pi} \left[\sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right) \right], S_{13} = \frac{m}{\pi} \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right) \right]$$

$$S_{14} = \frac{m}{\pi} \left[\sin\left(\delta + \frac{11\pi}{6}\right) - \sin\left(\delta + \frac{\pi}{2}\right) \right], S_{15} = \frac{m}{\pi} \left[\cos\left(\delta + \frac{11\pi}{6}\right) - \cos\left(\delta + \frac{\pi}{2}\right) \right]$$
(5.52)

and,

$$A_{21} = \frac{-S_{21}}{L_{s}}, A_{31} = \frac{-S_{31}}{L_{s}}, A_{41} = \frac{S_{41}}{L_{s}}, A_{51} = \frac{S_{51}}{L_{s}}$$
(5.53)

$$\begin{split} S_{21} &= S_{41} = \frac{m}{2\pi} \bigg[\sin \bigg(\delta + \frac{7\pi}{6} \bigg) - 3 \sin \bigg(\delta + \frac{11\pi}{6} \bigg) \bigg] \\ S_{31} &= S_{51} = \frac{m}{2\pi} \bigg[\cos \bigg(\delta + \frac{7\pi}{6} \bigg) - \cos \bigg(\delta + \frac{11\pi}{6} \bigg) \bigg] \end{split} \tag{5.54}$$

The $\hat{\mathbf{B}}$ matrix terms are given as follow,

$$B_{21} = \frac{-S_{31}V_{o}}{L_{s}}, B_{31} = \frac{S_{21}V_{o}}{L_{s}}, B_{41} = \frac{S_{31}V_{o}}{L_{s}}, B_{51} = \frac{-S_{21}V_{o}}{L_{s}}$$
(5.55)

$$B_{11} = \frac{2}{C_{o}} \sum_{n=1}^{4} B_{11}(n)$$
(5.56)

with the $B_{11}(n)$ terms being defined as,

$$B_{11}(1) = S_{13} \left\langle I_{\rm A} \right\rangle_{1}^{\rm R}, B_{11}(2) = -S_{12} \left\langle I_{\rm A} \right\rangle_{1}^{\rm I}, B_{11}(3) = S_{15} \left\langle I_{\rm B} \right\rangle_{1}^{\rm R}, B_{11}(4) = -S_{14} \left\langle I_{\rm B} \right\rangle_{1}^{\rm I}$$
(5.57)

In this case, the resulting vectors of small-signal state-variables, and inputs and outputs are given by,

$$\left\langle \hat{\mathbf{x}} \right\rangle = \left[\left\langle \hat{v}_{\mathrm{o}} \right\rangle_{0} \left\langle \hat{i}_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} \left\langle \hat{i}_{\mathrm{A}} \right\rangle_{1}^{\mathrm{I}} \left\langle \hat{i}_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} \left\langle \hat{i}_{\mathrm{B}} \right\rangle_{1}^{\mathrm{R}} \left\langle \hat{i}_{\mathrm{B}} \right\rangle_{1}^{\mathrm{I}} \right]^{T}$$
(5.58)

$$\left\langle \hat{\mathbf{u}} \right\rangle = \left[\left\langle \hat{\delta} \right\rangle_{0} \left\langle v_{i} \right\rangle_{0} \right]^{T}, \left\langle \hat{\mathbf{y}} \right\rangle = \left[\left\langle \hat{v}_{o} \right\rangle_{0} \left\langle \hat{i}_{i} \right\rangle_{0} \right]^{T}$$
(5.59)

5.3.3 Small-signal characteristics of the 3p-DAB in open-phase

The SSA and GSSA models developed in the previous sections are now used to analyze the smallsignal characteristics of the 3p-DAB in open-phase operation. This section has two main objectives: 1) evaluate the impacts of open-phase operation on the transient response and the stability of the 3p-DAB, and 2) compare the SSA and GSSA modeling methods for the analysis of the 3p-DAB in the open-phase case. The focus is on the transfer functions G_{vd} , G_{vg} , and Z_D . The parameters of model 4 (prototype) given in Appendix A are used in this section.

Analysis of $G_{\rm vd}$:

First, from Figure 5.14 a) to c), it is concluded that the open-phase operation does reduce the magnitude of control-to-output transfer function $G_{\rm vd}$. According to the GSSA model, this difference is equal to -7.5 dB at low frequency and decreases as the frequency increases toward the switching frequency $f_{\rm s}$. However, according to the SSA model, the difference between the normal and the open-phase cases is constant to -7 dB over the entire frequency range. This is slightly different from the -6 dB difference expected in section 5.3.1. This is explained by the fact that the gain of $G_{\rm vd}$ depends on the control phase-shift δ , which, as mentioned before, increases in open-phase condition for the same voltage and loading conditions. Regarding the change in the control phase-shift δ between the two operating modes, two different cases are possible as shown in Figure 5.15. They are explained below.

<u>Case #1:</u> At low power, if the converter is operated in range 1 in the normal operation mode, it may stay in range 1 in open-phase. The condition that must be met is the following,

$$P_{\rm o} \le \frac{m\pi V_{\rm i} V_{\rm o}}{12\omega_{\rm s} L_{\rm s}} \tag{5.60}$$

This leads to the following relationship between the phase-shift in normal operation $\delta_{3\varphi}$ and the phase-shift in open-phase operation $\delta_{2\varphi}$,

$$\delta_{3\varphi} = \frac{\delta_{2\varphi}}{2} \tag{5.61}$$

<u>Case #2:</u> At high power, if the converter is operated in range 1 in the normal operation mode, it may need to be operated in range 2 in open-phase. The condition that must be met in this case is the following,

$$P_{\rm o} > \frac{m\pi V_{\rm i}V_{\rm o}}{12\omega_{\rm s}L_{\rm s}}$$
(5.62)

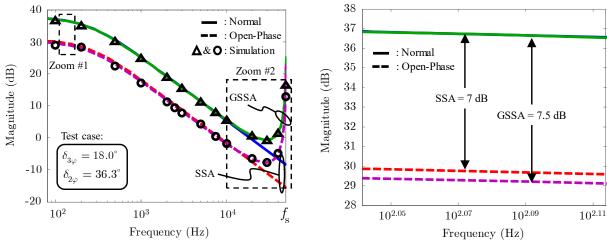
This leads to the following relationship between the phase-shift in normal operation $\delta_{3\varphi}$ and the phase-shift in open-phase operation $\delta_{2\varphi}$,

$$\delta_{3\varphi} = \frac{3}{4} \left(\delta_{2\varphi} - \frac{\left(\delta_{2\varphi} \right)^2}{\pi} \right) - \frac{\pi}{48}$$
(5.63)

Note that, for a given loading condition, if the converter is operated in range 2 in normal operation, it cannot be operated in open-phase condition for the same voltage and loading conditions. As shown in Figure 5.15, the test case analyzed in Figure 5.14 meets case #2 conditions; the phase-shift must be increased from $\delta_{3\varphi} = 18^{\circ}$ (range 1) to $\delta_{2\varphi} = 36.3^{\circ}$ (range 2) to keep the same output power $P_{o} = 144$ W. In the particular case of the SSA model, this means that the transfer function G_{vd} is given by (5.24) in normal operation (range 1), but it is given by (5.33) in open-phase operation (range 2) instead of (5.25) which applies to range 1 in open-phase. As a remark here, the -6 dB rule discussed in section 5.3.1 for G_{vd} evaluated with SSA only applies under case #1 conditions.

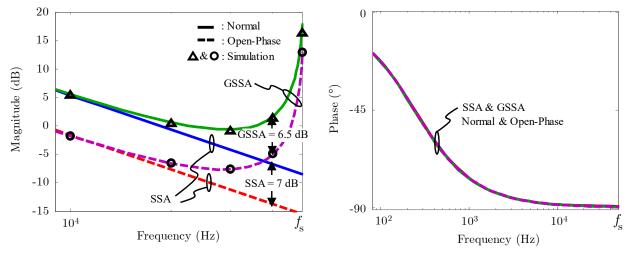
Furthermore, as shown in Figure 5.14 d), the GSSA model results confirm that the phase of G_{vd} is not affected in open-phase operation. Overall, since the gain of G_{vd} is reduced in open-phase but not its phase, for the same controller G_c , a degradation in the closed-loop transient response is expected. This will be confirmed experimentally in section 5.4.





(a) Magnitude of $G_{\rm vd}$

(b) Zoom #1 on the magnitude of $G_{\rm vd}$



(c) Zoom #2 on the magnitude of $G_{\rm vd}$

(d) Phase of $G_{\rm vd}$

Figure 5.14 : Impact of open-phase on the control-to-output transfer function (model 4)

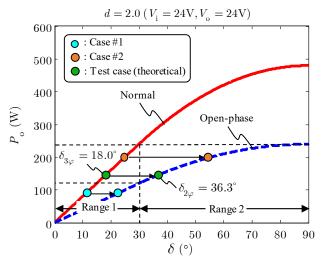


Figure 5.15 : Impact of open-phase on the control phase-shift (model 4)

Analysis of G_{vg} :

From Figure 5.16 a), it is also observed that SSA leads to the false conclusion that, for the same voltage and loading conditions, the transfer function G_{vg} is not affected in open-phase operation. In fact, according to GSSA in Figure 5.16 b), open-phase operation slightly reduces the magnitude of G_{vg} at low frequencies. As seen in Figure 5.16 a), this difference is, however, very small up to around 1/5th of the switching frequency f_s . For the test case parameters, the maximum reduction in the magnitude of G_{vg} is -4.5 dB at a frequency f = 30 kHz.

The error with the SSA model can be explained mathematically using the definitions of G_{vg} given in Table 5.1, and the equations for the calculation of the operating points for the normal and openphase cases. The analysis is done here for range 1. In range 1, for normal operation, the required phase-shift δ is obtained by solving (2.37) for δ . It leads to,

$$\delta_{3\varphi} = \frac{\omega_{\rm s} L_{\rm s} P_{\rm o}}{m V_{\rm i} V_{\rm o}} \tag{5.64}$$

Inserting (5.64) into the definition of the transfer function G_{vg} given by (5.26), and simplifying, it results in the following simplified version of the transfer function G_{vg} ,

$$G_{\rm vg,3\varphi}(s) = \left(\frac{V_{\rm o}}{V_{\rm i}}\right) \frac{1}{sRC_{\rm o} + 1}$$
(5.65)

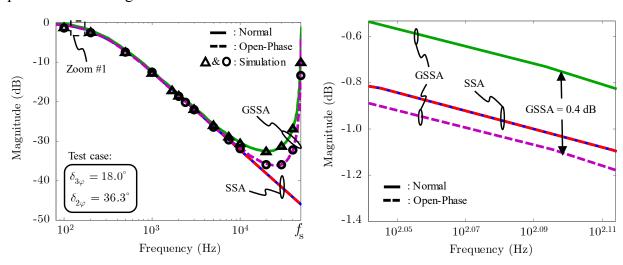
Similarly, in range 1, for open-phase operation, the required phase-shift δ is obtained by solving (5.14) for δ . It leads to,

$$\delta_{2\varphi} = \frac{2\omega_{\rm s}L_{\rm s}P_{\rm o}}{mV_{\rm i}V_{\rm o}} \tag{5.66}$$

Inserting (5.66) into the definition of the transfer function G_{vg} given by (5.27) and simplifying lead to the conclusion that,

$$G_{\rm vg,2\varphi}(s) = G_{\rm vg,3\varphi}(s) = \left(\frac{V_{\rm o}}{V_{\rm i}}\right) \frac{1}{sRC_{\rm o} + 1}$$
(5.67)

It can also be demonstrated that (5.67) is also valid in range 2 for both the normal and open-phase cases. This explains why, for the same input and output voltages V_i and V_o as well as the same



loading condition R, the magnitude of G_{vg} in Figure 5.16 is the same in normal and open-phase operations according to SSA.

(a) Magnitude of G_{vg}

(b) Zoom #1 on the magnitude of G_{vg}

Figure 5.16 : Impact of open-phase on the input-to-output transfer function (model 4)

Analysis of $Z_{\rm D}$:

From Figure 5.17 a), it is concluded that SSA also leads to the false conclusion that, for the same input and output voltages as well as loading conditions, the input impedance Z_D is not affected in open-phase operation. In fact, according to the GSSA model in Figure 5.17 b), the peak gain frequency is shifted from 1.8 kHz to 2.5 kHz in open-phase operation. According to Middlebrook's theorem [55], for a given input filter, it suggests that it tends to be less stable between 1 kHz and 2.1 kHz, but to be more stable at frequencies higher than 2.1 kHz.

As for G_{vg} , the error with the SSA model can be explained mathematically using the definitions of Z_D given in Table 5.1, and using equations (5.64) and (5.66) for the calculation of the control phase-shift for the normal and open-phase cases respectively. It leads to,

$$Z_{\mathrm{D},2\varphi}(s) = Z_{\mathrm{D},3\varphi}(s) = \left(\frac{R V_{\mathrm{i}}^{2}}{V_{\mathrm{o}}^{2}}\right) [sRC_{\mathrm{o}} + 1]$$
(5.68)

Again, it can also be demonstrated that (5.68) is valid in range 2. As for G_{vg} , it explains why, for the same voltages V_i and V_o as well as the same loading condition R, the magnitude of Z_D in Figure 5.17 is the same in normal and open-phase operations according to SSA.

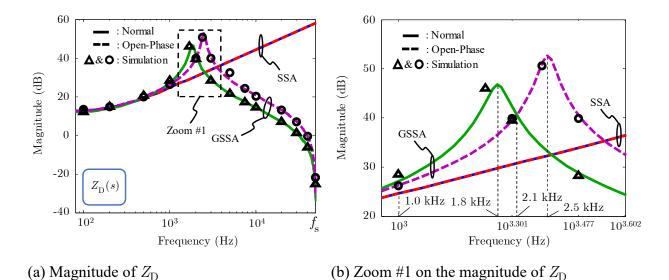


Figure 5.17 : Impact of open-phase on the input impedance (model 4)

5.4 Closed-loop transient analysis in open-phase

When switching from normal to open-phase operation with the same voltage and the same loading conditions, it must also be determined if the controller parameters need to be adjusted to keep an acceptable transient dynamic response.

To answer this question, the PI-controller G_c sized in section 3.6.2 using the transfer function G_{vd} of the 3p-DAB in normal operation is used. The characteristics of the designed PI-controller G_c are given in Figure 5.18. This controller is used for both simulation and experimental analyses. The impacts of keeping the same controller G_c when passing from normal to open-phase operation on the cross-over frequency $f_{\varphi m}$ and the phase-margin φ_m are also highlighted in Figure 5.18. Overall, it is concluded that the cross-over frequency $f_{\varphi m}$ is reduced from 1 kHz in normal operation to 535 Hz in open-phase operation. The phase-margin φ_m also slightly decreases from 65° to 63°.

By applying a step from 0 V to 24 V in the reference voltage on the prototype, the overall result is a higher overshoot (15% against less than 9%) and a slower transient response (time-to-peak of 1.6 ms against 0.5 ms). This is due to the reduced ability of the same PI-controller G_c to deal with disturbances in open-phase. This is mainly due to the important reduction in the cross-over frequency $f_{\varphi m}$. This situation can be considered acceptable in some practical applications, but the controller parameters may need to be adjusted in some cases. Furthermore, the simulation results in Figure 5.21 a) show that the designed controller allows a transition from normal to frozen leg and from frozen leg to open-phase without interruption. From Figure 5.21 b), it is also concluded that open-phase operation provides reduced current stress over the frozen leg operation. The results in Figure 5.21 c) also confirm that open-phase operation provides an increased power transfer capability over frozen leg operation because, for the same output power, the control phase-shift δ is smaller in open-phase than in frozen leg.

Moreover, experimental results also confirm that the converter can transition from normal to frozen leg (Figure 5.22), from frozen leg to open-phase (Figure 5.23), and from normal to open-phase (Figure 5.24) without interruption. The normal to frozen leg transition is performed by disabling the phase C gating signals s_5 and s_6 . The frozen leg to open-phase and normal to open-phase transitions are validated by opening the normally closed (NC) contact of a relay inserted in the phase C. The relay coil signal is used to trigger the oscilloscope and capture the event. A delay of 2.8 ms between the coil opening signal and the actual phase opening is introduced by the relay.

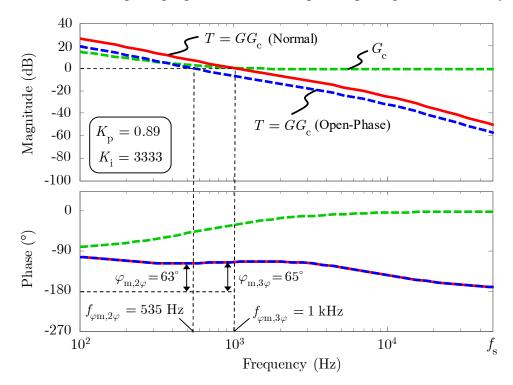


Figure 5.18 : Closed-loop small-signal characteristics in open-phase (model 4)

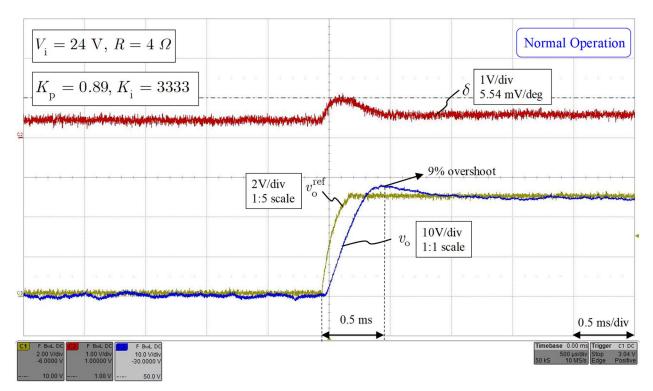


Figure 5.19 : Closed-loop response for the normal operation case (prototype)

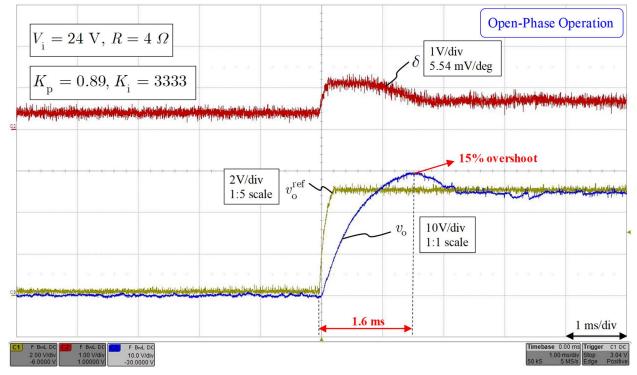


Figure 5.20 : Closed-loop response for the open-phase operation case (prototype)

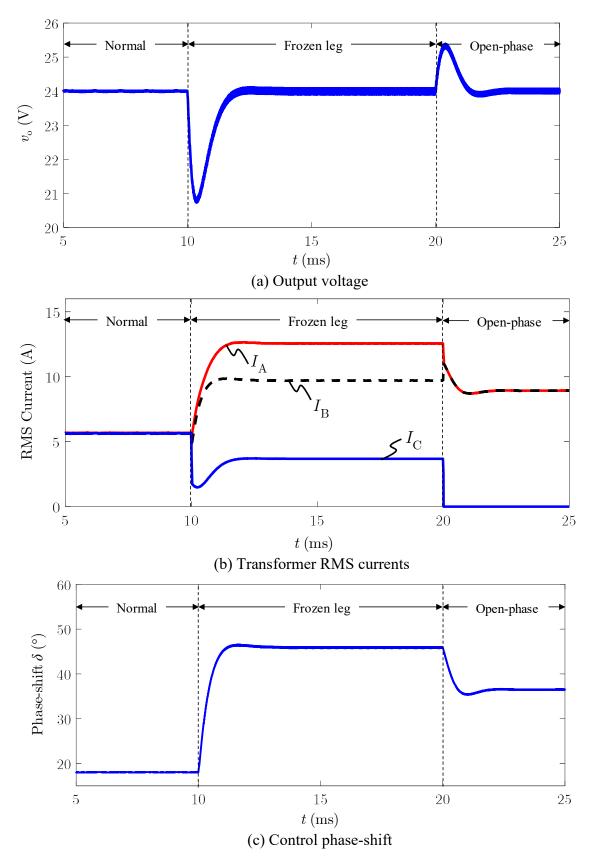


Figure 5.21 : Simulation results for the transitions between the operating modes (model 4)

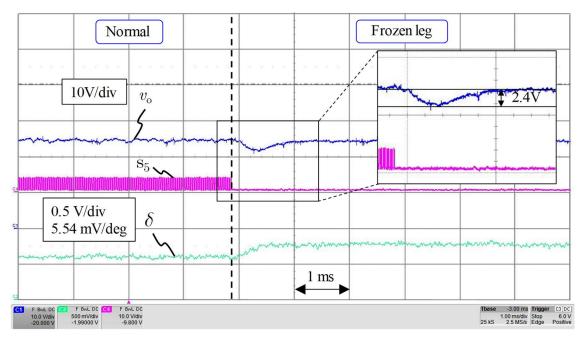


Figure 5.22 : Transition from normal to frozen leg (prototype)

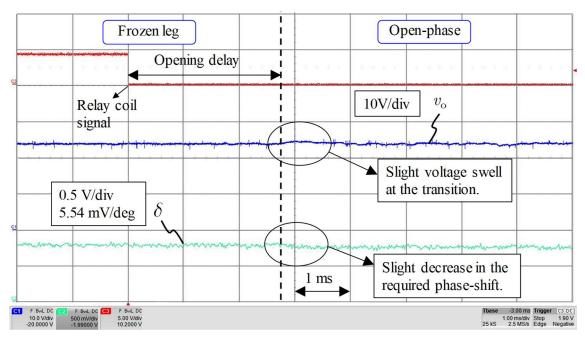


Figure 5.23 : Transition from frozen leg to open-phase (prototype)

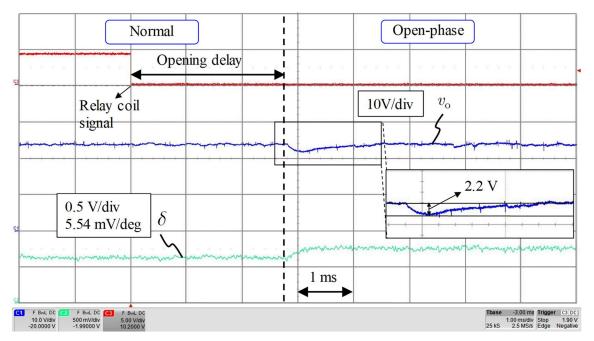


Figure 5.24 : Transition from normal to open-phase (prototype)

CHAPTER 6 CONCLUSIONS AND RECOMMENDATIONS

6.1 Summary of this thesis

The main objective of this thesis was to develop and validate accurate models for the analysis of integration of 3p-DAB converters into dc-grids. It fills gaps in the literature by demonstrating the efficiency, accuracy and usefulness of the developed small- and large-signal models. It is a key step forward toward describing the behavior of 3p-DAB converters for their integration in next-generation dc power systems. By comparing different modeling strategies, this thesis provides a comprehensive review of the advantages and limitations of different modeling approaches for simulation and analysis of 3p-DAB converters. Furthermore, it has been discovered that the 3p-DAB can be operated with one phase open. By studying the open-phase operation mode, this thesis shows how the investigated modeling strategies can be adapted to the analysis of the 3p-DAB in degraded operation. The main contributions and conclusions of this thesis are summarized as follows.

Small-signal modeling of the 3p-DAB

The successful integration of the 3p-DAB with other power systems components requires determining its frequency-domain small-signal characteristics accurately. The standard approach for small-signal modeling of the 3p-DAB converter is to use the state-space averaging (SSA) method. SSA benefits from being easier in representing the small-signal model as an equivalent circuit. It also provides a better insight of parameters influence. For the 3p-DAB, SSA leads to a dc-averaged model which neglects the ac dynamic of the three-phase transformer currents as well as the impact of the input and output bridges switching-functions. As shown in this thesis, SSA is generally precise enough for controller sizing using the control-to-output transfer function G_{vd} in addition to determining the output impedance Z_0 of the converter. However, SSA is not accurate for the stability analysis of the 3p-DAB because it lacks precision in evaluating the driving point input impedance Z_D and the null driving point input impedance Z_N . These two transfer functions are necessary for the application of Middlebrook's extra element theorem (EET) which is broadly used in practice by power electronic designers to avoid dynamic performance degradation or instability conditions in the presence of an additional input filter.

To overcome the limitations of SSA in stability analysis, a hybrid SSA and generalized state-space averaging (GSSA) model is proposed in this thesis. The GSSA model is used to calculate the driving point input impedance Z_D while the proposed hybrid model is used to evaluate the null driving point input impedance Z_N . The proposed approach allows reducing the number of timedomain simulation runs and experimental tests required to predict dynamic performance degradation and instability conditions. The main outcomes have been published in [50].

Large-signal modeling of the 3p-DAB

Previous work has shown that large-signal time-domain modeling of 3p-DAB converters is not trivial. The only resource in the literature proposed a discretized switching-function model in the $\alpha\beta$ frame which reduces the number of electrical nodes while preserving the switching-effect [62]. However, the reduction in the computation time compared to the ideal-model approach is not shown. Furthermore, although the reduction in the number of electrical nodes is a sign of a reduced computational effort, it cannot be used alone as an indicator for the reduction in computation time.

As a response to this breach in the literature, large-signal models of the 3p-DAB converter are developed, analyzed and implemented in EMTP. Three modeling strategies are investigated in this thesis: 1) the switching-function (SWF), 2) the generalized average modeling (GAM) and 3) the state-space averaging (SSA). The results showed that, even if SSA significantly reduces the computation time, it has a limited precision for studying phenomena beyond $1/5^{\text{th}}$ of the converter switching frequency f_s . Because they allow higher time-steps, the SWF and GAM models both offer significant reduction in the computation time compared to the ideal-model, while still preserving the converter frequency characteristics up to its switching frequency f_s . However, the maximum time-step with the SWF model is limited by the inclusion of the switching-effect, which, still leads to a high computation time for system-level studies. Overall, the proposed GAM model offers the best compromise in terms of accuracy, speed and wideband response. It is a key step toward efficient and accurate time-domain system-level simulation of next-generation dc-grids based on the 3p-DAB topology. The main results have been published in [61].

Open-phase degraded-operation of the 3p-DAB converter

Preceding research showed that the 3p-DAB converter can be operated in a frozen leg degradedmode [35]. However, the analytical characterization of the converter behavior under all voltage and loading conditions is not shown due to the important impact of the reactive current circulation in the faulty-phase on the voltage and current waveforms. In this thesis, open-phase fault-tolerant operation of the 3p-DAB converter has been proposed as an upgrade to frozen leg. It is confirmed experimentally on a closed-loop EPC2022 GaN-based small-scale prototype. The first benefit of open-phase operation is to simplify the analytical characterization of the 3p-DAB. The results presented in this thesis also showed that, even if it requires an additional switch (relay or contactor) to open the faulty-phase, the open-phase operation provides practical advantages over the frozen leg operation.

Steady-state analysis is performed to evaluate the impact of open-phase operation on the converter voltage and current waveforms, and its power transfer and ZVS capabilities. Small-signal analysis along with time-domain simulations are also provided to assess the impacts on the converter stability and its dynamic performance. The most important conclusions are as follows. 1) Open-phase operation requires no change to the modulation pattern. 2) Open-phase operation provides reduced current stress and an increased power transfer capability over frozen leg operation. 3) The power transfer capability in open-phase is theoretically half the normal operation, such that, for the same voltage and loading conditions, the control phase-shift needs to increase. 4) The stability of the 3p-DAB can be positively and/or negatively affected in open-phase operation. 5) For the same PI-controller, the transient response is negatively affected in open-phase operation such that the controller parameters may need to be adjusted if the dynamic of the converter is a metric that must be preserved in degraded-operation.

Overall, using a fault-tolerant topology, which provides intrinsic redundancy upon failure of switches, will undeniably facilitate the industrial acceptation of emerging semiconductor technologies such as silicon carbide (SiC) and gallium nitride (GaN). Accurate characterization of the 3p-DAB behavior in degraded-mode is also crucial for the development of reliable and resilient dc-grids which utilizes 3p-DAB as central elements. The main results have been submitted in IEEE Transactions on Power Electronics. The paper is currently accepted with major mandatory changes.

6.2 Future work

The recommendations for future areas of research are divided into two main categories: 1) improvements to the small- and large-signal models of the 3p-DAB, and 2) development and implementation of switch failure diagnosis schemes for the post-fault open-phase operation of the 3p-DAB converter.

6.2.1 Improvements to the developed small- and large-signal models

Inclusion of filters, losses, and higher harmonics in the small- and large-signal models

In this thesis, filters and losses (conduction and core losses, and capacitor ESR) are neglected in the proposed small- and large-signal models of the 3p-DAB converter. Furthermore, only the fundamental-frequency component of the transformer current is included in the generalized averaging small- and large-signal models.

The inclusion of filters increases the order of the small-signal models using the state-space averaging (SSA) method [90]. The inclusion of filters with the generalized averaging approach applied to the 3p-DAB is not shown in the literature. The inclusion of filters in the converter small-signal model is fundamentally different from the application of Middlebrook's theorem [55] which separates a priori the design of the converter from the input filter. In large-signal models implemented in time-domain simulation tools, filters can be considered as discrete RLC-elements or directly included in the converter model equations. When modeled as discrete RLC-elements, it increases the burden to solve the network equations. Conversely, when included in the converter model equations, it increases the control system equations solver task. The comparison of the computation time t_{con} with these two approaches is not provided in the literature.

Previous work on the 1p-DAB also showed that, neglecting losses, can reduce the accuracy of the 1p-DAB small-signal model [57]. However, when considering conduction losses with the SSA method, the equations describing the transformer current waveforms become piecewise exponentials instead of piecewise-linear. In this case, the calculation of the average value of the input and output currents is more complex. Its application to the 3p-DAB is not trivial. Conduction losses can also be considered with the generalized averaging approach as shown in [53] and [58]

for the 1p-DAB and the 3p-DAB respectively. The inclusion of core losses is not shown in these references. Furthermore, as shown in [53], the inclusion of the capacitor ESR with the generalized averaging method also increases the order of the resulting system of equations.

Neglecting higher harmonics with the generalized averaging method can also lead to errors in the determination of the steady-state operating point, which, further impacts the accuracy of the resulting small-signal models [58], [91]. Including higher harmonics in the generalized averaging method for large-signal analysis of the 3p-DAB will increase the computation time t_{cpu} , but the real impact is unknown.

Modeling of the 3p-DAB with constant power loads (CPLs)

In this thesis, the 3p-DAB converter under study is always supplying power to a purely resistive load *R*. However, in dc-grids, tightly regulated power electronics converter can act, within their control bandwidth, as constant power loads (CPLs) [26], [81]. As a consequence, CPLs behave as negative incremental resistances seen from their input terminals. Because of the presence of LC filters, the system can oscillate and becomes unstable [92].

As seen in this thesis, the load R is part of all the 3p-DAB transfer functions (G_{vd} , G_{vg} , Z_D , Z_N , and Z_o). When loaded with CPLs, it has been shown in [93] using the state-space averaging (SSA) method, that the control-to-output transfer function G_{vd} of the 3p-DAB is modified. The conclusion is that the small-signal stability is affected by the percentage of CPLs against resistive loads R. The impact on other transfer functions is not shown. The inclusion of CPLs in the generalized averaging approach applied to the 3p-DAB is also the ground for further research.

Modeling of the 3p-DAB with variable switching frequency

In this thesis, all the developed models assume that the 3p-DAB is controlled with a fixed switching frequency f_s modulation scheme. The phase-shift δ is the only controlled variable to regulate the output voltage v_o . Previous work on the 1p-DAB showed that the converter soft-switching operation may benefit from adjusting the switching frequency f_s [94], [95]. By analyzing the 3p-DAB converter transfer functions given in this thesis, it must be recognized that interactions between the control of f_s and δ is unavoidable. In other words, changing f_s impacts the operating

point of the converter which further influence the control-to-output transfer function $G_{\rm vd}$ relating the output voltage $v_{\rm o}$ to the phase-shift δ . High bandwidth control of both $f_{\rm s}$ and δ requires an accurate characterization of the converter small-signal characteristics with $f_{\rm s}$ considered as a control variable. Considering $f_{\rm s}$ as a control variable with the state-space averaging (SSA) method is quite trivial. If $f_{\rm s}$ is not slowly varied, the inclusion of $f_{\rm s}$ as a control variable is, however, challenging with the generalized averaging methodology [56].

6.2.2 Diagnosis of switch failure for post-fault open-phase operation

Fault-tolerant (FT) operation relies on effective fault-diagnosis (FD) methodologies [37]. In this thesis, post-fault open-phase FT operation is proposed to be triggered by a feedback fault signal coming from the gate drivers, similarly to previous work on frozen leg operation [35]. This covers only a limited number of failure cases in the 3p-DAB. Because it requires opening the faulty-phase, the detection and the identification of the faulty-phase is an important step to increase the potential use of the proposed open-phase FT operation. Recent work investigated the possibility to detect an open-circuit condition of one transistor using the polarity of the dc component in the transformer currents [96]. The detection of other conditions such as transistor short-circuit or failed diodes remains unaddressed. Since the 3p-DAB converter is composed of two three-phase inverters, many of the FD strategies and remedial actions proposed for three-phase inverters in the literature [97] are potential candidates for application with the 3p-DAB.

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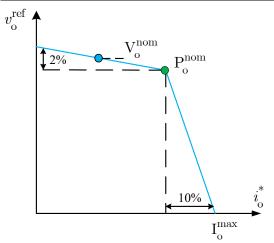
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Description	Symbol	Model 1	Model 2	Model 3	Model 4 (Proto.)
Referenced in Chapter	-	3,4	4	4	2, 3, 4, 5
Nominal Input Voltage	$V_{\rm i}^{\rm nom}$	600 V	750 V	380 V	24 V
Nominal Output Voltage	$V_{\rm o}^{\rm nom}$	37.5 V	380 V	110 V	24 V
Nominal Output Power	$P_{\rm o}^{\rm nom}$	1 kW	100 kW	10 kW	-
Load Resistor	R	_	_	_	4 Ω
Input Filter Inductor	$L_{\rm i}$	-	100 µH	50 µH	_
Input Filter Capacitor	$C_{\rm i}$	_	1100 µF	150 μF	164 µF
Output Capacitor	$C_{\rm o}$	150 μF	2200 µF	150 μF	164 µF
Transformer Series Inductance	$L_{\rm s}$	420 µH	10 µH	12 µH	2 µH
Transformer Ratio	M	16	1.732	3.45	0.866
Nominal Switching Frequency	$f_{ m s}$	50 kHz	20 kHz	50 kHz	50 kHz
$G_{\rm c}$ Cross-Over frequency	$f_{arphi \mathrm{m}}$	1.5 kHz	1.5 kHz	2.5 kHz	1 kHz
$G_{\rm c}$ Phase-Margin	φ_{m}	45°	45°	45°	65°
$G_{\rm f}$ Cut-Off Frequency	$f_{\rm o}$	5 kHz	5 kHz	5 kHz	7.9 kHz
Maximum Output Current (see figure below)	$I_{\rm o}^{\rm max}$	30 A		100 A	_
Modulator gain	$K_{ m m}$	-11.4 dB	-13.4 dB	-11.4 dB	-11.4 dB

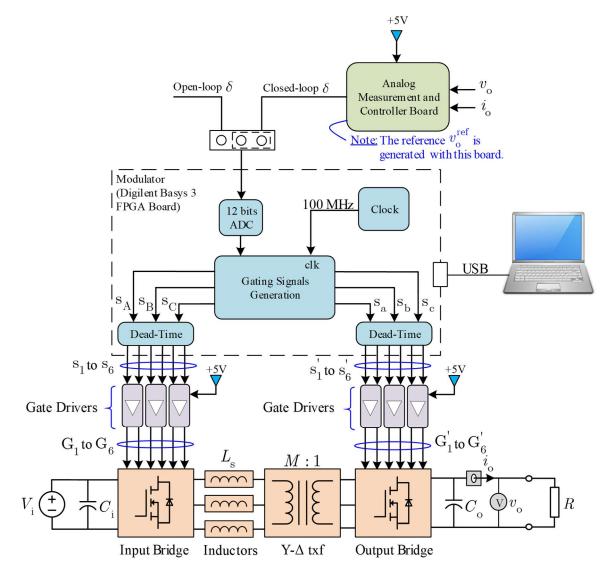
APPENDIX A – CONVERTER MODEL PARAMETERS

VI-characteristic for output current limitation:



APPENDIX B – SMALL-SCALE PROTOTYPE

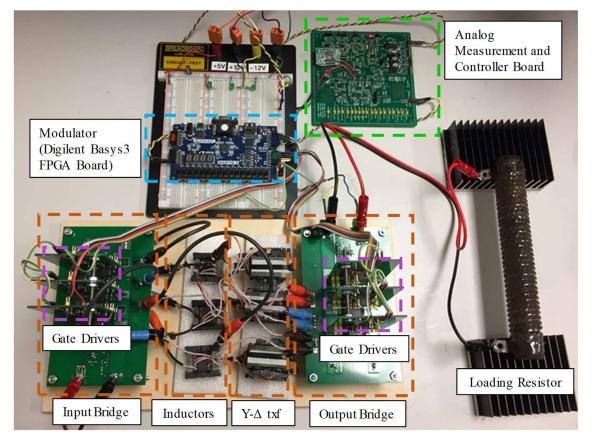
In this appendix, additional information on the EPC2022 GaN-based Y- Δ 3p-DAB prototype is provided. The figure below shows a simplified schematic of the prototype main elements:



The prototype is composed of two three-phase inverters using EPC2022 (100V, 90A) [89] GaN FET as switching elements. The series inductors are custom-built for the application. The three-phase transformer is fabricated using three custom-built single-phase transformers. The output voltage v_0 is fed back to an analog control board which filters, calculates the error between v_0 and v_0^{ref} , and generates the control phase-shift δ with a PI-controller. The analog value of the phase-shift δ is digitized using a 12 bits analog-to-digital converter (ADC). The digital value of

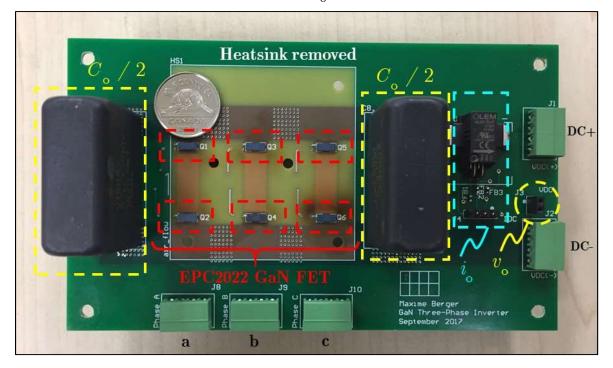
the control phase-shift δ is transformed by the FPGA into gating signals for each leg at the switching frequency $f_{\rm s}$. Note that the refreshing rate of the control phase-shift δ is $T_{\rm s} = 1 / f_{\rm s}$, i.e. that the FPGA read the new value of δ once every cycle $T_{\rm s}$. The FPGA code includes a dead-time $t_{\rm dead}$ between the signals going to the upper and lower transistors of the same leg. Since the clock frequency of the Basys 3 board is $f_{\rm clk} = 100$ MHz, the minimum theoretical dead-time is $1 / f_{\rm clk} = 0.01 \ \mu$ s. For all the experiments, the dead-time $t_{\rm dead}$ has been set to 0.1 μ s. The gating signals at the output of the Basys 3 board are then sent to the gate drivers (one gate driver for each leg) which isolate and amplify the signals going to the gate of the transistors. The gate driver design is based on Texas Instruments reference design TIDA-00785. It has been adapted to ensure compatibility with the Basys 3 board output signals.

The figure below shows an overview of the main hardware components:

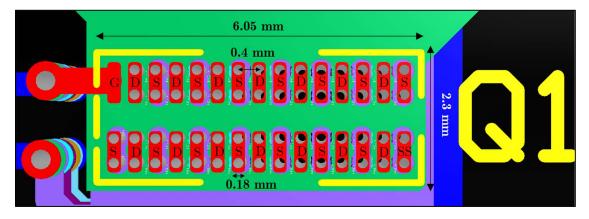


The power supply used for all the experiments is an EMS 40-25 (40VDC, 25A). The measurements are taken using a LeCroy HDO6104 oscilloscope with CP150 current probes and ADP305 differential voltage probes.

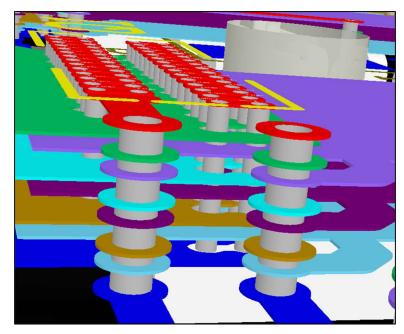
The figure below shows an example of the top layer of the three-phase output bridge. The input bridge is identical to the output bridge. The output capacitor $C_{\rm o}$ is made of two 82 µF film capacitors (Vishay MKT1820682015), such that $C_{\rm o} = 164$ µF.



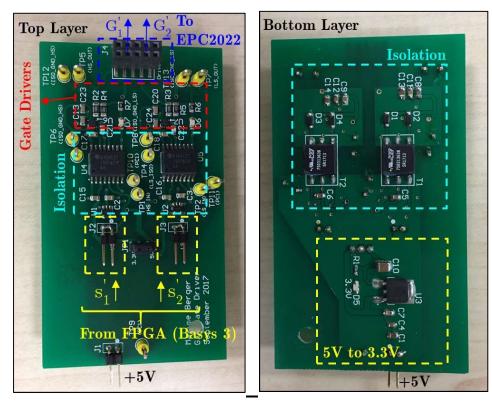
The figure below shows the footprint for the EPC2022 GaN FETs. It shows the arrangement of the gate (G), drain (D), source (S) and substrate (SS) pads as well as the main dimensions of the EPC2022 footprint.

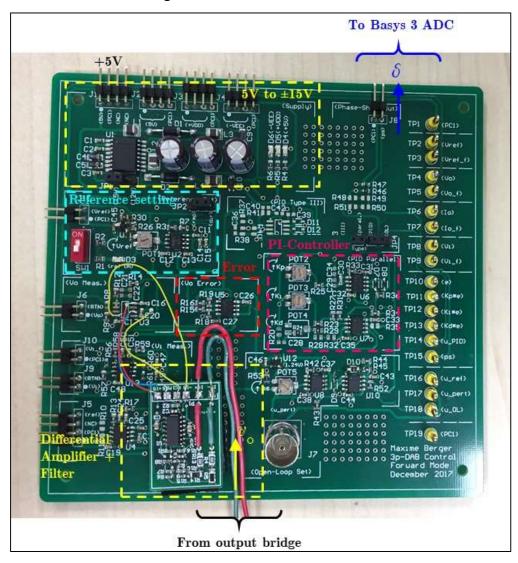


The inverter board is composed of 8 layers to allow a better extraction of both the heat and the current from the GaN FET pads. The layers connecting the drain (D) and the source (S) pads are interleaved to reduce the risk of short-circuit during the fabrication process:



The figures below show an example of the gate driver board for the phase a of the output bridge:





The figure below shows the analog measurement and controller board:

APPENDIX C – PIECEWISE-LINEAR MODEL IN NORMAL OPERATION

C.1 Mode definition and switching-functions:

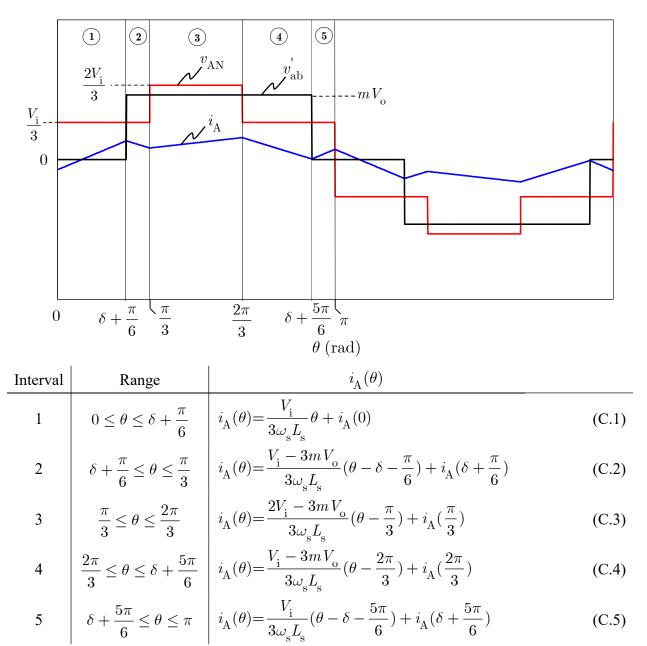
Mode	$0 \le \delta \le \frac{\pi}{6}$	s_1	s_3	\mathbf{s}_5	s'_1	s'_3	s'_5
Ι	$0 \le \theta \le \delta + \frac{\pi}{6}$	X		X			X
II	$\delta + \frac{\pi}{6} \le \theta \le \frac{\pi}{3}$	Х		X	Х		X
III	$\frac{\pi}{3} \le \theta \le \delta + \frac{\pi}{2}$	X			Х		X
IV	$\delta + \frac{\pi}{2} \le \theta \le \frac{2\pi}{3}$	X			Х		
V	$\frac{2\pi}{3} \le \theta \le \delta + \frac{5\pi}{6}$	X	X		Х		
VI	$\delta + \frac{5\pi}{6} \le \theta \le \pi$	X	Х		Х	X	

Legend : X = 1, (empty) = 0

Mode	$\frac{\pi}{6} \le \delta \le \frac{\pi}{2}$	s_1	s_3	\mathbf{s}_5	s'_1	s'_3	s'_5
Ι	$0 \le \theta \le \delta - \frac{\pi}{6}$	Х		Х		X	Х
II	$\delta - \frac{\pi}{6} \le \theta \le \frac{\pi}{3}$	X		X			X
III	$\frac{\pi}{3} \le \theta \le \delta + \frac{\pi}{6}$	X					X
IV	$\delta + \frac{\pi}{6} \le \theta \le \frac{2\pi}{3}$	X			Х		X
V	$\frac{2\pi}{3} \le \theta \le \delta + \frac{\pi}{2}$	X	X		Х		X
VI	$\delta + \frac{\pi}{2} \le \theta \le \pi$	X	X		X		

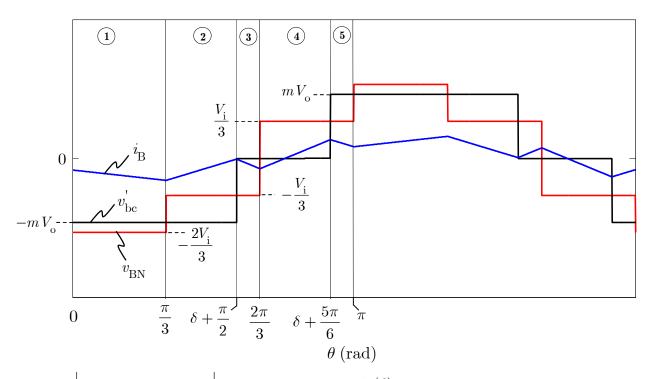
C.2 Piecewise-linear current equations:

For range 1 ($0 \le \delta \le \pi/6$):



$$i_{\rm A}(0) = \frac{\left[3mV_{\rm o} - 2V_{\rm i}\right]\pi}{9\omega_{\rm s}L_{\rm s}} \tag{C.6}$$

• Phase B:



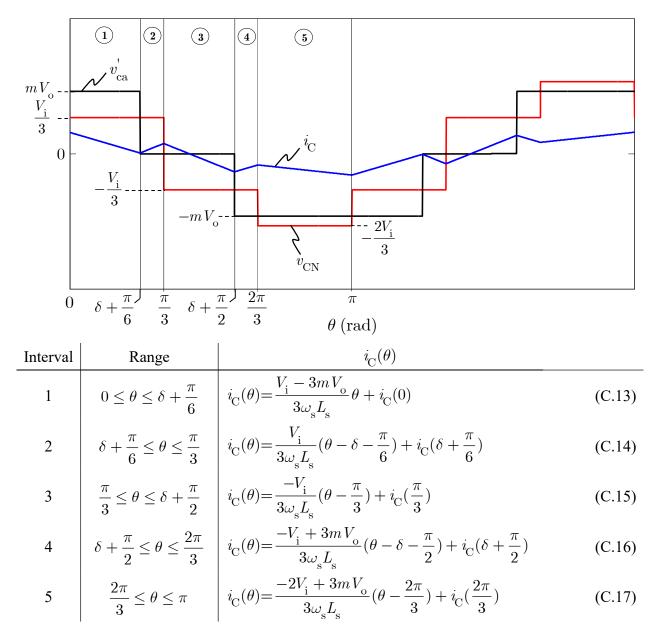
2
$$\frac{\pi}{3} \le \theta \le \delta + \frac{\pi}{2}$$
 $i_{\rm B}(\theta) = \frac{-V_{\rm i} + 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}(\theta - \frac{\pi}{3}) + i_{\rm B}(\frac{\pi}{3})$ (C.8)

3
$$\delta + \frac{\pi}{2} \le \theta \le \frac{2\pi}{3}$$
 $i_{\rm B}(\theta) = \frac{-V_{\rm i}}{3\omega_{\rm s}L_{\rm s}}(\theta - \delta - \frac{\pi}{2}) + i_{\rm B}(\delta + \frac{\pi}{2})$ (C.9)

4
$$\left| \frac{2\pi}{3} \le \theta \le \delta + \frac{5\pi}{6} \right| i_{\rm B}(\theta) = \frac{V_{\rm i}}{3\omega_{\rm s}L_{\rm s}} (\theta - \frac{2\pi}{3}) + i_{\rm B}(\frac{2\pi}{3})$$
 (C.10)

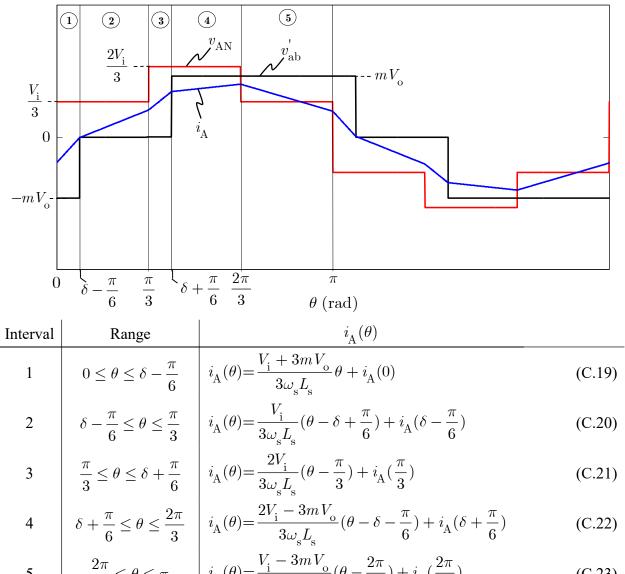
5
$$\delta + \frac{5\pi}{6} \le \theta \le \pi$$
 $i_{\rm B}(\theta) = \frac{V_{\rm i} - 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}(\theta - \delta - \frac{5\pi}{6}) + i_{\rm B}(\delta + \frac{5\pi}{6})$ (C.11)

$$i_{\rm B}(0) = \frac{\pi V_{\rm i} - 3m V_{\rm o} \left[3\delta + \frac{\pi}{2} \right]}{9\omega_{\rm s} L_{\rm s}} \tag{C.12}$$



$$i_{\rm C}(0) = \frac{\pi V_{\rm i} + 3m V_{\rm o} \left[3\delta - \frac{\pi}{2} \right]}{9\omega_{\rm s}L_{\rm s}} \tag{C.18}$$

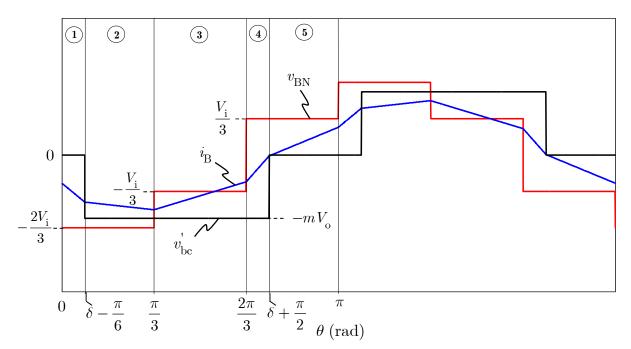
0 Phase A:



 $\begin{vmatrix} \frac{2\pi}{3} \leq \theta \leq \pi \end{vmatrix} \quad \begin{vmatrix} i_{\mathrm{A}}(\theta) = \frac{V_{\mathrm{i}} - 3mV_{\mathrm{o}}}{3\omega_{\mathrm{s}}L_{\mathrm{s}}}(\theta - \frac{2\pi}{3}) + i_{\mathrm{A}}(\frac{2\pi}{3}) \end{vmatrix}$ 5 (C.23)

$$i_{\rm A}(0) = \frac{-2\pi V_{\rm i} + 9m V_{\rm o} \left[\frac{\pi}{2} - \delta\right]}{9\omega_{\rm s}L_{\rm s}} \tag{C.24}$$

• <u>Phase B:</u>



Interval	Range	$i_{ m B}(heta)$	
1	$0 \le \theta \le \delta - \frac{\pi}{6}$	$i_{\rm B}(\theta) {=} \frac{-2V_{\rm i}}{3\omega_{\rm s}L_{\rm s}}\theta + i_{\rm B}(0)$	(C.25)

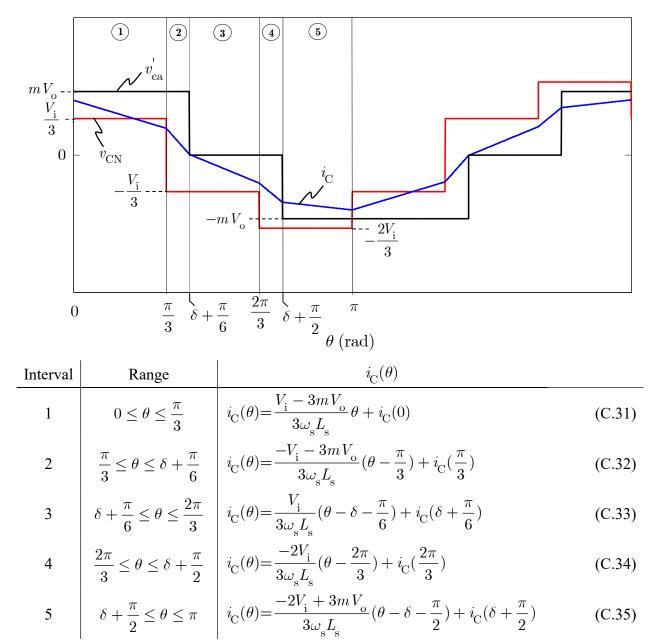
2
$$\delta - \frac{\pi}{6} \le \theta \le \frac{\pi}{3}$$
 $i_{\rm B}(\theta) = \frac{-2V_{\rm i} + 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}(\theta - \delta + \frac{\pi}{6}) + i_{\rm B}(\delta - \frac{\pi}{6})$ (C.26)

$$3 \qquad \frac{\pi}{3} \le \theta \le \frac{2\pi}{3} \qquad i_{\rm B}(\theta) = \frac{-V_{\rm i} + 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}(\theta - \frac{\pi}{3}) + i_{\rm B}(\frac{\pi}{3}) \tag{C.27}$$

$$4 \qquad \left| \begin{array}{c} \frac{2\pi}{3} \le \theta \le \delta + \frac{\pi}{2} \\ \end{array} \right| i_{\rm B}(\theta) = \frac{V_{\rm i} + 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}} (\theta - \frac{2\pi}{3}) + i_{\rm B}(\frac{2\pi}{3}) \tag{C.28}$$

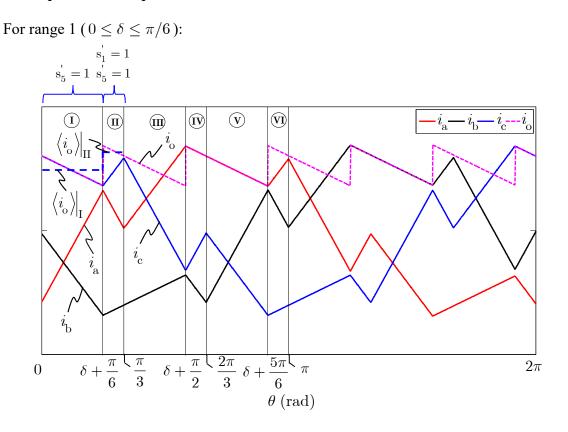
5
$$\delta + \frac{\pi}{2} \le \theta \le \pi$$
 $i_{\rm B}(\theta) = \frac{V_{\rm i}}{3\omega_{\rm s}L_{\rm s}}(\theta - \delta - \frac{\pi}{2}) + i_{\rm B}(\delta + \frac{\pi}{2})$ (C.29)

$$i_{\rm B}(0) = \frac{\left[V_{\rm i} - 3mV_{\rm o}\right]\pi}{9\omega_{\rm s}L_{\rm s}}$$
 (C.30)



$$i_{\rm C}(0) = \frac{\pi V_{\rm i} + 3m V_{\rm o} \left[3\delta - \frac{\pi}{2} \right]}{9\omega_{\rm s}L_{\rm s}}$$
(C.36)

C.3 Output current equations:



• For Mode I:

The output current is given by,

$$i_{\rm o} = i_{\rm c} = m \left(i_{\rm C} - i_{\rm B} \right)$$
 (C.37)

From (C.7) and (C.12), the current $i_{\rm B}$ is given by,

$$i_{\rm B}(\theta) = \frac{-2V_{\rm i} + 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}\theta + \frac{\pi V_{\rm i} - 3mV_{\rm o} \left[3\delta + \frac{\pi}{2}\right]}{9\omega_{\rm s}L_{\rm s}}$$
(C.38)

and from (C.13) and (C.18), the current $\,i_{\rm C}^{}\,$ is given by,

$$i_{\rm C}(\theta) = \frac{V_{\rm i} - 3mV_{\rm o}}{3\omega_{\rm s}L_{\rm s}}\theta + \frac{\pi V_{\rm i} + 3mV_{\rm o}\left[3\delta - \frac{\pi}{2}\right]}{9\omega_{\rm s}L_{\rm s}}$$
(C.39)

Inserting (C.38) and (C.39) into (C.37), and simplifying, it results that,

$$i_{o}(\theta) = m \left[\frac{V_{i} - 2mV_{o}}{\omega_{s}L_{s}} \theta + \frac{2\delta mV_{o}}{\omega_{s}L_{s}} \right]$$
(C.40)

Then, since the current is assumed linear, it can be written that,

$$\langle i_{\rm o} \rangle \Big|_{\rm I} = \frac{i_{\rm o}(0) + i_{\rm o}(\delta + \frac{\pi}{6})}{2}$$
 (C.41)

Using (C.40) to solve (C.41), it finally leads to,

$$\left\langle i_{\rm o} \right\rangle \Big|_{\rm I} = \left(\frac{m}{9\omega_{\rm s}L_{\rm s}}\right) \cdot \left[\left(\frac{9}{2}\delta + \frac{3\pi}{4}\right) V_{\rm i} + \left(9\delta - \frac{3\pi}{2}\right) m V_{\rm o} \right] \tag{C.42}$$

• For Mode II:

The output current is given by,

$$i_{o} = i_{a} + i_{c} = m(i_{A} - i_{B})$$
 (C.43)

such that,

$$\left\langle i_{o}\right\rangle \Big|_{\mathrm{II}} = m\left(\left\langle i_{\mathrm{A}}\right\rangle \Big|_{\mathrm{II}} - \left\langle i_{\mathrm{B}}\right\rangle \Big|_{\mathrm{II}}\right)$$
 (C.44)

with,

$$\langle i_{\rm A} \rangle \Big|_{\rm II} = \frac{i_{\rm A}(\delta + \frac{\pi}{6}) + i_{\rm A}(\frac{\pi}{3})}{2}$$
 (C.45)

$$\langle i_{\rm B} \rangle \Big|_{\rm II} = \frac{i_{\rm B}(\delta + \frac{\pi}{6}) + i_{\rm B}(\frac{\pi}{3})}{2}$$
 (C.46)

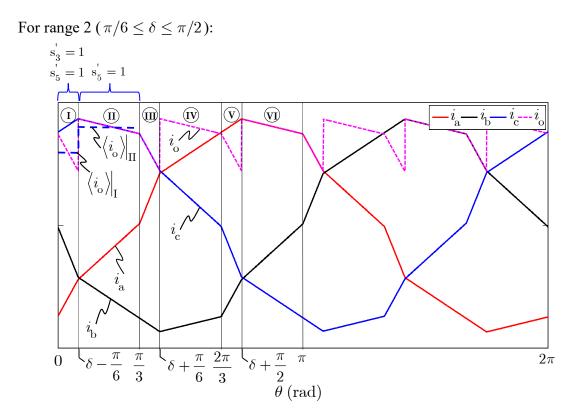
Using (C.1), (C.2) and (C.6) for determining $\langle i_A \rangle |_{II}$, and using (C.7) and (C.12) for determining $\langle i_B \rangle |_{II}$, equations (C.45) and (C.46) are reduced to,

$$\left\langle i_{\rm A} \right\rangle \Big|_{\rm II} = \frac{1}{9\omega_{\rm s}L_{\rm s}} \left[\left[\frac{3}{2}\delta - \frac{5\pi}{4} \right] V_{\rm i} + \left[\frac{9}{2}\delta + \frac{9\pi}{4} \right] m V_{\rm o} \right] \tag{C.47}$$

$$\left\langle i_{\rm B} \right\rangle \Big|_{\rm II} = \frac{1}{9\omega_{\rm s}L_{\rm s}} \left(-\left[3\delta + \frac{\pi}{2}\right]V_{\rm i} + \left[\frac{3\pi}{4} - \frac{9}{2}\delta\right]mV_{\rm o} \right)$$
(C.48)

Injecting (C.47) and (C.48) into (C.44), and simplifying, it yields that,

$$\left\langle i_{\rm o} \right\rangle \Big|_{\rm II} = \frac{m}{9\omega_{\rm s}L_{\rm s}} \left[\left(\frac{9}{2}\delta - \frac{3\pi}{4} \right) V_{\rm i} + \left(9\delta + \frac{3\pi}{2} \right) m V_{\rm o} \right] \tag{C.49}$$



• For Mode I:

Applying a similar procedure as for range 1, it can be demonstrated that $\langle i_{\rm o} \rangle |_{\rm I}$ is given by,

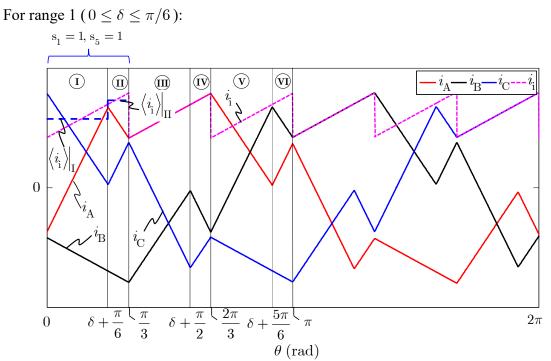
$$\left\langle i_{\rm o} \right\rangle \Big|_{\rm I} = \left(\frac{m}{9\omega_{\rm s}L_{\rm s}}\right) \cdot \left[3\pi V_{\rm i} + 9m V_{\rm o} \left(\delta - \frac{\pi}{2}\right)\right] \tag{C.50}$$

• For Mode II:

As for range 1, it can be demonstrated that $\left.\left\langle i_{\mathrm{o}}\right\rangle \right|_{\mathrm{II}}\,$ is given by,

$$\left\langle i_{\rm o} \right\rangle \Big|_{\rm II} = \frac{m}{9\omega_{\rm s}L_{\rm s}} \left[\left(\frac{9}{2}\delta + \frac{3\pi}{4} \right) V_{\rm i} + \left(9\delta - \frac{3\pi}{2} \right) m V_{\rm o} \right] \tag{C.51}$$

C.4 Input current equations:



• For Mode I:

The input current is given by,

$$i_{\rm i} = i_{\rm A} + i_{\rm C} = -i_{\rm B}$$
 (C.52)

with the current $i_{\rm B}$ given by (C.38). Since the current is linear, it can be written that,

$$\langle i_{i} \rangle \Big|_{I} = - \langle i_{B} \rangle \Big|_{I} = -\frac{i_{B}(0) + i_{B}(\delta + \frac{\pi}{6})}{2}$$
 (C.53)

Using (C.38) and simplifying, it yields to,

$$\left\langle i_{i}\right\rangle _{\mathrm{I}} = \left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}}\right) \cdot \left[\left(3\delta - \frac{\pi}{2}\right)V_{\mathrm{i}} + \left(\frac{9}{2}\delta + \frac{3\pi}{4}\right)mV_{\mathrm{o}}\right]$$
(C.54)

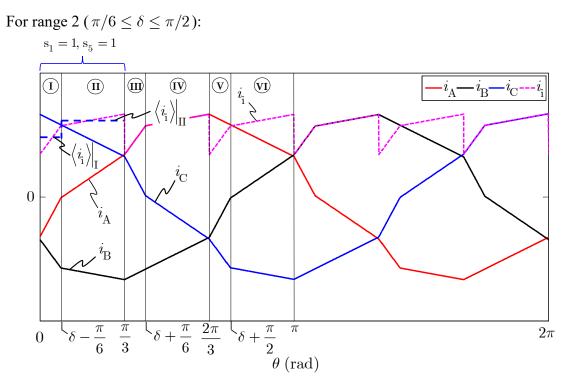
For Mode II:

The input current is also given by (C.52), such that,

$$\langle i_{\rm i} \rangle \Big|_{\rm I} = - \langle i_{\rm B} \rangle \Big|_{\rm I} = -\frac{i_{\rm B}(\delta + \frac{\pi}{6}) + i_{\rm B}(\frac{\pi}{3})}{2}$$
 (C.55)

with the current $i_{\rm B}$ also given by (C.38). Simplifying, it yields to,

$$\left\langle i_{i}\right\rangle_{\mathrm{I}} = \left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}}\right) \cdot \left[\left(3\delta + \frac{\pi}{2}\right)V_{\mathrm{i}} + \left(\frac{9}{2}\delta - \frac{3\pi}{4}\right)mV_{\mathrm{o}}\right]$$
(C.56)



• For Mode I:

Applying a similar procedure as for range 1, it can be demonstrated that $\langle i_1 \rangle |_{I}$ is given by,

$$\left\langle i_{i}\right\rangle \Big|_{I} = \left(\frac{1}{9\omega_{s}L_{s}}\right) \cdot \left[\left(3\delta - \frac{3\pi}{2}\right)V_{i} + 3\pi m V_{o}\right]$$
(C.57)

• For Mode II:

Similarly, it can be demonstrated that $\left< i_{i} \right> \right|_{II}$ is given by,

$$\left\langle i_{\rm i} \right\rangle \Big|_{\rm II} = \left(\frac{1}{9\omega_{\rm s}L_{\rm s}} \right) \cdot \left[\left(3\delta - \frac{\pi}{2} \right) V_{\rm i} + \left(\frac{9}{2}\delta + \frac{3\pi}{4} \right) m V_{\rm o} \right]$$
(C.58)

APPENDIX D – COMPLEMENT TO THE SSA MODEL IN NORMAL OPERATION

D.1 State-space equations for mode I and mode II:

- ▶ For range 1 ($0 \le \delta \le \pi/6$):
 - For Mode I: Inserting (C.42) into (3.9), it results in,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[9\delta m^{2} - \frac{3\pi}{2}m^{2} - \frac{9\omega_{\mathrm{s}}L_{\mathrm{s}}}{R}\right]}_{\mathbf{A}_{\mathrm{I}}} v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[\frac{9}{2}\delta + \frac{3\pi}{4}\right]}_{\mathbf{B}_{\mathrm{I}}} v_{\mathrm{i}}(t) \tag{D.1}$$

Inserting (C.54) into (3.10), it leads to,

$$i_{i}(t) = \underbrace{\left(\frac{m}{9\omega_{s}L_{s}}\right) \left[\frac{9}{2}\delta + \frac{3\pi}{4}\right]}_{\mathbf{C}_{I}} v_{o}(t) + \underbrace{\left(\frac{1}{9\omega_{s}L_{s}}\right) \left[3\delta - \frac{\pi}{2}\right]}_{\mathbf{D}_{I}} v_{i}(t)$$
(D.2)

For Mode II:

Inserting (C.49) into (3.9), it results in,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[9\delta m^{2} + \frac{3\pi}{2}m^{2} - \frac{9\omega_{\mathrm{s}}L_{\mathrm{s}}}{R}\right] v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[\frac{9}{2}\delta - \frac{3\pi}{4}\right] v_{\mathrm{i}}(t)}_{\mathbf{A}_{\mathrm{II}}}$$
(D.3)

Inserting (C.56) into (3.10), it leads to,

$$i_{\rm i}(t) = \underbrace{\left(\frac{m}{9\omega_{\rm s}L_{\rm s}}\right) \left[\frac{9}{2}\delta - \frac{3\pi}{4}\right]}_{\mathbf{C}_{\rm II}} v_{\rm o}(t) + \underbrace{\left(\frac{1}{9\omega_{\rm s}L_{\rm s}}\right) \left[3\delta + \frac{\pi}{2}\right]}_{\mathbf{D}_{\rm II}} v_{\rm i}(t) \tag{D.4}$$

- ▶ For range 2 ($\pi/6 \le \delta \le \pi/2$):
 - For Mode I:

Inserting (C.50) into (3.9), it results in,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[9\delta m^{2} - \frac{9\pi}{2}m^{2} - \frac{9\omega_{\mathrm{s}}L_{\mathrm{s}}}{R}\right]}_{\mathbf{A}_{\mathrm{I}}} v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[3\pi\right]}_{\mathbf{B}_{\mathrm{I}}} v_{\mathrm{i}}(t) \tag{D.5}$$

Inserting (C.57) into (3.10), it leads to,

$$i_{i}(t) = \underbrace{\left(\frac{m}{9\omega_{s}L_{s}}\right) \left[3\pi\right] v_{o}(t) + \underbrace{\left(\frac{1}{9\omega_{s}L_{s}}\right) \left[3\delta - \frac{3\pi}{2}\right] v_{i}(t)}_{\mathbf{D}_{I}}$$
(D.6)

For Mode II:

Inserting (C.51) into (3.9), it results in,)[

Inserting (C.51) into (3.9), it results in,

$$\frac{\mathrm{d}v_{\mathrm{o}}(t)}{\mathrm{d}t} = \underbrace{\left(\frac{1}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[9\delta m^{2} - \frac{3\pi}{2}m^{2} - \frac{9\omega_{\mathrm{s}}L_{\mathrm{s}}}{R}\right]}_{\mathbf{A}_{\mathrm{II}}} v_{\mathrm{o}}(t) + \underbrace{\left(\frac{m}{9\omega_{\mathrm{s}}L_{\mathrm{s}}C_{\mathrm{o}}}\right) \left[\frac{9}{2}\delta + \frac{3\pi}{4}\right]}_{\mathbf{B}_{\mathrm{II}}} v_{\mathrm{i}}(t) \tag{D.7}$$

Inserting (C.58) into (3.10), it leads to,

$$i_{i}(t) = \underbrace{\left(\frac{m}{9\omega_{s}L_{s}}\right) \left[\frac{9}{2}\delta + \frac{3\pi}{4}\right]}_{\mathbf{C}_{II}} v_{o}(t) + \underbrace{\left(\frac{1}{9\omega_{s}L_{s}}\right) \left[3\delta - \frac{\pi}{2}\right]}_{\mathbf{D}_{II}} v_{i}(t)$$
(D.8)

D.2 Averaged state-space matrices:

The averaged state-space matrices A, B, C and D in (3.15)–(3.18) are obtained by averaging their respective state-space matrices for modes I and II. For range 1, it is done by solving the following equations,

$$\mathbf{A} = \frac{3}{\pi} \left(\mathbf{A}_{\mathrm{I}} \left[\delta + \frac{\pi}{6} \right] + \mathbf{A}_{\mathrm{II}} \left[\frac{\pi}{6} - \delta \right] \right), \ \mathbf{B} = \frac{3}{\pi} \left(\mathbf{B}_{\mathrm{I}} \left[\delta + \frac{\pi}{6} \right] + \mathbf{B}_{\mathrm{II}} \left[\frac{\pi}{6} - \delta \right] \right)$$
$$\mathbf{C} = \frac{3}{\pi} \left(\mathbf{C}_{\mathrm{I}} \left[\delta + \frac{\pi}{6} \right] + \mathbf{C}_{\mathrm{II}} \left[\frac{\pi}{6} - \delta \right] \right), \ \mathbf{D} = \frac{3}{\pi} \left(\mathbf{D}_{\mathrm{I}} \left[\delta + \frac{\pi}{6} \right] + \mathbf{D}_{\mathrm{II}} \left[\frac{\pi}{6} - \delta \right] \right),$$
(D.9)

and the resulting averaged state-space matrices are given in (3.15) and (3.16).

For range 2, it requires solving the following equations,

$$\mathbf{A} = \frac{3}{\pi} \left(\mathbf{A}_{\mathrm{I}} \left[\delta - \frac{\pi}{6} \right] + \mathbf{A}_{\mathrm{II}} \left[\frac{\pi}{2} - \delta \right] \right), \ \mathbf{B} = \frac{3}{\pi} \left(\mathbf{B}_{\mathrm{I}} \left[\delta - \frac{\pi}{6} \right] + \mathbf{B}_{\mathrm{II}} \left[\frac{\pi}{2} - \delta \right] \right)$$
$$\mathbf{C} = \frac{3}{\pi} \left(\mathbf{C}_{\mathrm{I}} \left[\delta - \frac{\pi}{6} \right] + \mathbf{C}_{\mathrm{II}} \left[\frac{\pi}{2} - \delta \right] \right), \ \mathbf{D} = \frac{3}{\pi} \left(\mathbf{D}_{\mathrm{I}} \left[\delta - \frac{\pi}{6} \right] + \mathbf{D}_{\mathrm{II}} \left[\frac{\pi}{2} - \delta \right] \right)$$
(D.10)

and the resulting averaged state-space matrices are given in (3.17) and (3.18).

APPENDIX E – COMPLEMENT TO THE GSSA MODEL IN NORMAL OPERATION

In this appendix, the procedure to linearize the system of equations defined by (3.61)–(3.69) and (3.74)–(3.79) is summarized.

Linearizing the switching-functions:

Because of the sine and cosine terms in the definition of the output bridge averaged switching-functions, they must be first linearized. The procedure is shown here for $\langle \hat{s}'_1 \rangle_1^R$ and $\langle \hat{s}'_1 \rangle_1^I$.

• For the real part $\left< \hat{s}_1' \right>_1^R$:

First, recall from (3.75) that,

$$\left\langle \dot{s}_{1}\right\rangle_{1}^{R} = \frac{1}{\pi} \sin\left(\left\langle \delta \right\rangle_{0} + \frac{7\pi}{6}\right)$$
 (E.1)

Applying the following perturbation $\left<\delta\right>_0 = \delta + \left<\hat{\delta}\right>_0$, it yields to,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{R}} = \frac{1}{\pi} \sin\left(\left\{ \delta + \frac{7\pi}{6} \right\} + \left\langle \hat{\delta} \right\rangle_{0} \right) \tag{E.2}$$

Using the following trigonometric identity $\sin(A + B) = \sin(A)\cos(B) + \cos(A)\sin(B)$, the above equation can be developed into,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{R}} = \frac{1}{\pi} \left[\sin \left(\delta + \frac{7\pi}{6} \right) \cos \left(\left\langle \hat{\delta} \right\rangle_{0} \right) + \cos \left(\delta + \frac{7\pi}{6} \right) \sin \left(\left\langle \hat{\delta} \right\rangle_{0} \right) \right]$$
(E.3)

Assuming that $\langle \hat{\delta} \rangle_0$ is small, it can be approximated that $\cos(\langle \hat{\delta} \rangle_0) \approx 1$ and $\sin(\langle \hat{\delta} \rangle_0) \approx \langle \hat{\delta} \rangle_0$, such that (E.3) becomes,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{R}} = \frac{1}{\pi} \left[\sin \left(\delta + \frac{7\pi}{6} \right) + \cos \left(\delta + \frac{7\pi}{6} \right) \left\langle \hat{\delta} \right\rangle_{0} \right]$$
(E.4)

• For the imaginary part $\left< \hat{s}_1' \right>_1^1$:

Recall from (3.75) that,

$$\left\langle \mathbf{s}_{1}^{'}\right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \cos\left(\left\langle \delta \right\rangle_{0} + \frac{7\pi}{6}\right)$$
 (E.5)

Applying the same perturbation $\left<\delta\right>_0 = \delta + \left<\hat{\delta}\right>_0$, it yields to,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \cos\left(\left(\delta + \frac{7\pi}{6} \right) + \left\langle \hat{\delta} \right\rangle_{0} \right) \tag{E.6}$$

Using the following trigonometric identity $\cos(A + B) = \cos(A)\cos(B) - \sin(A)\sin(B)$, the above equation can be developed into,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \left[\cos \left(\delta + \frac{7\pi}{6} \right) \cos \left(\left\langle \hat{\delta} \right\rangle_{0} \right) - \sin \left(\delta + \frac{7\pi}{6} \right) \sin \left(\left\langle \hat{\delta} \right\rangle_{0} \right) \right]$$
(E.7)

Again, assuming that $\left\langle \hat{\delta} \right\rangle_0$ is small, such that $\cos\left(\left\langle \hat{\delta} \right\rangle_0\right) \approx 1$ and $\sin\left(\left\langle \hat{\delta} \right\rangle_0\right) \approx \left\langle \hat{\delta} \right\rangle_0$, the final result is the following,

$$\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \left[\cos \left(\delta + \frac{7\pi}{6} \right) - \sin \left(\delta + \frac{7\pi}{6} \right) \left\langle \hat{\delta} \right\rangle_{0} \right]$$
(E.8)

By applying a similar procedure to $\langle \dot{s}_3 \rangle_1^R$, $\langle \dot{s}_3 \rangle_1^R$, $\langle \dot{s}_5 \rangle_1^R$ and $\langle \dot{s}_5 \rangle_1^R$, it can be demonstrated that,

$$\left\langle \hat{\mathbf{s}}_{3}^{'} \right\rangle_{1}^{\mathrm{R}} = \frac{1}{\pi} \left[\sin \left(\delta + \frac{11\pi}{6} \right) + \cos \left(\delta + \frac{11\pi}{6} \right) \left\langle \hat{\delta} \right\rangle_{0} \right]$$
(E.9)

$$\left\langle \hat{\mathbf{s}}_{3}^{'} \right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \left[\cos \left(\delta + \frac{11\pi}{6} \right) - \sin \left(\delta + \frac{11\pi}{6} \right) \left\langle \hat{\delta} \right\rangle_{0} \right] \tag{E.10}$$

$$\left\langle \hat{\mathbf{s}}_{5}^{'} \right\rangle_{1}^{\mathrm{R}} = \frac{1}{\pi} \left[\sin \left(\delta + \frac{\pi}{2} \right) + \cos \left(\delta + \frac{\pi}{2} \right) \left\langle \hat{\delta} \right\rangle_{0} \right] \tag{E.11}$$

$$\left\langle \hat{\mathbf{s}}_{5}^{'} \right\rangle_{1}^{\mathrm{I}} = \frac{1}{\pi} \left[\cos \left(\delta + \frac{\pi}{2} \right) - \sin \left(\delta + \frac{\pi}{2} \right) \left\langle \hat{\delta} \right\rangle_{0} \right] \tag{E.12}$$

Linearization of the system of equations:

An example of the procedure to linearize the terms in (3.61)–(3.69) is shown here. The other terms can be obtained by applying a similar procedure. Let's develop the following term in (3.65),

$$-\frac{m}{L_{\rm A}} \left\langle S_{13}^{'} \right\rangle_{1}^{\rm R} \tag{E.13}$$

Since it is located in the first column of the matrix **A**, it multiplies the output voltage $\langle v_0 \rangle_0$ in the vector of state-variables $\langle \mathbf{x}(t) \rangle$, such that,

$$\left\langle i_{\mathrm{A}}\right\rangle_{1}^{\mathrm{R}} = -\frac{m}{L_{\mathrm{A}}} \left\langle S_{13}^{'}\right\rangle_{1}^{\mathrm{R}} \left\langle v_{\mathrm{o}}\right\rangle_{0} + [...]$$
(E.14)

Knowing from (3.66) that $\left\langle S_{13}^{'} \right\rangle_{1}^{\mathrm{R}} = \left\langle \mathbf{s}_{1}^{'} \right\rangle_{1}^{\mathrm{R}} - \left\langle \mathbf{s}_{3}^{'} \right\rangle_{1}^{\mathrm{R}}$, and applying the following perturbations $\left\langle v_{\mathrm{o}} \right\rangle_{0} = V_{\mathrm{o}} + \left\langle \hat{v}_{\mathrm{o}} \right\rangle_{0}, \left\langle i_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} = \left\langle I_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} + \left\langle \hat{i}_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}}, \text{ it leads to,}$ $\left\langle I_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} + \left\langle \hat{i}_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} = -\frac{m}{L_{\mathrm{A}}} \left[\left\langle \hat{\mathbf{s}}_{1}^{'} \right\rangle_{1}^{\mathrm{R}} - \left\langle \hat{\mathbf{s}}_{3}^{'} \right\rangle_{1}^{\mathrm{R}} \right] \cdot \left(V_{\mathrm{o}} + \left\langle \hat{v}_{\mathrm{o}} \right\rangle_{0} \right) + [...]$ (E.15)

Knowing that $\langle \hat{s}'_1 \rangle_1^R$ and $\langle \hat{s}'_3 \rangle_1^R$ are given by (E.4) and (E.9) respectively, the above equation can be developed into,

$$= -\frac{m}{\pi L_{\rm A}} \left[\left[\sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right) \right] + \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right) \right] \left\langle \hat{\delta} \right\rangle_0 \right] \times \left[V_{\rm o} + \left\langle \hat{v}_{\rm o} \right\rangle_0 \right] + \left[\dots \right]$$
(E.16)

which can be further expanded to,

$$\left\langle I_{\rm A} \right\rangle_{1}^{\rm R} + \left\langle \hat{i}_{\rm A} \right\rangle_{1}^{\rm R} = -\frac{m}{\pi L_{\rm A}} \left\{ \begin{array}{l} \sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right)\right] V_{\rm o} + \dots \\ \dots + \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right)\right] V_{\rm o} \left\langle \hat{\delta} \right\rangle_{0} + \dots \\ \dots + \left[\sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right)\right] \left\langle \hat{v}_{\rm o} \right\rangle_{0} + \dots \\ \dots + \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right)\right] \left\langle \hat{\delta} \right\rangle_{0} \left\langle \hat{v}_{\rm o} \right\rangle_{0} \right\} \right\}$$
(E.17)

Neglecting the constant terms related to the operating point and neglecting the second order term, (E.17) can be simplified to,

$$\left\langle \hat{i}_{\mathrm{A}} \right\rangle_{1}^{\mathrm{R}} = -\frac{m}{\pi L_{\mathrm{A}}} \left(\begin{bmatrix} \sin\left(\delta + \frac{7\pi}{6}\right) - \sin\left(\delta + \frac{11\pi}{6}\right) \end{bmatrix} \left\langle \hat{v}_{\mathrm{o}} \right\rangle_{0} + \dots \\ \dots + \left[\cos\left(\delta + \frac{7\pi}{6}\right) - \cos\left(\delta + \frac{11\pi}{6}\right) \right] V_{\mathrm{o}} \left\langle \hat{\delta} \right\rangle_{0} \right) + [\dots]$$
(E.18)

Equation (E.18) can be further rearranged into,

$$\left\langle \hat{i}_{A}\right\rangle_{1}^{R} = -\frac{1}{L_{A}} \left(S_{1} \left\langle \hat{v}_{o} \right\rangle_{0} + S_{2} V_{o} \left\langle \hat{\delta} \right\rangle_{0} \right) + [...]$$
(E.19)

with,

$$S_1 = \frac{m}{\pi} \left[\sin \left(\delta + \frac{7\pi}{6} \right) - \sin \left(\delta + \frac{11\pi}{6} \right) \right]$$
(E.20)

$$S_2 = \frac{m}{\pi} \left[\cos \left(\delta + \frac{7\pi}{6} \right) - \cos \left(\delta + \frac{11\pi}{6} \right) \right]$$
(E.21)

The term $-\frac{1}{L_A}S_1$ becomes A_{21} in the matrix $\hat{\mathbf{A}}$ given in (3.87) and the term $-\frac{V_o}{L_A}S_2$ becomes B_{12} in the matrix $\hat{\mathbf{B}}$ given in (3.88).

APPENDIX F – PIECEWISE-LINEAR MODEL IN OPEN-PHASE OPERATION

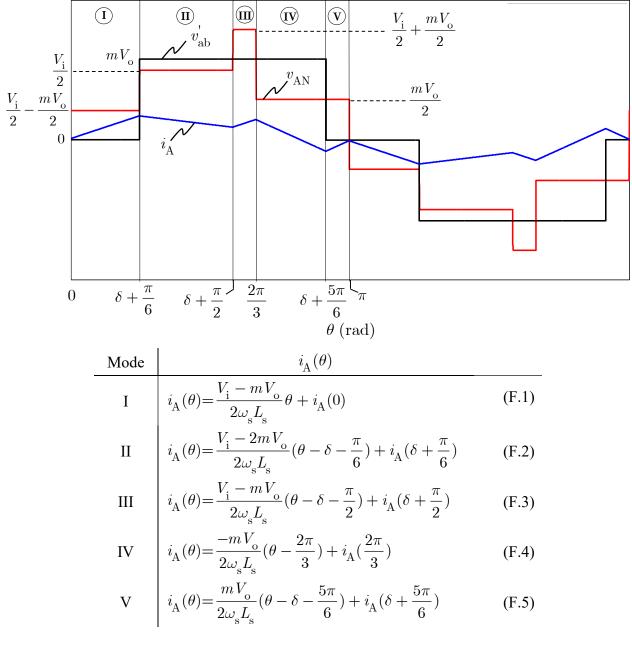
F.1 Mode definition and switching-functions:

Mode	$0 \le \delta \le \frac{\pi}{6}$	s_1	\mathbf{s}_3	s'_1	s'_3	s'_5
Ι	$0 \le \theta \le \delta + \frac{\pi}{6}$	X				X
II	$\delta + \frac{\pi}{6} \le \theta \le \delta + \frac{\pi}{2}$	X		X		X
III	$\delta + \frac{\pi}{2} \le \theta \le \frac{2\pi}{3}$	X		X		
IV	$\frac{2\pi}{3} \le \theta \le \delta + \frac{5\pi}{6}$	X	X	X		
V	$\delta + \frac{5\pi}{6} \le \theta \le \pi$	X	X	X	X	

Mode	$\frac{\pi}{6} \le \delta \le \frac{\pi}{2}$	\mathbf{s}_1	\mathbf{s}_3	s'_1	s'_3	s'_5
Ι	$0 \leq \theta \leq \delta - \frac{\pi}{6}$	Х			X	X
II	$\delta-\frac{\pi}{6}\leq\theta\leq\delta+\frac{\pi}{6}$	Х				X
III	$\delta + \frac{\pi}{6} \le \theta \le \frac{2\pi}{3}$	Х		Х		X
IV	$\frac{2\pi}{3} \le \theta \le \delta + \frac{\pi}{2}$	Х	Х	Х		X
V	$\delta + \frac{\pi}{2} \le \theta \le \pi$	X	Х	Х		

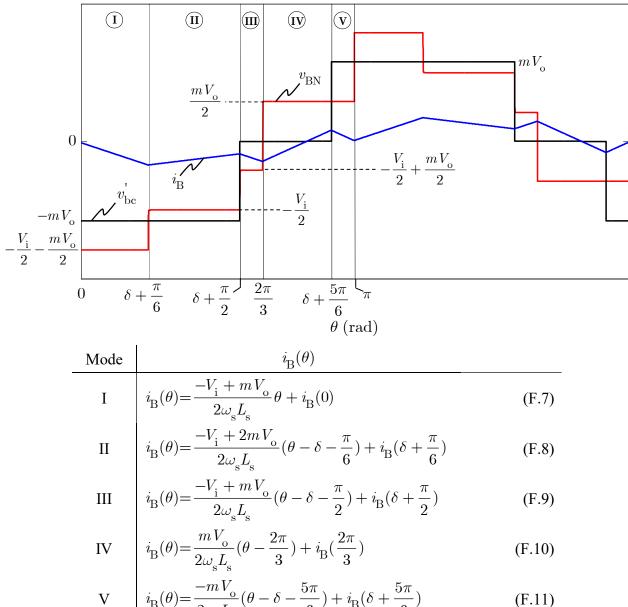
F.2 Piecewise-linear current equations:

For range 1 ($0 \le \delta \le \pi/6$):



$$i_{\rm A}(0) = \frac{\left[\left(3\pi + 6\delta \right) m V_{\rm o} - 2\pi V_{\rm i} \right]}{12\omega_{\rm s} L_{\rm s}} \tag{F.6}$$

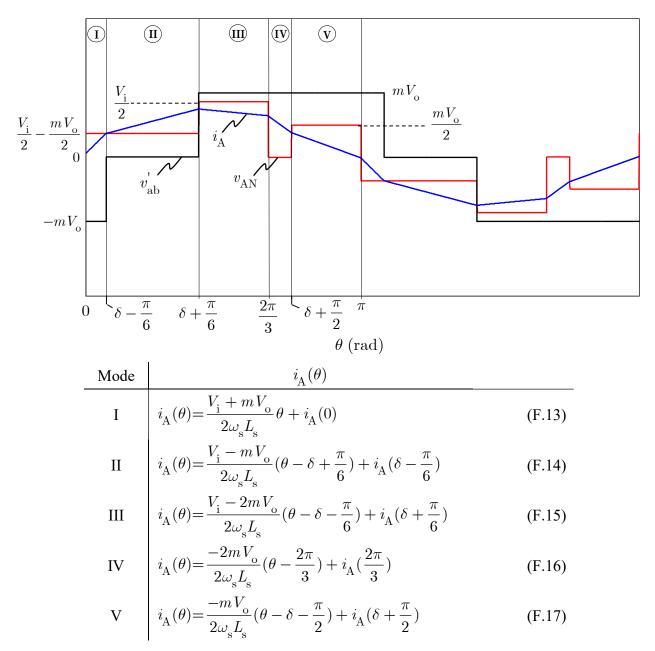
Phase B: 0



$$V \qquad i_{\rm B}(\theta) = \frac{-mV_{\rm o}}{2\omega_{\rm s}L_{\rm s}}(\theta - \delta - \frac{5\pi}{6}) + i_{\rm B}(\delta + \frac{5\pi}{6}) \tag{F.1}$$

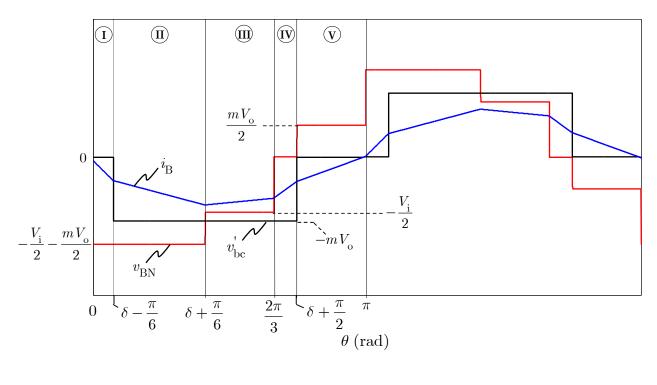
$$i_{\rm B}(0) = -i_{\rm A}(0)$$
 (F.12)

• Phase A:



$$i_{\rm A}(0) = \frac{\left[\left(5\pi - 6\delta \right) m V_{\rm o} - 2\pi V_{\rm i} \right]}{12\omega_{\rm s} L_{\rm s}} \tag{F.18}$$

• <u>Phase B:</u>



$$\label{eq:model} \begin{array}{c|c} \mbox{Mode} & i_{\rm B}(\theta) \\ \hline & \mbox{I} & i_{\rm B}(\theta) {=} \frac{-V_{\rm i} - m V_{\rm o}}{2 \omega_{\rm s} L_{\rm s}} \theta + i_{\rm B}(0) \end{array} \tag{F.19}$$

II
$$i_{\rm B}(\theta) = \frac{-V_{\rm i} + mV_{\rm o}}{2\omega_{\rm s}L_{\rm s}}(\theta - \delta + \frac{\pi}{6}) + i_{\rm B}(\delta - \frac{\pi}{6})$$
 (F.20)

III
$$i_{\rm B}(\theta) = \frac{-V_{\rm i} + 2mV_{\rm o}}{2\omega_{\rm s}L_{\rm s}}(\theta - \delta - \frac{\pi}{6}) + i_{\rm B}(\delta + \frac{\pi}{6})$$
 (F.21)

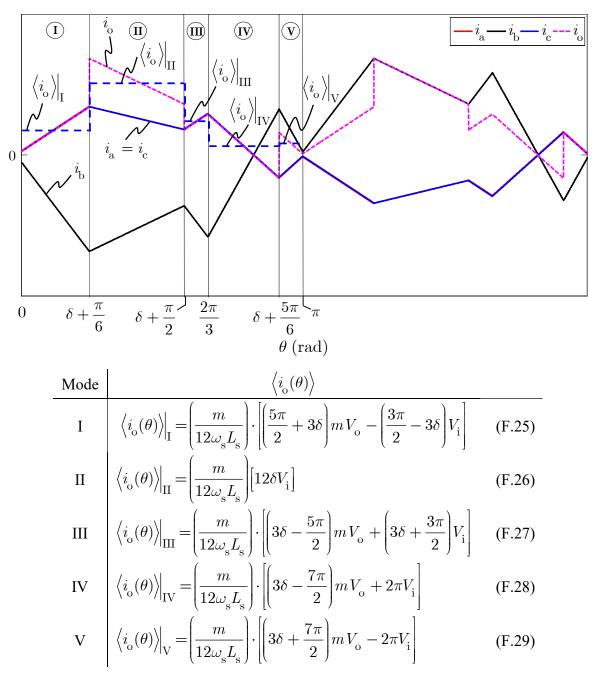
IV
$$i_{\rm B}(\theta) = \frac{2mV_{\rm o}}{2\omega_{\rm s}L_{\rm s}}(\theta - \frac{2\pi}{3}) + i_{\rm B}(\frac{2\pi}{3})$$
 (F.22)

$$\mathbf{V} \qquad i_{\mathrm{B}}(\theta) = \frac{mV_{\mathrm{o}}}{2\omega_{\mathrm{s}}L_{\mathrm{s}}}(\theta - \delta - \frac{\pi}{2}) + i_{\mathrm{B}}(\delta + \frac{\pi}{2}) \tag{F.23}$$

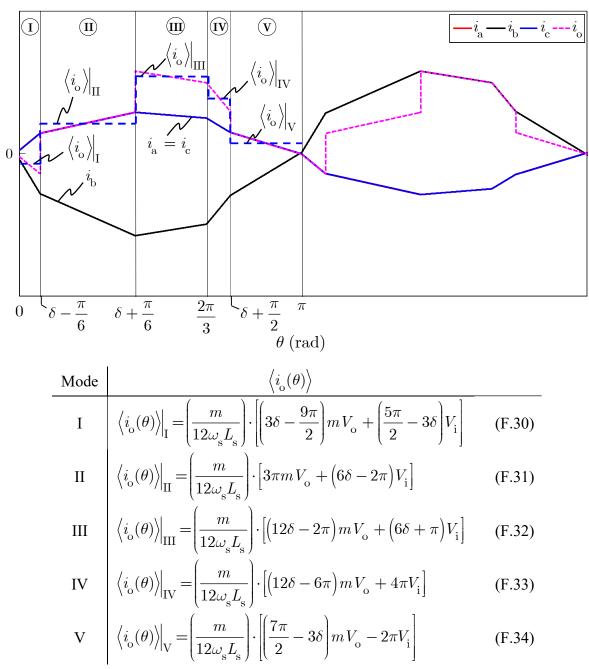
$$i_{\rm B}(0) = -i_{\rm A}(0)$$
 (F.24)

F.3 Output current equations:

For range 1 ($0 \le \delta \le \pi/6$):

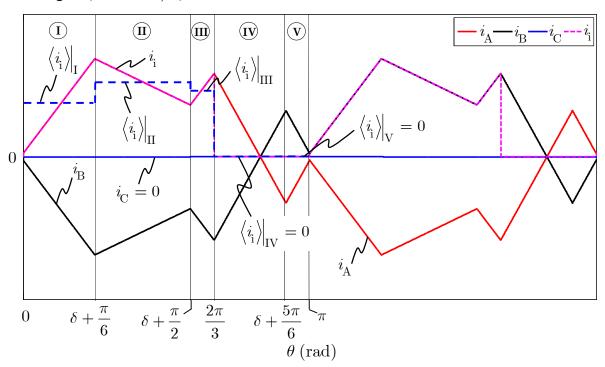


For range 2 ($\pi/6 \le \delta \le \pi/2$):



F.4 Input current equations:

For range 1 ($0 \le \delta \le \pi/6$):

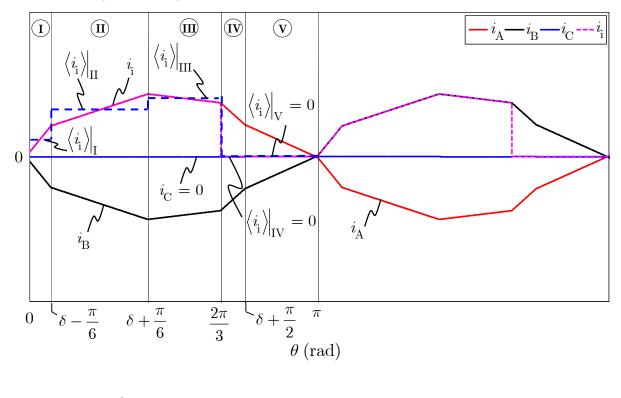


III
$$\left| \left\langle i_{i}(\theta) \right\rangle \right|_{\text{III}} = \left(\frac{1}{12\omega_{s}L_{s}} \right) \cdot \left[\left(3\delta - \frac{5\pi}{2} \right) m V_{o} + \left(3\delta + \frac{3\pi}{2} \right) V_{i} \right]$$
 (F.37)

$$IV \quad \left| \left\langle i_{i}(\theta) \right\rangle \right|_{IV} = 0 \tag{F.38}$$

$$\mathbf{V} \quad \left| \left\langle \dot{i}_{\mathbf{i}}(\theta) \right\rangle \right|_{\mathbf{V}} = 0 \tag{F.39}$$

For range 2 ($\pi/6 \le \delta \le \pi/2$):



$$\begin{split} \frac{\text{Mode}}{\text{I}} & \left| \left\langle i_{i}(\theta) \right\rangle \right|_{\text{I}} = \left[\frac{1}{12\omega_{\text{s}}L_{\text{s}}} \right] \cdot \left[\left(\frac{9\pi}{2} - 3\delta \right) m V_{\text{o}} + \left(3\delta - \frac{5\pi}{2} \right) V_{\text{i}} \right] & (\text{F.40}) \\ \text{II} & \left\langle i_{i}(\theta) \right\rangle \right|_{\text{II}} = \left[\frac{1}{12\omega_{\text{s}}L_{\text{s}}} \right] \cdot \left[3\pi m V_{\text{o}} + \left(6\delta - 2\pi \right) V_{\text{i}} \right] & (\text{F.41}) \\ \text{III} & \left\langle i_{i}(\theta) \right\rangle \right|_{\text{III}} = \left[\frac{1}{12\omega_{\text{s}}L_{\text{s}}} \right] \cdot \left[\left(6\delta - \pi \right) m V_{\text{o}} + \left(3\delta + \frac{\pi}{2} \right) V_{\text{i}} \right] & (\text{F.42}) \end{split}$$

$$IV \quad \left| \left\langle i_{i}(\theta) \right\rangle \right|_{IV} = 0 \tag{F.43}$$

$$\mathbf{V} \quad \left| \left\langle i_{\mathbf{i}}(\theta) \right\rangle \right|_{\mathbf{V}} = 0 \tag{F.44}$$