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Research Article

Nonlinear Circuit Analysis via Perturbation Methods and Hardware Prototyping

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Nonlinear signal processing is necessary in many emerging applications where form factor and power are at a premium. In order to make such complex computation feasible under these constraints, it is necessary to implement the signal processors as analog circuits. Since analog circuit design is largely based on a linear systems perspective, new tools are being introduced to circuit designers that allow them to understand and exploit circuit nonlinearity for useful processing. This paper discusses two such tools, which represent nonlinear circuit behavior in a graphical way, making it easy to develop a qualitative appreciation for the circuits under study.

1. Introduction

Portable and implantable, always-on electronics stand to benefit from analog signal processing, when only low levels of precision are necessary [1, 2]. To achieve sophisticated signal processing with low power and area overhead, an analog processor can exploit the fundamental nonlinear dynamics that are found in devices and simple circuits. So, the circuit designer must depart from the traditional linear systems paradigm, and learn to analyze and understand circuits from a nonlinear dynamical systems theory perspective.

In order to make nonlinear circuit design relevant to an engineer, it must be taught intuitively enough to foster creativity, yet rigorously enough to be of practical benefit. The two most popular tools for studying nonlinear circuits are harmonic balance and Volterra series. Within certain limitations, they are rigorous, but not necessarily intuitive. Since harmonic balance is simulation-based, it can be used to predict nonlinear behavior without ever requiring a deep understanding [3] of the circuit. Volterra series is an analytical tool that quickly leads to high entropy [4] mathematical expressions, from which the causative physical phenomena are hard to discern, much less purposefully manipulate.

To bridge the gap between rigor and intuition, we can use visual representation techniques. If the appropriate visualization is formed from rigorous definitions of a circuit's dynamics, then the human vision system, with its pattern recognition ability, will perceive the circuit's qualitative behavior [5].

We will present a filter block diagram for analyzing harmonic distortion that is derived from perturbation analysis. Unlike Volterra series kernels, our filter block diagram does not include multidimensional Fourier transforms, and so is accessible to an introductory-level engineering audience. In the second part of this paper, we will discuss the creation and use of phase plane plots of nonlinear circuits. We will describe how to rapidly create the phase plane plots with a reconfigurable hardware platform, instead of with a numerical simulator.

This paper is an expansion of the work presented in [6].

2. Regular Perturbation

Whenever designers want to get an analytical handle on the sources and causes of distortion, the most commonly-used tool is Volterra series analysis. If a problem is tractable using

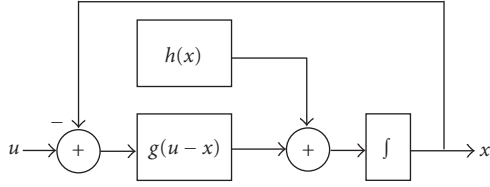


FIGURE 1: General block diagram form of a first-order circuit. The primary processing block is $g(\cdot)$, which is a nonlinear function of the input u and of x via feedback. The nonlinearity $h(x)$ models such nonidealities as finite output impedance.

Volterra series, then it can also be solved with perturbation theory, which will yield asymptotically-identical results [7].

There are certain problems for which Volterra series are ill-suited—multiple-time-scale behavior and multiple steady states, for instance [8]—that can be solved with perturbation theory. Despite the power of perturbation theory, it is still a relatively obscure concept in discussions about nonlinearity and distortion in analog circuits.

We therefore find it worthwhile to give a basic treatment of regular perturbation—the simplest perturbation method—as applied to distortion analysis of first-order analog circuits. In addition, a filter block diagram representation of the circuit will naturally evolve from our analysis, making it visually clear how the distortion terms are manifested, and how well-known tenets of low-distortion design, such as feedback, come about.

Consider the initial value problem

$$\dot{x} = f(t, x, \epsilon); \quad x(t_0) = x_0(\epsilon), \quad (1)$$

where ϵ is a small perturbation parameter such that $\epsilon = 0$ yields an analytically-soluble equation. If f is sufficiently smooth (the specific smoothness requirements of f are discussed in [9]), then the problem has a unique solution $x(t, \epsilon)$. As the solution for $\epsilon \neq 0$ may not be analytical, it can be approximated as a power series in ϵ to an accuracy of $O(\epsilon^{n+1})$. That is, we can write the solution as

$$x(t, \epsilon) = \underbrace{\sum_{i=0}^n (x_i(t) \epsilon^i)}_{\hat{x}(t, \epsilon)} + O(\epsilon^{n+1}), \quad (2)$$

where $\hat{x}(t, \epsilon)$ is the approximate solution. To conduct regular perturbation, we apply the substitution $x(t, \epsilon) \approx \hat{x}(t, \epsilon)$ to (1). The resulting system is then solved by equating like powers of ϵ . The following sections will illuminate this idea.

3. The Basic First-Order Circuit

Most common first-order analog circuits (simple amplifiers, buffers, switches, etc.) are of the form depicted in Figure 1. The governing equation is

$$\dot{x} = g(u - x) + h(x), \quad (3)$$

where u is the a.c. input signal, x is the a.c. output signal and $g(\cdot)$ and $h(\cdot)$ are nonlinear functions. The dependence of the

system on the output, other than through feedback to the input, is modeled by $h(x)$. In practice, $h(x)$ is typically some nonideality such as finite output resistance.

In order to apply perturbation analysis to (3), we begin by assuming that the input signal has a small amplitude. This is expressed as $u = \epsilon v$, where ϵ is a small perturbation parameter and v is a suitably-scaled version of the input signal. Note that with the definition of u , (3) is solvable via separation of variables for the special case $\epsilon = 0$.

With the introduction of the perturbation parameter ϵ , we can approximate the solution to (3) with the power series

$$x(t) \approx \sum_{i=1}^n \epsilon^i x_i(t). \quad (4)$$

Note the ϵ^0 term of (4) is set to 0. This corresponds to analyzing a circuit about its d.c. bias point, where the d.c. bias point is shifted to the origin. For ease of notation, define $z = u - x$. The approximation of z is defined similarly to (4), with $z_1 = v - x_1$ and $z_i = -x_i$, for all $i > 1$.

If ϵ is sufficiently small, then the functions $g(z)$ and $h(x)$ can be approximated by their truncated Taylor series as

$$\begin{aligned} g(z) &\approx g_1 z + g_{n-1} z^{n-1} + g_n z^n, \\ h(x) &\approx h_1 x + h_{n-1} x^{n-1} + h_n x^n. \end{aligned} \quad (5)$$

Functions g and h are assumed to be dominantly $(n-1)$ th-order nonlinearities, with $g_i = g^{(i)}(0)/i!$ and $h_i = h^{(i)}(0)/i!$. Equation (5) assumes $g(0) = h(0) = 0$, which, again, corresponds to analyzing a circuit about its d.c. bias point.

Substituting (4) and (5) into (3) and collecting powers of ϵ , we get the following set of first-order *linear* equations

$$\begin{aligned} \dot{x}_1 + (g_1 - h_1)x_1 &= g_1 v, \\ &\vdots \\ \dot{x}_k + (g_1 - h_1)x_k &= 0 \quad \forall k < n-1 \\ &\vdots \\ \dot{x}_{n-1} + (g_1 - h_1)x_{n-1} &= g_{n-1} z_1^{n-1} + h_{n-1} x_1^{n-1} \\ \dot{x}_n + (g_1 - h_1)x_n &= g_n z_1^n - n g_{n-1} z_1^{n-1} x_2 \\ &\quad + h_n x_1^n - n h_{n-1} x_1^{n-1} x_2. \end{aligned} \quad (6)$$

The ϵ^1 equation is the linearized portion of (3) with input v . Taking the Laplace transform of this equation, we write

$$X_1(s) = g_1 H(s) V(s), \quad (7)$$

where $H(s) = 1/(s + g_1 - h_1)$.

The ϵ^k equations ($k < (n-1)$) are filters with 0 input. As such, the steady state solutions of these equations is 0.

4. Harmonic Distortion Terms

The inputs of the ϵ^{n-1} equation are terms of z_1^{n-1} and x_1^{n-1} . To understand the implications of these terms to harmonic

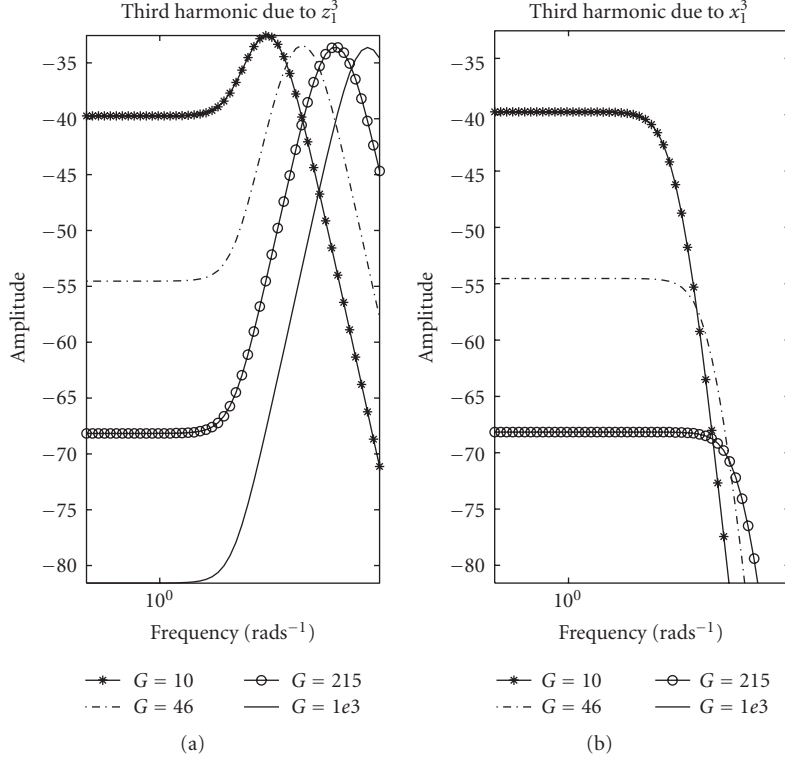


FIGURE 2: Magnitude-frequency plots of the third harmonic. The “gain”, G of the $g(z)$ function is varied from 10 to 1000. This causes the band-pass shape of the z_1^3 -contributed harmonic to shift to the right, while that contributed by x_1^3 falls in magnitude.

distortion, assume a single-tone input, $v = \cos(\omega t)$. This elicits the signals

$$\begin{aligned} x_1 &= g_1 |H(j\omega)| \cos(\omega t + \phi(j\omega)), \\ z_1 &= |1 - g_1 H(j\omega)| \cos(\omega t + \phi_{z1}(j\omega)), \\ &= \left| \underbrace{(s - h_1)H(j\omega)}_{H_{z1}(j\omega)} \right| \cos(\omega t + \phi_{z1}(j\omega)), \end{aligned} \quad (8)$$

Here we have defined $H_{z1}(s) = (1 - g_1 H(s))$. The phases $\phi(s)$ and $\phi_{z1}(s)$ are the arguments of $H(s)$ and $H_{z1}(s)$, respectively. The signals x_1 and z_1 are single tones of frequency ω as well, since they are merely linearly-filtered versions of v .

Raising z_1 and x_1 each to the $(n-1)$ th power produces harmonics as follows. If $(n-1)$ is odd(even), then odd(even) harmonics up to the $(n-1)$ th harmonic are generated. The amplitude of the $m\omega$ frequency term in x_1^{n-1} is

$$\frac{(n-1)!g_1}{((n+m-1)/2)!((n-m-1)/2)!2^{n-2}} |H(j\omega)|, \quad (9)$$

while that of the $m\omega$ frequency term in z_1^{n-1} is

$$\frac{(n-1)!}{((n+m-1)/2)!((n-m-1)/2)!2^{n-2}} |H_{z1}(j\omega)|. \quad (10)$$

After filtering in the ϵ^{n-1} equation, the amplitudes of these terms will be, respectively,

$$\frac{(n-1)!h_{n-1}g_1}{((n+m-1)/2)!((n-m-1)/2)!2^{n-2}} |H(j\omega)| |H(jm\omega)|, \quad (11)$$

$$\frac{(n-1)!g_{n-1}}{((n+m-1)/2)!((n-m-1)/2)!2^{n-2}} |H_{z1}(j\omega)| |H(jm\omega)|. \quad (12)$$

Analogous to that of the ϵ^{n-1} equation, the input to the ϵ^n equation has terms in z_1^n and x_1^n . In general, the x_2 terms are identically zero, except for the special case $n = 3$.

5. Feedback and Distortion

We now make some observations about the harmonic distortion results that were discussed in the previous section.

In the ϵ^{n-1} equation, the amplitude of the m th harmonic that the z_1^{n-1} term contributes is given by (12). We plot this amplitude expression, along with that of (11), as a function of frequency in Figure 2 for the third-order harmonic generated by a dominantly-third order nonlinearity. That is, $n = 4$ and $m = 3$. Also, we chose $h_1 = 1$, $h_3 = 1/3$, $g_1 = G$, $g_3 = G/3$, where G was varied from 10 to 1000.

Notice from the figure that if $g_1 \gg h_1$, then, for a given frequency, the amplitude of the z_1^{n-1} -contributed harmonic is greatly reduced. In fact, if we ensure $g_i \gg h_i$ for all i , then the

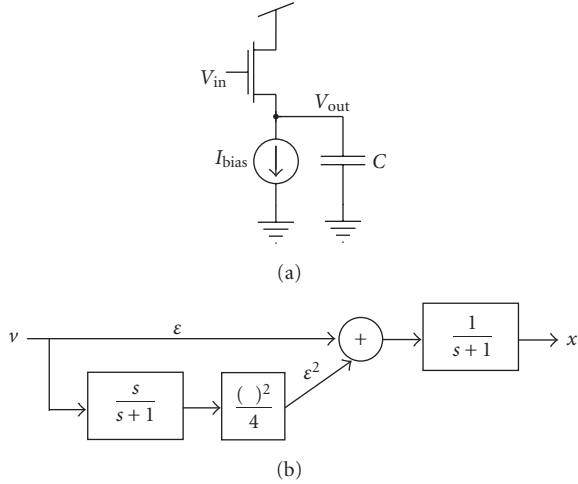


FIGURE 3: Source follower amplifier. (a) Circuit schematic. (b) Block diagram representation of source follower output. The fundamental harmonic is a low-pass filtered version of the input. The second order terms are generated by high-pass filtering the input, squaring and then low pass filtering. The total output is a power series of ϵ terms.

harmonic contribution of the x_1^{n-1} terms is negligible. This would mean that the distortion is effectively due only to z_1 , whose associated harmonics are band-pass filtered. This in turn means that the distortion can be kept small if the circuit is operated well below the corner frequency.

These two notions—that frequency and feedback gain can be sacrificed for higher linearity—conform with the traditional rules-of-thumb for low-distortion design.

6. Illustrative Examples

6.1. Source Follower Amplifier. According to KCL, the circuit equation of the source follower amplifier in Figure 3(a) is

$$C \frac{dV_{out}(t)}{dt} = F(V_{in}, V_{out}) - I_{bias}, \quad (13)$$

where the function F is defined as

$$F(V_{in}, V_{out}) = \frac{K}{2} (\kappa V_{in}(t) - V_{out}(t) - V_{th})^2, \quad (14)$$

if M_1 is in above-threshold saturation, and

$$F(V_{in}, V_{out}) = I_0 e^{(\kappa V_{in}(t) - V_{out}(t))/U_T}, \quad (15)$$

if it is in subthreshold saturation. The parameter K depends on transistor dimensions and doping and V_{th} is the threshold voltage. Also, κ , I_0 , and U_T have their usual meanings from the EKV MOSFET model [10].

Note that $I_{bias} = F(V_g, V_s)$, where V_g and V_s are the d.c. bias-points of the gate and source of M_1 , respectively. Let us define a *characteristic voltage*, V_c , as

$$V_c = \begin{cases} \frac{(\kappa V_g - V_s - V_{th})}{2}, & \text{above threshold} \\ U_T, & \text{subthreshold.} \end{cases} \quad (16)$$

Now, (13) can be nondimensionalized [9] by making the substitutions

$$\tau = \frac{I_{bias}}{(CV_c)} \cdot t; \quad u = \frac{\kappa v_{in}}{V_c}; \quad x = \frac{v_{out}}{V_c}, \quad (17)$$

where v_{in} and v_{out} are the a.c. portions of V_{in} and V_{out} . This gives the state-space equation of the source follower as

$$\frac{dx}{d\tau} = u - x + \frac{(u - x)^2}{4}, \quad (18)$$

for above threshold, and

$$\frac{dx}{d\tau} = u - x + \frac{(u - x)^2}{2}, \quad (19)$$

for the truncated Taylor expansion in subthreshold. The point is that, regardless of region of operation of M_1 , the nonlinear equation that describes the source follower has the same functional form. Relating the source follower equations to (3), we have $g(z) \sim z + z^2$ and $h(x) = 0$. As such, we expect the harmonic distortion terms to have a band-pass-like dependence on frequency. To show this, we will apply regular perturbation to (18).

First, define $u = \epsilon v$, where the small parameter ϵ is a scaled version of the input amplitude. That is, $\epsilon = A_{in}/V_c$. Also, taking $x = \epsilon x_1 + \epsilon^2 x_2$ and $z = u - x$ and equating like powers of ϵ up to ϵ^2 , we have

$$\epsilon^1 : \dot{x}_1 = v - x_1, \quad (20)$$

$$\epsilon^2 : \dot{x}_2 = \frac{z_1^2}{4} - x_2, \quad (21)$$

as depicted in Figure 3(b). Assume a pure-tone input, $v = \cos(\omega t)$. Equation (20) is the linear portion of the amplifier. Equation (21) is a linear filter with input $z_1^2/4$. The squaring produces a second-harmonic term as well as a d.c. offset. In addition, since $z_1 = v - x_1$, the second harmonic generated by the squaring is high-pass filtered. The overall effect is that x_2 is a band-pass filtered version of a second harmonic of v . Figure 4 is a plot of experimental data that corroborates our analysis. There is error in the second harmonic measurement due to the small amplitudes involved.

6.2. Unity Gain Buffer. Consider the unity-gain buffer depicted in Figure 5(a). It is formed by placing an operational transconductance amplifier (OTA) in negative feedback. If we operate the OTA above threshold, the describing equation is

$$C \frac{dV_{out}}{dt} = \sqrt{\kappa \beta I_{bias}} (V_{in} - V_{out}) \sqrt{1 - \frac{\kappa \beta (V_{in} - V_{out})^2}{4 I_{bias}}}, \quad (22)$$

while it is

$$C \frac{dV_{out}}{dt} = I_{bias} \tanh\left(\frac{\kappa (V_{in} - V_{out})}{2 U_T}\right), \quad (23)$$

for subthreshold operation. Notice that we have ignored the output conductance term, which is considered very small for OTAs.

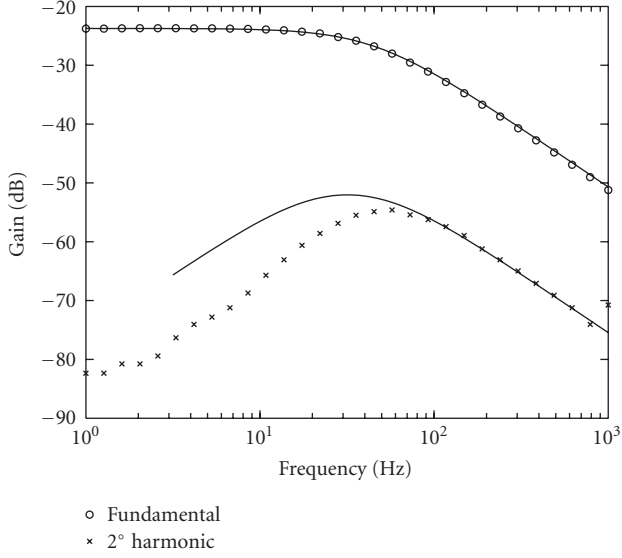


FIGURE 4: Magnitude-frequency response of source follower. Analytical prediction is in bold, and experimental data is plotted as “x”s and “o”s. The fundamental harmonic is a low-pass filtered version of the input. The second harmonic has a bandpass shape, as predicted by perturbation analysis.

We can define a characteristic voltage, V_c , as

$$V_c = \begin{cases} \frac{2U_T}{\kappa}, & \text{subthreshold} \\ \sqrt{\frac{I_{\text{bias}}}{\kappa\beta}}, & \text{above threshold.} \end{cases} \quad (24)$$

Then, with the following definitions

$$\tau = \frac{I_{\text{bias}}}{(CV_c)} \cdot t; \quad u = \frac{v_{\text{in}}}{V_c}; \quad x = \frac{v_{\text{out}}}{V_c}, \quad (25)$$

the nondimensional form of the unity-gain buffer’s describing equations (taken to the first few Taylor series terms) is

$$\frac{dx}{d\tau} = \begin{cases} (u - x) - \frac{(u - x)^3}{4}, & \text{above threshold} \\ (u - x) - \frac{(u - x)^3}{3}, & \text{subthreshold.} \end{cases} \quad (26)$$

Again, the functional form of the equations is identical, regardless of region of operation.

To calculate distortion terms, assume $u = \epsilon v$ is a pure-tone signal and proceed as usual. For subthreshold, the separated equations of ϵ are

$$\epsilon^1 : \dot{x}_1 = v - x_1, \quad (27)$$

$$\epsilon^2 : \dot{x}_2 = 0 - x_2, \quad (28)$$

$$\epsilon^3 : \dot{x}_3 = \frac{z_1^3}{3} - x_3. \quad (29)$$

These equations are depicted in the block diagram of Figure 5(b). Equation (27) is the linear portion of the

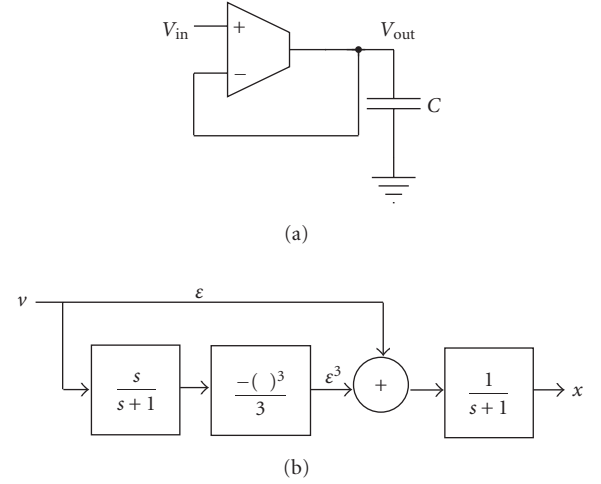


FIGURE 5: Unity gain buffer. (a) Circuit schematic. (b) Block diagram representation of output. The fundamental harmonic is a low-pass filtered version of the input. The third-order terms are generated by high-pass filtering the input, cubing and then low pass filtering. The total output is a power series of ϵ terms.

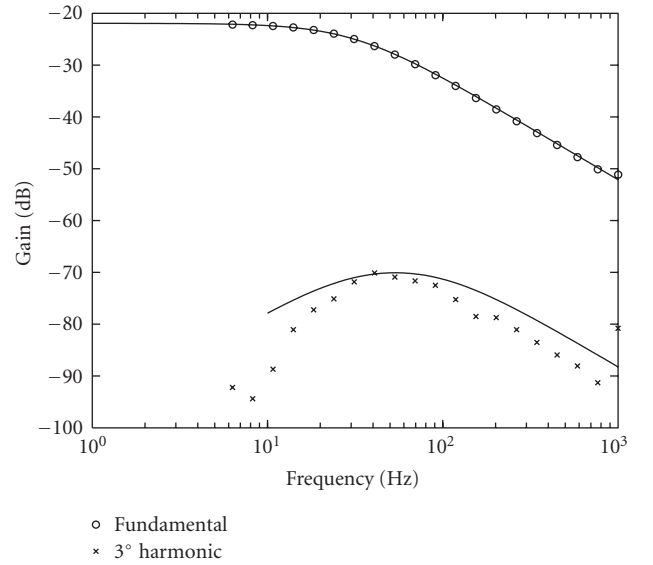


FIGURE 6: Magnitude-frequency response of unity-gain buffer. Analytical prediction is in bold, and experimental data is plotted as “x”s and “o”s. The fundamental harmonic is a low-pass filtered version of the input. The third harmonic has a bandpass shape, as predicted by perturbation analysis.

amplifier. Equation (28) is a linear filter with 0 input; it contributes no harmonics at steady state. Equation (29) is a linear filter with input $z_1^3/3$. The cubing produces a third-harmonic term as well as a fundamental-frequency term (this fundamental-frequency term will cause gain compression, which is not discussed in this paper). Since $z_1 = v - x_1$, the overall effect is that x_3 is a band-pass filtered version of a third harmonic of v , as shown in Figure 6.

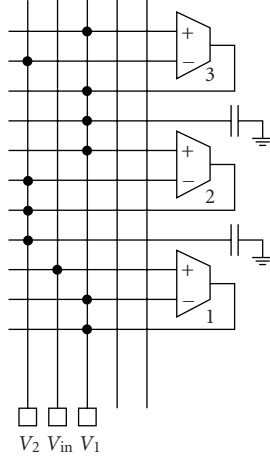


FIGURE 7: Compiled Circuit on an FPAA. Component terminals can be connected or disconnected using the switch matrix. In this illustration, connections are indicated by solder dots. The gain of each amplifier is programmable over a continuous range of values over a few decades.

6.3. Note on Above-Threshold versus Subthreshold Operation. The harmonic behavior of a circuit is similar for above- and subthreshold operation. In absolute numbers, however, above threshold operation yields less distortion. This is because the parameter $\epsilon = A_{in}/V_c$ is much smaller for above threshold than for subthreshold. Since the harmonics are multiplied by ϵ^i , the smaller ϵ seen in above threshold operation translates to lower distortion.

7. Field Programmable Analog Array

We have developed a field programmable analog array (FPAA) that can be configured to synthesis and analyze a vast variety of circuits [11]. Figure 7 depicts a simple, second-order filter compiled on our FPAA. More complex circuit configurations are possible, and would involve a larger number of the over 400 components in the FPAA. In this part of the paper we demonstrate the utility and versatility of the FPAA in analyzing the dynamics of a number of fundamental circuit blocks.

8. One-Dimensional Systems

8.1. Simple Current Mirror. Consider the simple current mirror depicted in Figure 8(a). From Kirchhoff's Current Law (KCL), it obeys the following differential equation:

$$C \frac{dV_g}{dt} = I_b - f(V_g), \quad (30)$$

where $f(V_g)$ is the drain current of transistor M1. Assuming M1 and M2 are identical and are both saturated, we have $f(V_g) = I_{out}$, which gives

$$C \frac{dV_g}{dt} = I_b - I_{out}. \quad (31)$$

For subthreshold operation in saturation, the current through transistors M1 and M2 is [10]

$$f(V_g) = I_{out} = I_0 e^{(\kappa V_g - V_S)/U_T}, \quad (32)$$

where I_0 is a pre-exponential constant dependent on the transistor's size and on doping concentrations. Also, κ is the body-effect coefficient and U_T is the thermal voltage. V_S is the source voltage, which, for this case, is zero. Setting $V_S = 0$ and taking the derivative of (32) with respect to time, we get

$$\frac{dI_{out}}{dt} = \frac{\partial}{\partial V_g} (I_0 e^{\kappa V_g/U_T}) \frac{dV_g}{dt}, \quad (33)$$

$$= \frac{\kappa}{U_T} I_{out} \frac{dV_g}{dt}, \quad (34)$$

which allows us to rewrite (31) as

$$\begin{aligned} \frac{CU_T}{\kappa I_b} \frac{dI_{out}}{dt} &= I_{out} \left(1 - \frac{I_{out}}{I_b} \right), \\ \tau \frac{dI_{out}}{dt} &= I_{out} \left(1 - \frac{I_{out}}{I_b} \right). \end{aligned} \quad (35)$$

The time constant is identified as $\tau = (CU_T)/(\kappa I_b)$.

Equation (35) happens to be the *logistic equation*, a simple model of population dynamics. It can be solved exactly either by separation of variables followed by partial fractions, or by solving it as Bernoulli's equation. The solution is

$$I_{out}(t) = \frac{I_b e^{t/\tau}}{e^{t/\tau} - 1 + I_b/I_{out_0}}, \quad (36)$$

where I_{out_0} is the initial value of I_{out} . We are lucky to have an exact solution to (35), given that it is a nonlinear differential equation. Even so, it is difficult to discern much useful information about I_{out} 's qualitative behavior from (36). For instance, it is not clear how the behavior of I_{out} might change with different initial conditions. To answer questions of this sort, it is helpful to do geometric analysis on the system's corresponding vector field.

Since the simple current mirror is a one-dimensional system, its vector field is represented as a flow on a line. The direction and speed of the flow are dictated by the right hand side (RHS) of (35). It is a quadratic, as shown in Figure 10. The I_{out} -intercepts are 0 and I_b . There is a maximum at $I_{out} = I_b/2$. The vector field is depicted as the arrows on the I_{out} axis. For positive values of dI_{out}/dt , I_{out} is increasing, meaning the arrows point to the right. For negative values of dI_{out}/dt , I_{out} is decreasing, meaning the arrows point to the left. When $dI_{out}/dt = 0$, there is no change in I_{out} and the circuit is said to be at equilibrium.

The vector field provides clear, qualitative information about the behavior of I_{out} . There are two equilibrium points, namely $I_{out} = 0$ and $I_{out} = I_b$. Note that the vector field flows away from $I_{out} = 0$. This equilibrium point is unstable, since the system will not recover from slight disturbances away from it. The vector field flows towards $I_{out} = I_b$, implying

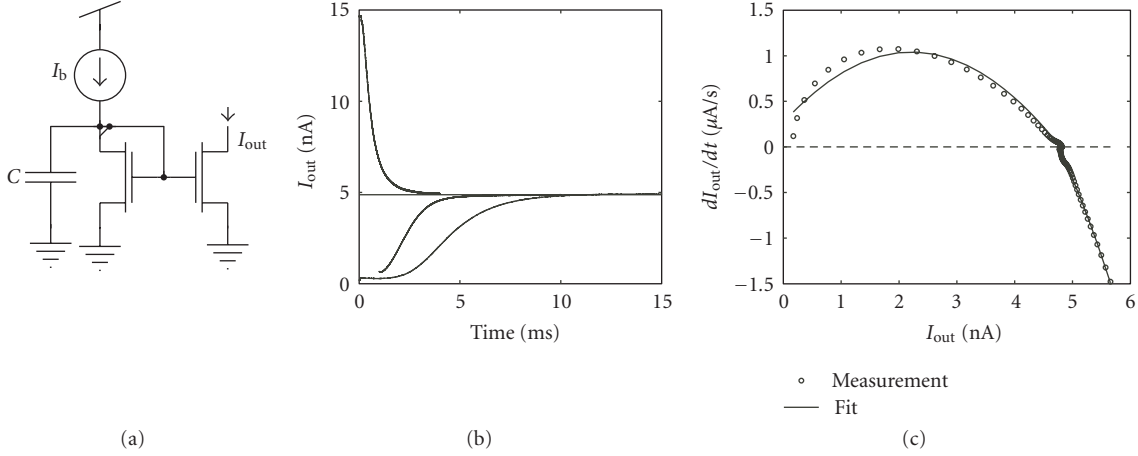


FIGURE 8: Simple current mirror. (a) Circuit that was compiled onto the FPAA. (b) Measured trajectories for different initial conditions. (c) Vector field derived from trajectory measurements. The origin is an unstable equilibrium point, while 5 is stable.

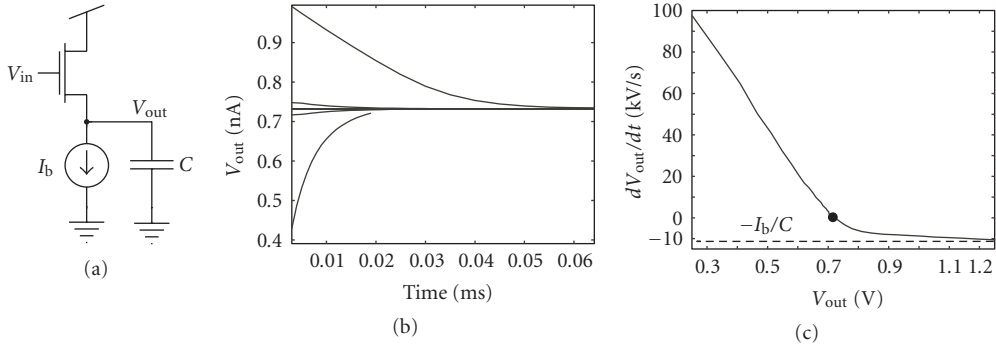


FIGURE 9: Source Follower Amplifier Acting as a Simple Peak Detector. (a) Circuit that was compiled onto the FPAA. (b) Measured step responses. (c) Vector field derived from step response measurements. The point (0, 0.7) is a stable fixed point.

that this is a stable equilibrium point. If the system is initially at $I_{out} = I_b$ and then experiences a small disturbance, it will tend back to the $I_{out} = I_b$ point.

The vector field in Figure 10 also gives information about the acceleration of I_{out} as it approaches the $I_{out} = I_b$ equilibrium point. For $0 < I_{out} < I_b/2$, the rate of change of I_{out} increases until it reaches a peak at $I_{out} = I_b/2$. Between $I_b/2$ and I_b , the system decelerates until the rate of change of I_{out} eventually becomes zero. For $I_{out} > I_b$, the rate of change of I_{out} steadily decreases until $I_{out} = I_b$. It is interesting to note that, for $I_{out} < I_b$, the rate of change of I_{out} is limited to a maximum of $I_b/(4\tau)$.

The geometric analysis predictions can be checked against experimental measurements of a current mirror that was compiled onto an FPAA. Figure 8(b) depicts various trajectories, or solutions, of the system of (35) for different initial conditions. Notice that trajectories that start at values lower than $I_{out} = I_b/2$ have a sigmoidal shape, with the point of inflection corresponding to the maximum rate of change of current $dI_{out}/dt = I_b/(4\tau)$. The parabolic shape of dI_{out}/dt can be extracted from these trajectories, and it is shown in Figure 8(c).

8.2. Simple Peak Detector. Assuming subthreshold operation, the KCL equation for the source follower amplifier of Figure 9(a) is the following.

$$C \frac{dV_{out}}{dt} = I_o e^{(\kappa V_{in} - V_{out})/U_T} - I_b. \quad (37)$$

Note that

$$\frac{d}{dt} e^{V_{out}/U_T} = \frac{e^{V_{out}/U_T}}{U_T} \frac{dV_{out}}{dt}, \quad (38)$$

in which case, the solution to (37) is

$$V_{out} = \kappa V_{in} + U_T \log \left(\frac{I_o}{I_b} - \left(\frac{I_o}{I_b} - e^{(V_{out_0} - \kappa V_{in})/U_T} \right) e^{-t/\tau} \right), \quad (39)$$

where $\tau = CU_T/I_b$ and V_{out_0} is the initial value of V_{out} .

The time that it takes for V_{out} to be within 10% of its final value is

$$t_{10} = \tau \log \left| \frac{I_o/I_b - e^{(V_{out_0} - \kappa V_{in})/U_T}}{I_o/I_b - e^{0.1\kappa V_{in}/U_T}} \right|. \quad (40)$$

For a large positive step input, $e^{(V_{out0} - \kappa V_{in})} \approx 0$, and (40) is approximately

$$t_{10} = t_{10+} \approx \tau \log \left| \frac{I_o}{I_o - I_b e^{0.1 \kappa V_{in}/U_T}} \right|. \quad (41)$$

For a large negative step input, $e^{(V_{out0} - \kappa V_{in})} \gg I_o/I_b$, and (40) becomes

$$\begin{aligned} t_{10} = t_{10-} &\approx \tau \log \left| \frac{I_b e^{(V_{out0} - \kappa V_{in})}}{I_o - I_b e^{0.1 \kappa V_{in}/U_T}} \right| \\ &= t_{10+} + \tau \left(\frac{V_{out0}}{U_T} - \kappa \frac{V_{in}}{U_T} \right) \log \left(\frac{I_b}{I_o} \right). \end{aligned} \quad (42)$$

Equations (41) and (42) indicate that the response of the peak detector is slower for a negative input step than it is for a positive input step. We surmise that if the input is continuously varying at a rate faster than $1/(t_{10-})$, then the output will be a reasonable representation of the input's peak values. Explaining the peak detector's behavior with (41) and (42) is rigorous, but depends on having to manipulate the expression of (39).

One way of avoiding the math is to employ intuitive descriptions of the charging action of the active device (i.e., the transistor) versus the discharging action of the current source [12]. A more rigorous approach is to apply nonlinear geometric analysis to the problem. Consider the plot of dV_{out}/dt versus V_{out} shown in Figure 9(c). We constructed it from a number of step response measurements (Figure 9(b)) that we took after compiling the source-follower amplifier onto the FPAA. A large negative input step corresponds to an initial value of $V_{out0} \gg V_{in}$. The rate of growth of V_{out} is bounded by I_b/C . For a large positive input step, however, $V_{out0} \ll V_{in}$, and the maximum rate at which V_{out} approaches V_{in} can be much greater than I_b/C . The maximum rate of approach in this case is limited only by the initial value, V_{out0} . As such, there is an asymmetry in the speed of the circuit's response to up-going versus down-going movements on the input. The effect of this asymmetry is that V_{out} tracks increasing V_{in} and not decreasing V_{in} , which is the behavior of a peak detector.

9. A Two-Dimensional System

Figure 11 depicts Lyon and Mead's classic second-order section [13]. It is a Gm-C filter with two poles that can be placed anywhere on the real/imaginary plane. We begin our analysis by writing down the governing equations for the circuit, assuming that the OTAs are based on subthreshold MOS transistor differential pairs:

$$\begin{aligned} C_1 \frac{dV_2}{dt} &= \frac{I_2}{k} \tanh \left(\frac{\kappa(V_1 - V_2)}{2U_T} \right), \\ C_1 \frac{dV_1}{dt} &= I_1 \tanh \left(\frac{\kappa(V_{in} - V_1)}{2U_T} \right) - I_3 \tanh \left(\frac{\kappa(V_2 - V_1)}{2U_T} \right), \end{aligned} \quad (43)$$

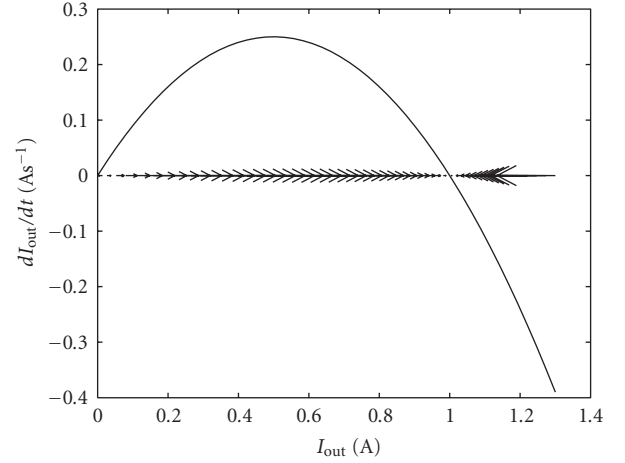


FIGURE 10: The vector field of the logistic equation is represented as a flow on the I_{out} axis. For positive values of dI_{out}/dt , I_{out} is increasing and the flow is to the right. For negative values of dI_{out}/dt , I_{out} is decreasing and the flow is to the left.

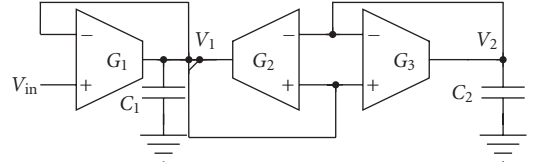


FIGURE 11: Second-Order Section. Varying the bias currents of the various amplifiers leads to interesting dynamics.

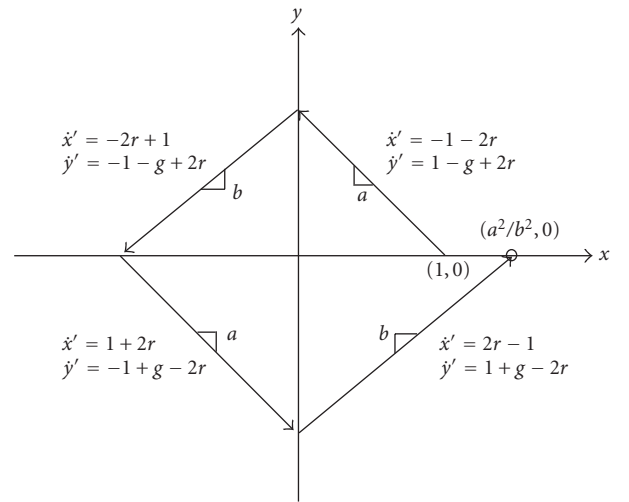
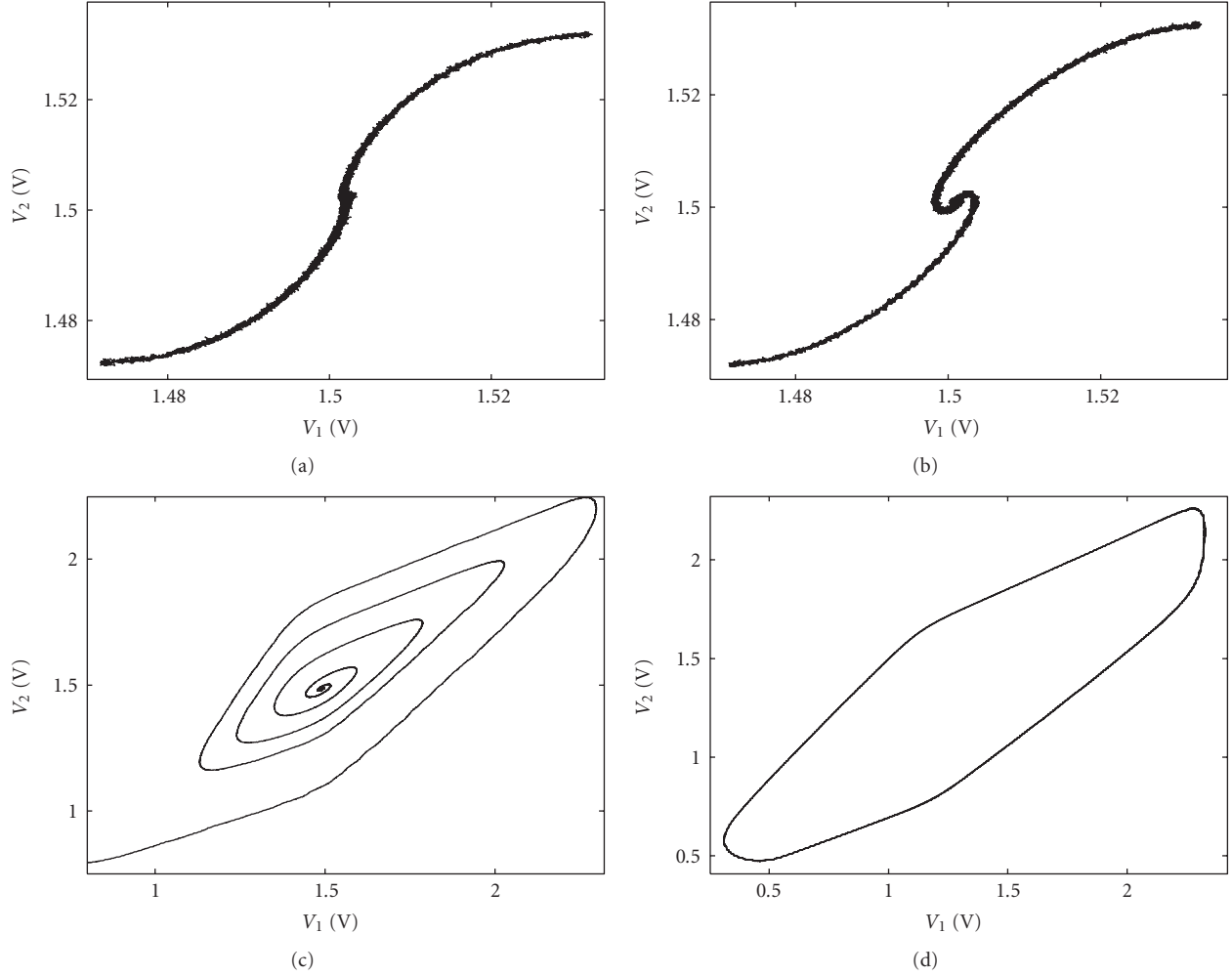


FIGURE 12: Sketch of SOS Phase Plane for Large Signals.

where $I_{1,2,3}$ are the bias currents of the OTAs. Also, k is the ratio of the C_2 to C_1 .

If we define

$$x = \frac{\kappa(V_1 - V_{in})}{2U_T}, \quad y = \frac{\kappa(V_2 - V_1)}{2U_T}, \quad (44)$$

FIGURE 13: SOS Experimental Phase Plane Results for Various Values of r .

then (43) become

$$\frac{2U_T C_1}{\kappa} \frac{dx}{dt} = -I_1 \tanh(x) - I_3 \tanh(y), \quad (45)$$

$$\frac{2U_T C_1}{\kappa} \frac{dy}{dt} = I_1 \tanh(x) + \left(I_3 - \frac{I_2}{k}\right) \tanh(y).$$

Further defining

$$\begin{aligned} I_1 &= I_{\text{bias}}, \\ I_2 &= gkI_{\text{bias}}, \\ I_3 &= 2rI_{\text{bias}}, \\ t &= \tau \cdot \frac{2U_T C_1}{\kappa I_{\text{bias}}}, \end{aligned} \quad (46)$$

where $g \geq 0$, we get the following dimensionless equation

$$\begin{aligned} \frac{dx}{d\tau} &= -\tanh(x) - 2r \tanh(y), \\ \frac{dy}{d\tau} &= \tanh(x) + (2r - g) \tanh(y). \end{aligned} \quad (47)$$

9.1. Small Signal Analysis. We can linearize (47) by replacing the RHS with its Jacobian, giving

$$\begin{bmatrix} \dot{x} \\ \dot{y} \end{bmatrix} \approx \begin{bmatrix} -1 & -2r \\ 1 & 2r - g \end{bmatrix} \begin{bmatrix} x \\ y \end{bmatrix}. \quad (48)$$

The origin is a fixed point. In fact, it is a unique fixed point, since (from (47)) $\tanh(x) = -2r \tanh(y) \Rightarrow x = y = 0$. The origin is stable for

$$r < \frac{1+g}{2}, \quad (49)$$

and unstable otherwise. It is a spiral for

$$\frac{1+g}{2} - \sqrt{g} < r < \frac{1+g}{2} + \sqrt{g}, \quad (50)$$

and a node otherwise.

9.2. Large Signal Analysis. For certain values of r and g , the nonlinearities of the second-order section causes it to suffer instability. In this region of parameter space, linear

analysis accurately predicts that the circuit is *small signal stable*, but completely fails to recognize that instability would occur for *large signals*. Mead addresses this issue in [14], but we will present a somewhat more thorough treatment of the problem, using phase-plane analysis and experimental verification with the FPAA.

For very large values of x and y , the tanh functions get saturated, and can each be approximated with a signum function. Equation (47) becomes

$$\begin{aligned}\frac{dx}{d\tau} &= -\operatorname{sgn}(x) - 2r \operatorname{sgn}(y), \\ \frac{dy}{d\tau} &= \operatorname{sgn}(x) + (2r - g) \operatorname{sgn}(y).\end{aligned}\quad (51)$$

Figure 12 shows the phase plane (x versus y) that corresponds to (51). The depicted motion is valid only if $1/2 < r < 1$. The gradient in the first and third quadrants is

$$a = \frac{2r - 1 + g}{1 + 2r}, \quad (52)$$

and that in the second and fourth quadrants is

$$b = \frac{2r - 1 - g}{1 - 2r}. \quad (53)$$

Observe that, with an initial condition of $(1, 0)$, (51) predicts that the positive x -axis will again be intercepted at $(a^2/b^2, 0)$. If $a^2/b^2 > 1$, then x and y will grow without bound. Stated in terms of the r and g variables, there is large signal instability if

$$r > \frac{g + \sqrt{g^2 + 4}}{4}. \quad (54)$$

Our analysis of the second-order section can readily be verified experimentally. We compiled the filter on the FPAA, as shown in Figure 7. The bias currents of all three OTAs are user-programmable, and varying them corresponds to varying the values of r and g . The FPAA thus allows us to explore the parameter space of the filter, and to observe changes in its qualitative behavior. It can effectively be used for bifurcation analysis.

Figure 13 shows the filter's phase plane plots for various values of r , with g kept fixed. Just as we predicted, there is a unique fixed point, which is initially stable, and gradually changes from a node to a spiral (Figures 13(a) and 13(c)). While linear analysis would predict these three responses as damped, slightly underdamped, and very underdamped, it fails to recognize the possibility of the fourth response, which is large-signal instability. In the fourth panel, r meets the criterion derived from nonlinear analysis, (54), and we observe oscillation. Further analysis and exploration of parameter space reveals that this second-order section is capable of low-distortion sinusoidal oscillation [15]. Such functionality is valuable in communication systems.

10. Conclusion

In this paper, we have introduced visual and graphical techniques for analyzing nonlinear circuit dynamics. Our

approach to studying harmonic distortion yields information about the various processing flows that are responsible for each harmonic term. The FPAA was used to rapidly create phase plane plots, which concisely encapsulate the nonlinear dynamics of the circuit under study. We have provided various examples of our techniques and have compared our predictions to experimentally-measured data.

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