provided by Taiwan Association of Engineering and Technology Innovation

# A Fast Anticollision Algorithm with Early Adjustment of Frame Length for the EPCglobal UHF Class-1 Generation-2 RFID Standard

Wen-Tzu Chen\*

Department of Transportation and Communication Management, National Cheng Kung University, Tainan, Taiwan.

Received 27 January 2016; received in revised form 16 February 2016; accepted 11 March 2016

#### Abstract

This paper develops a fast algorithm to identify a large number of tags in a short period of time duration in RFID systems. The main goal of the proposed algorithm is to meet the requirement in supply chain management. In this paper, we derive mathematical model to analyze the tag identify process. Based on the analysis, we can decide the optimal frame length and when the current read round is early ended. We find that the optimal frame length should be set to 1.72 times of the number of tags when the ratio between collision-slot duration and empty-slot duration is 4. Through the use of early adjustment of frame length, the RFID interrogator examines the fitness of frame length only at three time slots in each read round. The primary advantage of our algorithm is the ability to achieve a good compromise between throughput performance and computation complexity.

Keywords: RFID, tag identification, logistic management, read efficiency, Anti-collision algorithm

#### 1. Introduction

For managing a large amount of objects and identifying their IDs, the radio frequency identification (RFID) technology operating on ultra-high frequency (UHF) band is increasingly being used especially in logistic management. To meet the requirement of reading a dense population of objects, the EPCglobal developed an air interface, named UHF Class-1 Generation-2 or ISO 18000-6 Type C [1].

The algorithms for identifying multiple tags in RFID systems have been widely studied in the literature [2-6]. The EPCglobal UHF Class-1 Generation-2 specifies an algorithm based on dynamic frame slotted ALOHA (DFSA) to solve the RFID anticollision problem. The author in [4] indicated that the read performance of DFSA depends mainly on the accuracy of backlog estimate and frame length setting. For the DFSA algorithm, a maximum throughput of 36.8% can be achieved only if the frame length is set to the number of tags. One of the most interesting procedures is how to dynamically adjust frame length to achieve maximum throughput when a big difference exists between frame length and tag quantity.

#### 2. Proposed Algorithm

This paper proposes a simple and efficient anticollision algorithm for the EPCg lobal Cass-1 Generation-2 protocol and describes clear rules for early adjustment mechanism of frame length. This mechanism allows the interrogator to early end the current read round at any time slot and enter into a new round with a new frame length. Hence, the interrogator will examine whether the current frame length requires adjustment or not and how to adjust it if necessary. To achieve a good compromise between computation complexity and throughput performance, the proposed algorithm examines the fitness of frame length at only the three quartiles in each read round, i.e. at the  $L/4^{\text{th}}$ ,  $L/2^{\text{th}}$ , and  $(3/4)L^{\text{th}}$ slots. The procedures of the proposed method is shown in Fig. 1.

A key performe of anticollision algorithm is normalized throughput,

$$U = S \cdot T_s / (S \cdot T_s + C \cdot T_c + E \cdot T_E) \qquad (1)$$

S, C, and E are the numbers of successful, collision, and empty slots. The time intervals of the successful, collision, and empty slots are  $T_S$ ,  $T_C$ , and  $T_E$ , respectively.

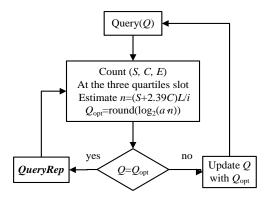


Fig. 1 Proposed anti-collision procedures

At the three time slots, the interrogator counts the number of singly occupied slots Si, the number of collision slots Ci, and the number of empty slots Ei. Note that i is equal to L/4, L/2, or (3/4) L here. The tag quantity to be read at the beginning of the round can be estimated by [6].

$$\hat{n} = (S_i + 2.39 \cdot C_i) \cdot L/i \tag{2}$$

Given tag quantity n, we attempt to determine an optimal frame length  $L_{opt}$  to maximize the throughput in (1). Taking the derivative of U with respect to L and setting the derivative to zero lead to an optimal frame length.

$$L_{\rm opt} = a \cdot n \tag{3}$$

In Eq. (3), a is a coefficient. Table 1 lists the coefficients for different values of TC/TE.

Table 1 Coefficients between $L_{opt}$ and $n$
--

$T_C/T_E$	$a(L_{opt}=a \times n)$
1	1
2	1.30
3	1.53
4	1.72
5	1.89
6	2.05
7	2.19
8	2.32
9	2.44

After computing the optimal frame length, the interrogator should check whether the current frame length is appropriate or not. The frame length must be a power of 2 due to the UHF Class-1 Generation-2 standard. Hence, the optimal Q value can be obtained by:

$$Q_{\rm opt} = \operatorname{round}(\log_2 L_{\rm opt})$$
 (4)

#### 3. Simulation Results

The read performance of the proposed algorithm and the reference schemes was examined by carrying out computer simulations based on the Monte Carlo technique.

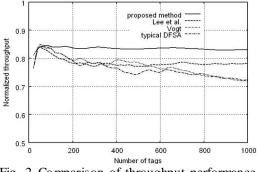
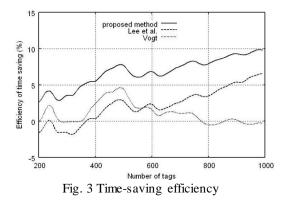


Fig. 2 Comparison of throughput performance for anticollision algorithms



The throughput performance of the proposed algorithm is presented in Fig. 2. As mentioned before, our method uses early adjustment mechanism for frame length and performs at most three examinations in each read round for the fitness of frame length. It can be found that our method has better throughput performance as compared with the other schemes. Particularly, for a large number of tags, i.e. 1000, the proposed algorithm can achieve up to 10% throughput improvement as compared with the typical DFSA. Another advantage of the proposed method is the stability. When the number of tags is increased, the proposed method can keep the throughput stable.

Fig. 3 presents the time-saving efficiency for different algorithms. It can be observed from Fig. 3 that our algorithm can obtain 5-10 % time-saving efficiency as the tag quantity is greater than 200.

## 4. Conclusions

This paper proposes an efficient RFID anticollision algorithm to improve the read performance for the EPCglobal UHF Class-1 Generation-2 standard. The results show that the proposed algorithm can achieve up to 400 tags/s read speed. For a large number of tags, i.e. 1000, the proposed algorithm can achieve up to 10% throughput improvement as compared with the typical DFSA. We believe that this advantage comes from the early adjustment mechanism for frame length. Another advantage of the proposed method is the stability. When the number of tags is increased, the proposed method can keep the throughput stable.

### Acknowledgement

The support of Taiwan's Ministry of Science and Technology, under Grant MOST 104-222 1-E-006-200 is gratefully acknowledged.

## References

- EPCg lobal, EPC radio-frequency identity protocols class-1 generation-2 UHF RFID protocol for communications at 860 MHz-960 MHz, version 1.2.0, Oct. 2008.
- [2] H. Vogt, "Efficient object identification with passive RFID tags," International Conference on Pervasive Computing, London, UK, pp. 98-113, 2002.
- [3] D. Lee, O. Bang, S. Im, and H. Lee, "Efficient dual bias Q-algorithm and optimum weights for EPC class 1 generation 2 protocol," 14<sup>th</sup> European Wireless Conference, pp. 1-5, June 2008.
- [4] W. T. Chen, "An accurate tag estimate method for improving the performance of an RFID anticollision algorithm based on dynamic frame length ALOHA," IEEE Trans. Autom. Sci. Eng, vol. 6, no. 1, pp. 9-15, Jan. 2009.
- [5] B. Knerr, M. Holzer, C. Angerer, and M. Rupp, "Slot-wise maximum likelihood estimation of the tag population size in FSA protocols," IEEE Trans. Communications, vol. 58, no. 2, pp. 578-585, Feb. 2010.
- [6] W. T. Chen, "A feasible and easy-toimplement anti-collision algorithm for the EPCg lobal UHF class-1 generation-2 RFID protocol," IEEE Trans. Autom. Sci. Eng., vol. 11, no. 2, pp. 485-491, Apr. 2014.