IEEE TRANSACTIONS ON POWER DELIVERY, VOL. 32, NO. 2, APRIL 2017

Reliability Analysis of MMCs Considering Submodule Designs with Individual or Series-Operated IGBTs

Jingli Guo, Student Member, IEEE, Jun Liang, Senior Member, IEEE, Xiaotian Zhang, Member, IEEE, Paul D. Judge, Student Member, IEEE, Xiuli Wang, Senior Member, IEEE, and Tim C. Green, Senior Member, IEEE

Abstract—The half-bridge-based modular multilevel converter (MMC) has emerged as the favored converter topology for voltage-source HVDC applications. The submodules within the converter can be constructed with either individual insulated-gate bipolar transistor (IGBT) modules or with series-connected IGBTs, which allows for different redundancy strategies to be employed. The main contribution of this paper is that an analytical method was proposed to analyze the reliability of MMCs with the consideration of submodule arrangements and redundancy strategies. Based on the analytical method, the relative merits of two approaches to adding redundancy, and variants created by varying the submodule voltage, are assessed in terms of overall converter reliability. Case studies were conducted to compare the reliability characteristics of converters constructed using the two submodule topologies. It is found that reliability of the MMC with series-connected IGBTs is higher for the first few years but then decreases rapidly. By assigning a reduced nominal voltage to the series valve submodule upon IGBT module failure, the need to install redundant submodules is greatly reduced.

Index Terms—Modular multilevel converter (MMC), redundancy analysis, reduced nominal voltage operation, reliability assessment, voltage capability.

I. INTRODUCTION

ULTILEVEL voltage source converters (VSCs) have been widely used in the High Voltage Direct Current (HVDC) applications. Numerous multilevel converter topologies have been reported, including Neutral Point Clamped (NPC) VSC [1], Flying Capacitor Converter (FCC) [1], Cascaded H-Bridge converter (CHB) [2] and Modular Multilevel Converter (MMC) [3]–[5]. Among these multilevel convert-

Manuscript received September 11, 2015; revised March 23, 2016; accepted May 12, 2016. Date of publication May 26, 2016; date of current version March 22, 2017. This work was supported in part by the National Natural Science Foundation of China under Grant 51261130471; in part by EPSRC under Grants EP/K006312/1, EP/I031707/1, and EP/I013636/1; and in part by the China Scholarship Council (201406280115). Paper no. TPWRD-01285-2015.R1.

J. Guo, X. Zhang, and X. Wang are with the School of Electrical Engineering, Xi'an Jiaotong University, Xi'an 710049, China (e-mail: guojingli.xjtu@ gmail.com; xiaotian@ieee.org; xiuliw@mail.xjtu.edu.cn).

J. Liang is with the School of Engineering, Cardiff University, Cardiff, CF24 3AA, U.K. (e-mail: LiangJ1@cardiff.ac.uk).

P. D. Judge and T. C. Green are with the Department of Electrical and Electronics Engineering, Imperial College London, London SW7 2AZ, U.K. (e-mail: p.judge12@imperial.ac.uk; t.green@imperial.ac.uk).

Color versions of one or more of the figures in this paper are available online at http://ieeexplore.ieee.org.

Digital Object Identifier 10.1109/TPWRD.2016.2572061



Fig. 1. Circuit schematic of Modular Multilevel Converter.

ers, the MMC has come to dominate HVDC applications because of its superior characteristics with respect to operational power losses, industrial scalability and failure management under severe fault conditions. The MMC is a modular converter, a schematic of which is shown in Fig. 1. Each arm of the MMC is comprised of a series arrangement of submodules, whose switching is coordinated to generate a highly sinusoidal output voltage. Each submodule (SM) is typically comprised of a half-bridge arrangement of power electronic switches, with a floating submodule capacitor. Since the concept of MMC was proposed by R. Marguardt in 2001 [3], various topologies based upon the modular multilevel concept have been developed for high voltage applications for both dc-ac conversion [4], [5] and dc-dc conversion [6]. In the HVDC transmission application, researchers have mainly focused their efforts on improving the performance of MMCs, including topology optimization [7], modulation [8], voltage balancing [9] and power loss evaluation [10]. However, availability, and therefore reliability, is also a key feature in selecting between HVDC converter station offerings. Published research on reliability of VSC-HVDC covers analytical methods [11], [12] and simulation methods [13]. Reliability analysis of semiconductors used in VSC-HVDC has also been carried out either on the end-of-life tests [14] or on analytical models of the lifetime of IGBTs [15].

In [16], the reliability of a two-level converter was modelled, and [17] conducted an industry-based survey about converters'

This work is licensed under a Creative Commons Attribution 3.0 License. For more information, see http://creativecommons.org/licenses/by/3.0/

reliability, in which the equivalent model of the converter is topology-blind. There has been little work reported on the reliability analysis of MMCs, especially that of MMCs with different submodule topologies.

Two distinct types of half-bridge MMCs have been brought forward by vendors, differing in the ways of using IGBTs in SMs, either single IGBTs [4] or series-connected IGBTs [5]. These two types of SMs are denoted as Individual Device Submodule (**ID-SM**) and Series Valve Submodule (**SV-SM**) in this paper. Their corresponding MMC variants are named as MMC_{ID} and MMC_{SV} respectively. The overall electrical characteristics of MMC built using either option are the same and have the same electrical circuit representation [18]. The differences are in the nominal voltage at which each SM is operated, and the number of SMs. The **ID-SM** has a smaller nominal voltage than **SV-SM**. For the same rated dc terminal voltage, more SMs are required in MMC_{ID}. Numerous other potential SM arrangements, such as the Double Clamped Submodule (DCSM), have also been proposed but are not considered in this paper [19].

Due to the high financial costs associated with unscheduled outages of HVDC systems, converters are designed with redundancy. In MMCs designed with single IGBTs within each SM valve, redundancy can be provided by including additional SMs within each arm of the converter [20]. Any faulted SMs need to be quickly bypassed by a mechanical switch. In MMCs that contain series connected devices within each SM, redundancy can also be provided by including additional IGBT devices within each valve inside the SM [5].

This paper intends to make a contribution of proposing an analytical method for the reliability assessment of MMCs with different submodule arrangements and different redundancy strategies. Reliability models of MMCs are presented based upon the failure rate of IGBTs within individual submodules, considering the cases where each submodule valve contains either individual devices, or series connected devices. Using this analytical model, the impact that the submodule arrangement will have on the overall converter reliability is evaluated, and the relative merits of the two approaches are compared in terms of reliability and voltage capability. Sensitivity analysis is conducted to consider the possible difference in the reliability of different IGBT packages used in the two topologies. The influence of a reduced nominal voltage operating mode of the submodule type containing series devices, on the converter reliability is also assessed.

II. STRUCTURE AND OPERATING PRINCIPLE

Fig. 2 shows a per-phase representation of an MMC, which has a nominal dc voltage V_{dc} and an ac voltage v_o . Each phase in an MMC is divided into two arms: the positive arm and the negative arm. A series stack of SMs and one inductor are series connected to constitute each arm. Each SM contains a capacitor and a half-bridge SMs. Two types of SMs, **ID-SM** and **SV-SM** are represented in Fig. 2(b) and (c) respectively. Each valve in **ID-SM** contains single IGBT module, while that in **SV-SM** is composed of several IGBT modules, i.e., *l* connected in series in Fig. 2(c). The nominal voltage of each SM is denoted by



Fig. 2. Configuration of one phase of a Modular Multilevel Converter (a) with two different SMs: (b) **ID-SM** with single IGBT module in each valve or (c) **SV-SM** with series-connected IGBT modules.

 V_{SM} , and the voltage blocking capability of each IGBT module is represented by V_D .

The IGBTs used within **ID-SMs** are typically a HiPak style case [21]. This style of package is comprised of an electrically isolated base-plate upon which insulating layers of ceramic material provide electrical isolation of silicon semiconductor dies. These dies are then connected together using bond-wires to internal bus bar structures to form the overall device. This type of package typically forms an open-circuit upon failure. This necessitates a fast mechanical bypass switch to be integrated into the SM to short-circuit the output terminals of the SM in the event of a failure.

The IGBTs used within **SV-SMs** are press-pack devices, designed for series operation. Series operation of IGBTs requires good dynamic voltage sharing performance of the devices, leading to more complicated gate-drive arrangements, and potential need for snubber circuits. Press-pack devices are designed in a vertical manner in which the Collector of the device is connected to one cooling plate of the package, and the Emitter is connected to other cooling plate. The devices are then clamped between heat-sinks to form a series valve. As the cooling plates are not electrically isolated, the heat-sinks form part of the conduction path. This double-sided cooling plate arrangement allows for more efficient cooling of the semiconductors. In addition, press-pack devices eliminate the need for internal bond-wires, which are a major source of failure within HiPak modules [22]. These press-pack devices are also designed to form a stable short-circuit in the event of a device failure. This allows redundancy to be built into each valve as in the event of a device failure, the remaining healthy devices within the valve split the voltage between them. As long as remaining devices have sufficient blocking voltage between them, the SM can continue to operate as normal. If enough IGBTs within a valve fail such that the remaining healthy IGBTs can no longer support the nominal voltage of the SM, the SM must either be taken out of service, or as proposed in [5], a Reduced Nominal Voltage (**RNV**) could be assigned to that SM. An MMC that uses this **RNV** mode will be referred to as MMC_{SVRNV} in this paper.

By installing redundant IGBT modules in a SM, its reliability is improved. The power loss of the SM, however, is increased. Similarly, when the number of redundant SMs in an MMC increases, the converter tends to be more reliable but with more power loss and higher cost. The number of IGBT modules and the number of SMs are related with the reliability and the power loss of MMCs. In this section, these concepts as the basis of the reliability modelling of MMCs have been discussed.

A. Number of IGBT Modules in a SM

Each valve in a SM should be able to safely operate at the nominal voltage of the SM. Otherwise, the IGBT modules might fail due to over-voltage. In real practices, a voltage margin for the IGBT module is needed, and the operation voltage V_i is typically set as $1/2 \sim 2/3$ of the IGBT module's withstanding voltage V_D . η denotes the de-rating factor of the IGBT module voltage, that is, $\eta = V_i/V_D$. In the **ID-SM**, each SM is comprised of 2 IGBT modules. In the **SV-SM**, which composes of a series of IGBT modules in each valve, IGBT modules share the SM voltage. The minimum number of IGBT modules required in one valve r can be obtained by

$$r = \left\lfloor \frac{V_{\rm SM}}{\eta \times V_D} \right\rfloor \tag{1}$$

where V_{SM} is the nominal voltage of each SM; η is the de-rating factor of the IGBT module voltage; V_D is the IGBT module's withstanding voltage.

B. Number of SMs in an Arm

The number of SMs in each arm of the converter is determined by the voltages at both dc and ac terminals.

The total nominal voltage of each phase leg should equal to the dc voltage. Let SW be a switching function to denote the SM's state. When SW = 1, the SM's output voltage is V_{SM} . When SW = 0, the SM's output voltage is 0. Then, n, the number of SMs in each arm, should be subjected to the following equation.

$$V_{\rm dc} = V_{+} - V_{-} = \left(\sum_{i=1}^{n} SW_{\rm pi} + \sum_{i=1}^{n} SW_{\rm ni}\right) V_{\rm SM} \qquad (2)$$

where V_{dc} is the nominal dc voltage of the converter; SW_{pi} and SW_{ni} are the switching functions for the *i*-th SM in the positive arm and the negative arm respectively.

In all phase units of an MMC, SMs can be controlled separately and selectively so that it is possible to adjust the arm voltage by controlling the number of "active" SMs. Each SM is capable of either bypassing itself or inserting its capacitor voltage. By controlling the number of inserted SMs within each arm of the converter, a sinusoidal stepped voltage waveform can be generated. Each arm has to be able to generate a voltage between the dc terminal voltage and zero volts [23]. In order to be capable of generating the required peak voltage, the required number of SMs in each arm is calculated as follows.

$$n \ge \left\lfloor \frac{V_{\rm dc}/2 + \hat{V}_o}{V_{\rm SM}} \right\rfloor \tag{3}$$

where \hat{V}_o is the peak ac voltage.

In order to generate an ac voltage with an amplitude of $V_{dc}/2$, the sum of SM voltages in one arm should be equal to the dc voltage. Then, the minimum requirement of SMs k can be calculated as follows.

$$k = \left\lfloor \frac{V_{\rm dc}}{V_{\rm SM}} \right\rfloor \tag{4}$$

The SM capacitance C is estimated by [24]:

$$C = \frac{2 \times S \times E_{\text{MMC}}}{6 \times n \times V_{\text{SM}}^2} \tag{5}$$

where S is the nominal capacity of the MMC; E_{MMC} is the nominal energy per MVA stored in the MMC, and it should be in the range of 30–40 kJ/MVA [5]; n is the number of SMs in each arm; V_{SM} is the nominal voltage of a SM.

C. Redundancy

The voltage capability of an arm can be considered to be the sum of the healthy SM voltages within that arm. In both MMC_{ID} and MMC_{SV} , the failure of an IGBT module can result in a reduction to the voltage capability. If the voltage capability of an arm decreases to a point whereby the arm is no longer capable of generating its required peak voltage, the converter will no longer be able to operate properly and forced shutdown and maintenance may become necessary.

HVDC systems are expected to have high reliability and availability. It is therefore required that the system have sufficient redundancy so that it may continue to operate until the next scheduled maintenance period, where repair and replacement can take place.

Let n_1 and n_2 represent the number of installed SMs in each arm of an MMC_{ID} and of an MMC_{SV} respectively. Then the number of assembled IGBT modules in the converters with **ID-SM** and **SV-SM** can be obtained by (6) and (7) respectively.

$$M_1 = 12 \times n_1 \tag{6}$$

$$M_2 = 12 \times n_2 \times l \tag{7}$$

where l is the number of series-connected IGBT modules in each valve of a **SV-SM**.

D. Series Valve Operation Modes

Given that l IGBT modules are installed in each valve of MMC_{SV} and at least r IGBT modules ($r \le l$) are required to endure the nominal voltage, l - r among them are redundant components. In normal mode, when the SM is in healthy state, its operation voltage is set as its nominal voltage. The voltage

of a SM with p IGBT modules surviving in the up value and q in the bottom value is given by:

$$V_{\rm sc}(p,q) = \begin{cases} V_{\rm SM}, & r \le \min\{p,q\} \le l \\ 0, & \min\{p,q\} < r \end{cases}$$
(8)

where $V_{\rm SM}$ is the nominal voltage of the **SV-SM**.

In the **RNV** operation mode, the SM can operate in the nominal voltage V_{SM} if the number of surviving IGBT modules is not less than r in both valves. If there are more than l - r faulty IGBT modules in any of the two valves, the SM will be assigned a lower voltage. It may not be realistic to expect a SM to continue operating if an excessive amount of IGBTs within its valve have failed. To account for this case, a minimum number of healthy IGBTs can be given by r_d . When the surviving IGBT modules is less than r_d , the SM fails and will be bypassed. Assuming that the operation voltage of an valve in a **RNV** mode is proportional to the number of surviving IGBT modules, the voltage of a SM is calculated as:

$$V_{\rm sd}(p,q) = \begin{cases} V_{\rm SM}, & r \le \min\{p,q\} \le l \\ \frac{\min\{p,q\}}{r} V_{\rm SM}, & r_d \le \min\{p,q\} < r \\ 0, & \min\{p,q\} < r_d \end{cases}$$
(9)

III. RELIABILITY MODELLING OF MMCS

There are many items of a plant that have significant impacts on the reliability of an HVDC system beyond the converter electronics, such as the transformers and cables. Even within the electronics, it must be acknowledged that both active components and passive components have impacts on the reliability of MMCs [17]. But this work aims to clarify how variations in the modular construction of the converter influence the reliability. This paper is, therefore, focused on the reliability of the converters with the arrangements of the IGBT modules and their operating modes taken into consideration. All IGBT modules in an MMC are assumed mutually independent. The assumption is made based on the modular operation of the MMC, which means each SM essentially operates as an independent powerconverter that sees an identical loading to every other SM within the overall converter [8]. Based upon the analysis of converter structure and operating principle illustrated in Section II, the reliability model of MMCs and the calculation procedure of voltage capability are proposed in this section.

Let $\lambda_d(t)$ be the failure rate of an IGBT module:

$$\lambda_d\left(t\right) = \lim_{\Delta t \to 0} \frac{R_d\left(t\right) - R_d\left(t + \Delta t\right)}{\Delta t R_d\left(t\right)} = -\frac{1}{R_d\left(t\right)} \frac{d\left[R_d\left(t\right)\right]}{dt} \tag{10}$$

where $R_d(t)$ is the reliability function of an IGBT module, that is the probability of the IGBT module operating without failure to time t.

From (10), the reliability function of an IGBT module is derived as:

$$R_d(t) = e^{-\int_0^t \lambda_d(t)dt} \tag{11}$$

Note that the electronic equipment's failure pattern is typically demonstrated by a Bathtub curve, in which the normal operating period is characterized by a constant failure rate [25].



Fig. 3. Reliability diagram of (a) ID-SM and (b) SV-SM.

The reliability function of an IGBT module is calculated by:

$$R_d(t) = e^{-\lambda_d t} \tag{12}$$

where λ_d is the failure rate of the IGBT module.

A. Impact of Redundant Structure on the Reliability of MMCs

The redundant structure contributes to the improvement of the reliability of MMCs. First, the lifetime of IGBT modules is extended. When redundant submodules are assembled in a converter arm, a lower voltage and cycle frequency are applied to each submodule. In an MMC_{SV} , if redundant IGBT modules are included within a valve, each IGBT module is subjected to a lower voltage. The lifetime of IGBT modules is influenced by various factors, including junction temperature, blocking voltage and cycle frequency [26]. Moreover, different types of IGBT modules are used in the two variants of MMCs, and the failure mechanisms of these two types of IGBTs are quite different [15]. Thus, redundant structure has different impact on the two variants of MMCs. Secondly, the lifetime of a converter arm, as well as that of a converter, is extended. In an MMC with redundant submodules, the faulty submodule is bypassed by a highly reliable high-speed switch when it fails during the operation. The converter will continue to operate. In the MMC_{SV} , the valve will continue to operate upon failures of some IGBT modules, as long as the remaining IGBT modules can endure the rated voltage of the valve. Some work has been done on the impact analysis of various factors on the reliability of IGBT modules. In [14], [26], damage accumulation experiments or numerical simulations were carried out for the considered type of IGBTs, and the lifetime of IGBTs was estimated by analytical models. This paper is focused on the system-level reliability analysis.

B. Reliability Model of MMCs in Normal Operation Mode

1) Reliability of a SM: Based on its topology and operating principle, the reliability diagram for the SM can be obtained. As shown in Fig. 3, IGBT modules in two valves are series connected in terms of reliability, which depicts that the SM works only if both valves are working.

For the **ID-SM**, of which each valve contains one IGBT module, the reliability is given by:

$$R_{s-ID}(t) = R_{du}(t) \times R_{db}(t)$$
(13)

where $R_{du}(t)$ and $R_{db}(t)$ are the reliability function of the IGBT module in the up valve and that of the IGBT module in the bottom valve respectively.

For the SV-SM, which consists of a series of IGBT modules, more IGBT modules are usually assembled than required. In a normal operation mode, each valve should be able to withstand the nominal voltage of the SM. If each value is composed of lIGBT modules and l - r are redundant, the valve will operate as long as any r IGBT modules work. The valve fails at the instant of the (l - r + 1)-th IGBT module's failure. Hence, each valve can be regarded as a *r*-out-of-*l* system, which is suitable for the system that continue to operate as long as any r out of l components are working [27]. The reliability of a valve is given by adding the probability of exactly r IGBT modules surviving to time t to the probability of exactly r + 1 IGBT modules surviving, and so on up to the probability of l IGBT modules surviving to time t. Given that all IGBT modules have the identical reliability function $R_d(t)$, the reliability of one valve can be calculated as follows.

$$R_{v}(t) = \sum_{i=r}^{l} C_{l}^{i} [R_{d}(t)]^{i} [1 - R_{d}(t)]^{l-i}$$
(14)

where r is the minimum number of IGBT modules required in a valve and is calculated using (1). A SM works only if both valves work. The reliability of one **SV-SM** is given by:

$$R_{s-SV}(t) = R_{\rm vu}(t) \times R_{\rm vb}(t) \tag{15}$$

where $R_{vu}(t)$ and $R_{vb}(t)$ are the reliability function of the up valve and that of the bottom valve respectively.

2) Reliability of an Arm: Note that redundant SMs are operating all the time, even when there have been no failures. The redundant SMs are assembled without any differences from other SMs. Without the consideration of **RNV** operation mode of SMs, the arm will operate as long as k out of n SMs are working. Thus, the arm can be represented by a k-out-of-n system model. The arm's reliability is given by:

$$R_{a}(t) = \sum_{i=k}^{n} C_{n}^{i} [R_{s}(t)]^{i} [1 - R_{s}(t)]^{n-i}$$
(16)

where $R_s(t)$ is the reliability function of the SM (in (13) and (15)); k is the minimum number of SMs required in each arm and is calculated using (4); n is the number of assembled SMs in each arm.

3) Reliability of a Converter: According to the reliability diagram shown in Fig. 4, a phase leg operates only if both the positive arm and the negative arm work. The reliability of one phase leg of an MMC can be calculated by:

$$R_p(t) = R_{\rm ap}(t) \times R_{\rm an}(t) \tag{17}$$

where $R_{ap}(t)$ and $R_{an}(t)$ are the reliability of positive arm and that of negative arm respectively, as shown in (16).

 sub-module 1
 sub-module 1

 i
 i

 sub-module n
 sub-module n

 positive
 negative

 arm
 arm

Fig. 4. Reliability diagram of one phase of an MMC.

Then, the reliability of a converter, which is composed of three phases, is obtained by:

$$R_{c}(t) = [R_{p}(t)]^{3}$$
(18)

The mean time to failure (MTTF) of the converter is calculated by:

$$MTTF = \int_0^{+\infty} R_c(t) dt$$
 (19)

And the failure rate of the converter is given by:

$$\lambda_{c}(t) = -\frac{\mathrm{d}\left[\ln R_{c}(t)\right]}{\mathrm{d}t} \tag{20}$$

This paper is focused on the reliability evaluation of two variants of half-bridge MMCs. However, the proposed analytical method and models can be applied to other MMC topologies with minor changes on the reliability function of submodules. Based on the analysis of submodule structure and operating principle, the reliability function of the submodule is derived firstly. By substituting the reliability function of submodules into (16)–(18), the reliability model of MMCs is established. For example, a DCSM contains 5 IGBT modules, and its reliability function is calculated by:

$$R_{s-DC}(t) = \prod_{i=1}^{5} R_{di}(t)$$
 (21)

where $R_{\text{di}}(t)$ is the reliability function of the *i*-th IGBT module. The reliability modelling procedure for MMCs with CDSMs is the same as that for MMCs with **ID-SM**s.

C. Reliability Model of MMC_{SV} With Reduced Nominal Voltage Mode

With the consideration of the **RNV** operation mode, the reliability of $MMC_{SV_{RNV}}$ can be modelled as follows.

1) Reliability of a SM: If a SM composes l IGBT modules in each valve, the operation voltage of the SM with p IGBT modules surviving in the up valve and q in the bottom valve is given by (9), and its corresponding probability can be calculated by:

$$P_s(t, p, q) = C_l^p C_l^q [R_d(t)]^{p+q} [1 - R_d(t)]^{2l-p-q}$$
(22)

where $R_d(t)$ is the reliability function of the IGBT module.

2) Reliability of an Arm: In an $MMC_{SV_{RNV}}$ which can operate with a lower voltage, the arm can be considered to be functional as long as the sum of the voltages of all SMs in the arm is greater than the rated dc voltage. The reliability of an arm



can be calculated by summing up the probabilities of the arm in healthy states.

$$R_{a} = \sum_{\Phi} \prod_{j=1}^{n} P_{sj}(t, p_{j}, q_{j})$$

$$\Phi = \{(p_{1}, \dots, p_{n}; q_{1}, \dots, q_{n}) \\ \left| \sum_{i=1}^{n} V_{si}(p_{i}, q_{i}) \ge V_{dc}, p_{i}, q_{i} \in \{0, 1, \dots, l\} \right\}$$
(23)

where P_{sj} is the probability of SM *j* operating with p_j IGBT modules surviving in the up valve and q_j in the bottom valve; V_{si} is the SM voltage ((9) for **RNV** mode); V_{dc} is the rated dc voltage; *l* is the number of IGBT modules in each SM; *n* is the number of SMs in the arm.

3) Reliability of a Converter: According to the reliability diagram shown in Fig. 4, the reliability of one phase leg and that of an MMC can be calculated by substituting the reliability function of an arm into (17) and (18) respectively. And the reliability indices, MTTF and the failure rate of the converter, can be obtained by (19) and (20).

D. Voltage Capability of MMCs

If redundant IGBT modules or redundant SMs are installed in an arm, the arm can generate a higher voltage than the nominal arm voltage. If the number of faulty SMs within an arm increases, the peak voltage which can be generated at the ac side will be reduced. In terms of the number and the location of faulty SMs, the arm has many possible states at time t. The expected voltage capability of an arm is defined as:

$$V_c(t) = \sum_i V_{ai} \times P_{ai}$$
(24)

where V_{ai} is the voltage capability of the arm when it is in state i; P_{ai} is the probability of the arm being in state i. The values of V_{ai} and P_{ai} are different for the two variants, MMC_{ID} and MMC_{SV}.

For the MMC_{ID}, given that each arm is composed of n SMs and n - k of them are redundant, the expected voltage capability of an arm at time t is calculated by:

$$V_{c-ID} = \sum_{j=0}^{n} j V_{\text{SM}} C_n^j [R_{s-ID}(t)]^j [1 - R_{s-ID}(t)]^{n-j} \quad (25)$$

where V_{SM} is the nominal voltage of an **ID-SM**; $R_{s-ID}(t)$ is the reliability of the **ID-SM**, namely, the probability of the **ID-SM** working at time t.

Suppose that each arm in an MMC_{SV} is composed of n SMs among which n - k are redundant. The expected voltage capability of an arm at time t can be obtained by:

$$V_{c-SV} = \sum_{\Theta} \left[\left(\sum_{j=0}^{n} V_{sj}(p_j, q_j) \right) \prod_{j=0}^{n} P_{sj}(t, p_j, q_j) \right]$$

$$\Theta = \{ (p_1, \dots, p_n; q_1, \dots, q_n) \\ | p_i, q_i \in \{0, 1, \dots, l\}, \forall i \in \{1, \dots, n\} \}$$
(26)

where P_{sj} is the probability of SM *j* operating with p_j IGBT modules surviving in the up valve and q_j in the bottom valve; V_{sj} is its corresponding operation voltage ((8) for normal mode and (9) for **RNV** mode); *l* is the number of IGBT modules in each SM; *n* is the number of SMs in the arm.

The standard deviation of the voltage capability is calculated by:

$$\sigma\left(t\right) = \sqrt{\sum_{i} V_{\rm ai}^2 P_{\rm ai} - V_c^2} \tag{27}$$

where V_{ai} is the voltage capability of the arm in state *i*; P_{ai} is the probability of the arm being in state *i*; V_c is the expected voltage capability.

E. Model Extension

In the previous subsections, it is assumed that the failure of an IGBT module is independent of the failure of any other IGBT modules. Note that some failures, in practice, might cause the outage of two or more IGBT modules, which are called common cause failures. The main common cause failures we have identified for the MMC include internal flash-over, isolation failure, control system failure and cooling system failure, and are independent of the submodule arrangement. To take the common cause failure into consideration, the proposed model needs minor modifications. Common cause failures are considered as virtual components and connected in series with other components/systems. With adequate information, including the failure rate and influenced components/systems, common mode failures are included in the reliability analysis of MMCs. For example, if the cooling pumps in a converter station fail, some or even all IGBT modules in the converter will fail due to overtemperature. As a consequence, the MMC might fail. This common mode failure is regarded as a virtual component with a failure rate of λ_{CMF} , and the corresponding reliability function $R_{\text{CMF}}(t)$ is assumed as $e^{-\lambda_{\text{CMF}}t}$. The reliability function of an MMC in (18) is then modified as:

$$R_{c}\left(t\right) = \left[R_{p}\left(t\right)\right]^{3} R_{\text{CMF}}\left(t\right) \tag{28}$$

where $R_{p}(t)$ is the reliability function of one phase leg of the MMC.

Moreover, external factors, such as control systems, have impacts on the reliability of MMCs. On one hand, the operation condition of IGBT modules might be influenced. For example, control strategies affect the cycle frequencies, voltage and temperature of IGBT modules. As a consequence, the reliability of IGBT modules is affected. Impacts of control strategies on IGBT reliability and lifetime prediction models of IGBT modules have been analyzed in [14], [15], [26], [28]. On the other hand, MMCs might fail due to failures in control systems. Particular submodules will fail as a consequence of failures of submodule level control systems. The submodule level control system is then regarded as an extra component which is connected in series with the influenced submodule. By multiplying (13) by the reliability function of the submodule level control system, the reliability function of the submodule is obtained. Failures of system-level, station-level and converter-level con-

TABLE I PARAMETERS OF CONVERTERS

Symbol	Quantity	Value
S	system capacity	1000 MVA
V_{dc}	rated dc voltage	$\pm 320 \text{ kV}$
\widehat{V}_o	peak ac phase voltage	320 kV
V_D	withstanding voltage of IGBT module	4.5 kV
η	de-rating factor of IGBT module's voltage	56%
V _{ID-SM}	nominal voltage of ID-SM	2.52 kV
V_{SV-SM}	nominal voltage of SV-SM	17.6 kV
E_{MMC}	energy stored in the MMC	30 kJ/MVA
λ_d	failure rate of the IGBT module	0.004 occ/year

trol systems cause the outage of the converter, and they are considered as extra components that are connected in series with the MMC. The reliability function of MMCs is derived by multiplying (18) by the reliability function of control systems. Thus, the impacts of control systems on the reliability of MMCs are included in the proposed model.

IV. CASE STUDY

MMC designs with a nominal capacity of 1000 MVA and a nominal dc voltage of ± 320 kV were used in reliability evaluation. The peak ac phase voltage was set to 320 kV. IGBT modules with a blocking voltage of 4.5 kV were considered and a de-rating factor of 56% for the voltage of IGBT modules was applied in both SM types. Therefore, the nominal voltage of the **ID-SM** with single IGBT module in each valve is 2.52 kV. Each ID-SM is composed of two IGBT modules. Each SV-SM is composed of 16 IGBT modules with one redundant IGBT module included in each valve [5]. Therefore, the SV-SM is rated at 17.6 kV nominal. For SMs that operate using the RNV mode, a minimum number of healthy IGBTs (r_d) of 5 is imposed. This removes unrealistic cases where SMs operate with nominal voltages that are significantly away from their healthy state voltage. This means that the lowest voltage an SM can be assigned is $\frac{5}{7}$ of the nominal voltage. The failure rate of the IGBT module was assumed as 0.004 occ/year based on statistical data in practical projects [29]. Parameters of the converters used in this comparison are shown in Table I. Simulations were conducted using MATLAB R2014a. The maintenance as well as the repair of components is not considered in this paper.

A. Reliability Comparison of MMCs

Three variants of MMCs, MMC_{ID} , MMC_{SV} and $MMC_{SV_{RNV}}$ are compared in terms of reliability and arm voltage capability in this subsection. The basic information of MMCs, including the number of IGBT modules, the number of SMs and the SM capacitance, is calculated according to Section II. Reliability, as well as the voltage capability, of MMCs is then evaluated based upon the modelling procedure in Section III.

In the MMC_{SV} , one redundant IGBT module is included in each valve, but no redundant SMs are included. At least 37 SMs are required in each arm to generate a dc voltage of ± 320 kV. The number of IGBT modules in each arm is $37 \times 16 = 592$, and

TABLE II Comparison of Two Variants of MMCs

	MMC _{ID}	MMC _{SV}
Levels	259	38
No. of SMs in each arm	258	37
No. of IGBT modules in each SM	2	16
No. of IGBT modules in each arm	516	592
SM Nominal Voltage (kV)	2.52	17.6
SM Capacitor (mF)	6.1	0.87
Redundant SMs per Arm	4	0
Redundant IGBTs per SM	0	2
MTTF (year)	1.25	2.04(non-RNV mode
		9.07(RNV mode)



Fig. 5. Failure rate of MMCs.

the initial voltage capability is 651.2 kV. For the same voltage level, each arm of an MMC_{ID} must contain at least 254 SMs, and here 4 redundant SMs are added to meet the same initial voltage capability as MMC_{SV} . There are $258 \times 2 = 516$ IGBT modules in each arm of an MMC_{ID} . Basic parameters of the two variants, i.e., SM number and IGBT module number, are presented in Table II. Two operation modes are applied to the MMC_{SV} . The allowable number of IGBT modules to be failed in a valve is one in the normal mode and 3 in the **RNV** mode respectively.

Reliability indices of MMCs, MTTF and the failure rate, are calculated using (19) and (20) respectively. MTTF of each variant is shown in Table II, while the failure rate is illustrated in Fig. 5. X-axis is time t, while Y-axis is MMCs' failure rate at time t. 1.6% redundant IGBT modules are added in the MMC_{ID}, and the redundant rate of IGBT modules in the MMC_{SV} is 14.3%. The MTTF of the MMC_{ID} is 1.25 years, which is less than that of the MMC_{SV}. The failure rate of the MMC_{ID} increases rapidly over time in the first few years. Moreover, the MMC_{SV} operating in **RNV** mode is more reliable than that in the normal mode.

The expected arm voltage capability and the corresponding standard deviations of the variants over time are calculated using (24)–(27) and shown in Fig. 6(a) and (b) respectively. In the



Fig. 6. (a) Expected voltage capability of an arm and (b) the corresponding standard deviation.

	MMC _{ID}	MMC _{SV}	MMC _{SVRNV}
Levels	259	38	38
No. of SMs in each arm	258	37	37
No. of IGBTs in each SM	2	16	16
No. of IGBTs in each arm	516	592	592
Voltage capability at $t = 0$ (kV)	650.16	651.2	651.2
Energy Storage at $t = 0$ (kJ/MVA)	30	30	30
Redundant SMs per Arm	4	0	0
Redundant IGBTs per SM	0	2	2
MTTF (year)}	1.25	$\begin{array}{l} 2.04 \left(\lambda_{SV} = 0.004 \right) \\ 2.91 \left(\lambda_{SV} = 0.0028 \right) \\ 5.09 \left(\lambda_{SV} = 0.0016 \right) \end{array}$	9.07

 TABLE III

 COMPARISON OF MMCS WITH DIFFERENT FAILURE RATES



first 10 years, the expected voltage capability of an arm in the MMC_{SV} in the normal operation mode is greater than that in the MMC_{ID} . IGBT modules have high reliability in the first few years. The probability of the **SV-SM** with redundant IGBT modules being in healthy state is greater than that of the **ID-SM**. Thus, the expected remaining voltage of the arm in MMC_{SV} in the first few years is greater than that in the MMC_{ID} . If the **RNV** mode of operation is not used, then the decrease in expected voltage capability of an MMC_{SV} increases over time and at some point exceeds that of the MMC_{ID} . The MMC_{SV} also has the most rapidly increasing standard deviation.

With greater expected values and smaller standard deviation, the voltage capability of an arm of the MMC_{SV} operating with the **RNV** mode is substantially better than that in non-**RNV** mode. This is because the **SV-SM** in the **RNV** mode can operate with 3 faulty IGBT modules in each valve, while that in the normal mode will be out of service if more than one IGBT module fails in a valve. The profile of the voltage capability of the **SV-SM** and the arm is much improved by **RNV** mode.

Monte Carlo-based simulations have been undertaken to validate the proposed analytical method. Simulations were conducted 30000 times to calculate the MTTF of converters. The MTTF of MMC_{ID} and that of MMC_{SV} are 1.25 years and 2.03 years respectively, which are approximately equal to the results

Fig. 7. Failure rate of MMCs ($\lambda_{ID} = \lambda_{SV_{RNV}} = 0.004; \lambda_{SV} = 0.004, 0.0028, 0.0016$).

obtained by using the proposed analytical method. Compared with the Monte Carlo-based simulation, the analytical method obtains more accurate values of MTTF. Moreover, except for MTTF, the failure rate of MMCs is calculated in the proposed analytical method, which provides more information about the reliability of converters and how redundancy influences the system reliability.

B. Influence of IGBT Module'S Failure Rate

It is important to note that different IGBT modules, HiPak and Press-pack style cases respectively, are used within **ID-SMs** and **SV-SMs**. It is therefore useful to collect the failure rate of different types of IGBT modules for the comparison of the reliability of MMCs. Statistical data about the reliability of different IGBT modules used in HVDC system is not available as most of MMC-based HVDC projects were put into operation recently. To investigate the impact of IGBT modules' lifetime on the voltage capability of MMCs, a sensitivity analysis was conducted. In the MMC_{ID} and the MMC_{SV RNV}, the failure rate



Fig. 8. (a) Expected voltage capability of an arm and (b) the corresponding standard deviation $(\lambda_{ID} = \lambda_{SV_{RNV}} = 0.004; \lambda_{SV} = 0.004, 0.0028, 0.0016)$.

of IGBT module λ_d was set to 0.004. In the MMC_{SV} operating in the normal mode, several values for λ_d were chosen, from 0.004 to 0.0016. The MTTF and failure rate of MMCs are calculated using (19) and (20) respectively. Basic parameters of converters are shown in Table III, and failure rates of MMCs over time are plotted in Fig. 7. As the failure rate of IGBT modules decreases, the reliability of the MMC_{SV} is improved. If the failure rate of IGBT modules decreases by 30%, the MTTF of the MMC_{SV} increases to approximately 3 years. When the failure rate of IGBT modules is further reduced to 0.0016 occ/year, the failure rate of the MMC_{SV} goes close to that of the MMC_{SV RNV} in the first five years, and increases more slowly afterwards.

Based on the modelling procedure in Section III, the expected voltage capability of an arm in the variants and the corresponding standard deviation are evaluated and shown in Fig. 8(a) and (b) respectively. If the failure rate of IGBT modules decreases, the descent rate of the expected voltage capability of the MMC_{SV} decreases. The profile of standard deviation over time is also improved. When the failure rate of IGBT modules in MMC_{SV} is less than 70% of that in MMC_{ID} ($\lambda_{SV} = 0.0028$), the MMC_{SV} has a better expected voltage capability in the first 20 years. If the failure rate is reduced to 0.0016, the voltage capability of the MMC_{SV} will be improved approximately match that of the MMC_{SV} operating in the **RNV** mode with the original λ_d .

C. Design Comparison of MMCs

Based on the reliability models and the voltage capability calculation procedure described in Section III, reliability-oriented design of MMCs is conducted in this subsection. The design objective chosen is that probability of a converter operating without failure should be not less than 0.998 in the first three years. The basic parameters, i.e., number of SMs and IGBT modules, in each variant are determined to meet this objective. The SM capacitor for each variant is kept to the value given in Table II. Comparison of the resultant MMCs in terms of component number and redundancy schemes are shown in Table IV.

TABLE IV Design Comparison of MMCs

	MMC _{ID}	MMC _{SV}	MMC _{SV_{RNV}}
Levels	271	41	38
No. of SMs in each arm	270	40	37
No. of IGBTs in each SM	2	16	16
No. of IGBTs in each arm	540	640	592
Voltage capability at $t = 0$ (kV)	680.4	704	651.2
Energy Storage at $t = 0$ (kJ/MVA)	31.37	32.33	30
Redundant SMs per Arm	16	3	0
Redundant IGBTs per SM	0	2	2
MTTF (year)	5.80	7.77	9.07



Fig. 9. Failure rate of MMCs—Design to meet Reliability ≥ 0.998 in the first three years.

In the case of MMC_{ID} , 16 redundant SMs (6.3%) were found to be required to meet the reliability target. 270 SMs are installed in each arm in the MMC_{ID} , and 16 among them are redundant.

The minimum number of SMs in each arm of the MMC_{SV} is 37 to generate a dc voltage of ± 320 kV. To meet the reliability objective, 3 more SMs are required in each arm of the MMC_{SV} in the normal mode, whilst no redundant SMs are needed for



Fig. 10. (a) Expected voltage capability of an arm and (b) the corresponding standard deviation—Design to meet Reliability ≥ 0.998 in the first three years.

a converter operating in the **RNV** mode. The initial expected voltage capability in the three variants are 680.4 kV, 704 kV and 651.2 kV respectively. The utilisation of SMs in the $\rm MMC_{SV_{RNV}}$ is better than that in the $\rm MMC_{ID}$. The overall energy storage value in the $\rm MMC_{SV_{RNV}}$ is also lower than the other two variants as no additional SMs are required.

The power-losses within MMC are dominated by the conduction losses within the semi-conductor devices [30]. The number of IGBTs within the arm can therefore be used as a measure of the expected relative efficiency of each converter. The $MMC_{SV_{PNV}}$ has been found to require a lower number of redundant SMs to achieve the same reliability as the MMC_{ID} gets. The total number of IGBT devices within the converter is significant because of the redundant IGBT modules within the SM. In the example case given the $\mathrm{MMC}_{SV_{RNV}}$ has approximately 9.6% more IGBTs. This indicates that if IGBTs with similar characteristics are used the MMC_{ID} may be expected to be more efficient, compared to the $MMC_{SV_{RNV}}$. As noted in Section II, the MMC_{SV} requires the use of press-pack devices which have higher performance than the HiPak modules typically used in MMC_{SV}. This may reduce the loss penalty imposed by including redundant IGBTs within each SM.

In terms of reliability, the MTTF and failure rate of MMCs are calculated based upon the procedure in Section III and shown in Table IV and Fig. 9. To meet the reliability target, more redundant IGBT modules have been added in both the MMC_{ID} and the MMC_{SV} . Compared to the results in Table II, the redundancy rate of IGBT modules in the MMC_{ID} is increased from 1.6% to 6.3%, and the MTTF increases from 1.25 years to 5.80 years. The MTTF of the MMC_{SV} is improved from 2.04 years to 7.77 years. Failure rates of all variants are close to 0 in the first five years. After that, the failure rate of the MMC_{SV} goes more slowly over time than the MMC_{ID} because higher redundant rate of IGBT modules is applied.

The expected arm voltages of all variants and the corresponding standard deviation are calculated using (24)–(27) and plotted in Fig. 10(a) and (b) respectively. Due to the required installation of redundant SMs, the MMC_{ID} and the MMC_{SV} have a larger initial voltage capability than the $MMC_{SV_{RNV}}$. As the expected number of failure of the IGBT modules increases over time, the expected voltage capability of the MMC_{SV} in the normal mode decreases significantly. The expected voltage capability of an arm in the $MMC_{SV_{RNV}}$ decreases relatively slowly. The $MMC_{SV_{RNV}}$ also has the smallest standard deviation.

In the MMC_{SV} operating in the normal mode, an expected voltage greater than the nominal dc voltage lasts available for almost 4.6 years if no redundant SMs are installed, shown in Fig. 6(a). With the installation of redundant SMs and also with the **RNV** operation mode, the probability of an arm being able to generate the nominal dc voltage increases. The expected voltage is greater than the dc nominal voltage for more than 11 years with 4 redundant SMs installed in each arm and nearly 12.5 years with the application of the **RNV** mode. The improvement of the converter's voltage capability by applying the **RNV** operation mode is seen to be greater than by installing redundant SMs.

V. CONCLUSION

In this paper, a detailed model of the reliability of the MMCs used for HVDC transmission has been presented. Two variants of the half-bridge submodule were considered: one with single IGBT and one with valves of several series-connected IGBTs. An additional operating mode of the series-valve topology was considered in which operation with reduced nominal voltage (RNV) is used after more than one IGBT failure per submodule. For each topology and mode, the reliability of the MMC was expressed analytically in terms of reliability of the IGBT modules and expressions found of the expected available arm voltage as function of time. The converter as a whole is deemed to have failed if any one of the six arms has an available voltage of less than the DC voltage required. A case study design was used to compare the converter topologies. The following conclusions are drawn.

The decrease in available arm voltage with time is initially slower with the series valve submodule than the individual device submodule. The former has a higher available voltage for the first few years but then the decrease rate accelerates and in future years the individual device fares better.

The RNV mode of operation applied to the series valve submodule is very effective in preventing the expected available arm voltage from decreasing quickly with time and greatly reduces the need to install redundant submodules. In effect it makes better use of redundancy within the submodules.

Comparing the three approaches for the same converter reliability target, the individual device submodule leads to the lowest number of IGBTs overall and by extension will have the lowest conduction power loss. The series valve submodule operated with RNV is placed second on this basis but has the additional advantage of using less capacitance.

ACKNOWLEDGMENT

Information on the data that support the results presented in this paper, including how to access them, can be found in the Cardiff University data catalog at http://dx.doi.org/10.17035/ d.2016.0009095138.

REFERENCES

- S. S. Fazel, S. Bernet, D. Krug, and K. Jalili, "Design and comparison of 4-kV neutral-point-clamped, flying-capacitor, and series-connected Hbridge multilevel converters," *IEEE Trans. Ind. Appl.*, vol. 43, no. 4, pp. 1032–1040, Jul./Aug. 2007.
- [2] P. Cortes, A. Wilson, S. Kouro, J. Rodriguez, and H. Abu-Rub, "Model predictive control of multilevel cascaded H-bridge inverters," *IEEE Trans. Ind. Electron.*, vol. 57, no. 8, pp. 2691–2699, Aug. 2010.
- [3] R. Marquardt, "Stromrichterschaltungen mit verteilten energiespeichern," German Patent DE, German Patent DE20122923(U1), 2001.
- [4] R. Marquardt and A. Lesnicar, "A new modular voltage source inverter topology," in *Proc. Conf. Rec. EPE*, 2003, pp. 1–10.
- [5] B. Jacobson, P. Karlsson, G. Asplund, L. Harnefors, and T. Jonsson, "VSC-HVDC transmission with cascaded two-level converters," in *CIGRÉ Session*, 2010, pp. B4–B110.
- [6] X. Zhang, T. Green, and A. Junyent-Ferre, "A new resonant modular multilevel step-down DC-DC converter with inherent-balancing," *IEEE Trans. Power Electron.*, vol. 30, no. 1, pp. 78–88, Jan. 2015.
- [7] G. T. Son, H.-J. Lee, T. S. Nam, Y.-H. Chung, U.-H. Lee, S.-T. Baek, K. Hur, and J.-W. Park, "Design and control of a modular multilevel HVDC converter with redundant power modules for noninterruptible energy transfer," *IEEE Trans. Power Del.*, vol. 27, no. 3, pp. 1611–1619, Jul. 2012.
- [8] S. Rohner, S. Bernet, M. Hiller, and R. Sommer, "Modulation, losses, and semiconductor requirements of modular multilevel converters," *IEEE Trans. Ind. Electron.*, vol. 57, no. 8, pp. 2633–2642, Aug. 2010.
- [9] P. Meshram and V. Borghate, "A simplified nearest level control (NLC) voltage balancing method for modular multilevel converter (MMC)," *IEEE Trans. Power Electron.*, vol. 30, no. 1, pp. 450–462, Jan. 2015.
- [10] Z. Zhang, Z. Xu, and Y. Xue, "Valve losses evaluation based on piecewise analytical method for MMC–HVDC links," *IEEE Trans. Power Del.*, vol. 29, no. 3, pp. 1354–1362, Jun. 2014.
- [11] S. Zadkhast, M. Fotuhi-Firuzabad, F. Aminifar, R. Billinton, S. O. Faried, and A. Edris, "Reliability evaluation of an HVDC transmission system tapped by a VSC station," *IEEE Trans. Power Del.*, vol. 25, no. 3, pp. 1962–1970, Jul. 2010.
- [12] J. Guo, X. Wang, Z. Bie, and Y. Hou, "Reliability modeling and evaluation of VSC-HVDC transmission systems," in *Proc. IEEE Power Energy Soc. Gen. Meeting—Conf. Expo.*, 2014, pp. 1–5.
- [13] K. Rudion, Z. A. Styczynski, A. G. Orths, M. Powalko, and H. Abildgaard, "Reliability investigations for a DC offshore power system," in *Proc. IEEE Power Energy Soc. Gen. Meeting*, 2013, pp. 1–5.
- [14] H. Huang and P. A. Mawby, "A lifetime estimation technique for voltage source inverters," *IEEE Trans. Power Electron.*, vol. 28, no. 8, pp. 4113–4119, Aug. 2013.

- [15] C. Busca *et al.*, "An overview of the reliability prediction related aspects of high power IGBTs in wind power applications," *Microelectron. Rel.*, vol. 51, no. 9–11, pp. 1903–1907, 2011.
- [16] J. Wang, M. Ding, and S. Li, "Reliability analysis of converter valves for VSC-HVDC power transmission system," in *Proc. IEEE Power Energy Eng. Conf., Asia-Pacific*, 2010, pp. 1–4.
- [17] S. Yang, A. Bryant, P. Mawby, D. Xiang, L. Ran, and P. Tavner, "An industry-based survey of reliability in power electronic converters," *IEEE Trans. Ind. Appl.*, vol. 47, no. 3, pp. 1441–1451, May/Jun. 2011.
- [18] H. Akagi, "Classification, terminology, and application of the modular multilevel cascade converter (MMCC)," *IEEE Trans. Power Electron.*, vol. 26, no. 11, pp. 3119–3130, Nov. 2011.
- [19] A. Nami, J. Liang, F. Dijkhuizen, and G. Demetriades, "Modular multilevel converters for hvdc applications: Review on converter cells and functionalities," *IEEE Trans. Power Electron.*, vol. 30, no. 1, pp. 18–36, Jan. 2015.
- [20] J. Dorn, H. Huang, and D. Retzmann, "Novel voltage-sourced converters for HVDC and FACTS applications," in CIGRÉ Symp., 2007, p. 314.
- [21] M. Rahimo, A. Kopta, R. Schnell, U. Schlapbach, R. Zehringer, and S. Linder, "2.5 kv–6.5 kv industry standard igbt modules setting a new benchmark in soa capability," presented at the PCIM, Nuremberg, 2004.
- [22] S. Kaufmann, T. Lang, and R. Chokhawala, "Innovative press pack modules for high power igbts," in *Proc. IEEE 13th Int. Symp. Power Semiconductor Devices ICs*, 2001, pp. 59–62.
- [23] T. Luth, M. Merlin, T. Green, F. Hassan, and C. Barker, "High frequency operation of a DC/AC/DC system for HVDC applications," *IEEE Trans. Power Electron.*, vol. 29, no. 8, pp. 4107–4115, Aug. 2014.
- [24] J. Peralta, H. Saad, S. Dennetière, J. Mahseredjian, and S. Nguefeu, "Detailed and averaged models for a 401-level mmc-hvdc system," *IEEE Trans. Power Del.*, vol. 27, no. 3, pp. 1501–1508, Jul. 2012.
- [25] G.-A. Klutke, P. C. Kiessler, and M. Wortman, "A critical look at the bathtub curve," *IEEE Trans. Rel.*, vol. 52, no. 1, pp. 125–129, Mar. 2003.
- [26] R. Bayerer, T. Herrmann, T. Licht, J. Lutz, and M. Feller, "Model for power cycling lifetime of IGBT modules—Various factors influencing lifetime," in *Proc. 5th Int. Conf. Integr. Power Syst.*, 2008, pp. 1–6.
- [27] P. A. Tobias and D. Trindade, *Applied Reliability*. Boca Raton, FL, USA: CRC, 2011.
- [28] M. Weckert and J. Roth-Stielow, "Lifetime as a control variable in power electronic systems," in *Proc. Emobility—Elect. Power Train*, 2010, pp. 1–6.
- [29] Vancouver Island Cable Project Application for Certificate of Public Convenience and Necessity, Sea Breeze Victoria Converter Corporation, B2-62, Feb. 2006..
- [30] P. Judge, M. Merlin, P. Mitcheson, and T. Green, "Power loss and thermal characterization of igbt modules in the alternate arm converter," in *Proc. IEEE Energy Convers. Congr. Expo.*, Sep. 2013, pp. 1725–1731.



Jingli Guo (S'14) received the B.S. (Hons.) in electrical engineering from the University of Electronics and Science Technology of China, Chengdu, China, in 2011 and is currently pursuing the Ph.D. degree in electrical engineering at Xi'an Jiaotong University, Xi'an, China.

From 2014 to 2015, she was a Visiting Student at Cardiff University, Cardiff, U.K. Her research interests include reliability modeling and assessment for HVDC systems and grid-connected inverters for distributed generation.



Jun Liang (M'02–SM'12) received the B.Sc. degree in electrical engineering from Huazhong University of Science and Technology, Wuhan, China, in 1992 and the M.Sc. and Ph.D. degrees in electrical engineering from the China Electric Power Research Institute, Beijing, China, in 1995 and 1998, respectively.

From 1998 to 2001, he was a Senior Engineer with China Electric Power Research Institute. From 2001 to 2005, he was a Research Associate at Imperial College, London, U.K. From 2005 to 2007, he was a

Senior Lecturer at the University of Glamorgan, Wales, U.K. Currently, he is a Reader at the School of Engineering, Cardiff University, Wales. His research interests include flexible ac transmission systems devices/HVDC, power system stability and control, power electronics, and renewable power generation.



Xiaotian Zhang (S'11–M'12) was born in Xi'an, China, in 1983. He received the B.S. (Hons.) and M.S. degrees in electrical engineering from Xi'an Jiaotong University, Xi'an, China, in 2006 and 2009, respectively, and the Ph.D. degree (Hons.) in electrical engineering and electronics from the University of Liverpool, Liverpool, U.K., in 2012.

Until 2015, he was with the Department of Electrical Engineering, Imperial College London, U.K. Currently, he is an Associate Professor in the School of Electrical Engineering at Xi'an Jiao-

tong University. His research interests include control and design of HVDC converters.



Xiuli Wang (M'99–SM'14) received the B.S., M.S., and Ph.D. degrees in electrical engineering from Xi'an Jiaotong University, Xi'an, China, in 1982, 1985, and 1997, respectively.

She has been with Xi'an Jiaotong University, Xi'an, China, since 1985, where she is currently a Professor with the School of Electrical Engineering. Her research interests include the power market, reliability assessment of power system, and integration of renewable power.



Tim C. Green (M'89–SM'02) received the B.Sc. (Eng.) degree in electrical engineering from Imperial College London, London, U.K., and the Ph.D. degree in electrical engineering from Heriot-Watt University, Edinburgh, U.K.

He was a Lecturer at Heriot-Watt University until 1994. Currently, he is a Professor of Electrical Power Engineering and Director of the Energy Futures Lab, an institute that promotes and stimulates multidisciplinary research, education, and translation in energy at Imperial College London. He leads the HubNet

Supergen consortium of 8 U.K. universities coordinating research in low carbon energy networks. He and his team have worked on many aspects of HVDC technology, covering converter optimization and control, fault management, and the dynamics of combined ac and dc networks. His research interest is in formulating the future form of the electricity network to support low carbon futures. A particular theme is how the flexibility of power electronics and control systems can be used to accommodate new-generation patterns and new types of loads, such as electric-vehicle charging, as part of the emerging smart grid. He also has interests in power electronics for the management of voltage and power flow in low-voltage networks.



Paul D. Judge (S'13) received the B.Eng. (Hons.) degree in electrical engineering from the University College Dublin, Dublin, Ireland, in 2012 and is currently pursuing the Ph.D. degree in power converter design for HVDC applications in the Control and Power Research Group at Imperial College London, London, U.K.

His main research areas are in power converter design and control, as well as power system integration of HVDC technology.