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Cite as: APL Mater. 7, 071105 (2019); <https://doi.org/10.1063/1.5096786>

Submitted: 19 March 2019 . Accepted: 13 June 2019 . Published Online: 16 July 2019

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Sajad Yazdani,<sup>1,2</sup> Tran Doan Huan,<sup>2,3</sup> Yufei Liu,<sup>4</sup> Raana Kashfi-Sadabad,<sup>2</sup> Raul David Montaño,<sup>1,5</sup> Jian He,<sup>4</sup> and Michael Thompson Pettes<sup>1,2,6,a)</sup>

## AFFILIATIONS

<sup>1</sup>Department of Mechanical Engineering, University of Connecticut, Storrs, Connecticut 06269, USA

<sup>2</sup>Institute of Materials Science, University of Connecticut, Storrs, Connecticut 06269, USA

<sup>3</sup>School of Materials Science and Engineering, Georgia Institute of Technology, Atlanta, Georgia 30332, USA

<sup>4</sup>Department of Physics and Astronomy, Clemson University, Clemson, South Carolina 29634, USA

<sup>5</sup>Department of Aerospace and Mechanical Engineering, University of Arizona, Tucson, Arizona 85721, USA

<sup>6</sup>Center for Integrated Nanotechnologies, Materials Physics and Applications Division, Los Alamos National Laboratory, Los Alamos, New Mexico 87545, USA

<sup>a)</sup> Author to whom correspondence should be addressed: [pettesmt@lanl.gov](mailto:pettesmt@lanl.gov)

## ABSTRACT

This work describes our discovery of the dominant role of highly charged interfaces on the electrothermal transport properties of PbS, along with a method to reduce the barrier potential for charge carriers by an order of magnitude. High temperature thermoelectrics such as PbS are inevitably exposed to elevated temperatures during postsynthesis treatment as well as operation. However, we observed that as the material was heated, large concentrations of sulfur vacancy ( $V_S$ ) sites were formed at temperatures as low as 266 °C. This loss of sulfur doped the PbS *n*-type and increased the carrier concentration, where these excess electrons were trapped and immobilized at interfacial defect sites in polycrystalline PbS with an abundance of grain boundaries. Sulfur deficient  $\text{PbS}_{0.81}$  exhibited a large barrier potential for charge carriers of 0.352 eV, whereas annealing the material under a sulfur-rich environment prevented  $V_S$  formation and lowered the barrier by an order of magnitude to 0.046 eV. Through *ab initio* calculations, the formation of  $V_S$  was found to be more favorable on the surface compared to the bulk of the material with a 1.72 times lower formation energy barrier. These observations underline the importance of controlling interface-vacancy effects in the preparation of bulk materials comprised of nanoscale constituents.

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Lead sulfide is a well-known thermoelectric material that has attracted attention due to the environmental abundance of sulfur.<sup>1,2</sup> Compared to the Pb–Se and Pb–Te, the Pb–S bond is stronger which makes PbS a more stable material.<sup>1,3</sup> PbS has a larger room temperature band gap (0.41 eV) than those of PbSe (0.29 eV) and PbTe (0.32 eV);<sup>4</sup> thus, the maximum  $zT$  can be obtained at a higher temperature.<sup>5</sup> This makes PbS favorable for high-temperature applications<sup>6,7</sup> considering its high melting temperature of 1391 K.<sup>8</sup> However, the lattice thermal conductivity of bulk PbS (2.50 W m<sup>-1</sup> K<sup>-1</sup> at room temperature) has been found to be higher than those of PbTe (1.99 W m<sup>-1</sup> K<sup>-1</sup>) and PbSe (1.62 W m<sup>-1</sup> K<sup>-1</sup>).<sup>9</sup> This large lattice thermal conductivity has hindered the effective use of PbS for

thermoelectric applications. Alloying,<sup>10</sup> nanoscale precipitations,<sup>3,11</sup> and bottom-up synthesis of nanostructures<sup>6,12,13</sup> have been utilized in order to reduce the thermal conductivity of PbS-based material systems. Generally, having a high density of grain boundaries of the appropriate sizes is appealing for thermoelectric applications as the lattice thermal conductivity can adequately be suppressed by scattering the majority of heat-carrying phonons. First principles calculations of thermal conductivity accumulation have predicted that nanostructures of ~10 nm can be used to effectively reduce the room-temperature thermal conductivity for PbSe, PbTe, SnS, and SnSe by as much as 80%–90%.<sup>14–16</sup> The advancements in solution-processed syntheses of nanocrystals have made achieving such size

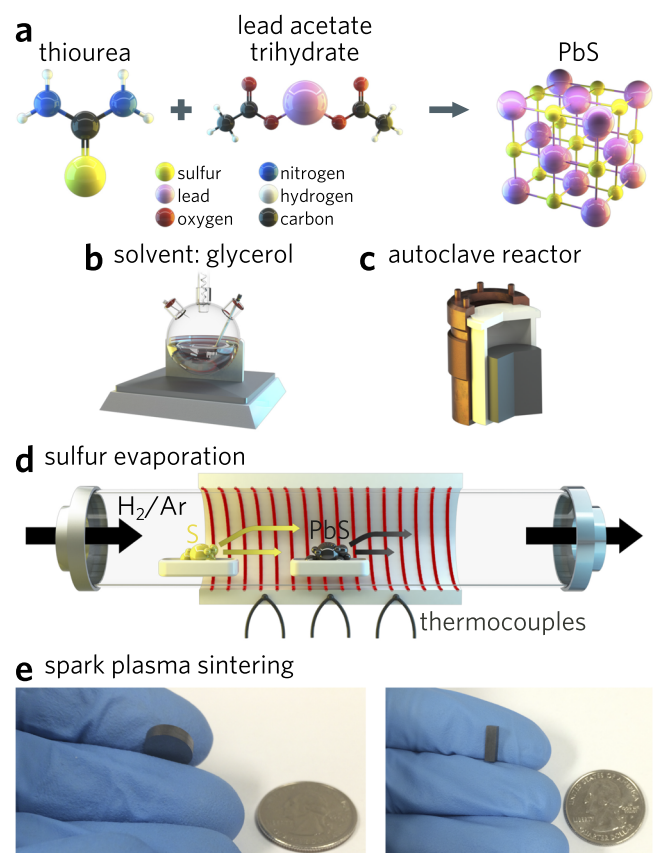
ranges quite feasible.<sup>17</sup> However, it is often reported that chemistry-processed materials exhibit much lower electrical conductivities when compared to conventional top-down (solid-state) materials due to the presence of point defects and remaining organic residues introduced during the synthesis. In order to remove the insulating ligands and organic species, most of these materials are annealed at high temperatures (above 400 °C) under vacuum or an inert gas environment.<sup>12,13,18,19</sup> This exposure to high temperatures can promote vacancy site formations. Sulfur losses have been reported to take place, both during material preparations<sup>20,21</sup> and during operations at high temperatures (above 400 °C).<sup>7,22</sup> The presence of point defects and interfaces can significantly alter the electronic properties of materials and dominate both the Seebeck coefficient and electrical conductivity. For example, while bulk Bi<sub>2</sub>Te<sub>3</sub> is known to be *p*-type for the stoichiometric and Bi-rich cases, Mehta *et al.*<sup>20</sup> reported a majority carrier reversal in solution-processed Bi<sub>2</sub>Te<sub>3</sub> pellets showing *n*-type behavior due to the presence of point defects. This majority carrier type reversal has also been observed through changes in the surface condition of (Bi<sub>1-x</sub>Sb<sub>x</sub>)<sub>2</sub>Te<sub>3</sub> nanoplates.<sup>23</sup> Point defects may also interact with dopants present in a thermoelectric material;<sup>24</sup> however, the dopant-defect interactions have not been fully understood. The abundance of grain boundaries and interfaces in solution-processed materials or mechanically processed nanomaterials<sup>25</sup> can make these interactions even more complicated. The structure of grain boundaries consisting of layers of disordered atoms is complex. These atomic layers can be considered as transitions between various orientations of crystallites with a high defect concentration.<sup>26</sup> It has been suggested that these interfacial defects can act as trapping states by immobilizing charge carriers.<sup>26–28</sup> Therefore, the number of available carriers for conduction decreases and the mobile carrier concentration can be much lower than the doping concentration.<sup>26</sup> In addition, the trapped-carriers cause the grain boundaries to be electrically charged, as has been reported for PbS<sup>25,29,30</sup> and PbTe.<sup>31,32</sup> These charged interfaces can act as potential barriers, obstructing the charge transfer and decreasing the mobility.<sup>26</sup> The charged layer is balanced by two layers of opposite charges on both sides of the interface<sup>27</sup> which results in the formation of a dipole layer with a width correlated with the strength of the potential barrier.<sup>33</sup> Fully understanding these interfacial defects is highly relevant to end-use performance since for waste-heat recovery applications, it is more preferable to operate at elevated temperatures from a thermodynamic stand point.<sup>6</sup>

This work investigates sulfur vacancy formation ( $V_S$ ) and its effect on the thermoelectric properties of solution-processed PbS. Although the abundance of grain boundaries in bottom-up materials is favorable for thermal conductivity reduction, defective interfaces can be charged by  $V_S$  and impede electrical transport. The sulfur concentration is measured after each synthesis and postsynthesis step in order to track the stage at which sulfur loss occurs. Through a theoretical investigation, the tendency of  $V_S$  formation is evaluated both from the surface and from the bulk of the material. In addition, the feasibility of the sulfur-loss prevention by annealing under a sulfur-rich environment is assessed.

Solvothermal reaction, high temperature annealing (600 °C), and spark plasma sintering (SPS) were used for the material preparation. High-temperature annealing can potentially enhance vacancy and defect density, and SPS is essential in order to sinter the grain

boundaries and obtain high density solid materials needed for thermoelectric energy conversion. Figure 1 illustrates the synthesis procedure. Thiourea and lead (II) acetate trihydrate were used as the sulfur and lead precursors, and glycerol was used as the solvent. The solvothermal reaction occurred in a Teflon lined autoclave reactor. The obtained sample, denoted as PbS-Solvo, was annealed under H<sub>2</sub>/Ar at 600 °C.

Other samples were prepared by annealing the materials under a sulfur-rich environment. During the annealing, a constant flow of sulfur was provided by evaporating the sulfur powder at 150 °C placed in an alumina boat located at the upstream of the PbS-Solvo sample under an H<sub>2</sub>/Ar carrier gas. The sulfurized samples were denoted as PbS-S and PbS-S-SPS, referring to the obtained products after sulfurization and after both sulfurization and SPS, respectively. For comparison, the annealing and SPS steps were also conducted on a reference sample made using Sigma-Aldrich PbS (#372595) with a different morphology denoted as PbS-ref.-SPS. After SPS and polishing, the obtained pellets were used for laser flash thermal diffusivity measurements. For the electrical and Seebeck coefficient measurements, rectangular bars were cut from the same pellet used for diffusivity measurements to ensure that the



**FIG. 1.** Illustration of PbS synthesis steps: (a) reaction scheme, (b) and (c) solvothermal autoclave reactions, and (d) sulfur annealing process under H<sub>2</sub>/Ar. (e) Representative densified pellet and rectangular bar obtained after spark plasma sintering (SPS) and used in electrothermal measurements.

doping concentration was maintained. The details of the sample treatments are listed in Table I. The synthesis steps and treatments were designed in order to answer the following questions: (i) at which synthesis step of solvothermal reaction, high-temperature annealing, or SPS, are  $V_S$  sites formed, (ii) can the sulfurization process prevent formation of  $V_S$ , (iii) how does the presence of interfaces affect electrothermal transport properties in samples with and without  $V_S$ , and (iv) how does a different microstructure morphology and grain boundary concentration affect the electrothermal transport properties.

Figure 2 gives the transmission electron microscopy (TEM), scanning TEM (STEM), scanning electron microscopy (SEM), and energy dispersive x-ray spectroscopy (EDS) analysis of the PbS samples after each synthesis step. The sample after the high-temperature Ar annealing (PbS) exhibited a dendrite morphology with high concentrations of grain boundaries, as shown in Figs. 2(a) and 2(c). Figures 2(b) and 2(d) show that the dendrite morphology was preserved after the sulfur treatment as can be seen from TEM and SEM images for PbS-S. Sintering of the grain boundaries and densification for both samples can be observed from the SEM images after SPS in Figs. 2(e) and 2(f). All samples demonstrated a uniform dispersion of Pb and S elements according to EDS analysis.

X-ray diffraction (XRD) patterns of all samples were assigned to the cubic phase of PbS, as shown in Fig. 3(a) and Table S1, supplementary material. The reference sample, PbS-ref.-SPS, showed a larger Scherrer crystallite size of 310 nm compared to those for PbS-SPS (199 nm) and PbS-S-SPS (206 nm). Thermogravimetric analysis (TGA) of the solvothermal product in Fig. 3(b) indicated three major weight loss peaks. The goal of the performed TGA analysis was to identify a temperature range at which any possible S-loss occurred and where  $V_S$  vacancy sites were formed. The first peak at 166 °C was attributed to the removal of chemisorbed water and solvents. The peak at 244 °C was assigned to the removal of the remaining glycerol [see Fig. 3(d) for the TGA analysis of glycerol]. Assuming that the third peak at 266 °C with a weight loss of 1.97% is due to the sulfur loss from the sample, a vacancy degree of  $x = 0.17$  is obtained based on  $PbS_{1-x}$ . This calculation was based on a mass-balance equation assuming that the initial sample was stoichiometric according to Ref. 34,

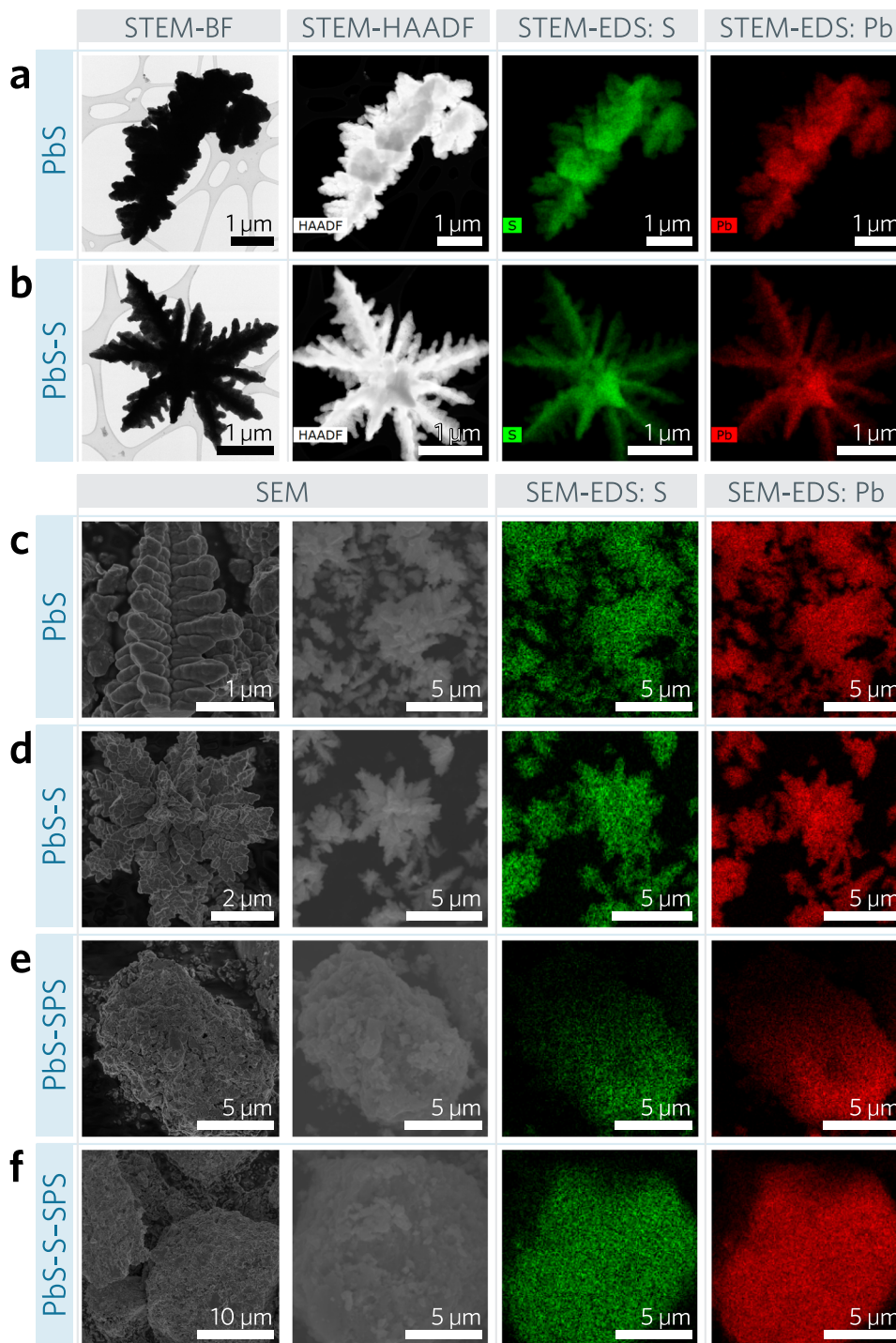
$$M_w(PbS) = [1 + (\% \text{ mass loss in } N_2)/100] \times M_w(PbS_{1-x}). \quad (1)$$

This assumption is subsequently verified by inductively coupled plasma optical emission spectroscopy (ICP-OES) analysis. After this major weight loss at 266 °C, only 0.3% weight loss is obtained over the remaining temperature range up to 600 °C. Figure 3(c) shows the differential scanning calorimetry (DSC) shift of the endothermic peak from 971 to 1053 °C after SPS, being an indication of grain boundary sintering by SPS.

ICP-OES analysis of the solvothermally synthesized sample (PbS-Solvo) revealed that the atomic ratio of S was only 0.46% higher than that of the Pb which indicated that the product of the solvothermal process did not have sulfur deficiency. Having a near stoichiometric as-synthesized sample verified our prior assumption for the calculation of  $x$  based on TGA. Interestingly, the  $H_2/Ar$  annealing at 600 °C induced a substoichiometric  $PbS_{1-x}$  sample with  $x = 0.16$ . This value is very close to the value obtained from TGA analysis ( $x = 0.17$ ), indicating that the sulfur loss temperature range was

TABLE I. Sample descriptions, inductively coupled plasma optical emission spectroscopy (ICP-OES) analysis results, sulfur under-stoichiometry degree, estimated lower and upper limits of carrier concentrations and the densities of trap states, and barrier height for charge carriers during transport.

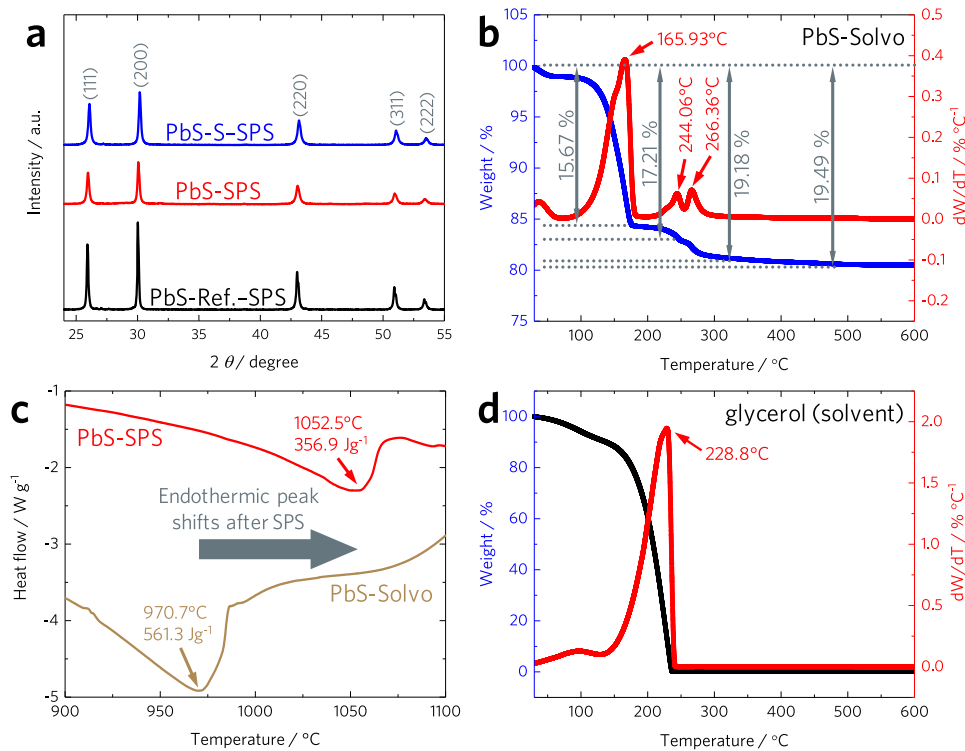
Sample	Sample description	Measured ICP-OES (atom %)		$x$ in $PbS_{1-x}$	Mobile carriers $n/p$ ( $cm^{-3}$ )	Estimated carrier concentration at 300 K			
		Pb	S			Trap state density $Q_t$ ( $cm^{-2}$ )	Trap state density $Q_t$ ( $cm^{-2}$ )	Trap state density $Q_t$ ( $cm^{-2}$ )	Barrier height (eV)
PbS-solvo	After the solvothermal reaction	49.77	50.23	-0.01	...	...	...	...	...
PbS	After argon annealing at 600 °C	54.38	45.62	0.16	...	...	...	...	...
PbS-SPS	After SPS at 600 °C	55.40	44.6	0.19	$3.72 \times 10^{19}$	$1.44 \times 10^{13}$	$7.40 \times 10^{21}$	$2.04 \times 10^{14}$	0.352
PbS-S	After sulfur treatment at 600 °C	48.51	51.49	-0.06	...	...	...	...	...
PbS-S-SPS	After SPS at 600 °C	49.18	50.82	-0.03	$1.8 \times 10^{18}$	$1.16 \times 10^{12}$	$1.17 \times 10^{21}$	$2.92 \times 10^{13}$	0.046
PbS-ref.-SPS (Sigma-Aldrich #372595)	After SPS at 600 °C	50.98	49.02	0.04	$1.7 \times 10^{18}$	$2.02 \times 10^{12}$	$1.56 \times 10^{21}$	$6.12 \times 10^{13}$	0.151



**FIG. 2.** TEM and HAADF-STEM images of the  $\text{PbS}_{1-x}$  samples: (a) after the  $\text{H}_2/\text{Ar}$  treatment (PbS) and (b) after sulfur treatment (PbS-S). SEM and EDS images for (c) PbS and (d) PbS-S are shown for comparison. SEM and EDS images of the  $\text{PbS}_{1-x}$  samples after SPS: (e) PbS-SPS and (f) PbS-S-SPS. The elemental mappings are based on the sulfur K series and lead M series.

identified properly. The SPS process at  $600^\circ\text{C}$  further increased the substoichiometry parameter to  $x = 0.19$  (sample PbS-SPS). Analysis of the sulfur treated sample, PbS-S, showed that the postsynthesis sulfurization indeed prevented sulfur deficiencies in the sample.

A value of  $x = -0.06$  was obtained for PbS-S, in which the negative sign indicated the presence of excess sulfur. During SPS, some of the excess sulfur was evaporated and  $x = -0.03$  was obtained for PbS-S-SPS. The reference sample (PbS-ref.-SPS.) was found to be slightly



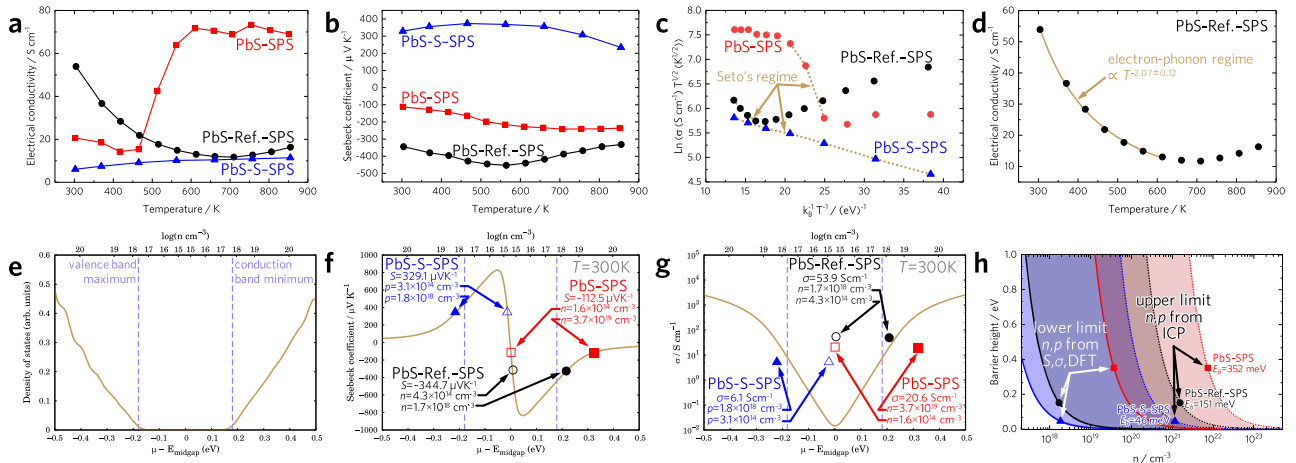
**FIG. 3.** (a) X-ray diffraction patterns of PbS samples after spark plasma sintering (SPS) at 600 °C. The peaks are assigned to the cubic PbS phase with space group  $Fm\bar{3}m(225)$  according to powder diffraction file #05-0592. (b) TGA analysis of the solvothermal product. (c) DSC analysis of the samples before and after SPS. (d) TGA analysis of glycerol, identifying the glycerol removal temperature range.

substoichiometric after SPS at 600 °C, with  $x = 0.04$ . A summary is given in Table I.

The performed TGA and ICP-OES analyses revealed several important observations: (i) sulfur loss occurred during the high-temperature annealing and not during the solvothermal process, (ii) the sulfur-loss peak was centered at 266 °C, and (iii) high temperature annealing (600 °C) in the presence of sulfur vapor prevented sulfur loss from the PbS. In order to gain deeper insight, density functional theory (DFT) calculations of  $V_S$  formation energy were performed on the surface as well as the bulk of the materials (Fig. S1, supplementary material). The PbS crystal with no vacancy was taken as the reference. Then, one sulfur atom was removed from either the surface or the bulk. The removal process was modeled both thermodynamically and kinetically using the nudged elastic band method coupled with DFT calculations. Our calculated transition pathways are given in the supplementary material, showing that removing a surface sulfur atom is far easier than removing a bulk (or deeper-layer) sulfur atom, which needs to overcome a barrier of more than 8 eV, about 1.7 times larger than the former case (see Fig. S1, supplementary material). This can explain the sulfur loss being a major event at 266 °C due to sulfur evaporations from layers closer to the outer surfaces, which is observable due to the dendritic morphology of PbS-Solvo.

Temperature-dependent electrical conductivity of the samples is shown in Fig. 4(a). The electrical conductivity of PbS-SPS with  $x = 0.19$  (the sample with the highest concentration of  $V_S$ ) exhibited a small decrease from 20.6 to 14.3  $S\ cm^{-1}$  as the temperature increased from 29 to 144 °C. From 191 to 337 °C, the electrical conductivity showed a rapid increase to 71.7  $S\ cm^{-1}$ . Above this range,

the electrical conductivity remains fairly constant. Such behavior can be caused by solid-state chemical reactions upon cycling as has been reported previously.<sup>29</sup> However, TGA analysis did not suggest the possibility of any reaction in the samples annealed and spark plasma sintered at 600 °C. The other reason for such a trend in the electrical conductivity can be due to a change in the majority carrier type due to increasing carrier concentration by thermal activation.<sup>19</sup> In such cases, a concurrent sign inversion in the measured Seebeck coefficient is expected. However, the PbS-SPS sample demonstrates an  $n$ -type behavior over the entire temperature range with no sign inversion, as shown in Fig. 4(b). Another explanation can be due to existence of potential barriers in the sample. As the temperature increases above  $\sim 450$  K, some of the charge carriers gain enough energy to overcome the barrier caused by charged grain boundaries according to the model proposed by Seto.<sup>26</sup> The sulfurized sample, PbS-S-SPS with  $x = -0.03$ , exhibited a steady increase in the electrical conductivity as temperature increased, as shown in Fig. 4(a). A maximum value of 14.4  $S\ cm^{-1}$  was measured at 582 °C. Interestingly, the Seebeck coefficient measurements showed  $p$ -type behavior over the entire temperature range with a maximum of  $S = 373\ \mu V\ K^{-1}$  at a temperature of 192 °C. The  $p$ -type behavior suggests the existence of excess sulfur with no  $V_S$ , which is consistent with the ICP-OES analysis. Additionally, although the sulfur vacancy formation was theoretically modeled with the assumption that the sulfur atom was completely removed from the sample (and therefore no interstitial sulfur formation), it is possible that there were sulfur atoms trapped by the complicated energy landscape, therefore making an additional contribution to the  $p$ -type behavior of the measured Seebeck coefficient.



**FIG. 4.** Temperature dependent (a) electrical conductivity and (b) Seebeck coefficient measurements of the PbS samples. (c) Logarithm of electrical conductivity times square root of temperature vs  $(k_B T)^{-1}$  allows for calculation of the barrier heights according to Seto's model.<sup>26</sup> (d) Modeling of the electrical conductivity of PbS-ref.-SPS sample based on electron-phonon dominant scattering. (e) DFT calculation of PbS electronic density of states. (f) Seebeck coefficient calculated by DFT/BoltzTraP vs relative chemical potential. Carrier concentrations can be estimated from comparison with room temperature experimental values. Degenerate regime carrier concentrations are indicated by solid symbols, and the values closer to the intrinsic regime are shown by open symbols. (g) Electrical conductivity,  $\sigma$ , calculated by DFT/BoltzTraP vs relative chemical potential shown in comparison with experimental values plotted at carrier concentrations obtained in (f). The sample chemical potentials are clearly not in the intrinsic regime. (h) Calculated barrier height vs carrier concentrations. The estimated carrier concentration values based on DFT were considered as the lower limit (solid lines), and the carrier concentrations obtained based on the ICP-OES analysis were considered as the upper limit (dotted lines).

The linear region of the curves of  $\ln(\sigma T^{1/2})$  vs  $1/(k_B T)$  in Fig. 4(c) was taken in order to estimate the barrier heights for charge carriers during transport based on the following expression:<sup>26</sup>

$$\sigma \propto T^{-1/2} \exp\left(-\frac{E_B}{k_B T}\right) \text{ for } NL > Q_t, \quad (2)$$

where  $T$ ,  $k_B$ ,  $E_B$ ,  $N$ ,  $L$ , and  $Q_t$  are the temperature, Boltzmann constant, barrier height, doping concentration, crystallite size, and trap state density, respectively.

Figure 4(d) illustrates that in contrast to PbS-SPS and PbS-S-SPS, the reference sample (PbS-ref.-SPS) shows a trend that is expected for degenerate/metallic electrical conduction dominated by electron-phonon scattering where below  $\sim 325^\circ\text{C}$ , the electrical conductivity is proportional to  $T^n$  with  $n = -2.07 \pm 0.12$ . This is slightly higher than the expected value of  $n = -3/2$ , possibly due to additional scattering mechanisms in the sample. However, at temperatures above  $325^\circ\text{C}$ , the behavior changes, and the electrical conductivity is seen to increase from 12 to  $16.3 \text{ S cm}^{-1}$  at  $388\text{--}612^\circ\text{C}$ , respectively. The different trend and values of the temperature dependent electrical conductivity of the PbS-SPS and PbS-ref.-SPS samples are attributed to the different grain boundary concentrations and the corresponding energy barriers introduced by the charged-grain boundaries. The distinct morphology of the original powders induced different grain boundary concentrations during the SPS although both samples were treated at the same temperature of  $600^\circ\text{C}$ . The density of the PbS-ref.-SPS sample ( $7.34 \text{ g cm}^{-3}$ ) was measured to be close to the theoretical value ( $7.6 \text{ g cm}^{-3}$ ) and higher than that of the PbS-SPS ( $6.28 \text{ g cm}^{-3}$ ). In addition, the  $x$  value of the PbS-ref.-SPS was found to be lower than that of the PbS-SPS (0.04 compared to 0.19, as shown in Table I) which can also contribute to the distinct temperature dependent electrical conductivities of these

samples. Figure 4(b) shows that the Seebeck coefficient of the reference sample exhibits an  $n$ -type behavior with a maximum  $S = -453.5 \mu\text{V K}^{-1}$  at  $T = 291^\circ\text{C}$ .

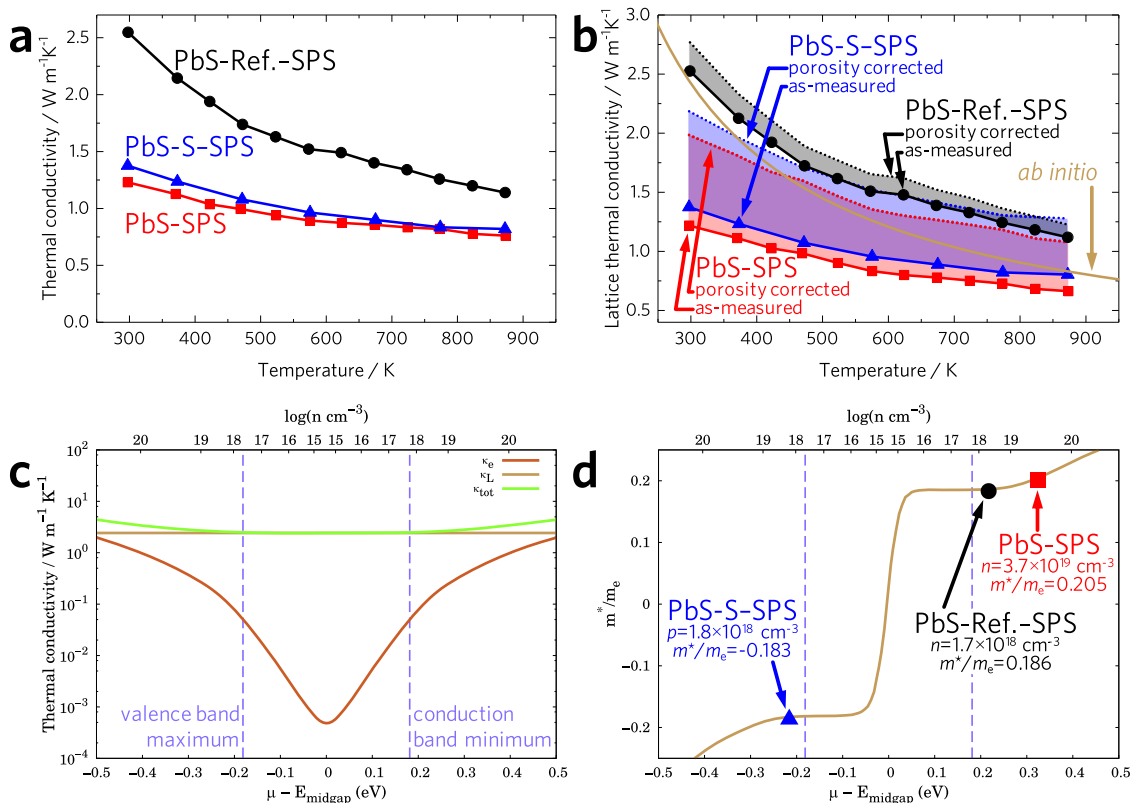
According to the trap state model given by Eq. (2),<sup>26</sup> the doping concentration  $N$  can be larger than the mobile carrier concentration  $n$  as a portion of the carriers is immobilized in the trap states at interfaces. In the current work,  $n$  is estimated by a comparison between the measured Seebeck coefficients and those obtained from *ab initio* calculations at room temperature using the BoltzTraP software package. For the measured Seebeck coefficient, there are two possibilities for corresponding carrier concentrations, as shown by solid and open symbols in Fig. 4(f). Using the electrical conductivity data and those from the DFT calculations shown in Fig. 4(g), the solid symbols were considered to estimate carrier concentrations for the samples as the values obtained in the intrinsic regime (open symbols) are very far from the measured electrical conductivities. The lower electrical conductivity of the  $p$ -type PbS-S-SPS sample can be due to its lower carrier concentration ( $p = 1.8 \times 10^{18} \text{ cm}^{-3}$ ) compared to that of the  $n$ -type PbS-SPS with a higher carrier concentration ( $n = 3.7 \times 10^{19} \text{ cm}^{-3}$ ) [Fig. 4(g)], as well as its higher concentration of grain boundaries. The measured values for both  $n$ -type and  $p$ -type cases were much lower than those predicted by DFT calculations, which is expected as the DFT model represents a single crystalline material with no grain boundary-induced energy barrier as opposed to our polycrystalline samples. Thus, we can view the DFT-predicted conductivities as upper values to the electrical conductivity at each carrier concentration. For the  $p$ -type sample, the calculated electrical conductivity and the measured electrical conductivity were  $\sigma_{\text{DFT}} = 27.7 \text{ S cm}^{-1}$  and  $\sigma_{\text{exp}} = 6.1 \text{ S cm}^{-1}$ , respectively. For the  $n$ -type sample, the calculated electrical conductivity and the measured electrical conductivity were  $\sigma_{\text{DFT}} = 509.1 \text{ S cm}^{-1}$

and  $\sigma_{\text{exp}} = 20.6 \text{ S} \cdot \text{cm}^{-1}$ , respectively. It can be seen that the electrical conductivity of the *p*-type sample was actually much closer to the predicted single crystalline value (22% of  $\sigma_{\text{DFT}}$ ) at its carrier concentration than that of the *n*-type sample (4% of  $\sigma_{\text{DFT}}$ ).

Using Seto's formulations,<sup>26</sup> the barrier height for charge carriers during transport vs carrier concentration is plotted in Fig. 4(h) for each sample. The lower limits of carrier concentration were estimated from experimental Seebeck measurements and DFT calculations, and the upper limits were based on the performed ICP-OES analysis. Based on Seto's formulations, in the range  $NL > Q_i$ , the height of the barrier decreases as the carrier concentrations increase ( $E_B \propto 1/N$ ). For the PbS-SPS ( $x = 0.19$ ), PbS-S-SPS ( $x = -0.03$ ), and PbS-ref.-SPS ( $x = 0.04$ ), the density of trap states and barrier heights of  $Q_t = 2.04 \times 10^{14}$ ,  $2.92 \times 10^{13}$ , and  $6.12 \times 10^{13} \text{ cm}^{-2}$  and  $E_B = 0.352$ ,  $0.046$ , and  $0.151 \text{ eV}$  were calculated, respectively [see Table I and Fig. 4(g)]. For PbS-ref.-SPS sample, the barrier height was calculated only in temperatures above 600 K in the linear region of the logarithmic curve shown in Fig. 4(c). Because the grain boundaries and morphologies of the PbS-SPS and PbS-S-SPS seemed to be similar using TEM and SEM analysis, the much higher barrier height of the former compared to the latter (sulfurized sample with no  $V_{\text{S}}$ ) is attributed to the high concentration  $V_{\text{S}}$  vacancies. Each  $V_{\text{S}}$  can introduce two negative charges which can essentially be trapped in

the interfaces creating a highly charged barrier against the charge transport. The different temperature-dependent electrical conductivity of the reference sample compared to solvothermally synthesized samples can be due to its much lower grain boundary density (through lower surface area and higher particle size), as shown in Fig. S2 in the supplementary material.

Temperature-dependent total thermal conductivity and lattice thermal conductivity of the samples are shown in Figs. 5(a) and 5(b). The expression  $\kappa_L = \kappa - \sigma LT$  can be used to extract the lattice thermal conductivity from the total thermal conductivity, where  $L$  is the Lorenz number and is a function of chemical potential. The Lorenz numbers were calculated based on the measured Seebeck coefficients according to Kim *et al.*<sup>35</sup> assuming single band transport (Fig. S3, supplementary material). PbS-SPS and PbS-S-SPS exhibited lower lattice thermal conductivities compared to those of PbS-ref.-SPS although all samples displayed anharmonic phonon-phonon dominated transport, indicating that thermal transport was not influenced as strongly by the grain boundaries in this regime, which can be the case for transport across even weaker van der Waals interfaces at room temperature.<sup>36</sup> The porosity-corrected thermal conductivity of PbS-ref.-SPS decreased more rapidly as the temperature increased approaching those of the other samples. Porosity corrected values of 1.081, 1.28, and  $1.23 \text{ W m}^{-1} \text{ K}^{-1}$  was measured



**FIG. 5.** (a) Measured temperature-dependent total thermal conductivity values of the PbS samples. (b) Lattice thermal conductivity of the samples without (symbols, solid lines) and with (dotted lines) porosity corrections; the Lorenz number has been obtained according to Ref. 35. (c) DFT calculated electronic, lattice, and total thermal conductivity of PbS at 300 K. (d) DFT calculated effective mass for PbS at 300 K.



for PbS-SPS and PbS-S-SPS and PbS-ref.-SPS, at a temperature of 600 °C, respectively. The porosity correction calculation was based on the measured density compared to that of the theoretical value according to the Eucken<sup>37,38</sup> and Russell<sup>39</sup> models. DFT calculations estimated the lattice thermal conductivity to be 0.833 W m<sup>-1</sup> K<sup>-1</sup> at ~600 °C. The DFT calculated total thermal conductivity and lattice and electronic contributions are shown in Fig. 5(c). The calculated effective mass values are demonstrated in Fig. 5(d). Additionally, prior to thermal diffusivity measurements using a NETZSCH LFA-457 Microflash, pyrex 7740 and pyroceram 9606 reference samples were measured and the obtained data were compared with that provided from NETZSCH Group. An excellent agreement was observed as shown in Fig. S4 of the [supplementary material](#).

Multiple runs of the electrical conductivity, Seebeck coefficient, and thermal conductivity measurements have been conducted (Fig. S5, [supplementary material](#)). First, thermal diffusivity measurements were conducted on the pellets obtained from SPS at 600 and 700 °C. Good agreement between the first and the second run of thermal conductivity measurements for the pellet obtained by SPS at 600 °C is shown in Fig. S5(a) of the [supplementary material](#) after conversion to thermal conductivity. Good reproducibility is also observed between the heating and cooling thermal conductivity curves of the sample obtained by SPS at 700 °C, as illustrated in Fig. S5(b), [supplementary material](#). After the thermal diffusivity measurements, the samples were cut into rectangular bars, and the surfaces were repolished for the electrical conductivity and Seebeck coefficient measurements using a ZEM-3 instrument. Figure S5(c) of the [supplementary material](#) compares the 1st and 2nd electrical measurement runs for a bar obtained from a pellet with SPS at 600 °C. Interestingly, in the second run, the sample exhibited higher values of electrical conductivity at temperatures below ~525 °C (e.g., 44.5 S cm<sup>-1</sup> compared to 14.9 S cm<sup>-1</sup> at room temperature for the 2nd and 1st runs, respectively). However, the electrical conductivity values for both runs approached 71.3 S cm<sup>-1</sup> at higher temperatures. This behavior can be explained by using the barrier height vs carrier concentration plot shown in Fig. 4(h). After each run at the temperature range up to 600 °C, the carrier concentration increases due to further sulfur evaporation. This increases the carrier concentration, and therefore, the barrier height decreases leading to the enhancement in electrical conductivity. Furthermore, in the high temperature regime, the charge carriers have gained enough thermal energy to overcome the barrier causing the electrical conductivities to approach the same value. Seebeck coefficients remain relatively close within the range of the measurement uncertainty despite expected sulfur evaporation [Fig. S5(d), [supplementary material](#)]. This can be explained since the carrier concentration is in the degenerate regime where the change in the Seebeck coefficient with carrier concentration remains fairly small. By contrast, the barrier height changes rapidly vs the carrier concentration (based on  $E_B \propto 1/N$ ). A similar trend was seen between the first and second run of the sample made from a pellet with SPS at a higher temperature of 700 °C [Figs. S5(e) and S5(f), [supplementary material](#)]. During the first run, the electrical conductivity decreased as the temperature increased from 302 to 417 K. However, as the temperature was further raised above 466 K, the electrical conductivity started to increase with temperature. For this first measurement, transport appeared to be governed by electron-phonon scattering at lower temperatures, whereas at the higher temperatures (above 466 K), it was more affected by

the grain boundary energy barrier. This can arise due to the more effective sintering of the grain boundaries in this sample as the SPS was conducted at the higher temperature of 700 °C. As the SPS temperature increases, the grain sintering is improved and the effects of the grain boundary energy barrier start to diminish. The better sintering is supported by the measured mass density of this sample (6.65 g cm<sup>-3</sup>) which was higher than that of the sample with SPS at 600 °C (6.28 g cm<sup>-3</sup>) but still lower than that of the reference sample (7.34 g cm<sup>-3</sup>). Interestingly, the 2nd run of this sample exhibited an electrical conductivity trend completely dominated by the electron-phonon scattering similar to the trend of the reference sample and single crystals with no grain boundaries. Both, the first and second measurement runs approached the same limit above 756 K.

A good agreement between the first and second run thermal conductivity measurements of the reference sample obtained by SPS at 600 °C is shown in Fig. S6(a) of the [supplementary material](#). The electrical conductivity and Seebeck coefficient measurements of two rectangular bars from the same pellet have been shown in Figs. S6(b) and S6(c) of the [supplementary material](#), indicating that the sintered pellet is homogeneous. A figure of merit for the PbS samples is shown in Fig. S7 of the [supplementary material](#).

In conclusion, the effects of highly charged interfaces on the electrothermal transport properties of PbS<sub>1-x</sub> were investigated in detail. Dendritic PbS<sub>1-x</sub> samples with  $x = 0.19$  and  $-0.03$  and a reference sample with  $x = 0.04$  were investigated in order to understand the effects of interfaces on transport properties. High concentrations of grain boundaries were observed for the samples synthesized using a solvothermal method with dendritic structures compared to the cubic morphology reference sample. Sulfur vacancies were formed during the annealing process with a major mass loss occurring at 266 °C. DFT calculations suggested that this loss could be attributed to the regions close to the particle surfaces as the  $V_S$  formation energy on the surface was 1.72 times lower than that through the bulk. A postsynthesis thermal sulfurization process prevented  $V_S$  vacancy formation, resulting in  $p$ -type samples. The electrical conductivity behavior of the substoichiometric PbS-SPS ( $x = 0.19$ ) and PbS-S-SPS ( $x = -0.03$ ) samples was dominated by thermionic transport through charged interface barriers, whereas the electrical conductivity behavior of PbS-ref.-SPS ( $x = 0.04$ ) was dominated by electron-phonon scattering at temperatures below ~325 °C due to lower concentration of grain boundaries. Barrier heights for charge carriers during electrothermal transport of  $E_B = 0.352$ , 0.151, and 0.046 eV were estimated for PbS-SPS, PbS-S-SPS, and PbS-ref.-SPS. The much larger barrier height occurring in PbS-SPS was found to arise from a high density of  $V_S$  defects charging the interfaces and thus raising the barrier potential. This investigation has described  $V_S$  site formation and revealed the crucial role of interfaces and interface-vacancy interactions in dominating the electrothermal transport properties of substoichiometric PbS.

See [supplementary material](#) for details of theoretical and experimental methods; XRD analysis of the samples obtained by SPS; calculated Lorenz number based on the measured Seebeck coefficient; thermal conductivity of reference samples pyrex 7740 and pyroceram 9606; thermal conductivity, electrical conductivity, and Seebeck coefficient measurements of the samples obtained by SPS at 600 and 700 °C; and 1st and 2nd run thermal conductivity measurements of

the reference sample, electrical conductivity, and Seebeck coefficient measurements of the reference samples conducted on two different rectangular bars from the same pellet.

This project was conceived and led by M.T.P. and S.Y. Experimental work and materials synthesis by M.T.P., S.Y., R.K.-S., and R.D.M. were supported by the U. S. National Science Foundation Award Nos. CAREER-1553987 (M.T.P., S.Y., and R.K.-S.) and REU-1560098 (M.T.P. and R.D.M.), the UConn Research Foundation, Award No. PD17-0137 (M.T.P., S.Y., and R.K.-S.), the U.S. Department of Energy Office of Science under Contract No. 89233218CNA000001 (M.T.P.), and a GE Graduate Fellowship for Innovation (S.Y.); theoretical and computational work was conducted by T.D.H. and supported by XSEDE through the computational resource allocation Grant No. TG-DMR170031.

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