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ABSTRACT

The recent development of semiconductor laser technologies for cost-effective telecom/datacom applications is reviewed in details in this paper. This includes the laser design, laser chip technology, laser packaging technology and other low cost lasers (chip + packaging). Some design and simulation examples in Archcom laser production are described first. A latest trend in the wafer scale testing/characterization/screening technology for low cost semiconductor laser mass production is discussed then. An advanced long wavelength high power single mode surface emitting laser with wafer scale characterization using our unique mask free focused ion beam (FIB) etching technology is also demonstrated. Detailed descriptions on our wide temperature range (-50 °C to +105 °C) G-PON distributed feedback (DFB) semiconductor lasers with high performance and low cost wafer design are included. Cost reduction innovations in laser package with our beam profile improved laser and optical feedback insensitive (OFBI) laser are also addressed.

Keywords: Cost effective, wafer-scale testing, surface emitting, mask free, un-cooled laser, tunable laser, integrated laser, surface mount, mode converted laser, isolation-free laser

1. INTRODUCTION

Cost effective telecom/datacom semiconductor lasers are becoming more and more important today with the increasing network traffic and decreasing profit margin. Both the carriers and component suppliers are seeking low cost solutions by developing new technologies and reducing manufacturing cost. The low cost requirement doesn't compromise on any performance yield. Actually the performance requirement to the semiconductor laser components today is much higher and tighter than before. It is very challenging to reduce the laser manufacturing cost while keeping even higher laser performances. In long term, technology development is more important than just relocating the manufacturing site to cost inexpensive countries. This invited paper will briefly review the recent technologies or trends of semiconductor lasers for cost-effective telecom/datacom applications and present some of low cost and high performance laser devices developed by the author team in detail.

This article will be divided into the following four sections for detailed discussions: laser design and simulation, laser chip technology, laser packaging technology and other low cost lasers (chip + packaging).

2. DESIGN AND SIMULATION

A right and good laser design determine the high performance and low cost of a semiconductor laser. Some one can even think of a low cost device business by choosing fab-less model with a successful device design only. Of course this is cost sensitive to the manufacturing partners, but could be one of the future options for semiconductor laser start up. Actually the fab-less option is popular in silicon industry and some compound semiconductor laser companies have been trying this business model by using standard world-class foundries in both laser chip and packaging. Some companies with design center in the labor expensive countries may also have manufacturing base in labor inexpensive countries.

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Fig. 1 Some simulated examples in our 1550 nm BH laser design for optimizing the quantum well (QW) active layer structure (A) L - I curve at room temperature with different QW number. (B) The L - I curve at high temperature (70 °C) with different QW number. (C) The L - I curve at high temperature (70 °C) with different QW strain. (D) Example of a 440 µm long cavity laser with two facets as cleaved BH FP laser at room temperature 25 °C and high temperature70 °C for comparison of simulated and fabricated lasers. This analysis helps to design our high power laser at high temperature for un-cooled low cost package.



(A) Laser cavity optimization design

(B) BH laser blocking layer optimization design

Fig. 2 Simulated examples in our 1550 nm BH laser design for optimizing (A) the laser cavity and facet mirror structure, (B) the blocking layer structure in our BH lasers with different P and N type layer thickness and doping level (unit: m⁻³). The leak value is the leakage channel width between the laser active layer and the N type blocking layer in lateral dimension (so called PPN leakage). A high performance design results in a low cost for our wafer fabrication because of the tolerant yield.

For example in our case, by only optimizing the wafer structure design and grating structure design, we have manufactured and achieved very wide temperature (155 °C temperature range) high performance DFB lasers for cost effective optical communication networks such as fiber to the home (FTTH) application with matured semiconductor material and matured semiconductor process technology, instead using special material and process which usually cause additional expense in mass production. This laser device performance will be addressed in another section later.

Semiconductor laser design and simulation technologies have been well developed by researchers and commercialized by design software companies ^{1–9}. We have also used the simulator to design our high power laser at high temperature for un-cooled low cost package. Fig. 1 shows some examples of our simulated 1550 nm BH laser output power data for optimizing our laser active layer strained quantum well (QW) structure design. Our fabricated laser result is very close to the simulated data when the similar parameters are used. The fabricated device performance will be described later in other section. Fig. 2 exhibits more examples of our 1550 nm BH lasers on (A) the laser cavity and mirror structure optimization design, (B) the blocking layer thickness and doping level optimization design. The leak value in the figure is the leakage channel width between the laser active layer and the N type blocking layer in lateral dimension (so called PPN leakage). A high performance design results in a low cost for our wafer fabrication because of the tolerant yield.

In the semiconductor laser design, since too many device parameters may affect the laser performance, a design of engineering (DOE) or Taguchi method is usually helpful to get to the right design efficiently.²

3. LASER CHIP TECHNOLOGY

3.1 Laser wafer epitaxial growth

The most of laser chip cost is from epitaxial growth such as metal organic chemical vapor deposition (MOCVD) which is a popular mass production tool, but the equipment, sources, and maintenance are very costly. One solution is to use commercially available laser epi wafer. This is good for some general or standard laser structures and some custom made laser structures. Usually Fabry Perot (FP) lasers are using this option. Standard DFB lasers may be difficult to use outsourcing epi wafer because grating fabrication and re-growth are critical processes. This had motivated the research and development of some special type DFB lasers with re-growth free technology, such as surface grating DFB lasers ¹⁰⁻¹² and laterally coupled (LC) DFB ridge lasers ¹³⁻¹⁵.

3.2 RW laser vs. BH laser

Ridge wave guide (RW) laser is the lower cost type of the most popular edge emitting semiconductor lasers compared to the BH type. It is easier to fabricate with higher process yield for RW laser production. However one may have to compromise on the laser performance such as higher threshold, lower optical power, poorer high temperature performance, non-circular beam profile (for example $25^{\circ} \times 40^{\circ} \text{ vs}$. $30^{\circ} \times 35^{\circ}$ for RW vs. BH). A higher laser threshold often leads to the laser in higher cost because it needs cooler for high temperature applications. One of the solutions is to make the ridge laser better performance and maintain the similar cost in wafer fabrication process. Reversed mesa ridge laser ¹⁶⁻¹⁸, laterally oxidized ridge laser including quantum dot laser ¹⁹⁻²², the ridge laser with laterally undercut etched current confinement structure ²³⁻²⁴ and other structures ²⁵⁻²⁶ are some typical examples.

3.3 InGaAsP laser vs. InGaAlAs laser

A better laser base material system is also a key factor in cost sensitive semiconductor lasers. For 1.3 μ m and 1.5 μ m wavelength semiconductor lasers used in the cost effective telecom and datacom networks, InGaAsP/InP is the conventional and most matured base material system. Wafer fabrication process has been well developed and yield is high in InGaAsP/InP lasers ^{25–31} mass production. Usually BH structure is needed for high temperature performance for this material system per the description above. A solution from the view of base material system is to use larger band gap off-set material for better carrier confinement to achieve better performance in high temperature. InGaAlAs/InP lasers ^{32–35} and GaInNAs/GaAs lasers ^{36–40} are typical examples, in which InGaAlAs/InP laser is leading the trend while GaInNAs/GaAs laser does not work out so far for mass production.

3.4 Edge emitting laser vs. surface emitting laser

Edge emitting laser is still dominant in the market especially for long wavelength laser sources. Vertical cavity surfacing emitting laser (VCSEL)⁴¹⁻⁴⁸ has been well developed especially with GaAs based material in short wavelength market.

Compared to the edge emitting type semiconductor lasers, surface emitting semiconductor lasers, including VCSEL and horizontal cavity surface emitting lasers (HCSEL)⁴⁹⁻⁵¹, are more cost effective due to less labor involvement in laser bar testing, bar coating in laser wafer manufacturing. In addition, we have developed focusing ion beam etched surface emitting laser (FIBESEL)⁵²⁻⁵³ with unique process flexibility and complete mask free technologies. This additional convenience makes post device process also possible. For example, a process-completed and chip-mounted but not sealed FP laser can still be easily processed into a single mode laser with cavity structure post-changed by the FIBE process⁵².

Table 1 lists the process comparison for edge emitting and surface emitting lasers, including VCSEL, HCSEL and FIBESEL in manufacturing flow. It is clearly shown the process step reduction in surface emitting laser manufacturing process. Especially the bar cleaving and bar fixture for facet anti-reflection (AR) coating and high reflection (HR) coating are very much labor intensive because so many bars generated from a wafer and the fixture for AR and HR coating is critical. The facet coating fixture also wastes several laser devices for each laser bar because it clamps each end of the laser bars. All these can be avoided in surface emitting laser manufacturing because of the wafer scale process and wafer scale auto testing. Of course, for R&D sampling, there is no significant difference in the cost, but in mass production, the cost is very different. The cost saving rank is also listed in the table for reference.

Process Steps	Edge Emitting Laser	VCSEL	HCSEL	FIBESEL
Epi-growth (standard laser)	1	1	1	1
Epi-growth (laser mirrors)		2		
Wafer process	2	3	2	2
Laser cavity/mirrors lithography			3	
Laser cavity/mirrors etching			4	3
Laser cavity/mirrors coatings			5	4
Wafer Scale testing (auto test)		4	6	5
Wafer cleaving into laser chips		5	7	6
Wafer cleaving into laser bars	3			
Initial laser bar testing	4			
Laser bar loading into fixture	5			
Laser bar front facets coating	6			
Laser bar rear facet coating	7			
Laser bar testing and screening	8			
Laser bar cleaving into chips	9			
Process cost saving rank	D	А	C	В
Remarks	High power	Low power	High power available	
	Wide temperature	Short λ	Wide temperature range	
	Much more laboring			Flexible
	from 3 to 9 steps			Mask free
	Rank D: higher cost			

Table 1Comparison of edge emitting and surface emitting lasers in manufacturing process. It is shown that
edge emitting laser has 9 steps, VCSEL has 5 steps, HCSEL has 7 steps, FIBESEL has 6 steps in total.

VCSEL is very good in applications like some datacom networks requiring less optical power. Using advanced focusing ion beam (FIB) technology, we have developed surface emitting long wavelength DFB laser with high power and wide temperature range. This is not achievable yet with the existing VCSEL technology. Compared to HCSEL, FIBESEL is more flexible and accurate in process of a nanometer scale. In addition, it needs no mask and lithography. A production model of the FIB machine is also very fast in speed and high in throughput.

Figure 3 shows a schematic drawing of our FIB etched surface emitting DFB laser in a side view. It is noted that the FIB etched mirror process is complete mask free and the FIB etched mirror location is accurate and flexible. This means we

can even make the laser mirrors or new cavities after the device process is all completed based on the requirements of applications, especially for some special and unique applications.



Fig. 3 A side view of the illustrated 1.5 µm wavelength DFB FIBESEL device fabricated with our unique mask free etching technology for high power and wide temperature cost effective telecom and datacom applications.

Figure 4 is a scan electron microscope (SEM) picture showing the DFB laser with (A) RW structure (B) BH structure. FIB etching process is also flexible to material and the shape of the surface. It can directly cut through the metal, dielectric film and semiconductor material. We opened an area with metal lift off on the laser wafer chip surface for light emitting window, not for process. The FIB etched mirror surface is very smooth, but the opposite side of the etched surface may have some roughness because of the re-deposition of the iron source. This can be improved by a cleaning process if both two etched facets are used as mirrors.



(A) RW DFB FIBESEL chip surface

(B) BH DFB FIBESEL chip surface

Fig. 4 A scan electron microscope (SEM) showing our 1.5 µm wavelength DFB FIBESEL chip surface with (A) ridge wave guide (RW) structure, (B) buried hetero (BH) structure

Figure 5 gives the output power and spectrum data example of our fabricated DFB FIBE laser device under different temperatures. Avery good single mode stability with the side mode suppression ratio (SMSR) above 40 dB was maintained in a wide temperature range. Fig. 6 demonstrates a group of the 10,000 hrs life test data at 100 °C, 200mA for our fabricated DFB FIBESEL devices. There is no degradation observed so far, which shows the high reliability of the FIB processed DFB laser. The life testing is still continuing.



Fig. 5 One example of the measured data from our 1.5 µm wavelength DFB FIBESEL laser chip surface emitting power and spectrum at 25 °C and 70 °C with a good single mode stability (side mode suppression ratio all above 40 dB at both room temperature and high temperature)



Fig. 6 10,000 hrs life time testing data at 100 °C, 200 mA of our DFB FIBESEL device

4. LASER PACKAGING TECHNOLOGY

The semiconductor laser chip is only a part of the laser module. Assembling the laser module is also a key technology in mass production. The packaging cost reduction may be done by parts elimination, alignment simplification, assembling automation, and the related key technology improvement. The advances in the electrical, mechanical, and optical design and materials of laser packages have resulted in performance improvements and cost reductions.

For example, the DFB laser and pump laser butterfly package are gradually reforming to mini-DiL, mini-butterfly, coaxial package. TO and TOSA/ROSA are also getting simpler in design. Fig. 7 shows some examples of Archcom semiconductor laser packages with the cost effective solution.

Recently a plastic package is used to replace some of the metal packages for further cost reduction. This represents the material solution for the low cost packaging. Others like flip chip bonding and passive alignment are also becoming common approaches. The hermitic package towards non-hermitic package is another example of the cost reduction.





(A) Cooled and un-cooled mini-DiL butterfly

(B) Un-cooled DFB TOSA and APD-TIA ROSA pair

Fig. 7 Pictures of some laser packages with (A) cooled and un-cooled Mini-DiL butterfly laser package (B) un-cooled DFB TOSA and APD-TIA ROSA pair for low cost applications.

5. OTHER LOW COST LASERS - (CHIP + PACKAGING)

5.1 Un-cooled laser chip and coaxial packaging

The elimination of thermal electric cooling (TEC) from coaxial package that requires the laser chip with better performance in high temperature. This increases the laser chip cost, but significantly decreases the laser packaging size and cost. It results in a final system cost down. This technology is not only for FP laser, now also extends to un-cooled DFB laser, un-cooled course wavelength division multiplexing (CWDM) laser, un-cooled electro-absorption modulated laser (EML), and so on.

Un-cooled semiconductor lasers are recently well developed for wide temperature applications⁵⁴. Typical un-cooled semiconductor lasers have been mainly developed by using Al included material for its better temperature characteristics, as discussed in section 3.3 in this paper. We also developed an extremely wide temperature DFB laser for the cost sensitive fiber to the home (FTTH) giga bit rate passive optical network (GPON) applications. Due to easy oxidization of Al included material, re-growth process is not preferred for this material system if manufacturing cost is sensitive. Usually high performance FP lasers and standard DFB lasers all require re-growth. This makes the Al included laser a little bit high cost than the most popular laser material. With optimized wafer design, we have used the most popular and matured InP material, which has much higher wafer process yield than the Al included material especially in BH structure lasers, to fabricate un-cooled DFB lasers with ultra wide temperature range.

The typical test data of our fabricated 1310 nm wavelength DFB lasers resulted in the laser threshold around 7 mA at room temperature, 4 mA at lower temperature (-40 °C), and 22 mA at high temperature (85 °C). The laser was measured with standard transmitter optical subassembly (TOSA) package. The laser output power all exceeded 4 mW in an ultra wide temperature range, and meet the requirement of FTTH system.

This laser maintains very good single mode stability in an ultra wide temperature range. Fig. 8 shows the spectrum at (A) extremely low temperature (-50 °C), (B) low temperature (-40 °C), (C) room temperature, and (D) high temperature of 105 °C. The side mode suppression ratio (SMSR) is above 40 dB at -50 °C and -40 °C, 48 dB at 25 °C, 42 dB at 105 °C. It has much more temperature margin to the FTTH requirement with ultra wide temperature range of 180 °C.

The above un-cooled 1310 DFB laser is very suitable for the low cost FTTH application including giga bit rate passive optical network (GPON) system optical network unit (ONU) that requires wide temperature range (usually from -40 °C to 85 °C) and single mode. The same un-cooled laser technology has also been applied to our 1490 nm DFB laser for FTTH GPON optical line terminal (OLT) and other 1310 nm and 1550 nm DFB laser applications such as course wavelength division multiplexing (CWDM) networks uses. Fig. 9 is an example of our extended temperature 1550 nm DFB laser for low cost application. Fig. 10 shows our CWDM DFB laser for both metro broad band and LX4 applications.



(C) Spectrum at room temperature of 25 °C

(D) Spectrum at high temperature of 105 °C

Fig. 8 Typical spectrum examples of our fabricated 1310 nm DFB laser under different temperatures. (A) was measured at -50 °C, (B) was measured at -40 °C, (C) was at room temperature and (D) was at 105 °C. They all demonstrated a very stable single mode with high enough side mode suppression ratio in a very wide temperate range of 155 °C. This created tremendous temperature margin for practical un-cooled wide temperature uses.

5.2 Tunable laser

Functional laser devices like the tunable lasers $^{55-63}$ including continuously wavelength tunable laser for fine tuning application $^{4, 64-65}$ are still somewhat high cost in each individual device, but they significantly reduce the inventory cost for systems especially in wavelength division multiplexing (WDM) networks, in addition to their added values in many other applications like measuring and sensing systems. As the technology advancing today, it will be expectable that every semiconductor laser is multi-functional in the future. This may reduce the number of needed components in the networks.



Fig. 9 An example of our extended temperature 1550 nm DFB laser for low cost application.



Fig. 10 An example of our CWDM DFB laser for both metro broad band and LX4 applications.

5.3 Integrated lasers

Similar to the above mentioned tunable lasers, the monolithically integrated devices like laser integrated with optical amplifier or other devices, such as modulator, mode converter, beam splitter, optical isolator, arrayed wave guide (AWG) and so on $^{66-71}$, bring many functions in a single monolithically integrated device which greatly reduces the size and cost of the systems or subsystems in optical telecom and datacom applications.

5.4 Surface mount laser

The above mentioned monolithic integrated laser devices may take more time for a full commercialization if larger scale monolithic integration is taken into account due to the yield and total cost balance issue. Sometimes the hybrid integration is more practical, especially in the integration with silicon based systems since the silicon laser is not available yet ⁷². In this case the surface mount laser technologies are working attractively for cost effective applications such as eliminating hermitic packaging, merging into silicon integrated circuits ⁷³⁻⁷⁵.

5.5 Mode converter laser

Edge emitting semiconductor lasers have an issue of imperfect beam profile because of the lateral and transverse confinement structure. This usually causes extra loss in optical coupling to other components and systems. Aspheric lens used in the laser module packaging is effective for improving the optical coupling efficient, but it is much expensive than the regular ball lens. Semiconductor laser with mode converter structure is a good solution to reduce the cost because of the low loss coupling with the improved beam profile ⁷⁶⁻⁷⁹.



Fig. 11 A far field pattern example of our fabricated SAG laser with the beam angle of 11.5x12 degree

Figure 11 is a measured far field pattern example of our fabricated selected area growth (SAG) laser. The edge emitting laser beam profile is almost circular in this case. The beam divergence angle is 11.5 x 12 degree. This greatly improved the laser beam profile by reducing the far field angles. It is significant in easy and low loss coupling to the fiber or other waveguide systems to result in a total system cost down. A future lens-free, and isolator-free system may become possible with further advancing of this technology.

5.6 Isolation free laser

Many semiconductor laser application systems need optical isolator to prevent the light feedback from the system to the laser source for keeping laser device in good lasing performance. To eliminate the isolator is effective in cost consideration since it is expensive to package an isolator in the module for cost sensitive applications. A gain-coupled or complex coupled DFB laser has been investigated for the good resistance to optical feedback ⁸⁰⁻⁸³. Mode converter included or some special designed grating structure lasers have also resistance to the reflection light.

With the simplest optical feedback insensitive (OFBI) technology by using a unique laser design, we have recently demonstrated a reflection resistant DFB laser that can withstand optical reflections with a return loss of only –7dB. For many applications, this DFB laser is perfectly suitable to use without an optical isolator which substantially reduces the packaging cost as shown in Fig. 12.



Fig. 12 Eye diagram of our optical feedback insensitive (OFBI) DFB laser with an optical return loss of -7dB. No degradation in the eye pattern was observed for an isolator free application.

In addition to the above optical feedback insensitive (OFBI) DFB lasers, another solution may be the monolithic integration of a wave guide type optical isolator and laser ⁸⁴. This is higher in cost compared to a single isolation free laser device, but still better than adding a separate isolator component in applications.

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