

Instability of a $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ film on a $\text{Ge}_x\text{Si}_{1-x}$ layer

W. S. Liu, J. S. Chen, and M.-A. Nicolet
California Institute of Technology, Pasadena, California 91125

V. Arbet-Engels and K. L. Wang
University of California, Los Angeles, California 90024

(Received 13 April 1992, accepted for publication 22 July 1992)

The stability of an amorphous $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ in contact with an epitaxial (100) $\text{Ge}_x\text{Si}_{1-x}$ layer obtained by partially oxidizing an epitaxial $\text{Ge}_x\text{Si}_{1-x}$ layer on a (100)Si substrate in a wet ambient at 700 °C is investigated for $x=0.28$ and 0.36 upon annealing in vacuum at 900 °C for 3 h, aging in air at room temperature for 5 months, and immersion in water. After annealing at 900 °C, the oxide remains amorphous and the amount of GeO_2 in the oxide stays constant, but some small crystalline precipitates with a lattice constant similar to that of the underlying GeSi layer emerge in the oxide very near the interface for both x . Similar precipitates are also observed after aging for both x . The appearance of these precipitates can be explained by the thermodynamic instability of $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ in contact with $\text{Ge}_x\text{Si}_{1-x}$. In water at RT, 90% of GeO_2 in the oxide is dissolved for $x=0.36$, while the oxide remains conserved for $x=0.28$.

The oxidation of $\text{Ge}_x\text{Si}_{1-x}$ has become of interest recently,¹⁻¹² due to the potential of GeSi devices and the importance of oxidation in device technology. It was initially reported that for GeSi samples of low Ge content ($x < 0.2$) at high temperature ($T > 800$ °C), a pure SiO_2 layer grows with Ge piling up behind it.¹⁻⁶ This resulting pileup of Ge greatly increases the interface trap density of the oxide³ and thus should be avoided if the oxide is to be used in a device environment. At low temperatures ($T < 700$ °C)⁹⁻¹² or for high Ge content,^{7,8} the Si supply at the reaction interface is restricted, and Ge can also be oxidized, resulting in the formation of a GeSi oxide. The oxide that formed at low temperature in wet ambient shows a uniform composition with the same Ge/Si ratio as that of the virgin GeSi epilayer. No pileup of Ge is observed in that case and the oxidation rate is very large.¹² The consequent low thermal budget of this wet oxidation process of GeSi at low temperature is of practical interest.

It has been reported that GeO_2 - SiO_2 glasses can, in the proper composition range, exhibit thermal coefficients of expansion that approximate that of the silicon (approximately $40 \times 10^{-7}/^\circ\text{C}$ between 25 and 700 °C).¹³ The interfacial stress between these two materials, indeed, is less than 10^9 dyn/cm² (almost stress free) over a wide composition range of GeO_2 .¹⁴ The thermal coefficient of expansion of Ge is about $70 \times 10^{-7}/^\circ\text{C}$ in the same temperature range. The $\text{Ge}_x\text{Si}_{1-x}\text{O}_2/\text{Ge}_x\text{Si}_{1-x}$ system therefore exhibits much closer thermal coefficients of expansion between the protective insulating layer and the underlying GeSi film than the $\text{SiO}_2/\text{Ge}_x\text{Si}_{1-x}$ system does. This fact is of interest especially for epitaxial GeSi films, whose metastability might be affected by this interfacial stress. However, the properties of the oxide, and especially its stability, need to be examined since the resulting interface between GeSiO_2 and GeSi is thermodynamically unstable and germanium

oxide is also known to be unstable at high temperature or in moisture. We present here the results of experiments that investigate the stability of $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ on GeSi upon annealing, aging, and exposure to moisture.

The GeSi samples for this study were grown epitaxially onto a (100)Si substrate by molecular beam epitaxy without intentional doping. One sample is a layer of $\text{Ge}_{0.36}\text{Si}_{0.64}$ about 470 nm thick, the other a layer of $\text{Ge}_{0.28}\text{Si}_{0.72}$ about 550 nm thick. Both films are elastically relaxed as determined from x-ray double-crystal diffractometry. Samples from the wafers were oxidized in a tube furnace at 700 °C for 1 h in wet ambient obtained by bubbling nitrogen gas through 95 °C de-ionized water. For the annealing test, the oxidized samples were vacuum annealed at 900 °C for 3 h. Other oxidized samples were stored in air at room temperature for about 5 months. Additional oxidized samples were placed in de-ionized water at room temperature from several hours up to several days. The water sometimes was heated up to the boiling point to accelerate the test. The oxide film of all these samples was investigated by transmission electron microscopy and backscattering spectrometry before and after the tests.

The backscattering spectra for the oxide of $\text{Ge}_{0.36}\text{Si}_{0.64}$ before and after the vacuum annealing test are indistinguishable. The oxide thus remains uniform. No Ge is lost during annealing. Similar results are also found in the case of the oxide of $\text{Ge}_{0.28}\text{Si}_{0.72}$. This is noteworthy because germanium dioxide tends to volatilize at elevated temperatures in the presence of pure germanium due to the following reaction: $\text{GeO}_2 + \text{Ge} = 2 \text{GeO}_{(g)}$.¹⁵ The conservation of Ge in the present case could be due to the absence of pure Ge or the action of the oxide as a protective cap.

Figure 1 shows cross-sectional transmission electron micrographs of the oxide of $\text{Ge}_{0.36}\text{Si}_{0.64}$ after 3 weeks and 5 months aging in air at room temperature and after the

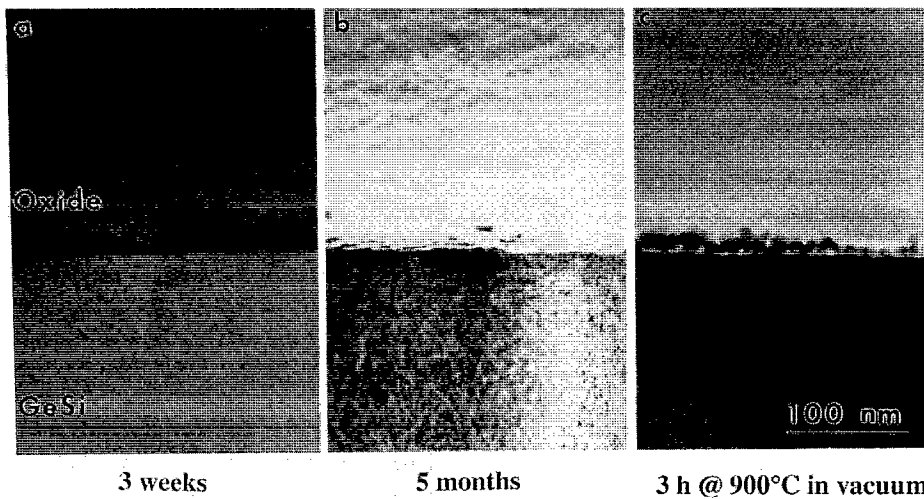


FIG. 1. Cross-sectional transmission electron micrographs of the GeSi oxide/GeSi layer interface for (a) 3 weeks aging in the air, (b) 5 months aging in the air, and (c) 900 °C annealing for 3 h after oxidation.

vacuum annealing test. Small precipitates emerge in the oxide near the interface and alter the initially sharp interface after 5 months aging and 900 °C annealing. In both cases, the precipitates are mostly located at about 10 nm from the interface. The size of the precipitate after 900 °C annealing is generally larger than that after 5 months aging at room temperature. The precipitates are crystalline with a lattice constant similar to that of the underlying GeSi layer, as shown in Fig. 2 which is a high-resolution micrograph of the oxide/GeSi region after 5 months aging. Similar features are also observed in the oxide of $\text{Ge}_{0.28}\text{Si}_{0.72}$ after the vacuum annealing and aging tests. By the lattice

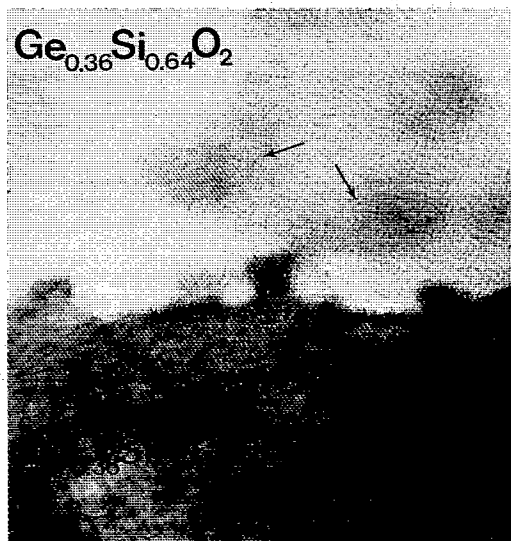


FIG. 2. High-resolution cross-sectional transmission electron micrograph of a $\text{GeSiO}_2/\text{GeSi}$ interfacial region after aging for 5 months in air at room temperature. Crystalline precipitates with a lattice constant similar to that of the underlying GeSi layer are observed near the interface as the arrows indicate.

constants, these precipitates could be elemental Ge, elemental Si, GeSi solution, or even tetragonal GeO_2 , which can grow epitaxially on the (110) plane of germanium.¹⁶ Thermodynamic arguments given as follows predict that these precipitates should be $\text{Ge}_x\text{Si}_{1-x}$ solid solution or in the limit, elemental Ge.

The Ge-Si-O ternary phase diagram calculated by Pain *et al.*¹⁰ has tielines connecting SiO_2 with GeO_2 and $\text{Ge}_x\text{Si}_{1-x}$ at 1000 K. The phase diagram is similar from room temperature to 900 °C according to our calculation similar to Pain *et al.* Any compound below the $\text{GeO}_2\text{-SiO}_2$ tieline, i.e., of composition $\text{Ge}_z\text{Si}_{1-z}\text{O}_{2-\delta}$ ($0 < z < 1$, $0 < \delta < 1$), will decompose into SiO_2 plus GeO_2 and Ge, or into SiO_2 and a $\text{Ge}_y\text{Si}_{1-y}$ solution, depending on what the ratio of $\text{O}/(\text{Ge} + \text{Si})$ is. Cross-sectional transmission electron micrographs show that the interface between the GeSi layer and the Si substrate is still sharp after 900 °C annealing, indicating only insignificant interdiffusion between them. We can thus exclude the Si substrate from the system and only consider the equilibrium between the oxide and the remaining GeSi layer. Thermodynamically, the oxide then becomes unstable when it contacts GeSi, since the ratio of $\text{O}/(\text{Ge} + \text{Si})$ for this system is less than 2 and will locate this system below the $\text{GeO}_2\text{-SiO}_2$ tieline in the Ge-Si-O phase diagram. To explain the particular location of the precipitates seen in Fig. 1 requires a model of the kinetics involved with nucleation and growth. This instability also exists with pure Si and is unacceptable from a device point of view.

The backscattering spectra of the oxide before and after immersion in water at room temperature for one day are shown in Fig. 3. After this test, the oxide remains uniform but with a Ge content that decreases from $\text{Ge}_{0.36}\text{Si}_{0.64}\text{O}_2$ to about $\text{Ge}_{0.05}\text{Si}_{0.95}\text{O}_2$, or equivalently, a reduction from 2000 to 1400 f.u./ cm^2 . This result means that about 90% of the GeO_2 is lost in the water. To see if GeO_2 could be totally dissolved in the water, the sample of Fig. 3

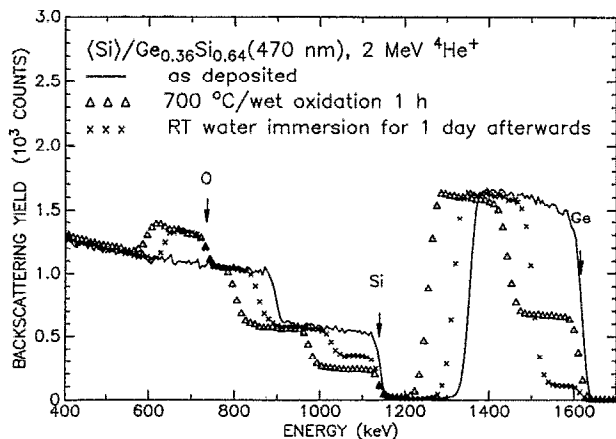


FIG. 3. 2 MeV $^4\text{He}^+$ backscattering spectra of a $\text{Ge}_{0.36}\text{Si}_{0.64}\text{O}_2$ layer before and after immersion in water at room temperature for 1 day (Near-normal radiation beam incidence; the scattering angle of detected particles: 170°).

was placed back in fresh de-ionized water for a week with water being replaced every day. No significant change was observed by backscattering spectrometry. The whole oxide including SiO_2 is totally lost after 16 h immersion in boiling water. For $\text{Ge}_{0.28}\text{Si}_{0.72}\text{O}_2$, the oxide is conserved after immersion in the water at room temperature for one day according to the backscattering spectrometry measurements, indicating the solubility of this oxide in the room temperature water is negligible. However, some amount of GeO_2 is lost after 16 h immersion in boiling water.

Hexagonal GeO_2 is known to be rapidly soluble in water; tetragonal GeO_2 , on the other hand, is only very slightly soluble in the room-temperature water ($<0.005 \text{ g/l}$).¹⁷ According to IR absorption spectrometry, the state of GeO_2 in the present $\text{Ge}_{0.36}\text{Si}_{0.64}\text{O}_2$ and $\text{Ge}_{0.28}\text{Si}_{0.72}\text{O}_2$ films is mostly in the form of tetrahedrally coordinated $[\text{GeO}_4]$ germanium (hexagonal form of GeO_2) with a little octahedrally coordinated $[\text{GeO}_6]$ germanium (tetragonal form of GeO_2).¹² By that observation, most of the GeO_2 should be water soluble in both cases. Most of the GeO_2 in $\text{Ge}_{0.36}\text{Si}_{0.64}\text{O}_2$ is in fact lost upon immersion in water at room temperature. $\text{Ge}_{0.28}\text{Si}_{0.72}\text{O}_2$, however, is completely water insoluble. Similar water tests have been performed on the $\text{GeO}_2\text{-SiO}_2$ glasses deposited on Si substrates by low pressure chemical vapor deposition.¹⁴ They also found a limit (about 20% GeO_2) below which the glasses is water insoluble. To what extent the unequal solubility of $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ films can simply be explained by a tetragonal versus hexagonal coordination of Ge thus remains an open question.

Compositional and structural inhomogeneities can also

affect the solubility. If enough SiO_2 surrounds GeO_2 , it can be shielded from attack by water even though it may be tetrahedrally coordinated. The ratio of $\text{GeO}_2/\text{SiO}_2$ below which the oxide is completely room-temperature water insoluble is then decided by the detailed structure of the oxide, which could be affected by the ways the oxide is formed. The effect of hot water could be to accelerate the breakup of this shielding until the remaining oxide is so porous that even the SiO_2 is eroded away.

The interface between $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ films and $\text{Ge}_x\text{Si}_{1-x}$ or Si is thermodynamically unstable. This instability leads to the formation of GeSi precipitates by a reduction of GeO_2 near the interface. The phenomenon is detectable a few months after the formation of the interface and storage at room temperature meaning that it is a major consideration in any proposed application of $\text{Ge}_x\text{Si}_{1-x}\text{O}_2$ in $\text{Ge}_x\text{Si}_{1-x}$ devices. The mixed oxides are also sensitive to moisture, in ways not explained yet.

This work was supported by the Semiconductor Research Corporation under a coordinated research program between Caltech (92-SJ-100) and UCLA (92-SJ-088). We thank N. M. Abuhadba and Dr. C. Aita at the University of Wisconsin-Milwaukee for IR analyses of some samples. The technical assistance of R. Gorris is also thankfully acknowledged.

- ¹O. W. Holland, C. White, and D. Fathy, *Appl. Phys. Lett.* **51**, 520 (1987).
- ²D. Fathy, O. W. Holland, and C. White, *Appl. Phys. Lett.* **51**, 1337 (1987).
- ³F. K. LeGoues, R. Rosenberg, T. Nguyen, F. Himpsel, and B. S. Meyerson, *J. Appl. Phys.* **65**, 1724 (1989).
- ⁴F. K. LeGoues, R. Rosenberg, and B. S. Meyerson, *Appl. Phys. Lett.* **54**, 644 (1989).
- ⁵D. Nayak, K. Kamjoo, J. C. S. Woo, J. S. Park, and K. L. Wang, *Appl. Phys. Lett.* **56**, 66 (1990).
- ⁶D. K. Nayak, K. Kamjoo, J. S. Park, J. C. S. Woo, and K. L. Wang, *Appl. Phys. Lett.* **57**, 369 (1990).
- ⁷J. Eugene, F. K. LeGoues, V. P. Keason, S. S. Iyer, and F. M. d'Heurle, *Appl. Phys. Lett.* **59**, 78 (1991).
- ⁸H. K. Liou, P. Mei, U. Gennser, and E. S. Yang, *Appl. Phys. Lett.* **59**, 1200 (1991).
- ⁹W. S. Liu, G. Bai, M.-A. Nicolet, C. H. Chern, V. Arbet, and K. L. Wang, *Mater. Res. Soc. Symp. Proc.* **220**, 259 (1991).
- ¹⁰D. C. Pain, C. Caragianis, and A. F. Schwartzman, *J. Appl. Phys.* **70**, 5076 (1991).
- ¹¹W. S. Liu, E. W. Lee, M.-A. Nicolet, V. Arbet-Engels, and K. L. Wang, *J. Appl. Phys.* **71**, 3626 (1992).
- ¹²W. S. Liu, E. W. Lee, M.-A. Nicolet, V. Arbet-Engels, K. L. Wang, N. M. Abuhadba, and C. R. Aita, *J. Appl. Phys.* **71**, 4015 (1992).
- ¹³R. H. Dalton, U.S. Patent No. 3,542,572 (1970).
- ¹⁴S. Rastani and A. Reisman, *J. Electrochem. Soc.* **137**, 1288 (1990).
- ¹⁵J. T. Law and P. S. Meigs, *J. Electrochem. Soc.* **104**, 154 (1957).
- ¹⁶Pieter Stroeve, *Integrated Circuits: Chemical and Physical Processing* (American Chemical Society, Washington, DC, 1985), p. 213.
- ¹⁷W. A. Albers, Jr., E. W. Valyocsik, and P. V. Mohan, *J. Electrochem. Soc.* **113**, 196 (1966).