# Synaptic dynamics in complex self-assembled nanoparticle networks

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#### Abstract

We report a detailed study of neuromorphic switching behaviour in inherently complex percolating networks of self-assembled metal nanoparticles. We show that variation of the strength and duration of the electric field applied to this network of synapse-like atomic switches allows us to control the switching dynamics. Switching is observed for voltages above a well-defined threshold, with higher voltages leading to increased switching rates. We demonstrate two behavioral archetypes and show how the switching dynamics change as a function of duration and amplitude of the voltage stimulus. We show that the state of each synapse can influence the activity of the other synapses, leading to complex switching dynamics. We further demonstrate the influence of the morphology of the network on the measured device properties, and the constraints imposed by the overall network conductance. The correlated switching dynamics, device stability over long periods, and the simplicity of the device fabrication provide an attractive pathway to practical implementation of on-chip neuromorphic computing.

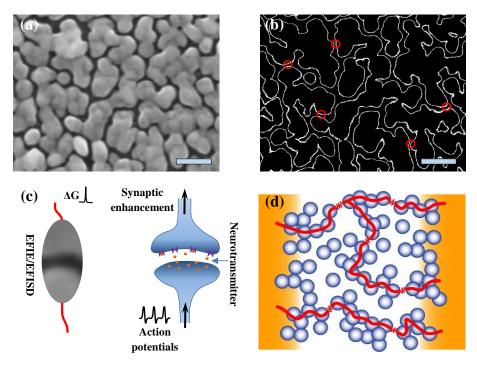
#### 1 Introduction

The quest to replicate the most advanced computer, i.e. the biological brain, has led to immense advancements in software and hardware emulation of the organic neuronal and synaptic structures.<sup>1–3</sup> The central concepts are that nanoscale switching elements, whose state (resistance) reflects the history of their inputs (hence the name 'memristors')<sup>4</sup> can be used to emulate the functions of biological synapses, and that networks of such elements could potentially replicate some of the functions of the brain.<sup>5–11</sup> Recent work has shown that memristor networks can potentially be used for many tasks, ranging from simple pattern recognition and associative learning<sup>12,13</sup> to more complex applications requiring greater functionality.<sup>6–8,11</sup>

A wide variety of types of memristive element<sup>14–16</sup> and types of network<sup>9–11</sup> have been proposed, each of which has strengths and weaknesses. Highly organised arrays of memristors<sup>7,17,18</sup> can take advantage of established lithography techniques, but suffer from the same cost and

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**Figure 1** (a) Scanning electron micrograph of a typical part of the nanoparticle network poised near percolation threshold. (b) Corresponding edge-marking emphasizing the percolating nature of the conduction paths along with the spatially distributed tunnel gaps, which are potential atomic-wire formation sites. Some are highlighted by red circles. The scale bars are 100 nm. (c) On receiving requisite action-potential a biological synapse has a finite probability for enhanced synaptic connection. Similarly voltage-stimulus on individual atomic-switch synapses results in atomic movements in the tunnel gap via electric-field-induced-evaporation (EFIE)/ electric-field-induced-surface-diffusion (EFISD) processes. There is a finite probability of atomic-wire formation in the tunnel-gap with an associated increase in conductance (measured as  $\Delta$ G). (d) Such synaptic reconfigurations of atomic-wires results in dynamical activity involving multitudes of interconnected signal pathways. Only a few representative pathways between the electrodes are depicted here.

fabrication limitations as traditional complementary metal-oxide-semiconductor (CMOS) devices.<sup>19,20</sup> Self-organised networks<sup>10,21</sup> might provide similar functionality using low-cost fabrication techniques as well as genuinely neuromorphic *structures* that have the built-in complexity that is believed to be important for brain-like functionality.<sup>22,23</sup>

Here we focus on networks of metallic nanoparticles which, when poised near the percolation threshold, exhibit complex switching behaviour due to the formation and annihilation of atomic scale wires within tunnel gaps in the network<sup>24</sup>. Switching events that cause increases (decreases) in device conductance  $G_{\uparrow}(G_{\downarrow})$  are understood<sup>24,25</sup> to be due to electric-field-induced formation<sup>26</sup> (electromigration-induced annihilation<sup>27</sup>) of atomic-wires in tunnel gaps within the network. These atomic-switches act as inorganic-synapses and multiple such switching events (synaptic reconfigurations) in different locations lead to complex switching dynamics. It has recently been shown that these synaptic networks can be fabricated such that devices work reproducibly for several months.<sup>28</sup>

Since the networks are complex, it is intrinsically difficult to separate out the switching of

the individual elements from the observed changes of the network as a whole. Here we apply controlled voltage pulses and characterize the resultant network dynamics. We show that there is a well-defined threshold voltage ( $V_T$ ) for the onset of switching, and that the atomic wire formation processes occur effectively instantaneously on the timescale of our existing measurement system. Synaptic reconfiguration with voltage stimulus close to  $V_T$ , where wire breaking processes occur on longer time scales and individual switching events can be resolved, simplifies the data analysis. The formation and annihilation of atomic-wires in individual synapses appear to be temporally stochastic, which is believed to be advantageous for some neuromorphic computation paradigms<sup>16,29</sup>. Each synapse is inter-connected with the complex network and we show here preliminary evidence suggesting heavy tailed distributions of inter-event-intervals (times between switching events), which are characteristic of correlations.<sup>9,30</sup> We also show that the morphology of the network controls its resistance and therefore shifts the measured conductance away from the quantised conductance values expected for individual atomic scale wires.<sup>24,31</sup> Finally we outline the prospects for further device and applications development.

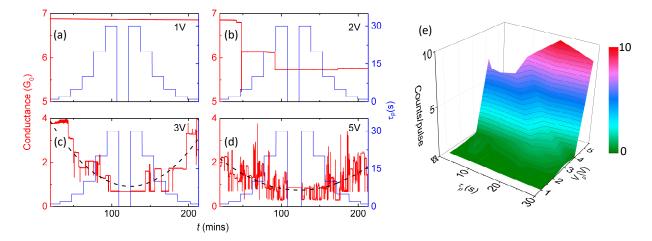
## 2 Experimental

Our devices are fabricated using simple cluster deposition techniques that have been described in detail previously<sup>28,32</sup>. Briefly, tin clusters (~ 8.5 nm) are deposited between Au contacts (100  $\mu$ m separation) in a controlled atmosphere containing a small amount of air ( $P_{dep} \sim 1 \times 10^{-5}$ Torr) with high relative humidity,<sup>28</sup> resulting in the morphology shown in Fig. 1 (a). Deposition is terminated when the device resistance reaches ~2k $\Omega$  ( $G \sim 6G_0$ , where  $G_0 = 2e^2/h$  is the quantum of conduction<sup>33,34</sup>).

## 3 Results I: Network dynamics

## 3.1 Synaptic network of atomic-switches

Near the percolation threshold, networks of self-assembled nanoparticles contain a multitude of interconnected atomic-switches.<sup>21,24,28</sup> Fig. 1 (a) shows a typical scanning electron micrograph (SEM) of such a network with percolating pathways. The corresponding edge marking in Fig. 1 (b) reveals the presence of gaps in these pathways, some of which are indicated by red-circles. These are potential zones for atomic-wire formation (and annihilation) which can be modelled<sup>14</sup> as inorganic counterparts of the organic synapses in the biological brain. The arrival of an appropriate action potential on a biological synapse has a finite probability of enhancing the synaptic connection in Fig. 1(c). In our inorganic-synapse (atomic-switch), the voltage-pulse stimulus can initiate electric-field induced atomic-rearrangements leading to formation of an atomic-wire bridging the gap, and thus enhancing the overall device conductance. Continuous current flow through these wires can reset the synapse via electromigration. The effect of individual synaptic reconfigurations on the measured device conductance i.e.  $G_{\uparrow}(G_{\downarrow})$  is dependent upon the spatial as well as temporal configuration, i.e. the position of the synapse in the network. A very simplistic depiction of such a network is shown in Fig. 1 (d). This complex synaptic network could be imagined as a dynamic reservoir<sup>12,35,36</sup> of interconnected and interdependent synaptic 'weights'. Reservoir computing (RC) using such atomic switch networks is believed to be a powerful tool for a range of problems including pattern recognition<sup>10,28,35,37</sup> and various waveform regression tasks<sup>38</sup>.

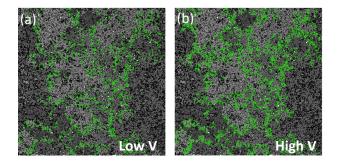


**Figure 2** Pulse width  $\tau_p$  and voltage  $V_p$  dependence of the switching behaviour. (a - d) Standardized pulse sequence ( $\tau_p$  from 2 - 30s) with  $V_p = 1, 2, 3$  and 5V. (e) Summary plot of the switching rate as a function of both  $\tau_p$  and voltage  $V_p$ . Note the dramatic increase in switching rate with increasing voltage.

#### 3.2 Network activity and event-rates

Excitation of the complex network with voltage pulses initiates synaptic reconfigurations as shown in Fig. 2. The variation of conductance of a typical device in response to our standard pulse sequence (pulse widths  $\tau_p$  from 2s to 30s and back to 2s) and when the pulse amplitude  $V_p$  is increased is shown in Figs. 2(a) to (d). The 1V and 2V pulses result in few switching events, whereas voltages above  $V_T \sim 3V$  lead to a characteristic dependence on pulse width (parabolae are guides to the eye) in which the long pulses result in a lower average *G*. There are absolutely no switching events for read voltages [ $\sim 100$  mV, centre regions in Fig. 2(a-d)]: this stability for sub-threshold voltages ( $V_p < V_T$ ) has been verified for extended periods (weeks) of time.<sup>28</sup> Every time a synapse connects/disconnects the electric field and current distribution is rearranged and subsequent synaptic activity is dependent upon the modified network state. As  $V_p$  is increased beyond  $V_T$  switching becomes more active and the devices reach a state in which the average conductance is  $\sim 2G_0$ . Active switching around this conductance value can be observed for many months.<sup>28</sup> Fig. 2(e) summarizes the dependence of the number of events/pulse as a function of  $\tau_p$  and  $V_p$ , and emphasises the dramatic increase in activity of the network for stronger excitation pulses (higher  $V_p$ ).

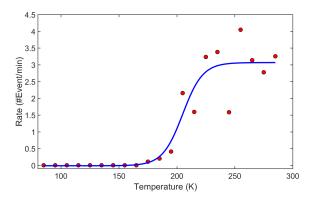
This enhanced device activity (higher rate of events) could be due to either increased activity of the *same synapses* or activation of *new previously inactive synapses*. To better understand this, results from numerical simulations<sup>21</sup> are shown in Fig. 3. Similar to experimental measurements, the voltage stimulus is applied on the left side of the network with right side being grounded. Various groups of ohmically connected particles are connected via the active synapses indicated by green markers (i.e. atomic-switches that reconfigured). As can be seen in Fig. 3(b) the overall density of the active synapses increases at higher voltage which suggests that the experimental observation of increased event-rates at higher voltages can be attributed, at least partially, to the activation of additional synapses. This can be understood intuitively as being due to the increased electric field in tunnel junctions in which the field was previously below a minimum value required to cause atomic motion. In the next section we explore the synaptic dynamics using temperature dependent measurements.



**Figure 3** Numerical simulation of network activity in a  $200 \times 200$  network (measured in units of particle size) at low and high voltages. The green markers denote the atomic-switches that reconfigured (active synapses) between the gray shaded ohmically connected groups of nanoparticles. The details about the simulations are discussed elsewhere<sup>21</sup>. The increased density as well as spatial distribution of the active synapses at higher voltages (b) indicates activation of previously inactive synapses when low-voltage was applied (a).

#### 3.2.1 Temperature dependence of event rates

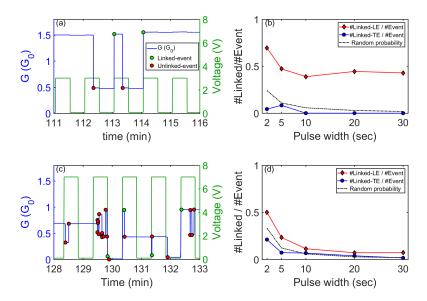
The synaptic activity can be arrested by decreasing the device temperature (*T*). As shown in Fig. 4 lowering the temperature below  $T_{th} \sim 200$ K, results in no switching events being observed even when 8V pulses are applied. Application of different voltage pulse amplitudes and pulsewidths (not shown here) leads to similar results. This demonstrates that the typical energy scale necessary for atomic-reconfiguration is  $\sim 17$  meV. This energy scale is very similar to the Debye temperature of Sn<sup>39</sup>. Below 200K the atomic motion is 'frozen out', effectively preventing diffusion of adatoms in the tunnel gaps and eliminating the atomic-wire formation processes.



**Figure 4** Typical temperature variation of the switching activity, with 8V pulses ( $\tau_p = 10s$ ), shows that there is no switching activity below T~200K. Switching is activated around 200K and the event rates saturate after 250K. The event rates are averaged every 10K and the solid line is sigmoid-fit.

#### 3.3 Threshold switching: archetype-A

We now explore the switching dynamics in the near threshold regime ( $V \sim V_{th}$ ) where the number of synaptic events is minimized and thus the switching behaviour simplifies. Any voltage pulse stimulus consists of four components i.e. a leading-edge (LE) where voltage increases from off (read voltage 0.1V) to on, trailing-edge (TE) when V decreases from on to off, and on and off regions. Fig. 5(a) presents a typical snapshot example of a G(t) trace with  $V_p$  close to but above  $V_T$ . The green markers label events at the pulse-edges as "linked-events" and the red



**Figure 5** (a, c) Typical snapshot of G(t) traces depicting the events linked and unlinked with the voltage pulse edges for both archetype-A and -B i.e. at low and high-voltages respectively. The green dots mark the linked-events at pulse-edges and red dots mark unlinked-events. (b,d) The pulse-width dependence of the fraction of linked-events that occur at leading (LE) and trailing (TE) pulse edges. Only the LE linked-events are observed to occur more frequently than would be expected from random statistics. The linked-events at the LE are predominantly up-events, e.g. at  $\tau_p$  = 30s for archetype A, 92% linked-events on LE are G<sup>↑</sup>.

markers denote events in the on region as "unlinked-events". As can be seen in Fig. 5(a) for V  $\sim V_{th}$  the  $G_{\uparrow}$  events occur almost exclusively at the leading edges of the pulses, showing that the electric-field-induced formation of atomic scale wires occurs on a timescale that is effectively instantaneous<sup>1</sup>. In contrast, all  $G_{\downarrow}$  events are unlinked, and occur after several seconds of current flow, consistent with the idea that electromigration breaks the atomic scale wires to reset the synapse. We identify this behavior as archetype-A.

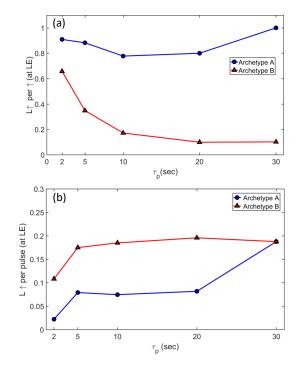
Analysis of the switching events over many measurement cycles shows the  $G_{\uparrow}$  events are predominantly observed at the leading-edge (LE) of the pulse [Fig. 5(b)]. The fraction of events that are linked at the LE (red-diamonds), as a function of the pulse width  $\tau_p$ , is much higher than possible from a random distribution of events (dotted-line). This indicates that a certain proportion of the tunnel gaps in the network respond instantaneously to the applied electric field. In contrast the number of linked-events at the TE is consistent with random occurrence, i.e. these events occur at the TE simply by chance, they are not driven by the changing electric field.

#### 3.4 $V > V_{th}$ : archetype-B

At higher voltages i.e.  $V > V_{th}$  the archetype-A behavior evolves into more complex archetype-B behavior. Fig. 5(c) depicts a typical G(t) snapshot and shows that, in addition to the linked-up events at LE, additional unlinked events (U $\downarrow$  and U $\uparrow$ ) occur. This behaviour is consistent for all  $\tau_p$  and across devices. Similar to archetype-A, the linked-events occur at the LE, rather than the TE.

Fig. 6(a) shows the variation of the number of linked-up (L<sup>↑</sup>) events at the LE as a fraction of

<sup>&</sup>lt;sup>1</sup>Ref.<sup>40</sup> shows that switching can take place on timescales of nanoseconds.

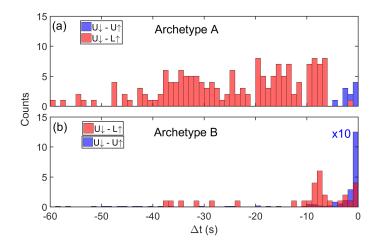


**Figure 6** (a) Variation of number of linked-up (L $\uparrow$ ) events at LE as a fraction of total  $\uparrow$  events ( $\uparrow = L \uparrow + U \uparrow$ ) for both archetypes. In archetype-A, L $\uparrow / \uparrow$  is nearly 1 indicating almost all  $\uparrow$  events occur as L $\uparrow$ . In archetype-B the L $\uparrow / \uparrow$  is nearly 0.7 at  $\tau_p = 2$ s and decreases as a function of  $\tau_p$ . This decrease in L $\uparrow / \uparrow$  for archetype-B is due to additional U $\uparrow$  events (in pairs of U $\uparrow$ -U $\downarrow$ ) events at higher voltages and longer  $\tau_p$ . (b) The fraction of LE induced L $\uparrow$  events (L $\uparrow /$  pulse) is in the range around 10-20% for both archetypes at all  $\tau_p$  (except low 2s).

the total  $\uparrow$  events ( $\uparrow = L \uparrow +U \uparrow$ ). For archetype-A,  $L\uparrow /\uparrow$  is nearly 1 i.e. 100%, with a negligible fraction of U $\uparrow$  for all  $\tau_p$ . In contrast, in archetype-B, the ratio  $L\uparrow /\uparrow$  decreases with longer  $\tau_p$  e.g. at  $\tau_p = 30 \approx 10\% \uparrow$  events are L $\uparrow$ , with  $\sim 90\%$  being U $\uparrow$  events. This is because at higher voltages and longer  $\tau_p$ , the increased switching activity results in additional U $\uparrow$  events. The number of linked-events per pulse on the other hand remains similar for both archetypes (around 10–20%), as is depicted in Fig. 6(b). Therefore at all voltages, a similar fraction of the LE induce L $\uparrow$  events.

The event-structure associated with both L<sup>↑</sup> and U<sup>↑</sup> events across both archetypes is shown in Fig. 7. As can be seen in Fig. 7(a), for archetype-A the L<sup>↑</sup> events are predominantly (75%) preceded by a U<sup>↓</sup> event. The wide distribution of times  $\Delta t$  between U<sup>↓</sup> and L<sup>↑</sup> events is understood to originate from the distribution of electric-field strengths in the gaps in the percolating network<sup>41</sup>. The electric field induced formation of atomic wire closes the tunnel gap as shown in Fig. 7 of Ref.<sup>28</sup>, on time-scales dependent upon both the electric field strength and the position of the synapse in the network. We believe this is similar to an integrate and fire process.<sup>42</sup>

In contrast, for archetype-B the U $\uparrow$  events are more common than L $\uparrow$ , and U $\uparrow$  are also predominantly preceded by a U $\downarrow$  with a short  $\Delta t$ . The narrower distribution of times between the events is due to the higher applied voltage in conjunction with the larger number of active synaptic sites in network. This means that when any one synapse changes state it is likely to cause a change in the state of other synapses, leading to multitude of synapses reconfiguring. Reconfiguration of these interdependent spatially separated synapses via the complex network corresponds to the existence of spatial correlations.



**Figure 7** The event-structures associated with linked-up (L $\uparrow$ ) and unlinked-up (U $\uparrow$ ) events for archetype-A and archetype-B. (a) archetype-A shows predominantly (75%) of the L $\uparrow$  events are preceded by U $\downarrow$  events with few U $\downarrow$ -U $\uparrow$  pairs. (b) The situation for archetype-B is different with dominance of U $\uparrow$  (note 10× scale change), typically preceded by an U $\downarrow$ . A much smaller number of L $\uparrow$  events are observed, also typically preceded by U $\downarrow$ .

#### 3.5 Synaptic pathways accessible in archetype A&B

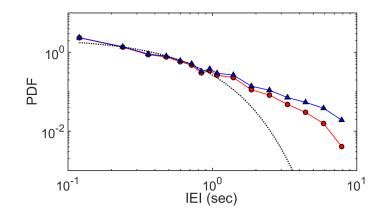
As stated in the introduction, all the synaptic elements in the network operate via application of local electric-field (and currents) and are fundamentally similar in character. The relative contribution from individual synapses to the conductance of the network, on the other hand, depends upon their spatial position in the percolating network and thus also determines the network dynamics.

In archetype-A, i.e. at low voltages, the accessible network is limited to a small number of "primary" conduction pathways connecting the two electrodes. The applied voltage drops across gaps in these pathways and generates high local electric fields. Thus a small number of available synapses are able to rapidly form atomic-wires resulting in L<sup>↑</sup> events at the LE (Fig. 6), with large  $\Delta$ G [see Fig. 10 and associated discussion]. In archetype-B, the synapses on the primary-pathways can still be reconfigured, but the higher voltages also activate additional network-branches. These "higher order" branches make a smaller contribution towards the overall network conductance and synaptic events on them therefore cause a smaller measured  $\Delta$ G.

#### 3.6 Temporal correlations

To be an effective dynamical reservoir, for example for RC applications, a network must exhibit temporal correlations in addition to the spatial correlations discussed in the previous section.<sup>9,35,36</sup> Indeed it is believed that spatio-temporal correlations are a feature of the biological brain and are a requirement of neuromorphic network architectures in general.<sup>9,23</sup> Distributions of random, uncorrelated events have statistical distributions of inter-event intervals (IEIs) which decay exponentially. Conversely, correlated non-Poisson processes, where the temporal occurrence of an event depends on the occurrence of previous events, can result in heavy-tailed IEI distributions.<sup>30,43</sup>

A probability density function (PDF) of IEIs for a typical device is shown in Fig. 8. As previously mentioned, there are no synaptic reconfigurations during the off-pulse and so events with IEIs  $\geq \tau_p$  are impossible. The raw IEI distribution (red) in Fig. 8 therefore shows strong downward curvature, and at first sight might be mistaken for an exponential decay. However, the



**Figure 8** Distribution of inter-event intervals (IEIs) from the device discussed in Figs. 5-7. The raw IEI probability distribution (red circle) shows strong downward curvature at IEI values close to the pulse duration ( $\tau_p = 10$ s,  $V_p = 8$ V) due to the effect of the limited pulse length on long-time correlations. In principle, these effects can be corrected for using the correction factor  $1/(\tau_p - IEI)$  which was applied for  $\tau_p = 10$  s (green triangles). Once this effect is removed, the IEI distribution appears to show a heavier-tail, suggesting temporal correlations.

dotted line shows clearly that the best fit exponential does not have a sufficiently long tail to account for the experimental distribution: the tail of the experimental distribution is truncated by the finite pulse length. A straightforward calculation leads to a correction factor of  $1/(\tau_p - IEI)$ that can be applied to remove this effect (blue,  $\tau_p = 10$ s). Even after this correction, the distribution is still slightly curved, and it is not possible to identify the precise functional form of the data.<sup>44</sup> Nevertheless the distribution is clearly more heavy-tailed than the exponential distribution, providing preliminary evidence of the temporally correlated network activity. Future experiments will explore the spatio-temporal dynamics in the network.

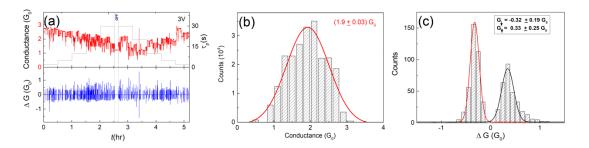
## 4 Results II: Network Morphology

We now discuss the interdependence of the synaptic activity of individual switches and the network morphology.

#### 4.1 Interrelationship between individual synapses and the network

Detailed analysis of archetype-A switching behavior ( $V_p = 3V$ ) for a different device is shown in Fig. 9. This data is similar to that shown in Fig. 2, and indeed all devices<sup>28</sup> show similar behaviour. In Fig. 9(a), as in Fig. 2(c, d), longer  $\tau_p$  leads to lower conductance since the electromigration induced disconnection of atomic wires dominates over the field-induced reconnection. Figs. 9(b) and (c) show that histograms of *G* and  $\Delta G$  for each event are strongly peaked. Importantly, the relatively narrow distributions indicate that the *individual* switching elements accessible in archetype-A exhibit well-defined changes in conductance, consistent with the formation of atomic scale wires<sup>24</sup> [if the switching elements have a large range of conductances, the resulting changes in conductance of the network must be broadly distributed]. The non-quantised measured values of  $\Delta G$  can be explained by the series resistance of the percolating film that surrounds each atomic scale wire – see further discussion in Section 4.2 below.

Fig. 10(a) shows separate histograms of the  $\Delta G$  data for representative  $\tau_p$ .  $G_{\downarrow}$  events exhibit peaks for  $\Delta G \sim -0.3G_0$  whereas  $G_{\uparrow}$  events are peaked at  $\Delta G \sim 0.3G_0$  only for small pulse widths. For longer pulse widths there are a significant number of large  $G_{\uparrow}$  events and the histogram is



**Figure 9** (a) Switching in response to a sequence of square voltage pulses with fixed  $V_p = 3V$  (just above  $V_T$ ) and variable  $\tau_p$  (2 – 30s). The conductance remains unaltered for read voltages of 0.1 V as seen in the flat section (OFF) in middle of the sequence. Bottom panel shows  $\Delta G$  for these switching events. (b) Histogram of the measured initial conductance for each switching event shows a broad peak around  $\sim 2G_0$ . (c) Histogram of sizes of switching events indicates a clear preference for  $\Delta G = \pm 0.3G_0$ . The solid lines in (b) and (c) are Gaussian fits to the distributions.

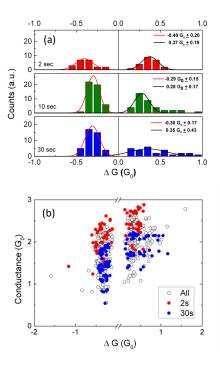
not strongly peaked. This behaviour can be understood as being the result of the breaking of multiple atomic scale wires during long pulses due to electromigration. This means that when the next pulse is applied there are more synapses available for formation of new wires and larger  $\Delta G$  are possible. Fig. 10(b) shows that  $\Delta G$  is not correlated with *G* for these switching events, consistent with switching events of the same size (i.e. quantised events) taking place at multiple sites in the complex network.

#### 4.2 Measured conductance changes and device morphology

In the present devices long-term stability of the switching behaviour was achieved by oxidation during deposition<sup>28</sup>, and so the resistance of the paths through the network is non-negligible. In contrast, in Ref.<sup>24</sup> the emphasis was on achieving devices that exhibit large switching events (large  $\Delta G$ ) and the observation of quantised resistance ( $G = NG_0$ ), which required a low series resistance. If an atomic scale wire is formed somewhere within a percolating resistive network, the rest of the network can be approximated as an equivalent circuit comprised of a series conductance  $\alpha G_0$  and a parallel conductance  $\beta G_0$  [ $\alpha$  and  $\beta$  could be any real numbers, whereas N is an integer; all conductances are in units of  $G_0$ ]. It then requires only a little algebra to show that the total conductance of the network is  $G = \alpha \beta/(\alpha + \beta)$  and the change in conductance is  $\Delta G = \alpha^2 N/[(\alpha + \beta)(\alpha + \beta + N)]$ .

Fig. 9 shows that, for this particular device and archetype-A behavior near  $V_{th}$ ,  $\Delta G \sim 1/3$  and  $G \sim 2$ . Although the positions of the peaks in the histograms of  $\Delta G$  and G vary to some extent from device to device, such well defined peaks are commonly observed for archetype-A. These peaks are visible because the voltage (corresponding to an electric field in the gaps in the network) is only sufficient to activate a small number of switching sites (synapses). Higher voltages activate more switching sites (Figs. 2(e)) and histograms of  $\Delta G$  exhibit power law behaviour as complex inter-dependent pathways through the network are activated – this will be discussed in detail elsewhere.

The results in Fig. 9 put strong constraints on the values of ( $\alpha$ ,  $\beta$ , N) that can provide solutions of the above equations. We find numerically that when N is restricted to physically reasonable<sup>45</sup> values less than 10, the values of  $\alpha$  and  $\beta$  that meet these constraints are limited to the range  $2 < \alpha < 5.5$  and  $\beta < 10$ . These solutions indicate that in order to achieve the



**Figure 10** (a) Histograms of switching events for different  $\tau_p$ . For shorter pulse widths the majority of events are centered around  $\pm 0.3G_0$ . At higher pulse widths ( $\tau_p \sim 20-30$ s) the efficient breaking of connections allows an increase in the number of large  $G_{\uparrow}$  events. (b) Interdependence of  $\Delta G$  and G with open circles representing all events and red and blue filled symbols representing the subset of the data with  $\tau_p = 2$ s and 30s respectively. The x-axis break indicates that events with  $\Delta G > 0.1G_0$  are included in the analysis.

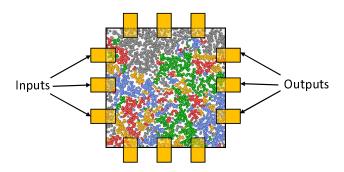
experimental results the parallel conductance cannot be too high, and the series conductance is restricted to a relatively narrow range. The range of  $\alpha$  corresponds to series resistances  $\sim$ 2 - 6 k $\Omega$ , while the parallel resistance must be bigger than  $\sim$  1 k $\Omega$ . The measured resistances (1-10k $\Omega$ ) of the percolating networks that exhibit switching effects are consistent with these ranges.

 $\alpha$  and  $\beta$  represent the electrical properties of the percolating network and so it follows that the morphology of the actual device must be such that it provides the required series and parallel resistances. The sizes of the nanoparticles and morphology of the network are controlled by coalescence during the deposition process<sup>28</sup>, and in light of this analysis it is clear that one of the functions of the deliberate oxidation process during deposition of the nanoparticles is to constrain the morphology of the network so as to allow observation of consistent switching behaviour.

## 5 Discussion and Conclusions

Our films of nanoparticles exhibit complex patterns of electrical switching behaviour at sites corresponding to tunnel gaps within the percolating network ('synapses'). Voltage pulses were used to control formation and annihilation of atomic scale wires at these sites, thereby modifying the device conductance. Shorter (longer) voltage pulses bias the network towards higher (lower) conductance as more (fewer) connections are formed across the network.

Even in the low  $V_p$  regime, switching at one site is influenced by the states of the other



**Figure 11** Schematic of the percolating network comprising of many groups of particles separated by tunnelling gaps surrounded by an array of contacts/electrodes which can be designated as input or output terminals depending on the computation task at hand.

synapses. This provides a first indication of the possibility that these percolating networks might exhibit the spatio-temporal correlations required for neuromorphic computing. As the applied voltage is increased, the switching dynamics become more complex because more synapses in the network are activated. We have explored the inter-relationships between the switching events that cause increases and decreases in conductance, and have shown that measured distributions of inter-event intervals are suggestive of the heavy-tailed distributions that are believed to reflect correlated behaviour.<sup>30,43</sup>

## 5.1 Applications

The atomic scale processes appear to be similar to those that occur in Ag/Ag<sub>2</sub>S atomic switches, which allow synaptic learning<sup>14</sup> and can potentially function on timescales of < 10ns<sup>40</sup>. The exploitation of this type of synaptic behaviour in self-organised networks is still in its infancy, but complex dynamics and spatio-temporal correlations are essential for development of neuromorphic applications. Such applications include, for example, spike timing dependent plasticity<sup>14</sup>, learning behaviour<sup>8,11,21</sup>, rate and population coding<sup>37</sup>, detection of temporal correlations<sup>16,29</sup> and pattern recognition<sup>5,9,12,46</sup>. It is clearly important now to explore the ultimate limits of the present switching processes and develop multi-contact devices that would allow demonstration of applications such as pattern recognition.

## 5.2 Prospects for further device development

One of the attractive features of the present networks is the simplicity of the device fabrication process<sup>28</sup> and the possibility of straightforward extension to a range of more complex devices. Fig. 11 shows a schematic of the percolating network, comprising many groups of particles separated by tunnelling gaps, surrounded by an array of contacts/electrodes which can be designated as input or output terminals. In order to be able to implement (for example) RC we envisage that initially the output signals will be recorded and linear regression tasks performed in software, but that in a future generation of devices the electrodes will provide connections between the percolating network and CMOS circuitry on the same chip, allowing implementation of the required algorithms in hardware. Various other configurations are of course possible where contacts can be used as gate electrodes and used to modulate the percolating network. This could be equivalent to modifying the synaptic weights in the interior of an artificial neural network.

## **Conflicts of interest**

There are no conflicts to declare.

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