

# On the role of polar molecules and the barrier for charge injection in OLEDs

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## ABSTRACT

Many electron transport layer materials (ETL) employed in state of the art organic light emitting diodes (OLEDs) are known to be polar. We combine for the first time simulations and electrical characterization of OLEDs based on polar ETL, in order to understand the impact of such materials on the device operation. Depending on the orientation of the dipole orientation, simulations predict either a benefit or a disadvantage of the polar ETL for the device performance. We also show that OLEDs featuring a polar material are perfectly suited for extracting mobility activation energy and Injection barrier from the anode to the ETL.

**Keywords:** Polar materials, OLEDs, simulation, characterization, injection barrier.

# 1. INTRODUCTION

OLED technology is gaining ground as next generation device for display and lighting applications. For mobile devices in display and lighting, the power efficiency is one of the most important parameters. Several paths are followed in order to increase the efficiency of OLEDs: both optical (like light scattering for lighting applications [1]) and electrical for example using highly conductive hole transport layers (HTLs) and electron transport layers (ETLs) in order to decrease the operation voltage of the device. ETL materials often show a non-zero permanent molecular dipole moment [2]. When deposited in a thin film stack configuration, this effectively leads to a layer with a positive sheet charge density on one side and a negative one on the other (see figure 1). So far, only experimental studies have been performed to investigate the impact of the polarity on the device operation, and only compact models based on equivalent circuits have been proposed to simulate such devices [2,3]. For the first time we show that the well-established drift-diffusion formalism implemented in SETFOS 4.3 [4] is able to cope with these materials leading to quantitative simulations of OLEDs embedding a polar ETL material. This allows us to both simulate the specific behavior introduced by such polar materials but also to give directions to improve the device operation. In this contribution we will mainly study a traditional bi-layer OLED as shown in figure 1, but similar effects and conclusions can be found for more complex dye-doped multilayer devices [5]. In this contribution we make an extensive use of impedance spectroscopy characterization and modelling. Indeed this technique is known to be highly sensitive to the charges in the device; therefore this characterization technique is well suited to study the effect of polar materials on the device operation.

In section 2 we will introduce the basics of impedance spectroscopy as well as the basics of the mathematical model. In the second part, we will compare the simulation results with experimental characterization of OLEDs embedding a polar ETL material. This allows us to benchmark our approach and to extract some device and materials parameters. Finally, in a last part we will use our model to simulate the potential benefit of the ETL polarity on the IV curve of the device.

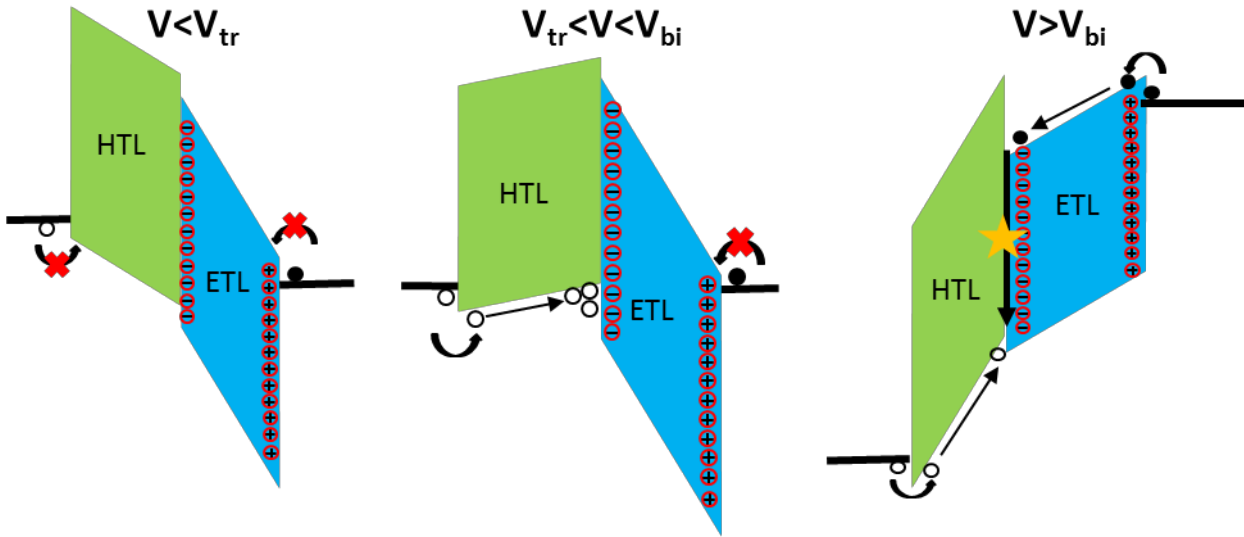


FIG. 1. Schematic band diagram of the bi-layer OLED used in this study. The ETL ( $\text{Alq}_3$ ) layer is supposed to have a molecular polar moment that is not randomly oriented, resulting in sheet charge densities on either side of the layer. This sheet charge density modifies the band diagram.

## 2. BASICS OF CHARACTERIZATION AND SIMULATIONS OF IMPEDANCE SPECTROSCOPY

Impedance spectroscopy is a well-known technique to study OLED device operation. It has been extensively used to characterize the doping concentration [6] or the mobility [7, 8] in organic and inorganic materials, as well as self-heating [9].

In this technique, the device is biased at a constant voltage  $V_0$ , on top of which small sinusoidal voltage ( $V_1$ ) is applied. The total voltage ( $V$ ) applied to the device is then:

$$V = V_0 + V_1 \cdot \sin(2\pi ft). \quad (1)$$

Here  $f$  represents the frequency and  $t$  the time. The basic concept of impedance spectroscopy is that the voltage  $V_1$  is small enough such that the device behaves in a linear regime. Therefore the admittance of the device can be written as:

$$Y = J_{ac}/V_{ac} = G + i\omega C. \quad (2)$$

Where  $J_{ac}$  represents the current flowing out of the device minus the steady state current and  $V_{ac} = V_1 \cdot \exp(i2\pi ft)$

Finally, an equivalent capacitance  $C$  and conductance  $G$  of the device can be calculated. By varying the frequency  $f$ , the capacitance versus frequency of the device can be measured, and by varying the constant voltage  $V_0$  one can measure the well-known C-V response of the device.

In order to simulate the device operation we use the well-established physical drift-diffusion formalism implemented in SETFOS 4.3. It consists of the continuity equation for electrons and holes coupled with the Poisson equation:

$$\text{div} \left( -\mu_n \cdot \mathbf{n} \cdot \vec{E} - \frac{k_b T}{q} \mu_n \cdot \overrightarrow{\text{grad}}(\mathbf{n}) \right) - R = -\frac{\partial \mathbf{n}}{\partial t} \quad (3)$$

$$\text{div} \left( \mu_p \cdot \mathbf{p} \cdot \vec{E} - \frac{k_b T}{q} \mu_p \cdot \overrightarrow{\text{grad}}(\mathbf{p}) \right) - R = -\frac{\partial \mathbf{p}}{\partial t} \quad (4)$$

$$\Delta V = -\frac{q}{\epsilon} (\mathbf{p} - \mathbf{n} + Q). \quad (5)$$

Where  $\mu_{n,p}$  represents the mobility of electrons ( $n$ ) and holes ( $p$ ), respectively,  $V$  the electrostatic potential,  $R$  the Langevin recombination rate and  $Q$  the electrostatic fixed charge. This latter quantity is of primary importance for the simulation of polar materials: as explained previously, the non-zero permanent dipole moment of the molecules associated with a preferred orientation during the deposition process effectively leads to a layer with a positive sheet charge density on one side and a negative one on the other side. In our simulations, these charged layers are modeled by introducing a fixed charge density in the Poisson equation.

Finally in order to simulate impedance spectroscopy, the three variables:  $\mathbf{n}$ ,  $\mathbf{p}$  and  $\mathbf{V}$  are replaced by [10]:

$$n = n_{eq} + \mathbf{n}_{ac} \cdot e^{i2\pi ft} \quad (6)$$

$$p = p_{eq} + \mathbf{p}_{ac} \cdot e^{i2\pi ft} \quad (7)$$

$$V = V_{eq} + \mathbf{V}_{ac} \cdot e^{i2\pi ft} \quad (8)$$

Where the subscript  $_{eq}$  denotes the equilibrium solution in steady state and the subscript  $_{ac}$  denotes the harmonic solution. Then, assuming, that the variables  $n_{ac}$ ,  $p_{ac}$  and  $V_{ac}$  are small enough (which is the basic assumption of impedance spectroscopy), the system is linearized and solved. The quantity  $J_{ac}$ , which represents the harmonic current flowing out of the device, can then be calculated as well as the equivalent admittance of the device. The approach described above has been implemented in the software SETFOS 4.3 [3] and allows for fast and reliable impedance spectroscopy simulations without having to solve the equations in a time domain regime which would be much more time consuming.

### 3. VALIDATION OF THE MODELLING APPROACH

#### 3.1 Capacitance-voltage characteristic

The Capacitance-voltage experiment (C-V) is known to be sensitive to the charge distribution in the device. Previous studies of Brütting et al. [2] have shown that the polar nature of Tris(8-hydroxyquinolino) aluminium ( $\text{Alq}_3$ ) induces a typical signature in the C-V experiment. Indeed, as in [2], and in figure 2 (left) two plateaus can be observed in the C-V plot. Interestingly, the transition voltage ( $V_{tr}$ ), at which the capacitance changes, is directly related to the thickness of the  $\text{Alq}_3$  layer. This transition voltage occurs at negative values of the applied bias, suggesting the presence of mobile charges in the device for voltages below the  $V_{bi}$  (built in voltage  $\approx 2\text{V}$ ). We find that C-V simulations of such devices, using the approach developed in the previous section, can nicely reproduce this experimental observation (figure 2 right). In order to simulate the polar nature of the  $\text{Alq}_3$ , a negative charge sheet density of  $\sigma=1.2\text{mC}/\text{m}^2$  was introduced at the interface between  $\text{Alq}_3$  and TPD and a positive one at the interface with the Calcium electrode, this value is in nice agreement with the one found in [11] using a Kelvin probe measurement.

In order to be sure that the steps in the C-V measurements and simulations (capacitance increase below the turn-on voltage) were a consequence of the polarity of the material, we have run the same simulations (not shown here) assuming, this time, no fixed charges in the device ( $Q=0$ ). As expected, the simulated C-V curves did not show any kinks for negative voltages.

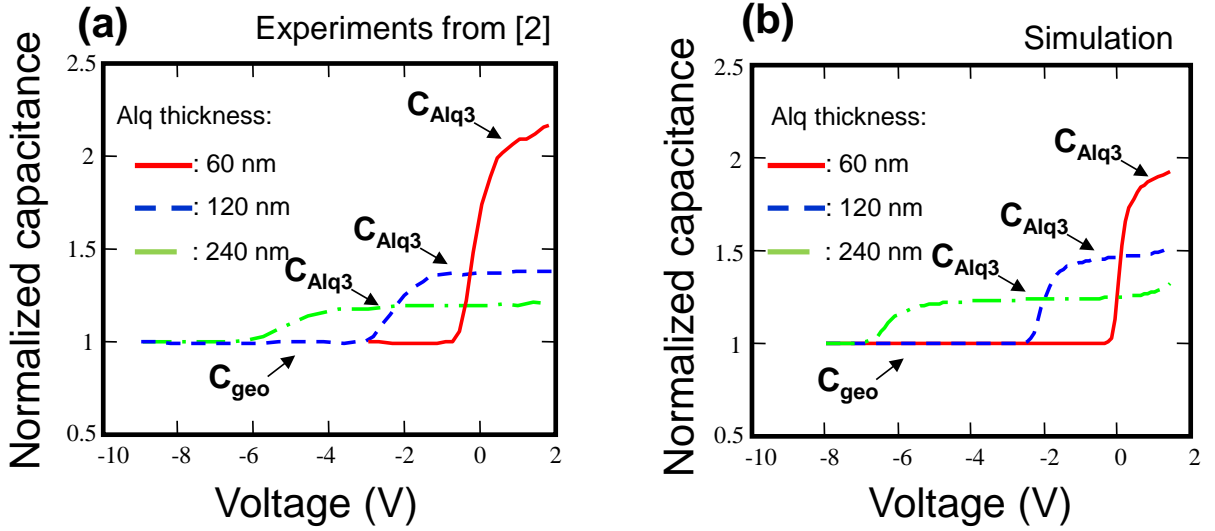
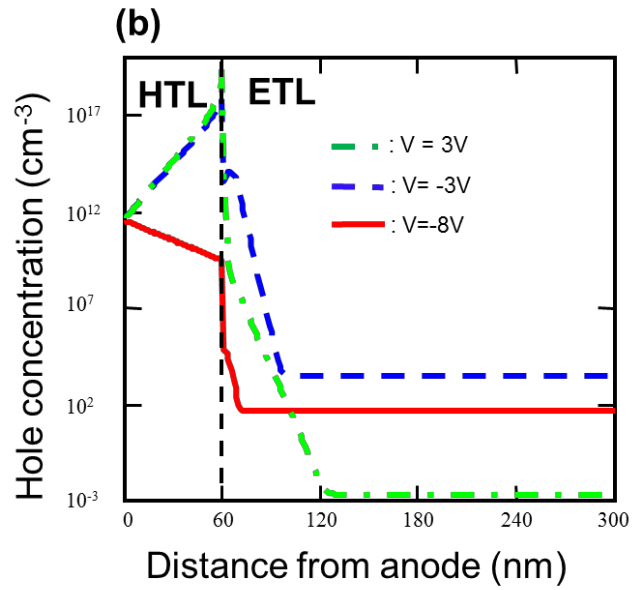
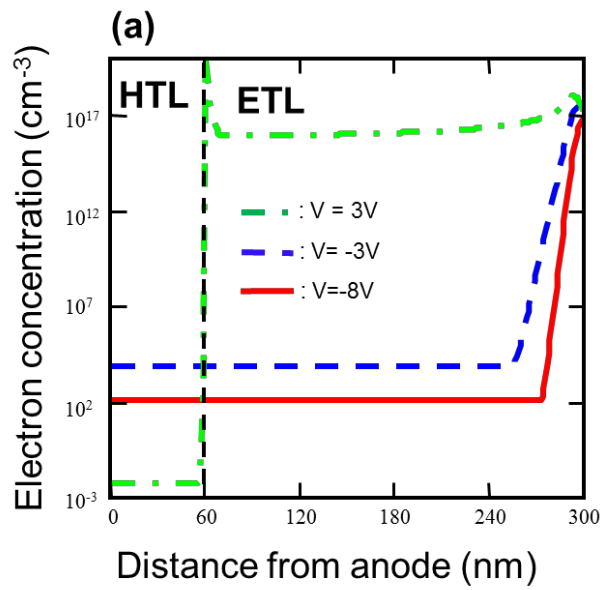


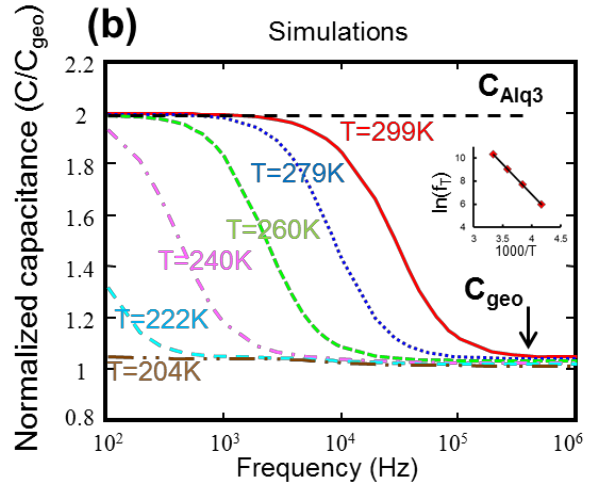
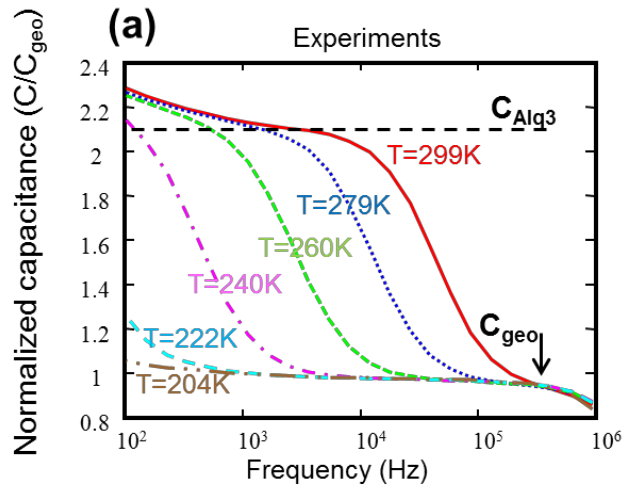
FIG. 2. Measured (left) from [2] and simulated (right) capacitance-voltage curves for OLEDs with different  $\text{Alq}_3$  layer thickness

In order to understand and explain the different regimes observed in the C-V curve we have simulated the hole and electron concentration in the device in three different regimes (Figure 3) namely for  $V < V_{tr}$  (red), for  $V_{tr} < V < V_{bi}$  (green) and for  $V > V_{bi}$  (green). In the following, we chose to simulate a device with a thick ETL (240nm) and with an HTL of 60 nm.

One can see that for  $V = -8\text{V}$  ( $V < V_{tr}$ ) no holes nor electrons are present in the device, the OLED behaves as a perfect insulator, the capacitance that is simulated and characterized is therefore equal to the geometrical capacitance of the full OLED

$$C_{geo} = \frac{\epsilon_{ETL} \cdot \epsilon_{HTL}}{\epsilon_{ETL} \cdot t_{HTL} + \epsilon_{HTL} \cdot t_{ETL}}$$





that the energy barriers holes have to overcome to jump from the anode to the HTL is  $\Delta E_{inj} = \Delta E_{CFT} - \Delta E_{CELIV}$ , where  $\Delta E_{inj}$  represents the injection barrier,  $\Delta E_{CFT}$  the activation energy extracted from the capacitance frequency experiment and  $\Delta E_{CELIV}$  the activation energy extracted from the CELIV experiment (i. e. the temperature activation of the mobility). The injection barrier was finally found equal to 0.25 eV.

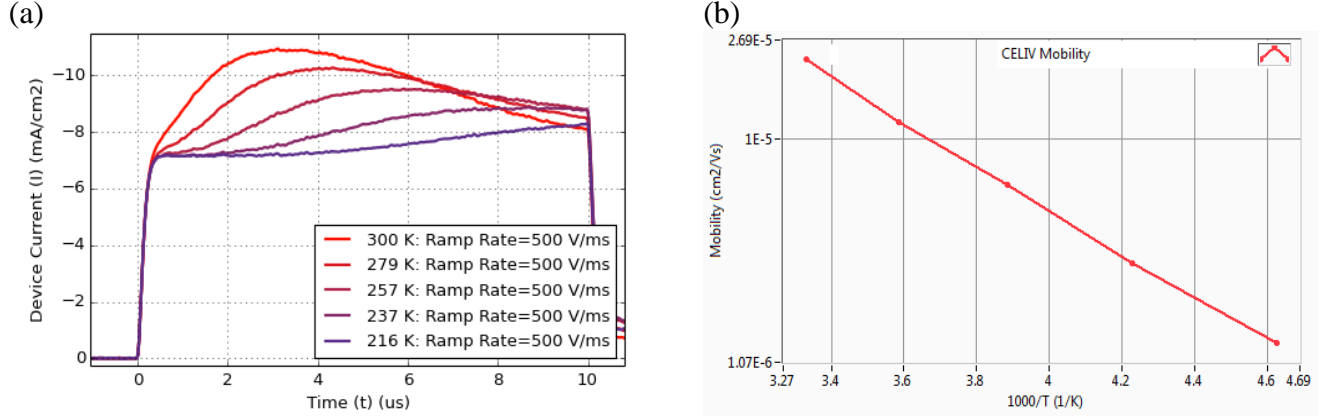


Fig. 5: CELIV measurements of a bilayer OLED as shown in figure 1 with a pre-bias voltage of 0V (a) and extracted mobility from the time position of the maximum of the CELIV signal.

## 5. IMPACT OF THE POLARITY ON THE IV CURVE

In figure 6 we show the simulated IV curve of a bilayer (ITO/PEDOT(30nm)/ *N,N'*-Di(1-naphthyl)-*N,N'*-diphenyl-(1,1'-biphenyl)-4,4'-diamine (NPB) (70nm)/Alq<sub>3</sub>(40nm)/Ca(15nm)/Al) OLED device for three different cases: assuming a non-polar ETL, with a polar Alq<sub>3</sub> and finally assuming a reversed polarity of the ETL (i. e. with a positive sheet charge density at the HTL/ETL interface). We can notice the increase of the current when the polar moment of the ETL is directed toward the NPB layer and a decrease of the current when the dipolar moment is inverted. This latter point has been experimentally observed by Noguchi et al.[15], where the orientation of the molecular dipoles of the ETL could be inverted using an alternative electron transport material: tris(7-propyl-8-hydroxyquinolinato) aluminum (Al(7-pyq)). The sheet charge densities found in [13] (1.1 mC.m<sup>-2</sup> for the Alq<sub>3</sub> and 3.1 mC.m<sup>-2</sup> for the 'inverted' ETL) were used for the simulations shown in figure 5. From these simulations we can conclude that introducing polar ETL can be beneficial for the OLED as the current can be increased leading to a decrease of the driving voltage and therefore an increase in power efficiency.

In order to understand the reason why the current is modified by using a polar ETL material, we have simulated the electron concentration inside the ETL layer (figure 5 right). We observe that when a negative sheet charge density is located at the HTL/ETL interface (and consequently with a positive sheet charge density at the cathode/ETL interface), the concentration of electrons is increased at the cathode interface, leading to an enhanced injection inside the ETL and thus increasing the current. When the polarity is inverted, the electron concentration is decreased leading to a reduced conductivity of the ETL reducing the current flowing through the device. These findings are in agreement with the work from Kinjo et al.[16] where they studied the Band diagram of Alq<sub>3</sub> and Al(7-pyq) using photoelectron spectroscopy and photoelectron yield spectroscopy.

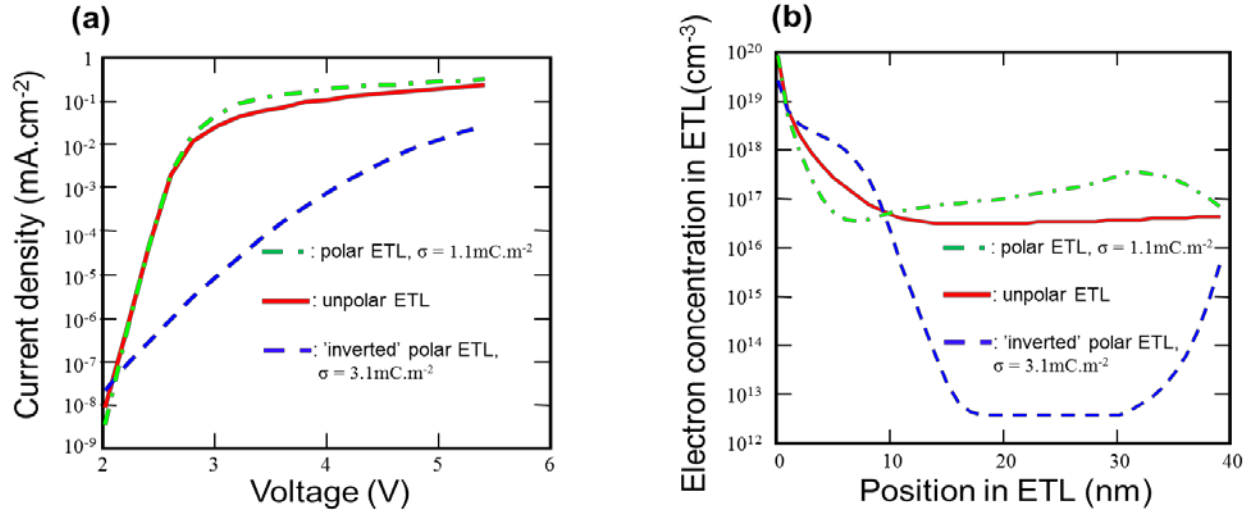


Fig.6: Simulated impact of the ETL polarity orientation on the IV curve of the OLED, and corresponding electron concentration in the ETL for an applied voltage of 3V.

## 6. CONCLUSION

In this contribution we have shown that polar materials can be simulated using the commercially available simulation software SETFOS 4.3 introducing charge sheets on both sides of the polar layer. The comparison of measured and simulated capacitance as a function of bias, frequency and temperature allows quantifying the polarity of the ETL material. In a second step, the temperature activation energy observed in the temperature dependent capacitance-frequency characterization was investigated. Combining temperature dependent CELIV and temperature dependent capacitance-frequency measurements (both available with PAIOS 2.2) we were able to distinguish which part of the activation energy has to be attributed to the injection barrier and to the mobility activation energy. Finally we have shown that the polarity of the ETL can be beneficial if correctly oriented in the device as it can enhance the electron injection in the Alq<sub>3</sub> layer.



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