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**Crystalline Perovskite Epitaxial Growth on Germanium (001)  
by Atomic Layer Deposition**

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**Crystalline Perovskite Epitaxial Growth on Germanium (001)  
by Atomic Layer Deposition**

**by**

**Shen Hu**

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## **Dedication**

*To my beloved family*

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# **Crystalline Perovskite Epitaxial Growth on Germanium (001) by Atomic Layer Deposition**

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The University of Texas at Austin, 2017

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Crystalline perovskites ( $ABO_3$ ) have aroused widespread attention in material science due to their multiple properties. This research uses atomic layer deposition (ALD) to achieve perovskite oxides ( $ABO_3$ ) deposition on Ge (001) for gate oxide applications in microelectronics devices. In particular, this work is mainly focused on the study of crystalline Sr-based perovskites  $SrMO_3$ , where  $M = Ti, Zr, Hf$ .

In this research work, the mechanism for the initial growth of perovskites on Ge by ALD has been studied. High resolution scanning transmission electron microscopy (STEM) images have shown that both of molecular beam epitaxy (MBE)-grown  $BaTiO_3$  films and ALD-grown  $SrHfO_3$  films have the same interface structure, which has a  $2 \times 1$  periodicity and with the alkaline earth metal (AEM) atoms between the Ge dimer rows. This result indicates that the ALD growth proceeds by forming the same Zintl-template layer that is purposely formed in MBE through formation of a 0.5-monolayer (ML) exposure to the AEM. The *in situ* XPS analysis has shown the same surface core level shift (SCLS) behavior results from half-cycle Sr or Ba precursor dosing on a bare Ge (001) surface as is observed following 0.5 -ML Sr or Ba exposure on Ge by MBE. These observations support the conclusion drawn from the STEM images.



Based on the previous study of SrTiO<sub>3</sub> (STO) and SrHfO<sub>3</sub> (SHO) on Ge (001), there is a trade-off between dielectric constant and leakage current in STO and SHO. This research has also studied SrHf<sub>x</sub>Ti<sub>1-x</sub>O<sub>3</sub> (SHTO) films with different Hf content  $x$  to see how composition and lattice constant affected the crystallization behavior. Crystalline SrZrO<sub>3</sub> films have also been deposited by ALD on Ge. The C-V and I-V measurements indicate that the SrZrO<sub>3</sub> yield the best results for dielectric properties compared to STO, SHO and SHTO. A new combined approach of oxygen plasma pre-treatment, Zintl template formation and atomic deuterium post treatment has been applied on this work to minimize the interface trap density, which has achieved  $8.56 \times 10^{11} \text{cm}^{-2} \text{eV}^{-1}$ .

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# Chapter 1: Introduction and overview

## 1.1. OVERVIEW

Over the past decades the increasing speed and lowering power consumption demands in microelectronics chips have been driving forces in complementary metal oxide semiconductor (CMOS) device research. So to satisfy the future developments on higher performance Complementary Metal Oxide Semiconductor (CMOS) devices monolithic integration is investigated herein. Figure 1.1 shows a schematic of a metal-oxide semiconductor field-effect transistor (MOSFET) structure.

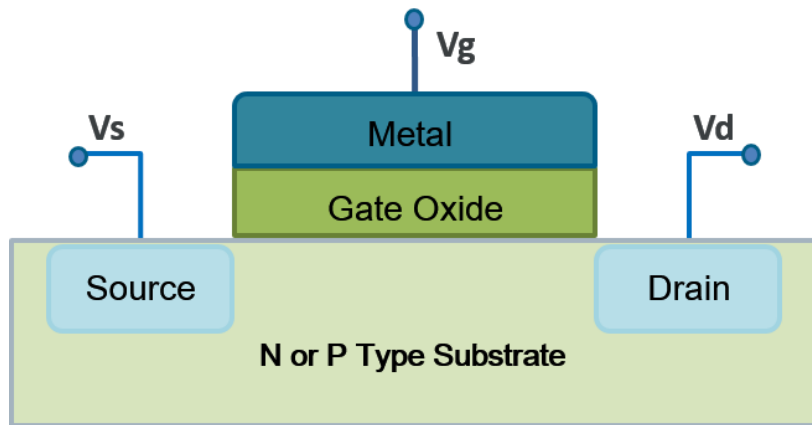


Figure 1.1. Schematic of a MOSFET structure.

In a MOSFET, the saturation drive current ( $I_{on}$ ) is a critical performance metric.<sup>1</sup> Since the charge component, such as electrons or holes, of  $I_{on}$  is dominated by the gate capacitance, better performance transistor requires higher gate capacitance,  $c$ , given by:

$$C = \frac{kA\epsilon}{d}$$



Where  $c$  is the gate capacitance,  $k$  is the dielectric constant of gate oxide,  $A$  is the covered area of the gate oxide,  $\epsilon$  is the permittivity of free space and  $d$  is the gate thickness. As the transistor feature sizes continue to scale smaller, the traditional gate oxide silica ( $\text{SiO}_2$ ) can no longer be employed due to unacceptably high leakage current in ultrathin ( $< 1$  nm)  $\text{SiO}_2$ .<sup>2,3</sup> So to achieve higher capacitance and maintain the same or more densely integrated transistors there has been a transition away from the use of  $\text{SiO}_2$  to the high dielectric constant  $k$  materials to satisfy future developments in CMOS devices.

Besides the high- $k$  materials for gate oxides, the channel materials are also important for transistor performance. Silicon is the most common substrate in the semiconductor industry due to its low cost and the manner by which the gate oxide can be grown. But further transistor scaling requires the channel materials to have higher mobility than Si, which can also lead to a higher driving current.<sup>1,4</sup> Based on the 2012 International Technology Roadmap for Semiconductors (ITRS), channels with high mobility are expected to be introduced by 2018.<sup>5</sup> Compared to Si, the electron and hole mobility for Ge are 3900 vs 1400  $\text{cm}^2/\text{Vs}$  and 1900 vs 470  $\text{cm}^2/\text{Vs}$ , respectively, which makes Ge viable to become the next generation channel material.<sup>1,6-8</sup> Additionally, Ge has a more unstable oxide than Si and it's easy to remove the native oxide on Ge to achieve a clean surface.<sup>9-11</sup>

To maintain the advantage of high mobility in the Ge substrate, an interface with a low density of trap states is desired since high interface trap density ( $D_{it}$ ) could consume the channel mobility.<sup>7</sup> Based on Si studies the significant defects at the oxide-substrate interface result from the substrate roughness and interface dangling bonds.<sup>12</sup> On the Ge-based heterojunctions, the dangling bonds and roughness are also the main cause for high  $D_{it}$ .<sup>7,13,14</sup>  $D_{it}$  values larger than  $10^{12} \text{ cm}^{-2}\text{eV}^{-1}$  are considered too high for device applications.<sup>1</sup> Thus, an interface with low defects is required for next generation CMOS fabrication.

## **1.2. RESEARCH BACKGROUND**

This research will explore crystalline perovskite deposition on Ge (001) by atomic layer deposition (ALD) as high- $k$  gate oxides and will explore how to reduce defects in the oxide and at the interface. The potential for achieving higher dielectric constant in crystalline structures is the reason to study crystalline perovskites instead of amorphous or polycrystalline films.<sup>15</sup>

### **1.2.1. ATOMIC LAYER DEPOSITION**

Thin film growth techniques include chemical vapor deposition (CVD) and physical vapor deposition (PVD), which are chemical process and physical process for thin film deposition, respectively. Developments in CVD led to a more advanced chemical growth technique, ALD. PVD encompasses various delivery methods such as evaporation, sputtering and molecular beam epitaxy (MBE). Among all these thin film growth techniques, ALD has unique characteristics. Compared to CVD, ALD has the advantages of self-limited and conformal coverage on high aspect ratio structures.<sup>16</sup> Compared with MBE, ALD has better thickness uniformity over nonplanar features<sup>17</sup> and can be achieved with a lower thermal budget since the ALD growth window is much lower than the temperatures employed in MBE to facilitate the surface mobility important in MBE-based crystalline growth. And in addition, ALD can have higher deposition rates than MBE and lower equipment cost. As a consequence, ALD is a promising route for high- $k$  crystalline oxide growth in semiconductor applications, such as for the Fin Field Effect Transistor (FinFET).

The principle for ALD is indicated in Figure 1.2. The substrate is typically maintained at a constant temperature in the ALD chamber. The metalorganic precursor is delivered into the chamber and adsorbs on the substrate surface to achieve saturation, the un-adsorbed precursor molecules in gas phase are purged out by the subsequent purge step. Then the second reactant is delivered to react with the adsorbed precursor and this step is also followed with a purge step. Thus, one unit cycle of ALD is completed and can be repeated this sequence can achieve the thin film deposition with atomic scale precision (Ångstrom level).<sup>16,18</sup>

Previously, we reported the successful growth of SrBO<sub>3</sub> perovskite materials, where B = Ti or Hf, on with Ge by ALD.<sup>14,19,20</sup> However, the mechanism for the initial growth of perovskites on Ge by ALD is not yet understood. One key commonality in the ALD process is that the Sr cyclopentadienyl precursor is dosed first on the clean Ge surface. High-resolution cross-section scanning transmission electron microscopy imaging of molecular beam epitaxy-grown BaTiO<sub>3</sub> and atomic layer deposition-grown SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> reveal a 2×1 periodicity at the interface the alkaline earth metal (AEM) atom between dimer rows.<sup>14,21</sup> Based on this observation, we believe that the initial ALD growth for SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> on Ge forms out of the same Zintl template as found in MBE growth. To understand the initial interface reactions during perovskite ALD on Ge, we studied the deposition of Sr and Ba molecular precursors on Ge using *in situ* X-ray photoelectron spectroscopy (XPS) and reflection high energy electron diffraction (RHEED) and compare the results with Sr and Ba deposition using Knudsen cells in an MBE system and with density functional theory calculations. This study will be discussed in Chapter 4.

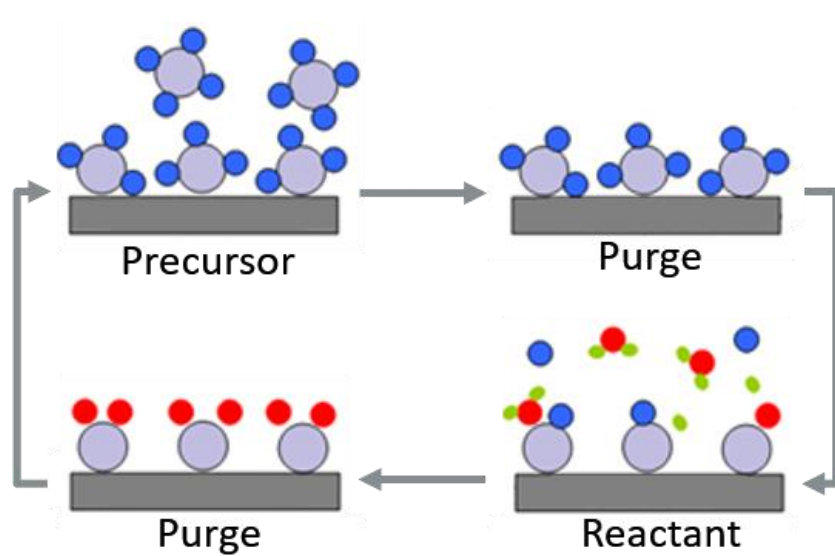


Figure 1.2. Schematic of ALD procedure.

### 1.2.2. CRYSTALLINE PEROVSKITE FOR HIGH- $k$ OXIDES APPLICATION

Crystalline perovskites ( $ABO_3$ ) have aroused widespread attention in material science due to their multiple properties. Especially, the epitaxial integration of crystalline perovskite oxides on semiconductors presents promising properties for numerous technological applications.<sup>22,23</sup> Replacing the cation A or B in perovskites  $ABO_3$  can lead to different properties of the crystalline perovskite (As shown in Figure 1.3.), which include being high- $k$ ,<sup>20,24–29</sup> ferromagnetic,<sup>30,31</sup> ferroelectric,<sup>32,33</sup> multiferroic,<sup>34,35</sup> or superconducting<sup>36</sup> materials.

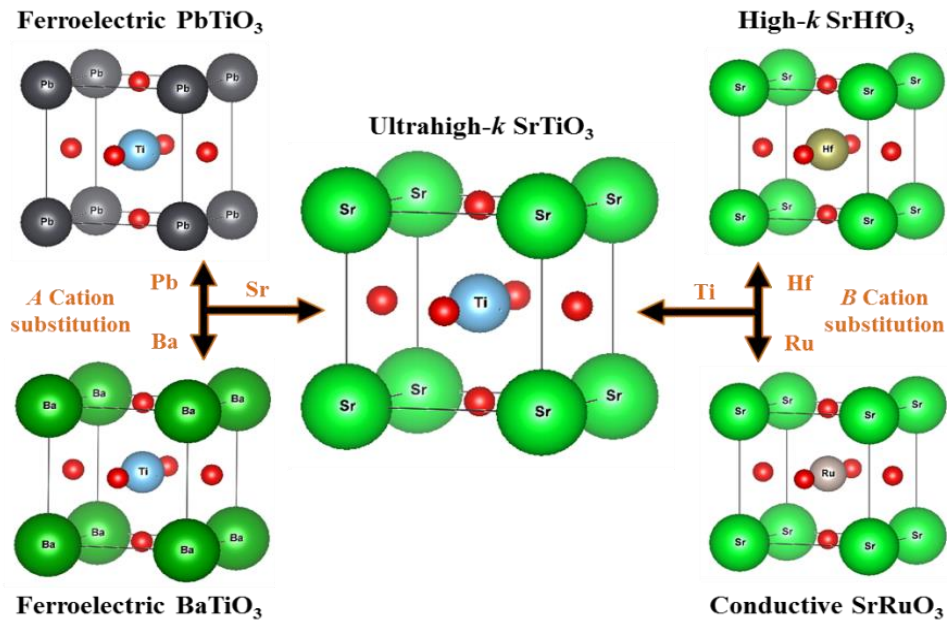


Figure 1.3. The versatility of the perovskite oxide is demonstrated by the simple substitution of the A or B cation. From ultrahigh- $k$  SrTiO<sub>3</sub> (center) to ferroelectrics PbTiO<sub>3</sub>/BaTiO<sub>3</sub> (left) and high- $k$  SrHfO<sub>3</sub> or conductive SrRuO<sub>3</sub> (right). Taken from “Atomic layer deposition of perovskite oxides and their epitaxial integration with Si, Ge, and other semiconductors.” Applied Physics Reviews 2, 041301 (2015).<sup>37</sup>

After McKee first reported the work of epitaxial growth of SrTiO<sub>3</sub> on silicon in 1998,<sup>38</sup> the study of crystalline perovskites on semiconductors has been widely expanded for device applications.<sup>14,20,39–44</sup> These studies include using the crystalline perovskite as a high- $k$  gate oxide for field-effect transistor device applications on various semiconductors such as Si, Ge, and GaAs.<sup>24–29,45–50</sup> The  $k$  value is targeted to be over 12 and preferably 25 to 35 for high dielectric materials.<sup>51</sup>

For crystalline ABO<sub>3</sub> perovskite compound growth, besides the higher mobility reason, Ge (001) may be a preferable substrate to Si (001) due to the lattice match with ABO<sub>3</sub> perovskites. The in-plane atomic spacing on the Ge (001) surface and Si (001)

surface are 3.992 Å and 3.840 Å, respectively.<sup>24-26</sup> Ge (001) will have less lattice strain than Si for a crystalline high- $k$  gate stack, like SrHfO<sub>3</sub> (lattice constant  $a=4.069$  Å), which has -1.9% and -5.6% (compressive) strain on Ge and Si, respectively. For SrTiO<sub>3</sub> ( $a=3.905$  Å) and the strains are 2.2% and -1.7% Ge and Si, respectively. For BaTiO<sub>3</sub> ( $a=4.005$  Å), the strains are -0.3% and -4.1%, respectively. Therefore, compared to Si (001), Ge (001) may more readily facilitate crystalline ABO<sub>3</sub> growth.

The previous work in our group has studied the growth of SHfO<sub>3</sub> (SHO) and STiO<sub>3</sub> (STO) on Ge by ALD.<sup>19,20</sup> Based on these studies, there was a trade-off between the dielectric properties of SHO/Ge and STO/Ge heterojunctions. The dielectric constants for STO and SHO were  $k\sim 90$  and  $k\sim 17$ . The leakage current for STO was around 10 A/cm<sup>2</sup> at 0.7 MV/cm with 0.7 nm equivalent oxide thickness (EOT) and for SHO was  $6.3\times 10^{-6}$  A/cm<sup>2</sup> at 1.0 MV/cm with EOT=1.0 nm.<sup>19,20</sup> Since the high performance MOSFET requires a high dielectric constant gate oxide and low leakage current, the high- $k$  materials that optimize the dielectric properties are required. One approach to tuning the dielectric constant and leakage current is to combine the attributes of STO and SHO through SrHf <sub>$x$</sub> Ti <sub>$1-x$</sub> O<sub>3</sub> growth, by adjusting the Hf content  $x$  to get the desired dielectric properties. This is presented in Chapter 3. Another idea is to achieve SrZrO<sub>3</sub> deposition on Ge (001) by ALD. SrZrO<sub>3</sub> has the comparable conduction band offset (CBO) with SrHfO<sub>3</sub> on Ge, which are 1.77 eV and 2.17 eV for SrZrO<sub>3</sub>/Ge and SrHfO<sub>3</sub>/Ge, respectively, and meanwhile has a higher dielectric constant  $k$  around 30.<sup>20,55</sup> The SrZrO<sub>3</sub>/Ge should have better electrical properties, which are expected to be intermediate between STO and SHO. Compare to the reported leakage current versus EOT for gate oxides on Ge (001) in the literature, the SrZrO<sub>3</sub>/Ge heterojunction is a promising material to improve the performance into the dashed red cycle shown in Figure 1.4. Several groups have studied SZO deposition by molecular beam epitaxy (MBE) or pulse laser deposition (PLD).<sup>24,55,56</sup>

But the SrZrO<sub>3</sub> has never been achieved by ALD on Ge. So in this research, we will also study the SrZrO<sub>3</sub> deposition on Ge (001) by ALD for high-*k* gate oxide application. This is presented in Chapter 5.

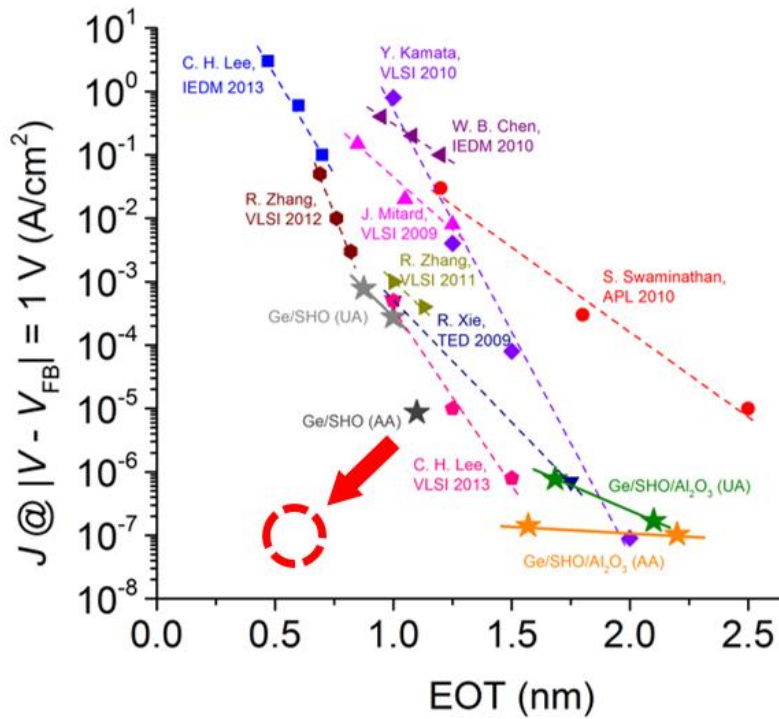


Figure 1.4. Leakage current versus EOT reported as the state of the art in recently reported work of gate oxides on Ge. Taken from “A Low-Leakage Epitaxial High-*k* Gate Oxide for Germanium Metal–Oxide–Semiconductor Devices.” *ACS applied materials & interfaces*, 8(8): 5416-5423, (2016).<sup>57</sup>

### 1.2.3. CHALLENGES OF INTERFACE DEFECTS

Since Ge has higher mobility than Si, which can result in higher drive current  $I_{on}$ , this research choose Ge as the substrate. To maintain the advantage of high mobility in Ge substrate, a high quality interface is desired since high interface trap density ( $D_{it}$ ) could

consume the channel mobility.<sup>7</sup> Previous work in the Ekerdt group found SHO to have  $D_{it}$  around  $4 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$ .<sup>20,57</sup> The nature of defects at interface of Ge-based heterojunction are mainly formed by dangling bonds<sup>7,13</sup> and the roughness of Ge substrate could also result in the high  $D_{it}$ .<sup>12,14</sup> Several groups have employed different methods to degrade the interface trap density,<sup>57-65</sup> such as the air annealing method to reduce the  $D_{it}$  of crystalline SrHfO<sub>3</sub>/Ge to around  $2 \times 10^{12} \text{ cm}^{-2} \text{ eV}^{-1}$ .<sup>57</sup> Kim's group used La<sub>2</sub>O<sub>3</sub>-incorporation in HfO<sub>2</sub> dielectrics on Ge and achieved the  $D_{it}$  value  $\sim 10^{12} \text{ cm}^{-2} \text{ eV}^{-1}$ .<sup>58</sup> One of the most widely introduced method is using GeO<sub>x</sub> as a passivation layer, which can yield a  $D_{it}$  value on the order of lower  $10^{11} \text{ cm}^{-2} \text{ eV}^{-1}$ .<sup>60,63-67</sup> But this method increases the EOT with the introduction of passivation layer. So herein, a new approach without sacrificing the EOT is studied in this research by introducing atomic deuterium to treat the Ge heterojunction interface.

Takagi's group studied post deposition annealing to treat Al<sub>2</sub>O<sub>3</sub>/GeO<sub>x</sub>/n-Ge in different ambients of N<sub>2</sub>, forming gas, H<sub>2</sub>, D<sub>2</sub>, atomic H and atomic D. Eventually the atomic D treatment led to the best effective electron mobility.<sup>68</sup> So we employed this method to lower the interface trap density in Chapter 5 since dangling bonds in the interface might be another reason to increase the  $D_{it}$ .<sup>14</sup> Based on the study of Lyding, D desorption yield is about 50 times lower on Ge than the H yield on Si as shown in Figure 1.5.<sup>69</sup> The similar results should be present at the buried interface in my studies with Ge substrates.

In addition, the previous work showed that high annealing temperature above 650 °C could lead to interfacial reactions, which increase the  $D_{it}$ .<sup>20</sup> Ge steps can lead to anti-phase boundary formation in the film. In this research, the SZO is expected to crystalize at lower elevated temperature than SHO to minimize interfacial reactions due to lighter B site atomic weight.<sup>14</sup> New surface treatments for the Ge substrate will be examined in Chapter



5 study the impact of flatter Ge (001) surfaces.<sup>10</sup> Finally atomic deuterium is used to heal dangling bonds at the Ge surface in Chapter 5.

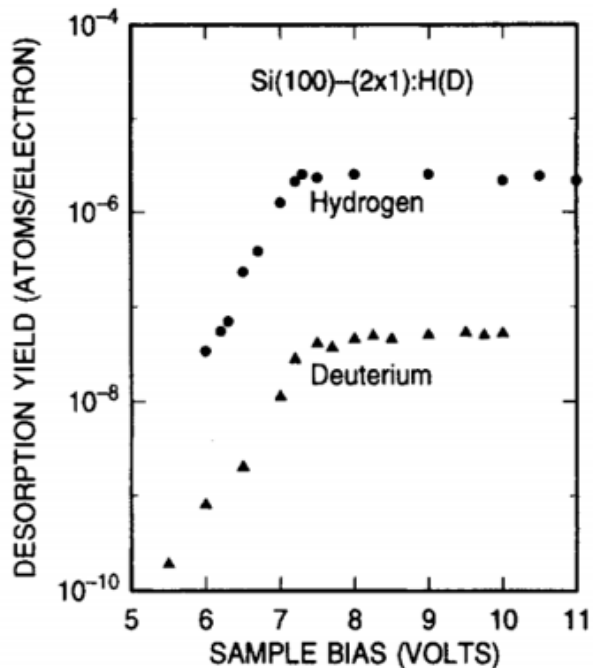


Figure 1.5. Comparison of the STM-induced desorption yields of hydrogen and deuterium from Si(100)-(2×1):H(D) as a function of the sample bias voltage. Taken from “STM-induced H atom desorption from Si (100) isotope effects and site selectivity” *Chemical Physics Letters*, 257 (1) 148-154, (1996).<sup>69</sup>

### 1.3. RESEARCH OBJECTIVES AND CHAPTER OVERVIEW

The aim of this research is to study the growth of the crystalline Sr-based perovskites SrBO<sub>3</sub>, where B = Ti, Zr, Hf, on Ge (001) by atomic layer deposition and understand the mechanism for initial ALD growth on Ge (001). In addition, this work will also explore the defect nature of the films and interfaces and achieve favorable dielectric

properties for gate oxide applications in microelectronics devices. Particularly, the specific objectives of this research in each chapter are listed as follows:

Chapter 2 introduces about the specific equipment used in this research. A commercial MBE, a home-built hot wall ALD chamber and a commercial x-ray photoelectron spectroscopy analyzer are combined within on system, which allows us to achieve *in situ* deposition and characterization by X-ray photoelectron spectroscopy (XPS). A home-designed atomic deuterium chamber is also attached on this system for *in situ* post treatment. The experimental procedure will also be discussed, especially the conditions for film deposition by ALD and other *ex situ* characterization methods such as x-ray reflectivity (XRR), x-ray diffraction (XRD), aberration-corrected scanning transmission electron microscopy (STEM), capacitance-voltage (C-V), and current-voltage (I-V) measurements.

Chapter 3 discusses the growth of crystalline  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  (SHTO) films on Ge (001) substrates by atomic layer deposition. Samples were prepared with different Hf content  $x$  to explore if strain, from tensile ( $x = 0$ ) to compressive ( $x = 1$ ), affects film crystallization temperature and how composition affects properties. Amorphous films grow at 225 °C and are crystallized into epitaxial layers at annealing temperatures that varied monotonically with composition from ~530 °C ( $x = 0$ ) to ~660 °C ( $x = 1$ ). Transmission electron microscopy reveals abrupt interfaces. Electrical measurements reveal 0.1 A/cm<sup>2</sup> leakage current at 1 MV/cm and dielectric constant of 30 for  $x = 0.55$ . This work indicates that the SHTO can combine the superiorities of STO and SHO, and adjusts the dielectric properties by changing the Hf content  $x$  but it still exhibits higher leakage current than expected for CMOS application.

Chapter 4 presents the study of the initial growth of  $\text{ABO}_3$  perovskites on Ge (001) by atomic layer deposition, where A = Ba, Sr and B = Ti, Hf, Zr, and compares the behavior

with a MBE-grown template – chemically and structurally – to understand the mechanism(s) for crystalline  $ABO_3$  growth by ALD on Ge (001). Via *in situ* X-ray photoelectron spectroscopy (XPS), reflection high-energy electron diffraction (RHEED) and density functional theory (DFT), we analyze the surface core level shifts and surface structure during the initial growth of  $ABO_3$  perovskites on Ge (001) by ALD. We find that the initial dosing of the barium- or strontium- bis(triisopropylcyclopentadienyl) precursors on a clean Ge surface produces a surface phase that has the same chemical and structural properties as the 0.5-monolayer Ba Zintl layer formed when depositing Ba by MBE. Similar binding energy shifts are found for Ba, Sr and Ge when using either chemical or elemental metal sources. The observed germanium surface core level shifts are consistent with the flattening of the initially tilted Ge surface dimers using both molecular and atomic metal sources. Similar binding energy shifts and changes in dimer tilting with alkaline earth metal adsorption are found with density functional theory calculations. High angle angular dark field scanning transmission microscopy images of  $BaTiO_3$ ,  $SrZrO_3$ ,  $SrHfO_3$  and  $SrHf_{0.55}Ti_{0.45}O_3$  reveal the location of the Ba (or Sr) atomic columns between the Ge dimers. The results imply that the organic ligands dissociate from the precursor after precursor adsorption on the Ge surface, producing the same Zintl template critical for perovskite growth on Group IV semiconductors during molecular beam epitaxy. This work also indicates that using the MBE-grown Zintl template for ALD growth can prevent surface carbon contamination, which could possibly lower the defects at the interface.

Chapter 5 studies  $SrZrO_3$  growth on both bare Ge (001) and Zintl template by ALD, and explores variables such as the growth temperature, cycle ratio etc. This work is based on the study in the previous two chapters and demonstrates that  $SrZrO_3$  may be the best option for high- $k$  dielectric application in the crystalline perovskite group of  $SrBO_3$ , where B is the element (Ti, Zr and Hf). The  $SrZrO_3/Ge$  heterojunction has the comparable

conduction band offset (CBO) with SrHfO<sub>3</sub> on Ge, which are 1.77 eV and 2.17 eV for SrZrO<sub>3</sub> and SrHfO<sub>3</sub> on Ge, respectively, and meanwhile also higher dielectric constant  $k$  around 30. On the other hand, since SrZrO<sub>3</sub> can achieve crystallization at lower temperature at ~590 °C than SrHfO<sub>3</sub> (650 °C), it's easier to have better crystallinity in SrZrO<sub>3</sub> and little interface reaction, which could result in lower interface defects. Several new methods including Zintl template preparation, oxygen plasma pre-treatment and atomic deuterium post treatment are applied to reduce the interface trap density. MOSCAP devices are fabricated based on selective samples for electrical characterization and interface trap density measurement. The electrical properties for SrZrO<sub>3</sub>/Ge heterojunction in this work are reported as 30 dielectric constant and leakage current of  $2.14 \times 10^{-8}$  A/cm<sup>2</sup> at 1 MV/cm with an EOT = 0.8 nm. The best  $D_{it}$  for treated sample is eventually obtained as  $8.56 \times 10^{11}$  cm<sup>-2</sup>eV<sup>-1</sup>. The work reports the deposition of crystalline SrZrO<sub>3</sub> on Ge (001) by ALD for the first time and applies a new strategy to minimize the  $D_{it}$  value.

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## Chapter 2: Research methodology

### 2.1. RESEARCH FACILITIES

The majority of experiments are conducted on the equipment in Materials Physics Laboratory. This equipment shown in Figure 2.1 combines the custom-built ALD reactor with a commercial MBE system (DCA 600) and an X-ray photoelectron spectroscopy (XPS) analysis chamber (VG Scienta R3000).<sup>1</sup> In addition, a home-built atomic deuterium chamber (D-chamber) is also attached to the system for *in situ* treatment.

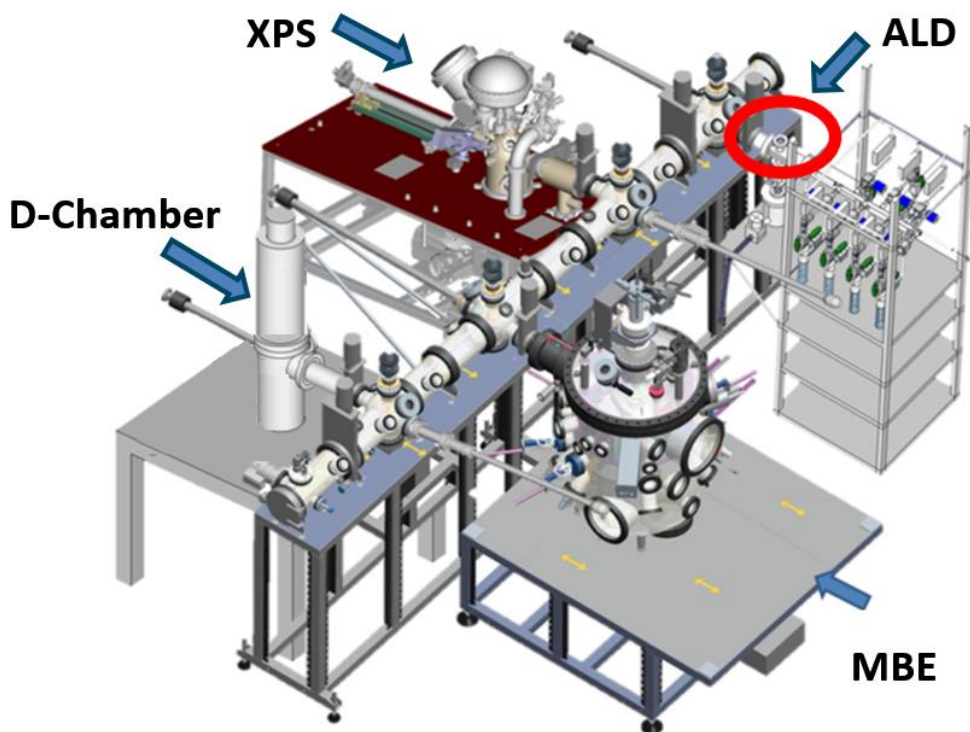


Figure 2.1. Schematic of the research facilities

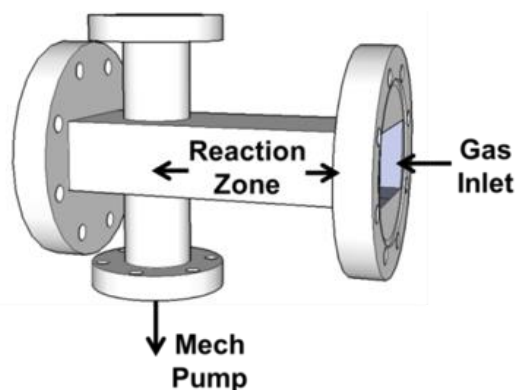


Figure 2.2. Perspective drawing of the ALD chamber.<sup>1</sup>

*In situ* XPS was performed with a monochromatic Al K $\alpha$  source at 1486.6 eV using a VG Scienta R3000 electron energy analyzer. The analyzer was calibrated with a clean Ag foil such that the Ag 3d<sub>5/2</sub> core level is at a binding energy of 368.28 eV. The XPS analysis chamber can also perform the low energy ion-scattering spectroscopy (LEISS) measurement. The MBE is equipped with six effusion cells (four standard and two high-temperature), a four-pocket electron beam evaporator, a quartz crystal microbalance (QCM), a reflective high energy electron diffraction (RHEED, Staib Instruments operating at 21 keV) system and a radio frequency plasma source of both oxygen and nitrogen. The QCM can calibrate the flux rate. The RHEED enables the observation of post deposition annealing process in real time. The plasma source can be applied for sample treatment before and after the growth. The ALD consists of eight ports for precursor and H<sub>2</sub>O reactant saturators, a manifold equipped with ALD valves from Swagelok and mass flow controllers for each ports and a custom-built, hot wall stainless steel chamber for deposition. The ALD chamber is rectangular with the length of 20 cm and volume of 460 cm<sup>3</sup> (as shown in Figure 2.2).

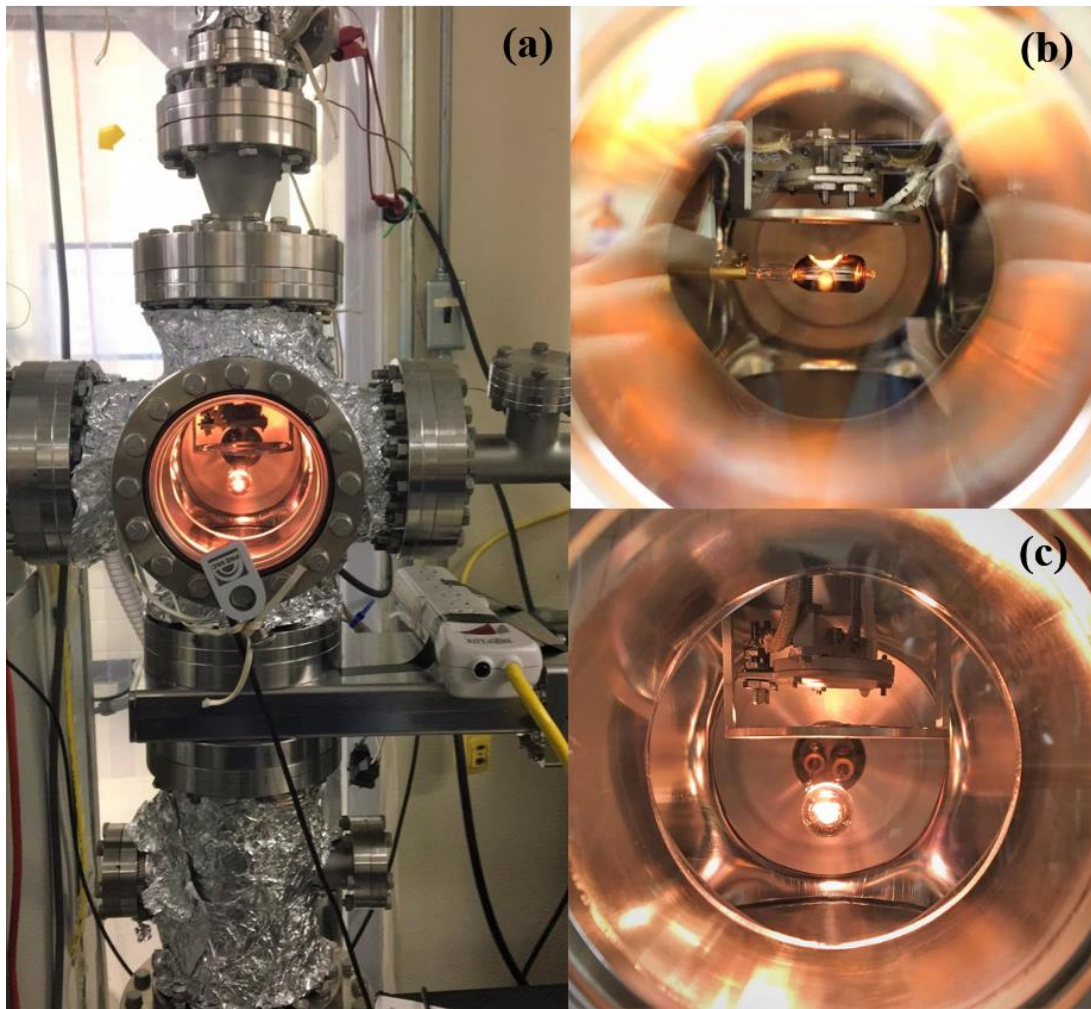


Figure 2.3. (a) Image of atomic deuterium chamber (D-chamber), (b) inside side view and (c) inside front view of the D-chamber.

The last portion for the system is the home-built D-chamber. As shown in Figure 2.3.(a), a six-cross chamber is used to combine the heater from the top, a tungsten filament from the back, a transfer arm for sample insertion and removal from the right and the turbo pump at the bottom. The left side is attached to the main system to enable transfer sample in vacuum for *in situ* treatment. Figure 2.3.(b) and (c) indicate the heater-sample-filament arrangement. For *in situ* treatment the distance from heater to sample is around 2 cm and

sample to filament is around 4 cm. The heater, which is supplied from Momentive Performance Materials, is made of pyrolytic boron nitride (PBN). The resistance for the heater at room temperature is measured to be 9.8-13.5 ohms. The maximum power and current allowed for the heater are 663 W and 12 A, respectively. The heater position is fixed above the sample grabber as shown in Figure 2.4.(b). The grabber is connected to a linear motion as shown in Figure 2.4.(a) to enable grabber movement in the vertical direction. For sample treatment, the distance from the heater to grabber is kept at 2 cm so that radiation can heat the sample to  $\sim 350^{\circ}\text{C}$  when heater temperature set as  $450^{\circ}\text{C}$  with the filament on.

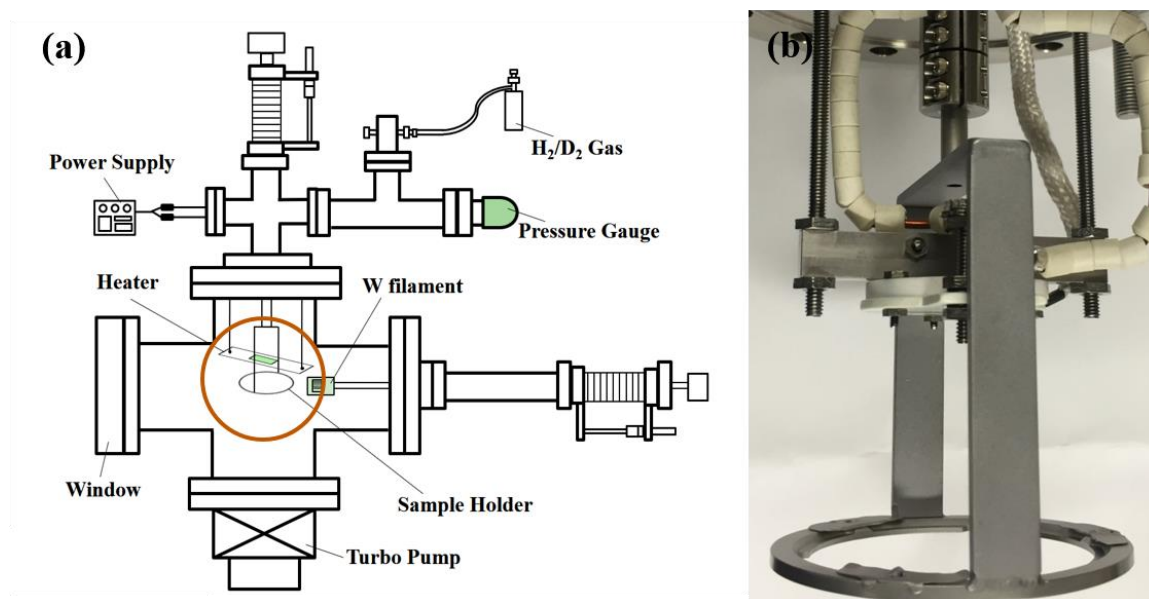


Figure 2.4. Schematic of the D-chamber (a) and (b) the image of heater and sample grabber, which corresponds to the cycle region in (a).

The tungsten filament is also connected to a linear motion manipulator so that it can be translated in the horizontal direction and it is positioned at 4 cm below the sample when exposing the sample to atomic deuterium (as shown in Figure 2.4.(a)). The tungsten filament is a 400 W Osram Xenophot bulb, which has part of the glass enclosure removed to expose the filament. A current of 5.2 A was supplied by a DC power supply (KEPCO, MSK10-10M) to the tungsten filament and produces a temperature above 1800 K, which was calibrated by a thermal pyrometer. At this temperature, the tungsten filament could crack deuterium molecule to generate atomic deuterium.<sup>2</sup> The pressure of introduced deuterium gas (99.999%; Matheson) is controlled by a leak valve and monitored by a 943 Model cold cathode gauge from MKS instrument to maintain at  $5.0 \times 10^{-6}$  torr in the chamber. The flux of atomic deuterium is estimated around  $2 \times 10^{13}$  D/cm<sup>2</sup>·s based on previous work.<sup>3</sup>

## 2.2. GENERAL EXPERIMENT METHOD

The Ge (001) substrate is prepared from a 4-inch Ge wafer (Sb-doped,  $\rho \sim 0.04 \Omega\text{-cm}$ ) from MTI Corp. as described previously.<sup>4-6</sup> The as-received Ge wafer is diced into approximately  $18 \times 20 \text{ mm}^2$  pieces, degreased with acetone, isopropyl alcohol and water, dried and then two methods are introduced to remove the carbon from the surface. The first one is to expose the sample to ultraviolet light/ozone to remove residual carbon. The other treatment involves transferring the sample into the MBE chamber followed by the oxygen plasma treatment. The surface  $\text{GeO}_2$  is removed by annealing at 700 °C in vacuum ( $< 2 \times 10^{-9}$  Torr) for 1 hr. The resulting  $2 \times 1$ -reconstructed Ge (001) surface is verified by *in situ* RHEED. The clean Ge (001) sample is transferred *in vacuo* to an XPS chamber to verify the Ge surface composition. Following this characterization, the sample is then transferred

*in situ* and exposed to varying doses of elemental Sr and Ba in the MBE chamber, or exposed to the metal precursors or for perovskite film deposition in the ALD chamber.

For elemental deposition in the MBE chamber the substrate is maintained at 600 °C following protocols in previous reports.<sup>1,7</sup> The Ba and Sr fluxes are calibrated with a quartz crystal monitor to a rate of approximately 1 ML/min, allowing for controlled submonolayer dosing. Coverages of Ba or Sr are reported in ML equivalents for MBE-dosed samples, where a ML is defined to be the atomic density of the Ge (001) unreconstructed surface ( $6.26 \times 10^{14}$  atoms/cm<sup>2</sup>). RHEED is used to monitor surface structure *in situ* and in real time. The MBE-dosed samples is cooled to 200 °C before transferring the sample into the XPS analysis chamber.

For deposition in the ALD chamber the sample is maintained at 225 °C, the typical temperature used for BaTiO<sub>3</sub>, SrHfO<sub>3</sub> and SrTiO<sub>3</sub> growth.<sup>4,5,8</sup> The precursors and conditions used for this study are listed in Table 2.1. H<sub>2</sub>O is used as the oxygen source at room temperature. The ALD chamber is maintained at a total pressure of 1 Torr using Ar carrier gas during dosing and less than  $1 \times 10^{-6}$  Torr when in standby mode. An ALD half-cycle refers to a 2-s exposure to the precursor stream followed by a 20-s Ar purge (referred to as 1 half-cycle herein). A complete cycle refers to a 2-s precursor exposure, 20-s argon purge, 2-s deionized water exposure, and another 20-s argon purge (referred to as 1 cycle herein). The ALD-dosed samples are transferred out of the ALD chamber without cooling the sample and subsequently cooled to room temperature in the transfer line.

For the SZO growth by ALD, to achieve the slightly Sr rich in grown film, the cycle ratio needs to be adjusted as Sr: Zr = 2:3. The quaternary SHTO oxides growth is more complicated, which is accomplished by adjusting the number of *m* SHO subcycles (Sr: Hf=1:1) and *n* STO (Sr:Ti=2:1) subcycles to vary the Hf content (*x*) in SHTO. Different film thickness is controlled by the total number of supercycles *l*.

Table 2.1. ALD precursors for each element

| Element | Precursor name                              | Temperature/<br>°C | Vendor            |
|---------|---|--------------------|-------------------|
| Sr      | Strontium Bis(triisopropylcyclopentadienyl) | 130                | Air Liquide       |
| Ba      | Barium Bis(triisopropylcyclopentadienyl)    | 150                |                   |
| Ti      | Titanium tetraisopropoxide                  | 40                 | Sigma-<br>Aldrich |
| Zr      | Tetrakis (dimethylamido) Zirconium (IV)     | 60                 |                   |
| Hf      | Hafnium formamidinate                       | 115                | Dow<br>Chemical   |

### 2.3. *EX SITU* CHARACTERIZATION

Based on the previous experiments,<sup>4,5</sup> the films can be amorphous as grown, so post-deposition annealing is required to obtain the single crystalline film. The RHEED is used to observe transformation from amorphous to crystalline in real time and XPS is used to analyze the composition and the elemental oxidation states in the grown films and at the interfaces. For *ex situ* characterization, the film thickness is measured by X-ray reflectivity (XRR) and crystallinity is analyzed by X-ray diffraction (XRD) and rocking curve on a Rigaku Ultima IV system with a Cu K $\alpha$  source. The aberration-corrected scanning transmission electron microscopy (STEM) is employed here to investigate the grown film and interface. The samples are prepared via standard cross-section method with Ar ion milling and STEM images are taken with a JEOL ARM 200F operated at 200 kV. The STEM analysis work is performed by our collaborator Dr. David J. Smith at Arizona State University.

To study the electrical properties, which include dielectric constant  $k$ , leakage current  $I$  and interface trap density  $D_{it}$ , samples are selected to fabricate the MOS capacitor

structures and the capacitance-voltage (C-V) and current-voltage (I-V) measurements are performed on an Agilent B1500A semiconductor device parameter analyzer with a Cascade Microtech probe station. The electrical measurements are a collaboration with Dr. Edward Yu's group in the ECE department at UT. Some theoretical modeling is also introduced to support this research and this part is assisted by Dr. Alexander A Demkov in Physics department at UT. My primary focus is centered on the ALD growth, materials characterization, the building of atomic deuterium source chamber, and collaboration with other research groups for the theoretical modeling and device testing.

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## Chapter 3: Monolithic Integration of Perovskites on Ge(001) by Atomic Layer Deposition: A Case Study with $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$

### 3.1. INTRODUCTION

As the transistor feature sizes continue to scale ever smaller there has been a transition away from the use of conventional materials for the channel, Si, and the gate oxide,  $\text{SiO}_2$ .<sup>1</sup> Germanium has electron and hole mobility of 3900 and 1900  $\text{cm}^2/\text{Vs}$ , respectively, compared to 1400 and 470  $\text{cm}^2/\text{Vs}$  at 300 K, respectively, in Si. For this reason, Ge is being considered for p-type metal-oxide semiconductor field effect transistors (MOSFET).<sup>1,2</sup> There are multiple considerations in selecting a gate oxide material including the dielectric constant, band offset, leakage current, interface trap density ( $D_{it}$ ), and ease of manufacturing.<sup>3,4</sup> Various groups have reported gate oxides on Ge in MOSFETs, including  $\text{TiO}_2/\text{Al}_2\text{O}_3$ ,  $\text{ZrO}_2$ ,  $\text{LaAlO}_3$  on an interfacial layer of  $\text{SrGe}_x$ ,  $\text{HfO}_2$  on an interfacial layer of  $\text{Y}_2\text{O}_3$ -doped  $\text{GeO}_2$ ,  $\text{Y}_2\text{O}_3$  on a  $\text{GeO}_x$  interfacial layer, and  $\text{HfO}_2$  with  $\text{Al}_2\text{O}_3$  to suppress  $\text{HfO}_2$ - $\text{GeO}_x$  intermixing.<sup>5-10</sup>

Amorphous oxides generally have lower dielectric constants than the crystalline form. However, the absence of grain boundaries in amorphous films is a potential advantage as grain boundaries can serve as defect trap sites.<sup>11</sup> Crystalline oxides have been reported on silicon<sup>12-14</sup> and germanium.<sup>13,15</sup> These crystalline oxides on semiconductors (COS) can offer high dielectric constants, perfection of the crystal structure at the oxide/semiconductor interface, and the possibility to coherently bond across the interface and minimize dangling bonds.<sup>12,13,16</sup> Many COS examples are for oxides grown using molecular beam epitaxy (MBE). We recently reported an all-chemical growth process for

S. Hu, M. D. McDaniel, A. Posadas, C. Hu, H. Wu, E. T. Yu, D. J. Smith, A. A. Demkov, and J.G. Ekerdt, MRS Commun. 6, 125 (2016).  
Comments: I designed and conducted the experiment.

SrTiO<sub>3</sub> (STO) and SrHfO<sub>3</sub> (SHO) on Ge (001) using atomic layer deposition (ALD) that illustrates a potentially scalable integration route to crystalline oxides on germanium.<sup>17,18</sup>

SHO has a large band gap of 6.1 eV with favorable conduction band offset (~2.2 eV) and valence band offset (~3.2 eV) with Ge.<sup>19,20</sup> This is in contrast with Ti-based perovskites, where the Ti 3d states yield negligible conduction band offsets with Si and Ge (~0.1–0.5 eV).<sup>21–23</sup> Incorporation of Hf into the alloy provides an upward shift of the d-states, which improves the conduction band offset, and increases the lattice constant, which may affect epitaxy. The dielectric constants were  $k \sim 90$  and  $k \sim 20$  for thin films of STO and SHO, respectively. Capacitor structures showed the leakage current for STO was around 10 A/cm<sup>2</sup> at 0.7 MV/cm with equivalent oxide thickness (EOT) of 0.7 nm and the leakage current for SHO was less than 10<sup>-5</sup> A/cm<sup>2</sup> at 1.0 MV/cm with EOT of 1.0 nm.<sup>17,18</sup> In the previous studies amorphous films were deposited during ALD and the crystalline films formed after annealing at temperatures from 530 to 660 °C. The present study was undertaken to explore the role of strain on the crystallization temperature and composition on the film properties by growing alloys of SrHf<sub>x</sub>Ti<sub>1-x</sub>O<sub>3</sub> (SHTO) by ALD on Ge (001). The lattice constants of 3.905 Å and 4.069 Å for bulk STO and SHO, respectively, lead to 2.2% and -1.9% strain with the Ge (001) substrate for fully-strained, commensurate films affording a composition for which the lattice constant will match Ge (001) surface spacing along the [110] direction.

### 3.2. EXPERIMENT

The Ge substrates (18×20 mm<sup>2</sup>) are diced from a 4-in Ge wafer (n-type, Sb-doped, 0.029-0.054 Ω·cm resistivity from MTI Corp.) The sample preparation procedure and experimental system are described in previous work.<sup>17,18</sup> The wafer pieces are cleaned with

acetone, isopropyl alcohol and deionized water in an ultrasonic bath for 10 min each. After drying with nitrogen the sample is exposed to UV/ozone for 30 min to remove residual carbon contamination. The sample is mounted on a molybdenum puck and loaded into the vacuum system immediately and transferred to a molecular beam epitaxy (MBE) chamber. The sample is annealed and deoxidized in vacuum ( $<2 \times 10^{-9}$  Torr) by heating from 200 to 500 °C at 20 °C min<sup>-1</sup> and then from 550 to 650 °C at 10 °C min<sup>-1</sup>, annealed at 650 °C for 1 hr, and finally cooled to 200 °C at 30 °C min<sup>-1</sup>. This procedure produces the 2×1-reconstructed clean Ge (001) surface, which is essential as the starting surface for perovskite ALD.<sup>17,18</sup> *In situ* reflection high-energy electron diffraction (RHEED) is used to verify the surface order. Figure 3.1 shows a representative surface after this procedure.

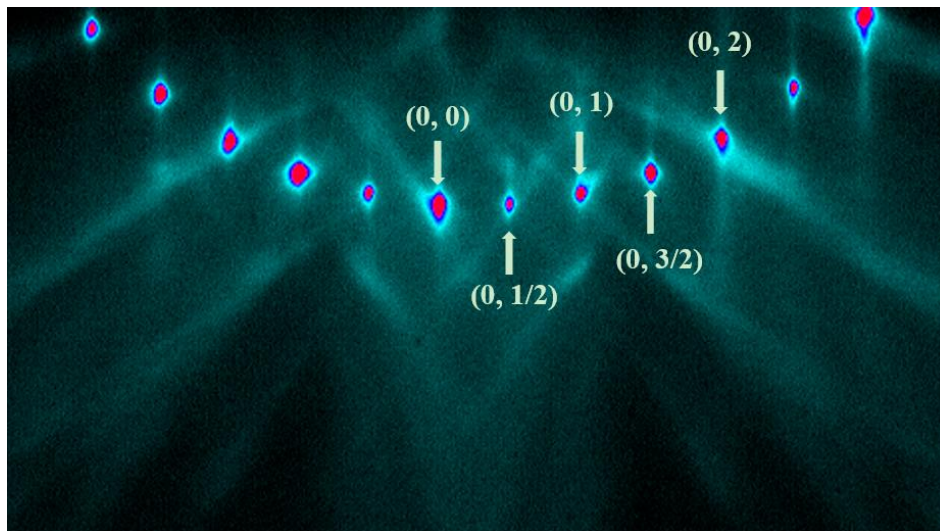


Figure 3.1. After 1 hr vacuum annealing at 650 °C, the Ge 2×1 reconstructed surface is observed by RHEED. The Kikuchi lines indicate the surface cleanliness and good long range order of the substrate, which are important for ALD growth on Ge (001). Image taken along the <110> direction.

The Ge substrate with the 2×1-reconstructed surface is transferred *in situ* to the ALD chamber,<sup>24</sup> where it is allowed to equilibrate for ~15 min at the growth temperature of 225 °C. Film growth is performed at 1 torr using strontium bis(triisopropylcyclopentadienyl) [Sr(<sup>i</sup>Pr<sub>3</sub>Cp)<sub>2</sub>] (HyperSr), hafnium formamidinate [Hf(fmd)<sub>4</sub>] (Hf-FAMD), titanium tetraisopropoxide [Ti(O-<sup>i</sup>Pr)<sub>4</sub>] (TTIP), and water as the co-reactant (oxygen source) is H<sub>2</sub>O.<sup>17,18</sup> The Sr, Hf, and Ti precursors were heated to 130, 115 and 40 °C, respectively. The quaternary compound was grown using the dosing and purging times indicated in Figure 3.2. For each metalorganic precursor, a 2-s dose time saturates the surface;<sup>17,18</sup> the co-reactant water is dosed for 1 s. Following each precursor or water dose, a 15-s Ar purge is required. Our previous work showed that excess Sr was required to initiate the growth of STO on Ge. For this reason the Sr:Ti cycle ratio is 2:1<sup>18</sup> and we adopted that protocol herein. Similarly, SHO growth on Ge used a Sr:Hf cycle ratio of 1:1 and that was used herein.<sup>17</sup> The cycle ratios in Figure 3.2 produce SHTO films that are slightly Sr-rich (Sr/(Sr+Hf+Ti)) and a 1:1 (Sr:Ti+Hf) film requires some of the Sr:Ti cycle ratios to be 1:1 rather than 2:1. The quaternary SrHf<sub>x</sub>Ti<sub>1-x</sub>O<sub>3</sub> oxide was grown by adjusting the number of *m* SHO subcycles and *n* STO subcycles to vary the Hf content (*x*). Different film thickness is controlled by the total number of supercycles *l*.

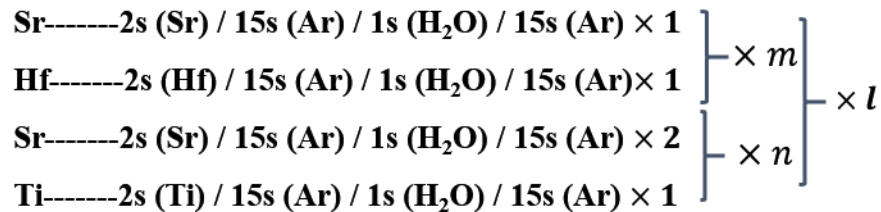


Figure 3.2. Schematic of the growth method for SrHf<sub>x</sub>Ti<sub>1-x</sub>O<sub>3</sub> films by ALD. The subcycles *m* for SHO and *n* for STO are adjusted to realize different Hf content *x*. Different film thicknesses were realized by adjusting the number of *l* supercycles.

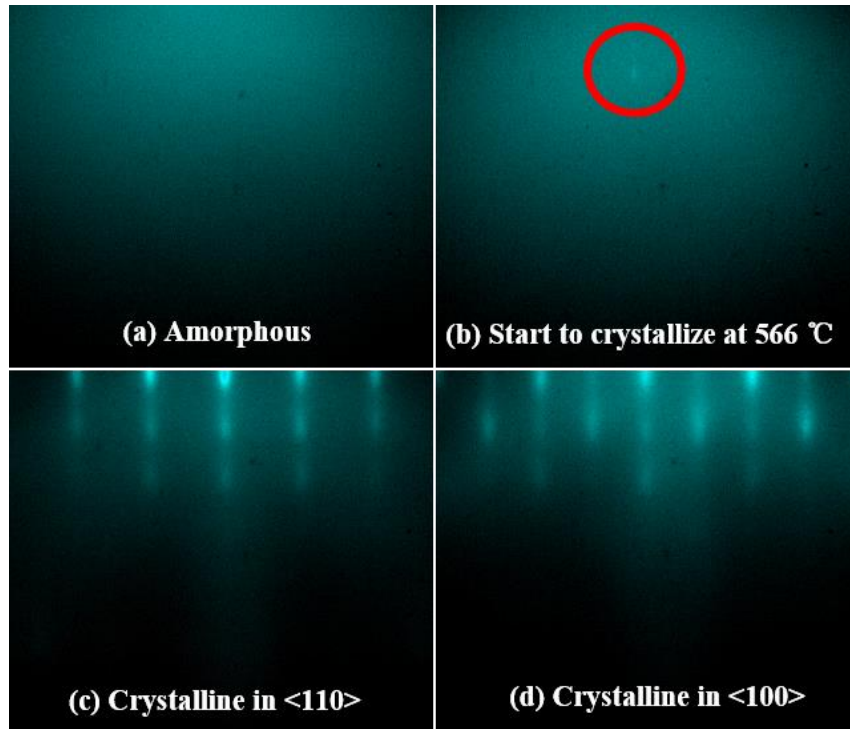


Figure 3.3. RHEED images for an 11.4-nm  $\text{SrHf}_{0.34}\text{Ti}_{0.66}\text{O}_3$  film showing (a) the as-grown amorphous film, (b) the first pattern spot appearing at 568 °C, and (c) and (d) images were taken at 200 °C following annealing at 588 °C for 5 min. The beam was aligned along  $\langle 110 \rangle$  and  $\langle 100 \rangle$  directions for (c) and (d), respectively. This film was grown with a  $m:n = 1:1$  subcycle ratio and  $l=20$  supercycles.

Following ALD growth, the sample is transferred back to the MBE chamber and annealed while monitoring the surface in real time with RHEED to follow the transformation from amorphous to crystalline. The substrate is heated from 200 to 500 °C with a 20 °C  $\text{min}^{-1}$  ramp rate, followed by a ramp rate of 10 °C  $\text{min}^{-1}$  as the temperature is increased further. Figure 3.3 shows a representative transformation for an 11.4-nm  $\text{SrHf}_{0.34}\text{Ti}_{0.66}\text{O}_3$  film. The temperature at which spots in the RHEED pattern emerge is monitored and this is referred to as the crystallization temperature. The temperature is then increased by 20 °C and held at this as annealing temperature for 5 min to fully crystallize

the sample. The sample is then cooled to 200 °C at 30 °C min<sup>-1</sup> and transferred from the MBE chamber.

The films were characterized by *in situ* x-ray photoelectron spectroscopy (XPS) to analyze the composition and uniformity using monochromatic Al K $\alpha$  source at 1486.6 eV and a VG Scienta R3000 analyzer, which is calibrated by a silver foil. High-resolution spectra are measured five times and summed up for the Sr (3*d*, 3*p*) Ti 2*p*, O 1*s*, C 1*s*, Hf 4*f* and Ge 3*d* features. The measurement settings were 50 meV steps with 157 ms/step dwell time and 100 eV pass energy with a 0.4 mm analyzer slit width, which resulted in 350 meV effective resolution. The stoichiometry and Hf content *x*, which is defined as the ratio of Hf to (Hf+Ti), for the SrHf<sub>*x*</sub>Ti<sub>1-*x*</sub>O<sub>3</sub> films are calculated by the integrated area of the Sr 3*d*, Ti 2*p* and Hf 4*f* peaks. The atomic sensitivity factors (ASF) for Sr 3*d*, Ti 2*p* and Hf 4*f* are set as 1.843, 2.001 and 2.639, respectively.<sup>25</sup>

The thickness and crystallinity of the SHTO films were measured by x-ray reflectivity (XRR) and x-ray diffraction (XRD) on a Rigaku Ultima IV system with a Cu K $\alpha$  source. The interface of selected samples was examined by cross-sectional transmission electron microscopy (TEM). The samples were prepared via standard cross-section method with Ar ion milling. Aberration-corrected scanning transmission electron microscopy (STEM) was used for further interface study. The TEM images were taken with a JEOL 2010F and STEM images were taken with a JEOL ARM 200F. Electron-energy-loss spectroscopy (EELS) composition mapping was also applied to investigate the elemental distributions in the growth direction.

Electrical properties (dielectric constant *k* and leakage current *I*) were established for some samples by fabricating standard metal oxide semiconductor capacitor (MOS capacitor) structures. The films had a top electrode of TaN applied by sputtering and 15  $\mu$ m photolithographic features were defined with a SF6-based plasma etch. After building

up the MOS capacitor structure the back side of the wafer was scratched and silver paste was applied to form the bottom electrode. The capacitance-voltage (C-V) and current-voltage (I-V) measurements were performed on an Agilent B1500A semiconductor device parameter analyzer with a Cascade Microtech probe station.

### 3.3. RESULTS AND DISCUSSION

#### 3.3.1. DEPOSITION AND CRYSTALLIZATION OF $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$ FILMS

Films with Hf content  $x$  distributed from 0 to 1 and with thickness between 8.0 to 14.0 nm were grown. SHTO films were deposited with subcycle ratios (Figure 3.2.)  $m:n$  from 1:3 to 7:1. STO ( $x = 0$ ) and SHO ( $x = 1$ ) films were deposited with around 100 cycles producing films that were 9.7 and 12.6 nm thick, respectively. SHTO films were grown with a total of  $l \times (2m+3n)$  cycles as the composition was varied.

The XRD and rocking curve measurements confirmed the crystallization that was indicated by RHEED. Figure 3.4 shows the  $\theta$ -2 $\theta$  XRD and rocking curve around the film (002) reflection at  $2\theta = 45.45 \pm 0.5^\circ$  for a film with a composition of  $\text{SrHf}_{0.47}\text{Ti}_{0.53}\text{O}_3$ . This sample was grown with a  $m:n=2:1$  subcycle ratio and  $l=14$  supercycles. The crystallization temperature was found to be 612 °C. *Ex situ* XRR measurement indicates the thickness is 13.9 nm. The (002) reflection at  $2\theta = 45.45 \pm 0.5$  leads to an out-of-plane lattice constant of  $c = 3.989 \pm 0.005$  Å. The rocking curve scan around the SHTO (002) reflection reveals a full-width half-maximum (FWHM) of  $1.3^\circ$ . The best quality STO and SHO films grown by ALD had FWHM of  $0.8^\circ$  and  $1.2^\circ$ , respectively.<sup>17,18</sup>

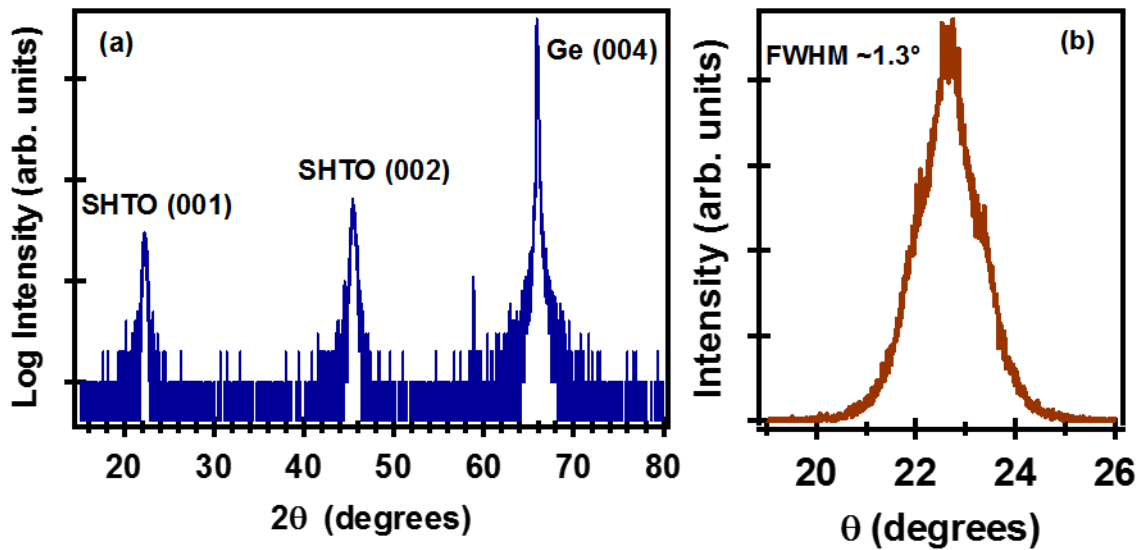


Figure 3.4. X-ray diffraction pattern (a), and rocking curve (b), for 13.9-nm  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  ( $x=0.47$ ) film grown on Ge by ALD, and annealed at  $632^\circ\text{C}$  for 5 minutes in vacuum. The peak of the SHTO (002) reflection is at  $2\theta = 45.45 \pm 0.5^\circ$  and the rocking curve for the (002) reflection has a full width at half maximum (FWHM) of  $1.3^\circ$ .

Figure 3.5.(a) illustrates that the temperature for crystallization onset increases monotonically with increasing Hf content from  $510^\circ\text{C}$  for STO to  $640^\circ\text{C}$  for SHO. The bulk lattice constants for SHO and STO are  $4.069 \text{ \AA}$  and  $3.905 \text{ \AA}$ , respectively. By assuming SHTO forms a substitutional alloy, and by applying Vegard's law the bulk SHTO lattice constants (a) can be estimated as a function of  $x$  and these are represented by the red line in Figure 3.5.(b). From Ge surface spacing of  $3.992 \text{ \AA}$  along the  $\langle 110 \rangle$  direction we compute the in-plane strain that should result from a fully-strained, commensurate film at room temperature and present this as the green line in Figure 3.5.(b). The fully-strained films vary from tensile (2.2 % for  $x = 0$ ) to compressive (-1.9 % for  $x = 1$ ). At room temperature a value of  $x \sim 0.53$  should give zero strain.



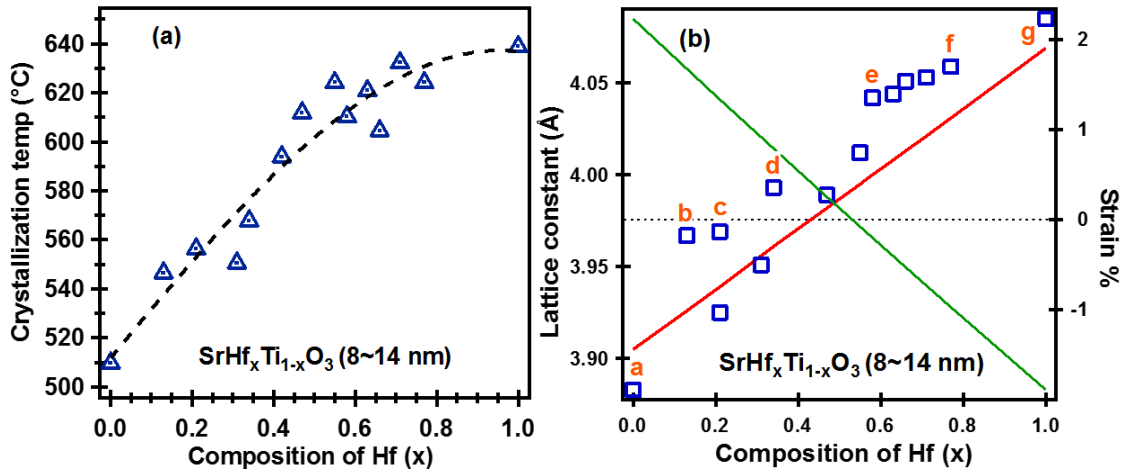


Figure 3.5. (a) Crystallization temperature  $T$  versus Hf content  $x$  for 8- to 14-nm thick  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  films. The dashed line is drawn to guide the eye. (b) Predicted dependence of the bulk lattice constant and strain with Hf content  $x$  at room temperature. The red line indicates the cubic lattice constant  $a$  of bulk  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  based on Vegard's law. The green line presents the strain for commensurate and fully-strained SHTO films on Ge. The squares are the experimental out-of-plane lattice constants  $c$  for different  $x$ ; the squares with letters indicate that samples with Sr-rich compositions of around 55% and other squares indicate samples for which the Sr composition varied from 49% to 51%.

The coefficient of thermal expansion for Ge is approximately one order of magnitude less than a composition averaged value for SHTO alloys,<sup>26–28</sup> over the 500-650 °C annealing window the  $x$ -value that matches the Ge separation distance decreases from  $x \sim 0.4$  to  $x \sim 0.3$ . We sought to determine how interface strain influenced crystallization. The monotonically increasing crystallization temperature with  $x$  (Figure 3.5.(a)) suggests that the annealing temperature does not depend on the strain between the substrate and the SHTO alloy but is rather dependent on the atomic mass of the elements that comprise the alloy. Since the atomic mass of Hf is much heavier than Ti, it will require more thermal energy to move Hf to the correct location of the perovskite structure to crystallize compared to Ti, similar to what has been found for A-site cations in  $\text{ATiO}_3$  perovskites.<sup>29</sup>

Both Sr-rich or Sr-lean stoichiometry in STO films grown on STO produced an out-of-plane lattice constant that was greater than expected for a fully strained film.<sup>30</sup> The SHTO out-of-plane lattice constants  $c$  are determined from the (002) XRD reflections and are also presented in Figure 3.5.(b). In general the experimental  $c$ -values are greater than  $a$  for compressive films and less than  $a$  for tensile films, consistent with expectations for commensurate films. However, squares with letters in Figure 3.5.(b) correspond to samples that have Sr-enrichment around 55% and could be expected to have out-of-plane lattice constants that are greater than fully-strained stoichiometric films. A fully-strained stoichiometric STO film on Ge should have an out-of-plane lattice constant of 3.852 Å at room temperature based on a Poisson ratio for STO of 0.232.<sup>31</sup> The experimental STO value (Square (a) in Figure 3.5.(b)) is 3.883 Å due to Sr-enrichment. Similarly Squares (b), (c) and (d) correspond to samples with out-of-plane lattice constants that are greater than the bulk, cubic lattice constant. Whereas, films with similar compositions as (b), (c) and (d), which should be under tension if fully strained, display out-of-plane lattice constants less than  $a$ .

### 3.3.2. *IN SITU* XPS STUDY AND COMPOSITION UNIFORMITY

In situ XPS was performed on the SHTO films before and after annealing. Figure 3.6 presents results for a SrHf<sub>0.56</sub>Ti<sub>0.44</sub>O<sub>3</sub> film grown with an  $m:n=3:1$  subcycle ratio and  $l=11$  supercycles. This film started to crystallize at 633 °C had an out-of-plane lattice constant of 4.04 Å. Figures 3.6.(a)-(e) present the Sr 3*d*, Ti 2*p*, O 1*s*, C 1*s* + Sr 3*p* and Hf 4*f* core levels, respectively. The Sr 3*d*<sub>3/2</sub> and 3*d*<sub>5/2</sub> peaks are located at binding energies of 135.5 eV and 133.8 eV, respectively, which indicates that the Sr is fully oxidized (Sr<sup>2+</sup>) in the SHTO film.<sup>32</sup> Similarly, the Ti 2*p* and Hf 4*f* features in Figures 3.6.(b) and (e),

respectively, correspond to fully oxidized Ti ( $\text{Ti}^{4+}$ ) and Hf ( $\text{Hf}^{4+}$ ). Figure 3.6.(d) shows that there is no carbon peak at the C 1s position of 285 eV.

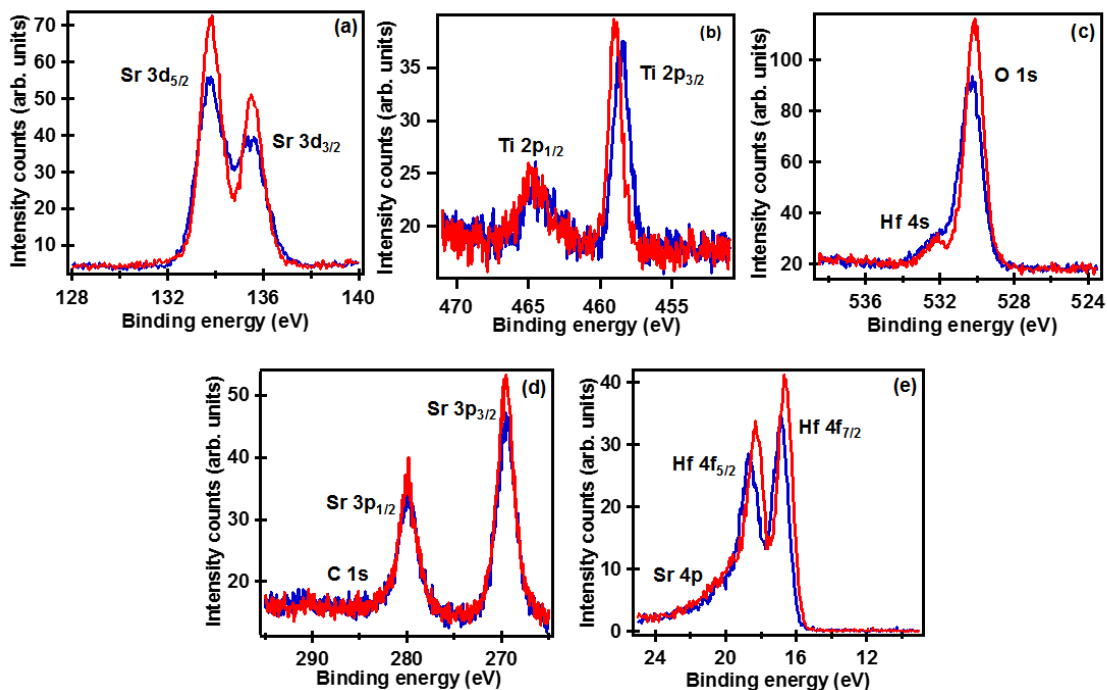


Figure 3.6. X-ray photoelectron spectra for Sr 3d (a), Ti 2p (b), O 1s (c), C 1s and Sr 3p (d) and Hf 4f (e) in a  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  ( $x=0.56$ ) film grown by ALD on Ge (001). The blue line in each figure corresponds to the spectrum post-deposition annealing and the red line corresponds to the spectrum after annealing at 654 °C for 5 min.

The Hf content  $x = 0.56$  and stoichiometry are determined by integrating the areas of the Sr 3d, Ti 2p and Hf 4f features. The Sr:(Hf+Ti) ratios for films in this study reveal a stoichiometry that is consistent with an  $\text{ABO}_3$  perovskite. Previous work in our group has shown that slightly Sr-rich films (i.e., A-rich) crystallize more readily on Ge (001) than B-rich films.<sup>17,18</sup> The Sr composition (viz.,  $\text{Sr}/(\text{Sr}+\text{Hf}+\text{Ti})$ ) of all crystallized films falls in the range from 50% to 56%. Some films outside this range, such as 47% Sr, are still observed to crystallize. However, the RHEED images for such films (not shown) suggest rough

surfaces and imply lower crystalline quality. The sample in Figure 3.6 has the ratio of A:B of 55:45. The value of  $x = 0.56$  for a  $m:n = 3:1$  subcycle ratio suggests that Ti is more readily incorporated into the SHTO alloy during ALD.

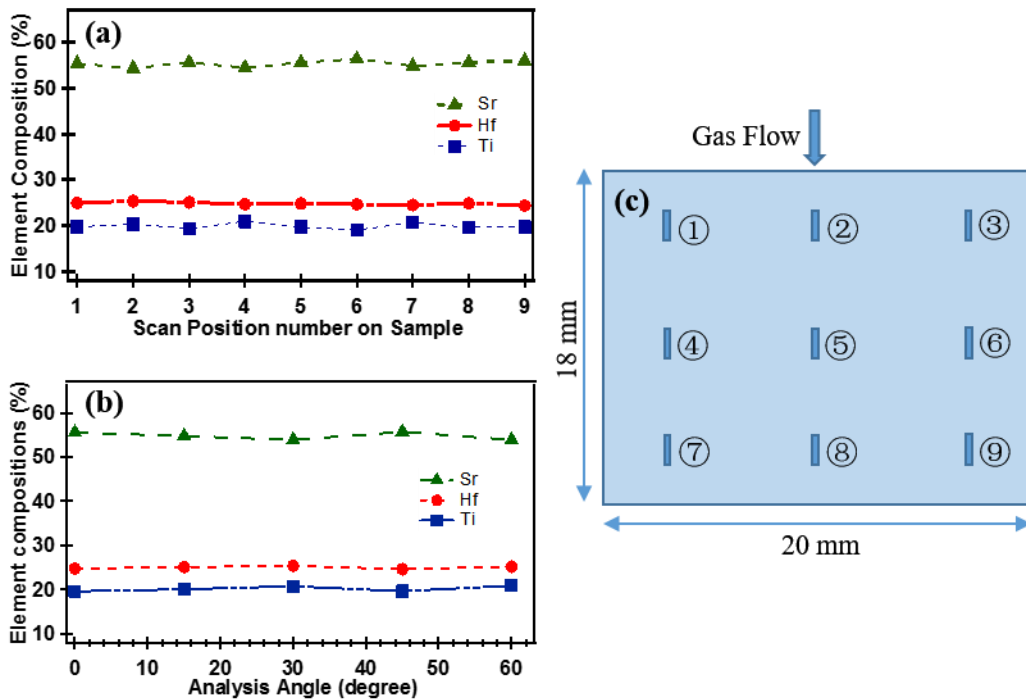


Figure 3.7. (a) XPS position analysis and (b) angle-resolved XPS at location Number 5 for the  $\text{SrHf}_{0.56}\text{Ti}_{0.44}\text{O}_3$  film after annealing  $654\text{ }^\circ\text{C}$ . (c) Schematic showing the sampling points and orientation in the ALD chamber for the  $18 \times 20\text{ mm}^2$  Ge substrate. The Numbers 1 to 9 denote different XPS sampling points; an area of  $3 \times 1\text{ mm}^2$  is probed at each sample point. The Sr, Hf and Ti compositions are  $55.3 \pm 1.0\%$ ,  $24.8 \pm 0.5\%$  and  $20.0 \pm 1.0\%$ , respectively, for the nine positions. The analysis angles in (b) of  $0, 15, 30, 45$  and  $60^\circ$  correspond to sampling depths of  $6.2, 6.0, 5.4, 4.4$  and  $3.1\text{ nm}$ , respectively. For all the AR-XPS scans, the Sr, Hf and Ti compositions have ranges of  $54.8 \pm 0.9\%$ ,  $25.0 \pm 0.6\%$  and  $20.3 \pm 0.6\%$ , respectively.

Compositional uniformity across the Ge substrates and throughout the films was probed with XPS. Figure 3.7 presents results for the film discussed in Figure 3.6 after

annealing at 653 °C for 5 min. The composition is uniform across the film and constant with gas flow direction, as would be expected for an ALD process. Angle-resolved XPS (AR-XPS) was performed at wafer position Number 5 to examine uniformity of the composition through the thickness of the film. Based on the universal escape depth curve<sup>33</sup> we estimate nearly 97% of the signal comes from a depth of no more than ~6.2 nm from the SHTO surface when the sample is fixed on the horizontal plane (*i.e.*, 0°). The analysis angles (Figure 3.7.(b) of 0, 15, 30, 45 and 60° correspond to sampling depths of 6.2, 6.0, 5.4, 4.4 and 3.1 nm, respectively. For the AR-XPS scans that sampled depths of 6.2 to 3.1 nm from the free surface, the Sr, Hf and Ti compositions were 54.8±0.9%, 25.0±0.6% and 20.3±0.6%, respectively, confirming the composition uniformity with depth of the film.

### 3.3.3. MICROSTRUCTURE

Previous work in our group showed that annealing temperature affected the interface trap density of SHO films. Films annealed at less than 650 °C retained an abrupt Ge-SHO interface. SHO annealed at 700 °C and 750 °C resulted in an interfacial layer evidenced by Ge XPS features that are suggestive of a hafnium germanide and a noncrystalline layer at the SHO-Ge interface.<sup>17</sup> TEM and HAADF analysis was performed on a 14-nm SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> sample which was grown with subcycle ratio  $m:n= 3:1$ , supercycle  $l=11$  but Sr:Ti=1:1 in subcycle  $n$  so that it had lower Sr composition that is 49%. A film with good thickness uniformity and surface smoothness is visible in Figure 3.8.(a). Figures 3.8.(b) and (c) show high-angle annular-dark-field (HAADF) STEM images of the SHTO-Ge interface and Figure 3.9 shows enlargements from selected regions in Figure 3.8.(c). An abrupt interface without an obvious interfacial layer is found when the SHTO was annealed at 645 °C, which is consistent with previous work.<sup>17</sup> EELS

composition mapping was performed to investigate the element distribution across the interface. The EELS data presented in Figure 3.10 indicate that there is no interdiffusion across the Ge-SHTO interface and that the SHTO composition is spatially uniform.

Local grain tilting is apparent in Figure 3.8.(b). The solid lines  $mn$  and  $nl$  represent the Hf/Ti plane adjacent to the Ge (001) surface and the dotted line is an extension of line  $mn$ . The included angle indicates a local crystal tilt near the Ge surface of  $\sim 2.2^\circ$ . Grain tilting can be caused by several factors, including lattice mismatch and the resulting film strain.<sup>34,35</sup> The  $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  film is expected to be under compressive strain through the crystallization and cooling steps since  $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  will have about -0.1 % compressive strain at room temperature (Figure 3.5.(b)). During the transformation from amorphous into a crystalline film, nucleation likely occurs at the Ge-SHTO interface to produce the epitaxial film. The presence of a stepped surface with a step height of  $\sim 5.658/4 = 1.414 \text{ \AA}$ , as visible in Figure 3.8.(b) and Region 1 in Figure 3.8.(c), will impact the lateral area over which an epitaxial crystalline domain can grow before it encounters a step that is not commensurate with the SHTO step height.

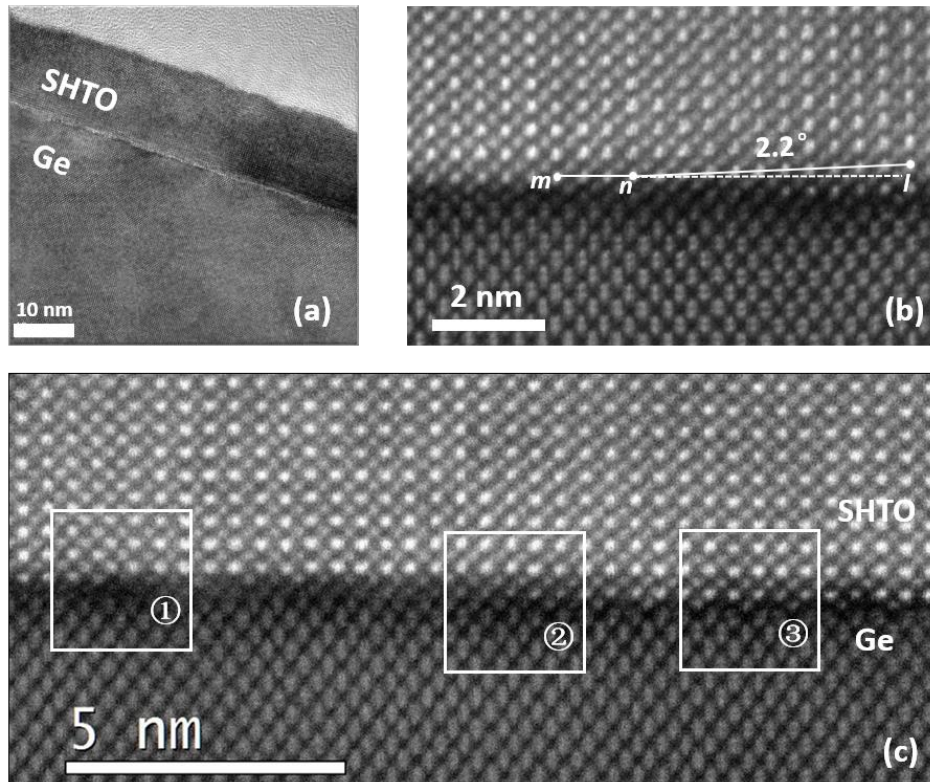


Figure 3.8. Transmission electron micrographs of 14.0-nm  $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  with post-deposition annealing at  $645^\circ\text{C}$  for 5 minutes: (a) TEM image showing the cross-sectional view of the SHTO film, (b) and (c) high-resolution HAADF STEM images showing details of the SHTO-Ge interface. In figure (b), the full lines  $mn$  and  $nl$  indicate the Hf/Ti plane, the dotted line is the extension of  $mn$ . The included angle between  $nl$  and the dotted line is  $\sim 2.2^\circ$ . Regions ①, ② and ③ in Figure (c) are selected for further analysis in Figure 3.9.

An antiphase boundary (APB) is visible in Region 1 of Figure 3.8.(c) as illustrated in the enlargements in Figures 3.9.(a) and (c). One possible reason is that crystallites initiated on adjacent Ge regions extend laterally to intersect above the surface step leading to APB formation in the crystalline film.<sup>36</sup> The Ge columns near the APB in Figure 3.9.(a) and (c) are misaligned with the Hf/Ti columns and some unbonded (to Sr) Ge is visible at the interface, which is consistent with APB observed for STO grown by MBE on Si(001).<sup>36</sup>

Since the misalignments can result in increased Ge–Sr bond length, dangling Ge bonds may also be formed. Both of the column misalignments and the possible Ge dangling bonds at the interface may contribute to the electrical trap states observed for SHO.<sup>17</sup> Surface steps without the formation of an APB are also indicated in Figure 3.9.(b) and (e).

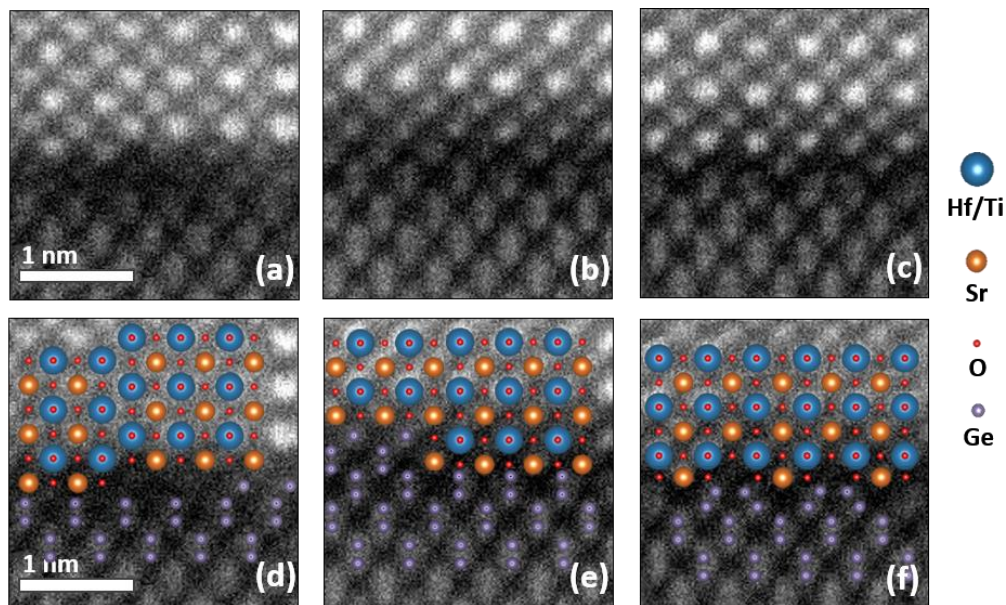


Figure 3.9. HAADF STEM images (a), (b) and (c) showing enlarged portions of regions ①, ② and ③ in Figure 3.8 (c), respectively. (d), (e) and (f) HAADF STEM images with atomic structure overlaid for (a), (b) and (c), respectively.

The enlarged images in Figures 3.9.(c) and (f) indicate that some Ge retains the  $2\times 1$  reconstruction present in the starting surface (Figure 3.1) during ALD and annealing. The local interface structure in Figures 3.9.(b)/(e) and 5(c)/(f) is similar to that found for BaTiO<sub>3</sub> deposited by molecular beam epitaxy on a Ge (001) surface that first had  $\frac{1}{2}$  monolayer of Sr deposited to form a Zintl layer.<sup>37</sup> This might indicate that Sr orders at the Ge (001) surface during the initial ALD cycles to form a Zintl layer with Ge.



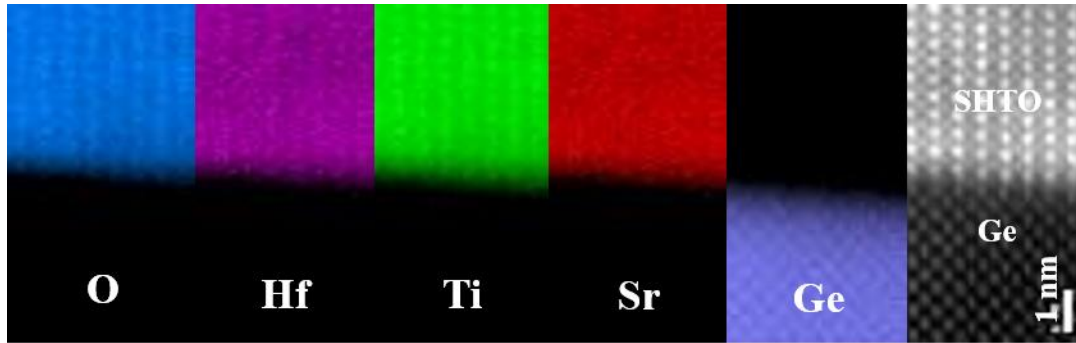


Figure 3.10. Electron-energy-loss spectroscopy (EELS) mapping to determine the elemental distribution in selected region for 14.0-nm  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  with  $x = 0.55$ , post-deposition annealed at  $645^\circ\text{C}$  for 5 min.

### 3.3.4. ELECTRICAL PROPERTIES

The crystallization temperatures for 4-nm thick STO and SHO were around  $510$  and  $640^\circ\text{C}$ , respectively.<sup>17,18</sup> At  $700^\circ\text{C}$ , an interfacial layer forms at the Ge-SHO interface that is  $<0.5$  nm thick. This interfacial layer may contribute to the  $D_{it}$  values that were as low as  $2 \times 10^{12} \text{ cm}^{-2}\text{eV}^{-1}$  for crystalline SHO and increased with higher annealing temperatures.<sup>17</sup> To test the dielectric performance, the  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  film with  $x = 0.55$  was used to fabricate a MOS capacitor. The microstructure for this film revealed the presence of an APB in the near surface region and partial  $2 \times 1$  reconstruction at the Ge-SHTO interface and these could be sources of interface trap states. The  $C-V$  and  $I-V$  curves are shown in Figures 3.11.(a) and (b), respectively. From Figure 3.11.(a), the capacitance achieves saturation at around  $1.93 \mu\text{F}/\text{cm}^2$ , corresponding to a relative dielectric constant of  $k \sim 30$ . The  $I-V$  measurement shows leakage current of  $0.1 \text{ A}/\text{cm}^2$  at  $1 \text{ MV}/\text{cm}$  with an  $\text{EOT} = 1.8 \text{ nm}$ . Previous work in our group showed that the dielectric constant for SHO (4.6 nm) and STO (15 nm) is around 17 and 90, respectively.<sup>17,18</sup> If  $k$  is linearly dependent

on Hf content,  $k$  for the 14.0 nm  $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  film is expected to appear at  $x=0.82$ , which indicates that the dielectric constant is more heavily weighted by Hf. The leakage current for SHO is  $6.3 \times 10^{-6}$  A/cm<sup>2</sup> at 1 MV/cm with an EOT = 1.0 nm and for STO is 10 A/cm<sup>2</sup> at 1 MV/cm with an EOT = 0.7 nm. Ignoring the EOT difference, which will cause even higher leakage current for this  $\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  sample if it was considered, the leakage current of SHTO shows two orders of magnitude improvement over STO. However, the leakage current was too high to allow reliable  $D_{it}$  to be measured and understand any relation to the microstructure.

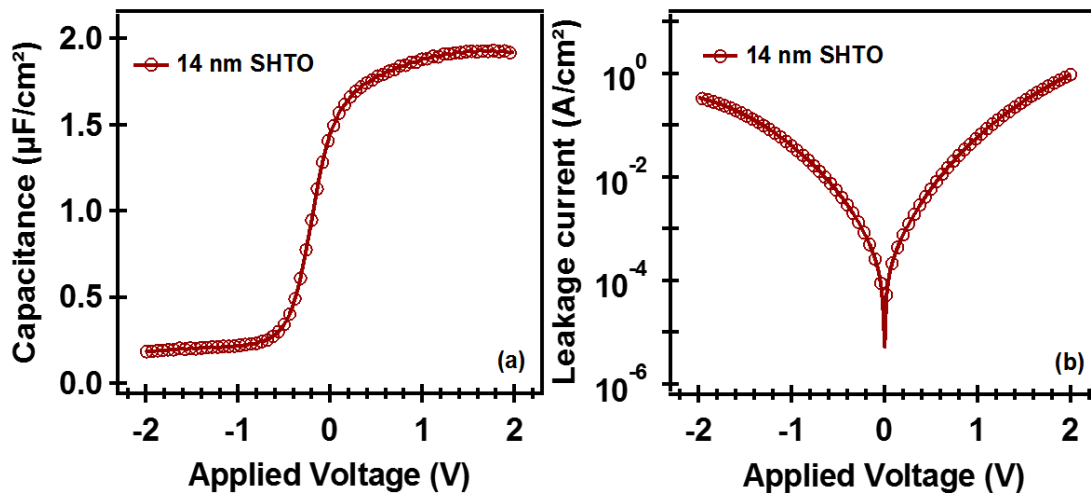


Figure 3.11. Capacitance-voltage (C-V) measurement (a) and current-voltage (I-V) measurement (b) for 14.0-nm  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  ( $x = 0.55$ ) film on Ge. The dielectric constant  $k=30$  and leakage current is  $0.1$  A/cm<sup>2</sup> at 1 MV/cm with EOT = 1.8 nm.

### 3.4. SUMMARY

In this study, amorphous  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  films have been grown on Ge (001) by ALD and annealed between 530°C to 660 °C to crystallize the film into a heteroepitaxial layer.

Matching the lattice constant for the SHTO alloy with the substrate by adjusting  $x$  did not lower the SHTO crystallization temperature rather the crystallization temperature increased monotonically with increasing Hf content. *In situ* XPS and AR-XPS analysis verified the stoichiometry and uniformity of the grown films. The *ex situ* XRR, XRD and rocking curve measurements were used to determine the thickness, lattice constant and crystallinity, respectively. The interface of a selected sample was explored by STEM and EELS mapping. The interface was abrupt with no apparent interdiffusion between the substrate and the SHTO layer. Regions of the Ge surface retained the  $2\times 1$  reconstruction. Film defects included grain tilting and APBs that are attributed to steps at the Ge (001) surface. Capacitance-voltage ( $C-V$ ) and current-voltage ( $I-V$ ) measurements show the dielectric constant  $k=30$  and a leakage current  $I = 0.1 \text{ A/cm}^2$  (at  $1 \text{ MV/cm}$  with  $\text{EOT} = 1.8 \text{ nm}$ ) after the standard MOS capacitor was fabricated from a  $14.0\text{-nm}$  SHTO film with Hf content  $x=0.55$ .

These results for SHTO films illustrate that ALD growth of quaternary perovskites directly on Ge (001) is feasible and that the properties can be adjusted with B-site substitution. Understanding the influence of strain and film microstructure on the  $D_{it}$  for crystalline  $\text{ABO}_3$  layers on Ge necessarily requires systems that do not feature Ti on the B-site since Ti  $3d$  states yield negligible conduction band offset with Ge, and require systems that crystallize at or below  $650 \text{ }^\circ\text{C}$  to maintain an abrupt interface.

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## Chapter 4: Zintl layer formation during perovskite atomic layer deposition on Ge (001)

### 4.1. INTRODUCTION

In 1998, McKee et al. reported that by using a submonolayer of alkaline earth metal (AEM), specifically strontium, one could enable the epitaxial growth of the perovskite  $\text{SrTiO}_3$  on Si.<sup>1</sup> Since then, many groups have carried out studies of the growth of crystalline perovskite materials on various semiconductors, such as Si, Ge, and GaAs, for use as a gate oxide for field effect transistor applications.<sup>2-13</sup> At the same time, several groups also studied the details of the seemingly crucial sub-monolayer AEM template layer on Si (001),<sup>14-21</sup> which has been identified as being of Zintl character.<sup>14,15,17,18</sup> This Zintl template layer on Si strongly suppresses  $\text{SiO}_2$  formation during the initial perovskite oxide nucleation process,<sup>9,17,22-24</sup> while simultaneously lowering the interface energy.<sup>14-17</sup> The presence of this Zintl template formed by half monolayer (ML) AEM coverage is necessary for direct epitaxy of a perovskite on Si.

After removing the native oxide in ultrahigh vacuum, the Si (001) and Ge (001) surfaces reconstruct via dimerization to form a  $2 \times 1$  structure. When AEM atoms adsorb on the  $2 \times 1$ -reconstructed Si (001) surface, the most favorable bonding location for the AEM atoms is the four-fold site in the trough between two dimer rows, which has been confirmed both experimentally and theoretically.<sup>14,16,19,22,25</sup> Once the AEM atoms fill all the available four-fold sites on Si (001), the surface acquires the character of a Zintl phase.<sup>14</sup>

The Allen electronegativities of Sr, Ba, Si and Ge are 0.963, 0.881, 1.916 and 1.994, respectively.<sup>26</sup> Because the electronegativity for an AEM atom is much less than that of Si,

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Comments: I designed and conducted the experiment.

the AEM atoms will donate electrons to Si and form the AEM–Si Zintl bond.<sup>14–17,21,27</sup> In the case of Sr on Si, one Sr atom will donate its two valence electrons to the neighboring Si dimer atoms such that each Si dimer atom will receive one electron. This extra electron fills the dangling bond at the down atom of the dimer, removing the dimer tilt (which is a Peierls distortion).<sup>15</sup> The 2×1-reconstructed Ge surface exhibits the same dimer tilting phenomenon as in Si.<sup>15,28</sup> The Ge atoms in a given dimer can become more stable if one of them transfers charge to the other so that the unbounded orbital of the receiving atom becomes filled and that of the donating atom becomes empty. Because of the nature of the empty and filled orbitals, this transfer results in the dimer tilting on a clean surface.<sup>29</sup>

Due to similar electronegativity for Si and Ge, the AEM atoms are expected to have similar charge transfer behavior on Ge as that on Si, and it would be reasonable to expect the template model developed for Si applies to Ge. While there is less work reported either theoretically or experimentally for AEM atoms on Ge (001) compared to Si (001) there are noticeable differences between the systems.<sup>30–32</sup> Higher-order surface reconstructions are found with 0.5-ML Sr on Ge (100) compared to Si (100), with a 9×1 reconstruction found on Ge.<sup>14,15,21,31</sup> However, a 2×1 reconstruction has been reported for 0.45-ML Ba on Ge (001).<sup>30</sup> Finally, the surface restructuring found with Ge (001) has been associated formation of a Sr-Ge surface alloy and lower Ge–Ge binding energy as compared with a surface ad-layer on Si (001).<sup>31,32</sup>

The epitaxial integration of crystalline perovskites on semiconductors by atomic layer deposition presents an ideal platform to explore numerous technological applications since perovskites can be ferromagnetic,<sup>33,34</sup> ferroelectric,<sup>35,36</sup> multiferroic,<sup>37,38</sup> or superconducting.<sup>39</sup> Previously, we reported the successful growth of SrBO<sub>3</sub> perovskite materials, where B = Ti or Hf, on Ge by ALD.<sup>40–42</sup> However, the mechanism for the initial growth of perovskites on Ge by ALD is not yet understood. One key commonality in the

ALD process is that the Sr cyclopentadienyl precursor is dosed first on the clean Ge surface. High-resolution cross-section scanning transmission electron microscopy imaging of molecular beam epitaxy-grown BaTiO<sub>3</sub> and atomic layer deposition-grown SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> reveal a 2×1 periodicity at the interface with the AEM atom between dimer rows.<sup>42,43</sup> Based on this observation, we believe that the initial atomic layer deposition (ALD) growth for SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> on Ge forms out of the same Zintl template as found in molecular beam epitaxy (MBE) growth. To understand the initial interface reactions during perovskite ALD on Ge, we studied the deposition of Sr and Ba molecular precursors on Ge using *in situ* X-ray photoelectron spectroscopy (XPS) and reflection high energy electron diffraction (RHEED) and compare the results with Sr and Ba deposition using Knudsen cells in an MBE system and with density functional theory calculations.

## 4.2. EXPERIMENT

The Ge (001) substrate is prepared from a 4-inch Ge wafer (Sb-doped,  $\rho \sim 0.04 \text{ } \Omega\text{-cm}$ ) from MTI Corp. as described previously.<sup>40-42</sup> The as-received Ge wafer is diced into approximately 18×20 mm<sup>2</sup> pieces, degreased with acetone, isopropyl alcohol and water, dried and then exposed to ultraviolet light/ozone to remove residual carbon, and transferred into the growth and characterization system. The surface GeO<sub>2</sub> is removed by annealing at 700 °C in vacuum ( $< 2 \times 10^{-9}$  Torr) for 1 hr. The resulting 2×1-reconstructed Ge (001) surface is verified by *in situ* RHEED (Staib Instruments operating at 21 keV). The clean Ge (001) sample is transferred *in vacuo* to an XPS chamber to verify the Ge surface composition. Following this characterization, the sample is then transferred *in situ* and exposed to varying doses of elemental Sr and Ba in the MBE chamber, or exposed to the



metal-organic barium bis(triisopropylcyclopentadienyl) [ $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$ ] or strontium bis(triisopropylcyclopentadienyl) [ $\text{Sr}(\text{}^i\text{Pr}_3\text{Cp})_2$ ] precursors in the ALD chamber.<sup>44</sup>

For elemental deposition in the MBE chamber the substrate was maintained at 600 °C following protocols in previous reports.<sup>44,45</sup> The Ba and Sr fluxes were calibrated with a quartz crystal monitor to a rate of approximately 1 ML/min, allowing for controlled submonolayer dosing. Coverages of Ba or Sr are reported in ML equivalents for MBE-dosed samples, where a ML is defined to be the atomic density of the Ge (001) unreconstructed surface ( $6.26 \times 10^{14}$  atoms/cm<sup>2</sup>). RHEED was used to monitor surface structure *in situ* and in real time. The MBE-dosed samples were cooled to 200 °C before transferring the sample into the XPS analysis chamber. For deposition in the ALD chamber the sample was maintained at 225 °C, the typical temperature used for BaTiO<sub>3</sub> or SrTiO<sub>3</sub> growth.<sup>40,46</sup> The Ba and Sr precursors, barium bis(triisopropylcyclopentadienyl) [ $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$ ] and strontium bis(triisopropylcyclopentadienyl) [ $\text{Sr}(\text{}^i\text{Pr}_3\text{Cp})_2$ ], were supplied by Air Liquide.<sup>47</sup> The Ba precursor was heated to 150 °C and the Sr precursor was heated to 130 °C to produce sufficient vapor pressure for dosing in ALD.<sup>46,48</sup> The ALD chamber was maintained at a total pressure of 1 Torr using Ar carrier gas during dosing and less than  $1 \times 10^{-6}$  Torr when in standby mode. An ALD half-cycle refers to a 2-s exposure to the precursor stream followed by a 20-s Ar purge (referred to as 1 half-cycle herein). A complete cycle refers to a 2-s precursor exposure, 20-s argon purge, 2-s deionized water exposure, and another 20-s argon purge (referred to as 1 cycle herein). The ALD-dosed samples were transferred out of the ALD chamber without cooling the sample and subsequently cooled to room temperature in the transfer line.

*In situ* XPS was performed with a monochromatic Al K $\alpha$  source at 1486.6 eV using a VG Scienta R3000 electron energy analyzer. The analyzer was calibrated with a clean Ag foil such that the Ag 3d<sub>5/2</sub> core level is at a binding energy of 368.28 eV. For high

resolution scans of the Sr  $3d$ , Ba  $3d$ , Ge  $3d$  and C  $1s$  core levels, we use a pass energy of 100 eV in combination with a 0.8 mm entrance slit to yield a minimum linewidth of 300 meV. An incident angle of  $0^\circ$  was used for Sr  $3d$ , Ba  $3d$ , Ge  $3d$  features while an incident angle of  $60^\circ$ , which limits the sampling depth to about 5.0 nm,<sup>49</sup> was used for the C  $1s$  feature. The XPS results were analyzed with CasaXPS (Version 2.3.16 PR 1.6).

The interface structures of samples with a crystalline perovskite oxide grown on top were examined by high-angle annular-dark-field scanning transmission electron microscopy (HAADF STEM). The samples were prepared via standard cross-section method with Ar ion milling. The aberration-corrected STEM images were taken with a JEOL ARM 200F operated at 200 kV. The beam convergence angle was set to 20 mrad and the HAADF images were recorded with collection angle of 90-150 mrad.

### 4.3. THEORY

To clarify experimental results, in particular photoemission, density functional theory (DFT) calculations were performed using the Vienna *ab initio* Simulation Package (VASP) with projector augmented-wave (PAW) pseudopotentials.<sup>50-55</sup> The exchange-correlation energy was calculated in the local density approximation (LDA)<sup>56</sup> and in the generalized gradient approximation (GGA).<sup>57,58</sup> Two germanium pseudopotentials<sup>54,55</sup> were used for both the LDA and GGA calculations: one included only the valence  $4s^24p^2$  electrons, and the other treated the semi-core  $3d$  electrons as valence electrons as well, for a configuration of  $3d^{10}4s^24p^2$ . For strontium and barium, the semi-core  $s$  and  $p$  states are treated as valence states, so the electron configurations are  $4s^24p^65s^2$  and  $5s^25p^66s^2$ , respectively. A plane wave cutoff of 650 eV was utilized for all calculations performed, and a  $6 \times 12 \times 2$  Monkhorst-Pack  $k$ -point grid<sup>59</sup> was used for the  $2 \times 1$  structures. The  $k$ -point

sampling was modified accordingly for the  $2\times 2$  and  $4\times 4$  structures to maintain the same  $k$ -point density in the irreducible wedge of the Brillouin zone. The electronic energy was converged to  $10^{-6}$  eV and the cells were relaxed until the interatomic forces were smaller than  $10^{-2}$  eV/angstrom. For the LDA calculations, the theoretical lattice constants were  $a_{Ge} = 5.625 \text{ \AA}$  and  $a_{Ge} = 5.646 \text{ \AA}$  with and without the  $3d$  semi-core electrons, respectively. For the GGA calculations, the theoretical lattice constants were  $a_{Ge} = 5.758 \text{ \AA}$  and  $a_{Ge} = 5.783 \text{ \AA}$  with and without the  $3d$  semi-core electrons, respectively. The slabs used were 14 layers thick (three and a quarter unit cells), and the thicker slabs (used only in some LDA calculations) were 22 layers thick (five and a quarter unit cells). All slabs had  $15 \text{ \AA}$  of vacuum to prevent spurious interactions with their periodic images. All clean germanium slabs were relaxed until the known surface reconstruction was achieved.<sup>60,61</sup> The  $2\times 1$  slabs had only one tilted dimer on the surface, and the  $2\times 2$  and  $4\times 4$  had multiple dimers tilted in alternating directions. One-half of a monolayer (ML) of barium and strontium atoms were placed in the troughs between the surface dimers,<sup>14,17</sup> and then all the atoms were allowed to relax. After relaxation, the surface dimers in the presence of alkaline-earth atoms are no longer tilted. That is to say, there is no difference between the up and down germanium atoms of a dimer after relaxation. This is illustrated for the fully relaxed  $4\times 4$  slab surface with and without barium in Figure 4.1. The surface dimers are 4.6% shorter with the barium adatoms than they are on the clean germanium surface, and are 0.8% shorter with the strontium adatoms.

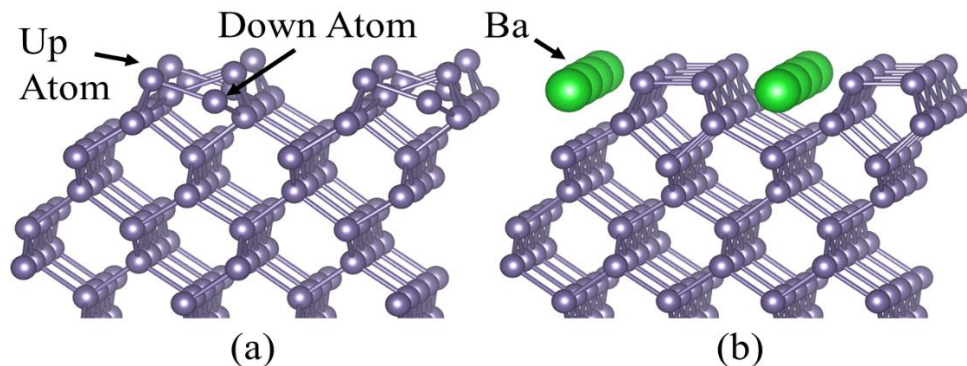


Figure 4.1. (a) Relaxed, clean 4×4 germanium slab surface, exhibiting trough and dimer rows, and alternating tilt angles in the dimers. (b) Relaxed 4×4 germanium slab surface after adding 0.5 ML of barium atoms to the surface. The surface dimers flatten in the presence of alkaline-earth ions.

#### 4.4. RESULTS

Figure 4.2.(a) shows the x-ray photoelectron spectra for the Ba  $3d_{5/2}$  feature for increasing Ba coverage on Ge (001) as deposited by MBE. As the Ba coverage increases, the Ba  $3d_{5/2}$  main peak is found at a binding energy of 781.03 eV, 780.81 eV and 780.63 eV for 0.3 ML, 0.5 ML and 1 ML, respectively. Figure 4.2.(b) shows RHEED patterns corresponding to 0, 0.3 and 0.5 ML of Ba on Ge (001). The clean surface (0-ML Ba) displays the 2×1 reconstruction expected for a bare surface after removal of the oxide layer.<sup>40</sup> RHEED images (not shown) reveal the intensity of the 2×1 spot changes with low coverage but does not fully disappear; it initially weakens at 0.17 ML and then slowly recovers its full intensity after about 0.3 ML and does not significantly change between 0.3 and 0.5 ML. The RHEED images for 0.3- and 0.5-ML Ba are of a 2×1-reconstructed surface. The 2×1-reconstructed surface following 0.5-ML Ba is consistent with results reported by Cattoni et al., for 0.45-ML Ba.<sup>30</sup> Higher-order reconstructions have been

reported for 0.5-ML Ba and Sr on Ge (001).<sup>31</sup> We also observe higher-order reconstructions of Ge (001) for 0.5-ML-MBE Sr on Ge (001) (Figure 4.3), whereas one ALD Ba half-cycle leads to a  $2\times 1$ -reconstructed Ge (001) surface (not shown). More study is required to reconcile the different reconstructions for MBE-based 0.5-ML AEM on Ge (001), but this is beyond the scope of this study.

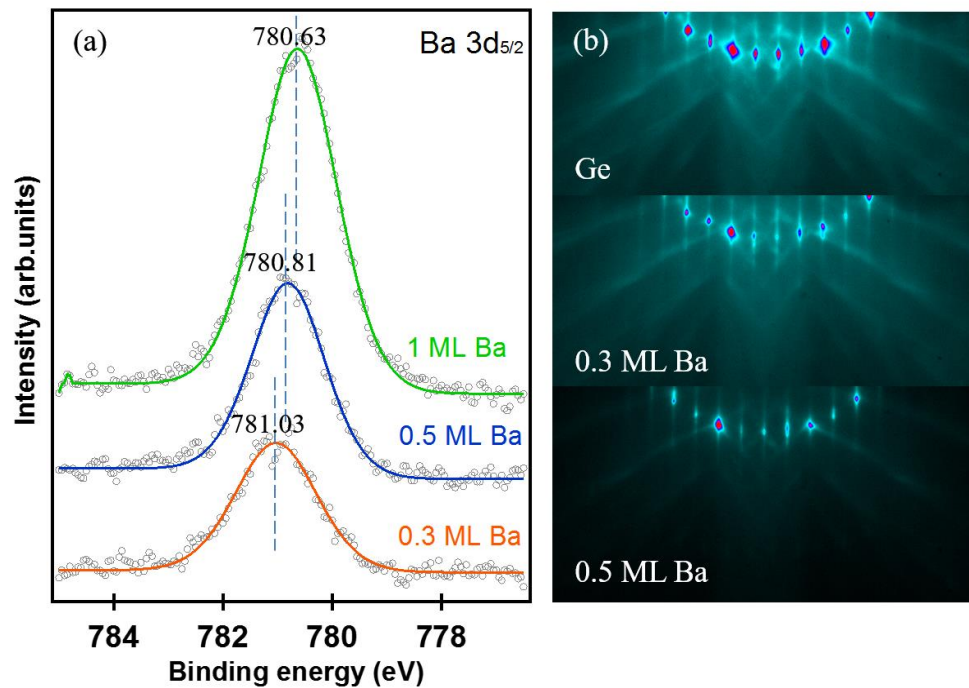


Figure 4.2. (a) X-ray photoelectron spectra of Ba  $3d_{5/2}$  for different Ba coverage on Ge (001). Grey circles indicate the experiment data and the spectral fitting results are shown by solid colored lines. The peak position for each coverage is marked by a dashed line. (b) RHEED images of clean Ge surface before deposition and after deposition of 0.3-ML and 0.5-ML Ba, all images are taken at 200 °C substrate temperature and the beam is aligned along the [110] azimuth.

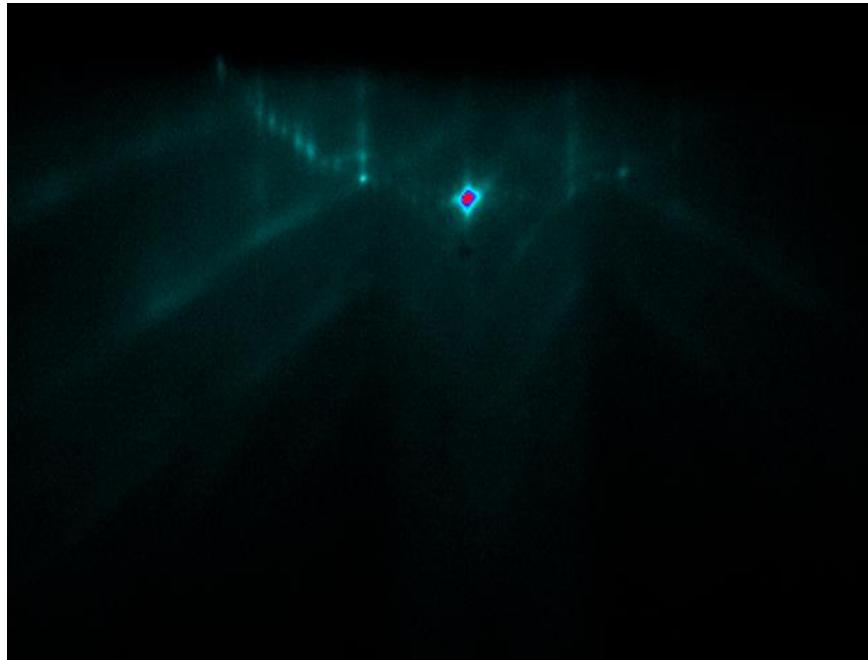


Figure 4.3. RHEED patterns of the Ge (001) surface after depositing of 0.5 ML of Sr. The image is taken at 200 °C and the beam is aligned along the [110] azimuth.

Figure 4.4 is a comparison of the Sr  $3d$  and Ba  $3d_{5/2}$  spectra for MBE dosing and for ALD precursor dosing using a half cycle. Figure 4.5 shows the corresponding changes in the Ge  $3d$  spectra for MBE and ALD dosing with Sr and Ba. We look at the 0.5-ML coverage in more detail since this coverage leads to the  $2\times 1$  Zintl layer that is critical for enabling epitaxial growth of  $\text{SrTiO}_3$  or  $\text{BaTiO}_3$  on Si and Ge by MBE.<sup>1,2,43,45,62–64</sup> Since the background region for Ge is not flat in the 140 to 130-eV range, the spectral data for Sr were smoothed with the Savitzky Golay function using Igor Pro 6.10. For Sr deposition, the Sr  $3d_{5/2}$  peak position shifted from 134.30 eV for 0.5-ML exposure to 133.96 eV for 1.0-ML exposure while the bulk Ge  $3d_{5/2}$  peak shifted from 29.28 eV for bare Ge (001) to 29.60 eV for 0.5-ML Sr exposure. The Sr  $3d_{5/2}$  peak for Ge exposed to one ALD half-cycle is at 134.30 eV with the bulk Ge  $3d_{5/2}$  peak shifted to a higher binding energy of 29.69 eV compared to the bare Ge (001) surface. The results for Ba exposure parallel those of Sr;

the bulk Ge  $3d_{5/2}$  peak shifted to a higher binding energy for 0.5-ML Ba or one Ba ALD half-cycle compared to the bare surface binding energy (Figure 4.5.(b)) and the Ba  $3d_{5/2}$  peak shifted to lower binding energy with increasing Ba coverage (Figure 4.2.(a)).

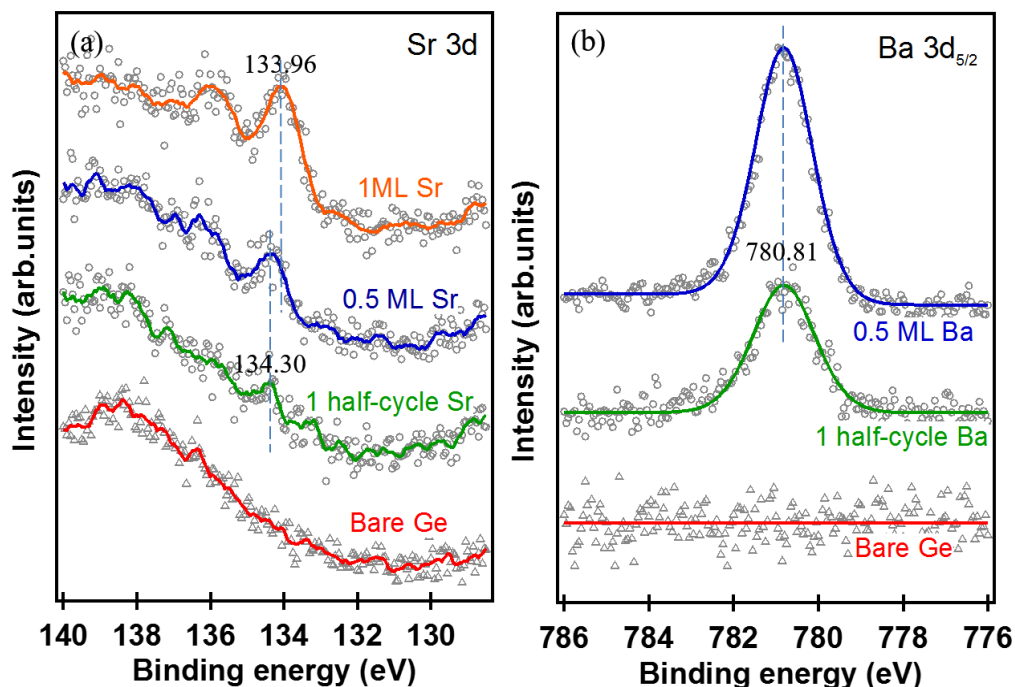


Figure 4.4. X-ray photoelectron spectra of (a) Sr  $3d$  and (b) Ba  $3d_{5/2}$  for Sr and Ba deposited on Ge (001) by ALD and MBE. The grey circles are the experimental data and the spectral fitting results are shown by the solid colored lines. The peak positions for Sr  $3d_{5/2}$  and Ba  $3d_{5/2}$  are marked by the dashed lines.

A 2-s exposure to the Sr or Ba precursor in the first half-cycle of ALD leads to Sr and Ba coverages of approximately 0.2 ML, based on a comparison of the XPS integrated areas for the respective ALD half-cycle exposures and the 0.5-ML exposures in Figure 4.4. Since the XPS signals for Ba were more intense than those of Sr and were not affected by the background feature from pure Ge, additional studies were conducted with Ba. Figure

4.6.(a) presents the Ba 3*d* spectra following a 2-s and a 4-s exposure to Ba(<sup>1</sup>Pr<sub>3</sub>Cp)<sub>2</sub>. The Ba 3*d*<sub>5/2</sub> peak area remained unchanged indicating the saturation coverage is already reached with the 2-s exposure.

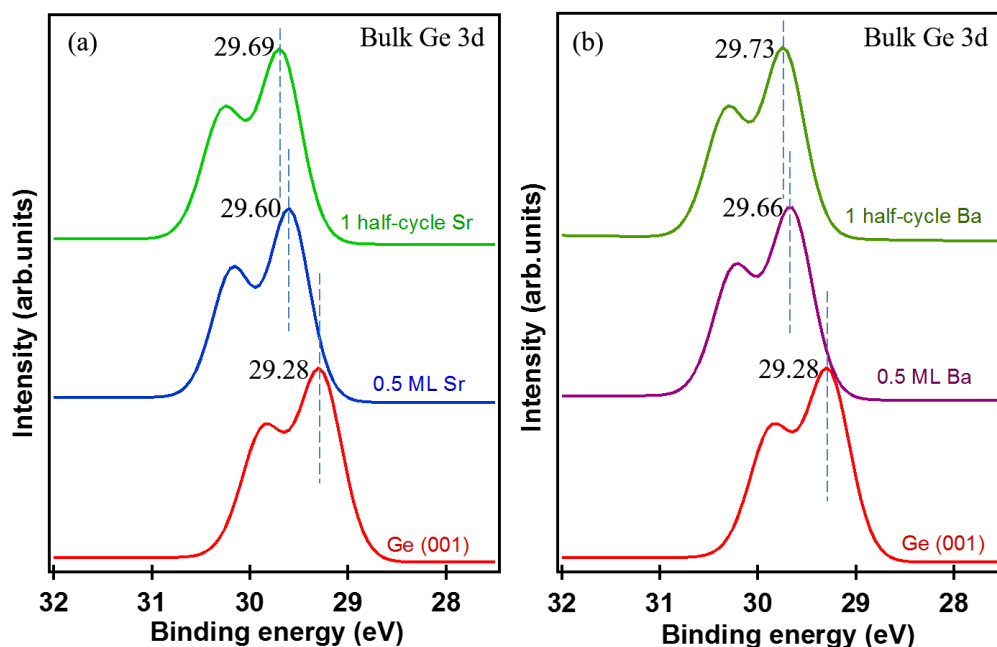


Figure 4.5. X-ray photoelectron spectra of Ge 3*d* for bare Ge, 0.5-ML coverage by MBE and one ALD half-cycle deposition for (a) Sr deposition and (b) Ba deposition. The Ge 3*d*<sub>5/2</sub> peak position is marked by a dashed line.

The nature of the adsorbed Ba precursor could not be determined with certainty since the sample had to be transferred from the ALD chamber, through a transfer line at around  $2 \times 10^{-9}$  Torr into the analysis chamber. Below we present evidence that the surface contains completely and/or partially dissociated precursor, molecular precursor, and ligands following adsorption. During sample transfer, we suspect the residual water in the ALD chamber reacts with the sample surface and effectively completes the ALD cycle. Evidence for this includes the observation that after the sample is moved from the ALD



chamber to the analysis chamber and then back to the ALD chamber for another 2-s exposure to  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$ , the Ba 3d signal intensity increases (Figure 4.6.(b)). Repeating this process leads to a further increase in the Ba 3d signal.

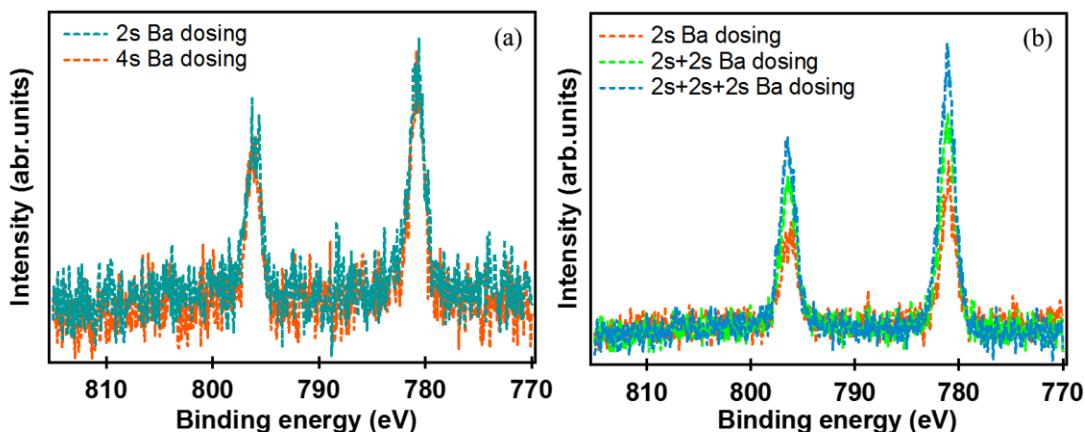


Figure 4.6. (a) Ba 3d X-ray photoelectron spectra after 2-s Ba exposure using the standard half-cycle and after 4-s of exposure followed by the 20-s Ar purge. (b) Ba 3d spectra following a 2-s exposure to the Ba precursor in the ALD chamber and transferring the sample into the analysis chamber, and then repeating this process on the same sample a second time (2s + 2s), and then a third time (2s + 2s + 2s).

Figure 4.7 presents the C 1s X-ray photoelectron spectrum following exposure of a clean Ge (001) surface to the ALD chamber for 1 min and the spectrum following the standard 2-s half-cycle ALD exposure to  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$ . Carbon, likely in the form of precursor  $\text{}^i\text{Pr}_3\text{Cp}$  ligands that persist in the ALD chamber background, adsorbed on the clean Ge (001) surface without exposing the surface to the  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$  precursor. Using the relative sensitivity factors for Ba and C and that a 0.2-ML coverage for Ba results from a half-cycle exposure, we estimate the C coverage to be 1.0 ML.<sup>65</sup> Putting this amount of carbon in perspective, each  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$  molecule contains 1 Ba and 28 C atoms so the surface could easily adsorb the 5 $\times$ -equivalents of C:Ba from adsorbed ligands or ligand

fragments. This carbon level remains constant while Ba accumulates on the surface during the experiments presented in Figure 4.6.(b). Figure 4.8 presents the corresponding C 1s spectra to accompany Figure 4.6.(b) and shows that the C 1s signal remained constant. We note there is no carbon detected in SrHfO<sub>3</sub>, SrTiO<sub>3</sub>, SrTi<sub>x</sub>Hf<sub>1-x</sub>O<sub>3</sub> films that have been grown by ALD on Ge (100) from these precursors so carbon reported herein is somehow associated with the Ge surface.<sup>40-42</sup>

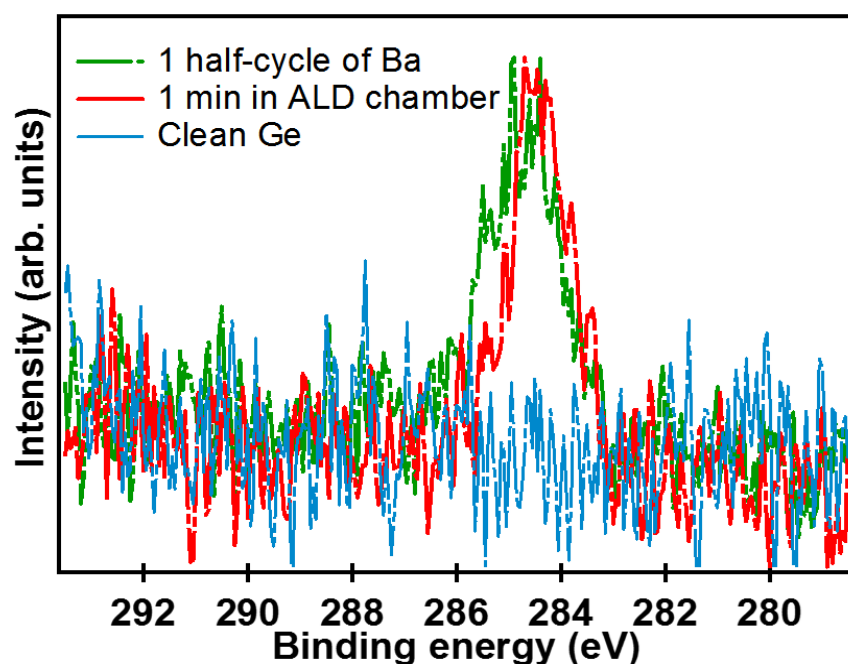


Figure 4.7. C 1s X-ray photoelectron spectra of a clean Ge surface after GeO<sub>2</sub> removal and thermal annealing, for a clean Ge surface that was moved into the ALD chamber for 1 min and then transferred to the analysis chamber, and a clean Ge surface after exposure to the Ba ALD half-cycle.

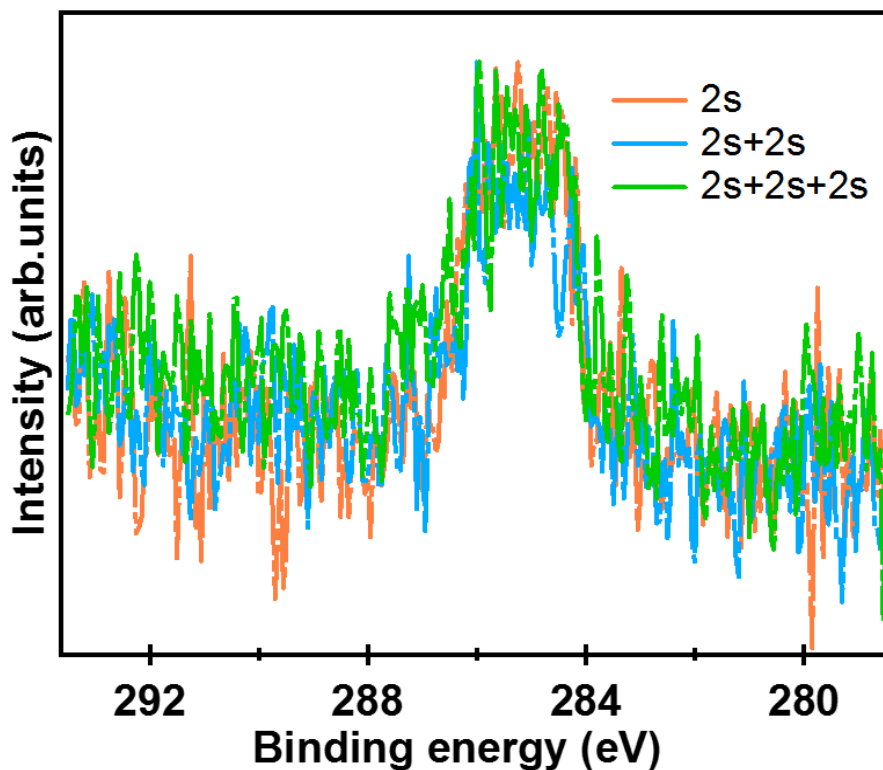


Figure 4.8. C 1s X-ray photoelectron spectra following a 2-s exposure to the Ba precursor in the ALD chamber and transferring the sample into the analysis chamber, and then repeating this process on the same sample a second time (2s + 2s), and then a third time (2s + 2s + 2s).

Evidence in support of some  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$  molecules adsorbing intact following the first half-cycle is provided in Figure 4.9. The Ba peak area is unchanged in Figure 4.9.(a) after one complete cycle when the sample is subjected to the vacuum of the transfer line ( $\sim 2 \times 10^{-9}$  Torr) and the analysis chamber ( $\sim 1 \times 10^{-9}$  Torr) in between the first half-cycle precursor dose and the second half-cycle water dose. When the Ge surface is exposed one complete ALD cycle continuously, *i.e.*, precursor dose, Ar purge, water dose, and Ar purge, the Ba peak area corresponds to 0.4 ML (Figure 4.9.(b)). If some, or all, molecularly-adsorbed  $\text{Ba}(\text{}^i\text{Pr}_3\text{Cp})_2$  desorbs during transfer to the analysis chamber (Figure 4.9.(a)) at the

lower background pressure, a lower Ba signal in Figure 4.9.(a) should be expected compared to Figure 4.9.(b) where the full 2-s exposure coverage was present to react with water in the second half-cycle.

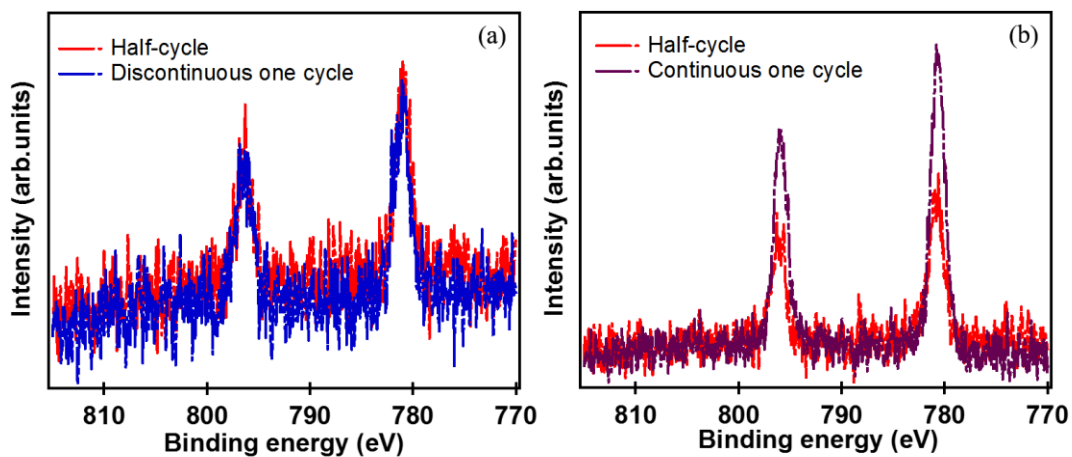


Figure 4.9. Ba 3d X-ray photoelectron spectra when (a) the water exposure in the second half-cycle is performed after transferring the sample into the analysis chamber and back into the ALD chamber, and (b) the water exposure of the second half-cycle is performed before transferring the sample into the analysis chamber.

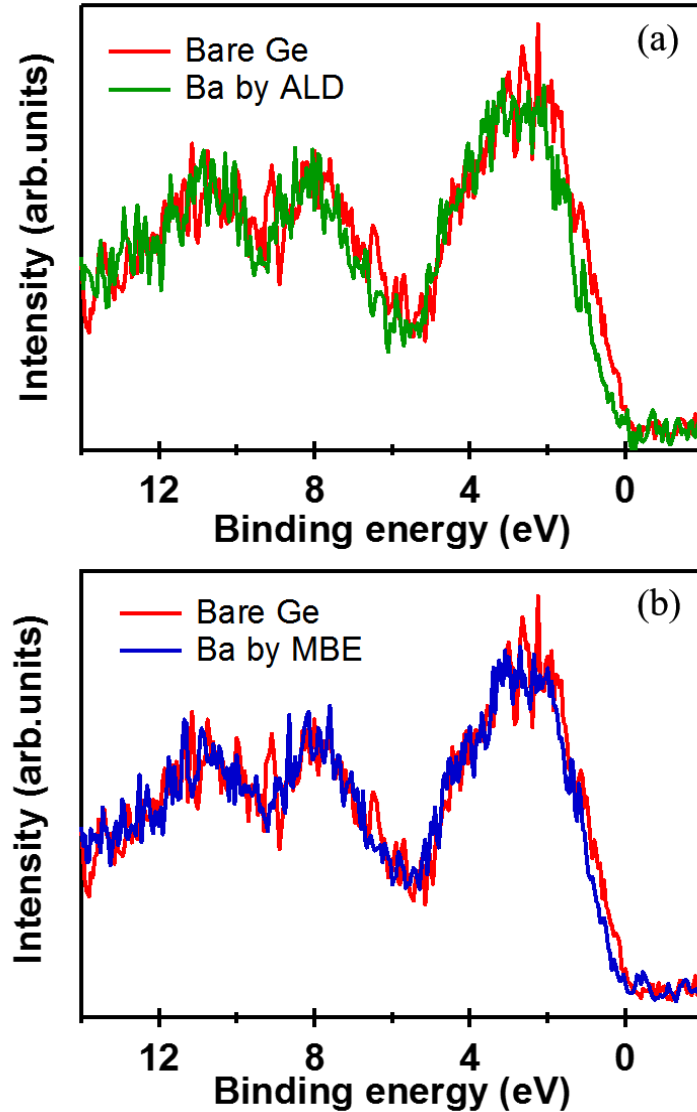


Figure 4.10. Valence band comparisons for bare Ge (001) with (a) one Ba ALD half-cycle and (b) 0.3-ML Ba by MBE.

As shown in Figure 4.5, both Sr and Ba deposition on Ge (001) by ALD and MBE shifted the bulk Ge 3d peak towards higher binding energy compared to bare Ge (001). This Ge binding energy shift is attributed to electron transfer from the AEM (Sr or Ba) to Ge. The charge transfer was verified by comparing the shift of the valence band edge,

which was determined by the linear extrapolation method<sup>66</sup> to the shift in the Ge core levels. Figure 4.10.(a) presents the valence band spectra for clean Ge (001) and after adsorption of one Ba ALD half-cycle, which produces 0.2 ML of Ba, on a different Ge (001) sample. Figure 4.10.(b) presents the valence band spectra for clean Ge (001) and after adsorption of 0.3 ML of Ba in the MBE chamber on a different Ge (001) sample. The Ge valence band edge shifted by 0.34 eV to higher binding energy after the Ba ALD half-cycle and by 0.22 eV to higher binding energy after 0.3-ML-MBE Ba deposition.

Surface core level shifts (SCLS) were explored to further understand the effect of the charge transfer from the alkaline earth metal to Ge, with the results presented in Figure 4.11 for Ba, Figure 4.12 for Sr and Figure 4.13 for bare Ge. Constraints used for deconvolution of the Ge 3*d* spectrum were obtained from the literature. Ge 3*d*<sub>3/2</sub> and Ge 3*d*<sub>5/2</sub> components have an intensity ratio of 0.67 and a separation of 0.585 eV.<sup>67</sup> The surface core level shifts for Ge were calculated based on full final-state theory and, relative to the bulk component, found to be -0.67 eV for the up atom, -0.39 eV for the down atom, and -0.16 eV for second layer atoms.<sup>28</sup> A Ge<sup>+1</sup> component with a relative binding energy of +0.7 eV is also included in the fitting.<sup>40</sup> Using these parameters the Ge 3*d* peak for bare Ge (001) was fitted as shown in Figure 4.13. As illustrated in Figure 4.13 when -0.67 eV was used for the up atom of the tilted Ge dimer, the fit to the lower energy shoulder was poor. Figure 4.13 also presents the fit that results when a relative binding energy of -0.89 eV is used for the up atom instead but with all the other energies (*e.g.*, -0.39 eV for the down atom) kept the same as the values in the literature. This relative energy of -0.89 eV produced the lowest residual standard error when fitted in CasaXPS, and was used herein for the remainder of the study.

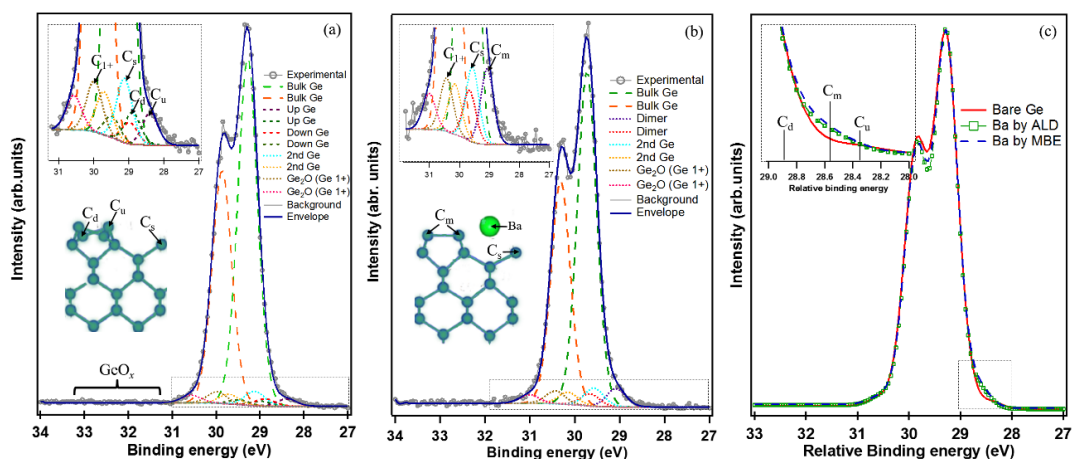


Figure 4.11. Spectral fitting of the high resolution Ge 3d spectrum for (a) bare Ge (001) and (b) after deposition of Ba by one half-cycle; the dashed frame is presented in a magnified view in the insets and the related structure models are inserted for (a) and (b). (c) Presents an overly of the fitted envelope curves for Ge 3d from (a) and (b) and 0.5-ML Ba by MBE; the dashed frame magnifies the lower binding energy shoulder and the solid black lines indicate the relative component positions.

Figure 4.11.(a) presents the fit for bare Ge (001); the spectrum was deconvoluted into five components (bulk Ge, C<sub>1+</sub> for Ge<sup>1+</sup>, C<sub>s</sub> for second layer Ge, C<sub>d</sub> for the down Ge of the Ge dimer, and C<sub>u</sub> for the up Ge of the Ge dimer). The bulk Ge component was found to have a binding energy of 29.28 eV for Ge 3d<sub>5/2</sub>. After depositing Ba by ALD, based on studies on Si (100), the charge transfer from Ba to Ge is expected to symmetrize (flatten) the surface dimer so that the C<sub>u</sub> and C<sub>d</sub> components will merge into a single dimer component C<sub>m</sub>.<sup>17</sup> The Ge 3d spectrum for one Ba ALD half-cycle on Ge (001) is shown Figure 4.11.(b), which was deconvoluted into four components; the bulk Ge component of Ge 3d<sub>5/2</sub> is at 29.73 eV, the C<sub>1+</sub> and C<sub>s</sub> components retained their relative position to bulk Ge, and a dimer component C<sub>m</sub> was fitted at -0.65 eV relative to bulk Ge. The dimer component C<sub>m</sub> for 0.5-ML-Ba coverage (not shown) was found at -0.64 eV relative to bulk Ge. The fitted-Ge 3d curves are presented in Figure 4.11.(c) after artificially shifting the

peaks for one Ba ALD half-cycle and 0.5-ML-MBE Ba to have the same bulk energy as bare Ge (001), *i.e.*, 29.28 eV, in order to visually compare the lower binding energy shoulders. The inset to Figure 4.11.(c) shows the bare Ge sample has less signal intensity at the  $C_m$  position than the other two samples, consistent with a flattened dimer after adsorbing Ba.

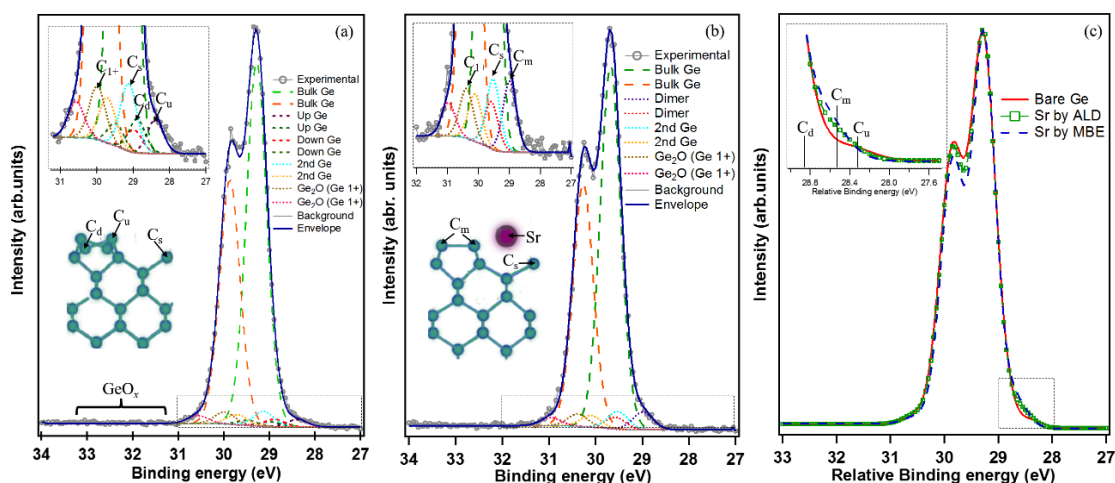


Figure 4.12. Spectral fitting of the high resolution Ge 3d spectrum for (a) bare Ge (001) and (b) after deposition of Sr by one half-cycle; the dashed frame is presented in a magnified view in the insets and the related structure models are inserted for (a) and (b). (c) Presents an overly of the fitted envelope curves for Ge 3d from (a) and (b) and 0.5-ML Sr by MBE; the dashed frame magnifies the lower binding energy shoulder and the solid black lines indicate the relative component positions.

The same analysis was applied to Ge 3d spectra after one Sr ALD half-cycle and 0.5-ML-MBE Sr adsorption with similar results as Ba (Figure 4.12). The  $C_m$  dimer component position for Sr by ALD was found at -0.69 eV and at -0.62 eV for 0.5-ML-MBE Sr.



In density functional theory, the approximate binding energy of a core electron can be calculated as the difference between the energies of the ground state system and the system with a core electron removed from the core:<sup>68</sup>

$$E_B = E(n_c - 1) - E(n_c).$$

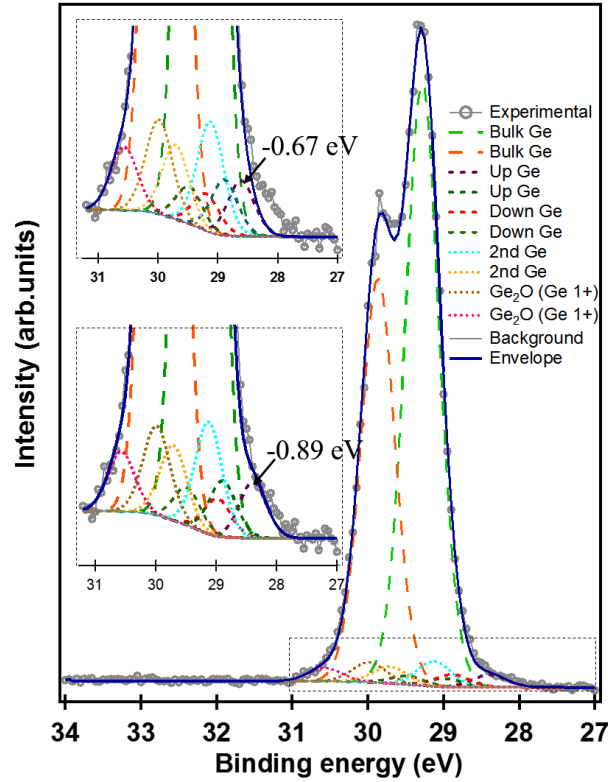


Figure 4.13. Spectral fitting of the high resolution Ge  $3d$  spectrum for bare Ge. The dashed frame is presented in a magnified view in the insets for two different deconvolution fits. In the top inset the surface core level shift for up-Ge of the tilted dimer is fit to  $-0.67$  eV. In the bottom inset the surface core level shift for up-Ge of the tilted dimer is fit to  $-0.89$  eV. A better fit of the Ge  $3d$  feature results when a core level shift of  $-0.89$  eV is used.

The “ejected” electron is placed in the valence or conduction band to keep the overall system neutral. In this approximation, it is assumed that the core hole remains localized on the atom at which it is created. This is the so-called final-state approximation,

in which the surrounding electrons are allowed to screen the core hole.<sup>28,68,69</sup> The binding energy of a core electron depends on the surrounding environment. The local environment at the surface of a solid is different than the local environment in the bulk, which leads to a shift in the binding energy. This SCLS is given by

$$E_{SCLS} = [E_{\text{surface}}(n_c - 1) - E_{\text{surface}}(n_c)] - [E_{\text{bulk}}(n_c - 1) - E_{\text{bulk}}(n_c)].$$

Table 4.1: LDA calculations of the SCLSs in eV

|                          | LDA, $4s^24p^2$ |           | LDA, $3d^{10}4s^24p^2$ |           | LDA, $4s^24p^2$ , thick slab |           |
|--------------------------|-----------------|-----------|------------------------|-----------|------------------------------|-----------|
|                          | Up atom         | Down atom | Up atom                | Down atom | Up atom                      | Down atom |
| Ge, 2×1 slab             | -0.60           | -0.36     | -0.78                  | -0.46     | -0.63                        | -0.40     |
| Ge, 2×2 slab             | -0.48           | -0.31     | -0.67                  | -0.44     |                              |           |
| Ge, 4×4 slab             | -0.39           | -0.12     |                        |           |                              |           |
| Ge, 2×1 slab,<br>½ ML Ba | -0.88           | -0.88     | -1.34                  | -1.34     | -0.92                        | -0.92     |
| Ge, 4×4 slab,<br>½ ML Ba | -0.94           | -0.94     |                        |           |                              |           |
| Ge, 2×1 slab,<br>½ ML Sr | -0.67           | -0.68     | -0.79                  | -0.79     | -0.71                        | -0.71     |
| Ge, 4×4 slab,<br>½ ML Sr | -0.74           | -0.74     |                        |           |                              |           |

Note: LDA is organized by system size and valence electron configuration used. Note that there is no difference in the SCLS for the up and down atoms in systems with barium and strontium. This is because the surface dimers flattened, so there is no longer an up and a down atom.

Table 4.2: Summary of predictions and measurements of the SCLS of the germanium surface from the literature and from this work.

|  | SCLS (eV) – up atom | SCLS (eV) – down atom |
|--|---------------------|-----------------------|
| Pehlke and Scheffler (DFT-LDA) <sup>28</sup> | -0.67               | -0.39                 |
| Cho et al. (DFT-LDA) <sup>70</sup>           | -0.73               | -0.33                 |
| Le Lay et al. (experimental) <sup>71</sup>   | -0.59               | -0.22                 |
| This work (experimental)                     | -0.89               | -0.39                 |

In these calculations, we took the atoms in the middle of our slabs as bulk-like.<sup>17,28,69</sup> The results of our calculations of core level shifts using LDA are displayed in Table 4.1. In general, for the clean germanium surface, for most configurations considered, the SCLSs are in good agreement with previous theoretical and experimental work, a summary of which is presented in Table 4.2. With the GGA functional and  $4s^24p^2$  ( $3d^{10}4s^24p^2$ ) valence electron pseudopotentials, we found the SCLSs to be -0.95 eV (-0.68 eV) for the up atom and -0.29 eV (-0.38 eV) for the down atoms for the  $2\times 1$  slab. Our theoretical results also qualitatively agree with the experimental results in this work. In particular, both experiment and theory show the surface dimers flatten after the adsorption of the AEM, resulting in a SCLS towards lower binding energy that is the same for all surface Ge atoms.

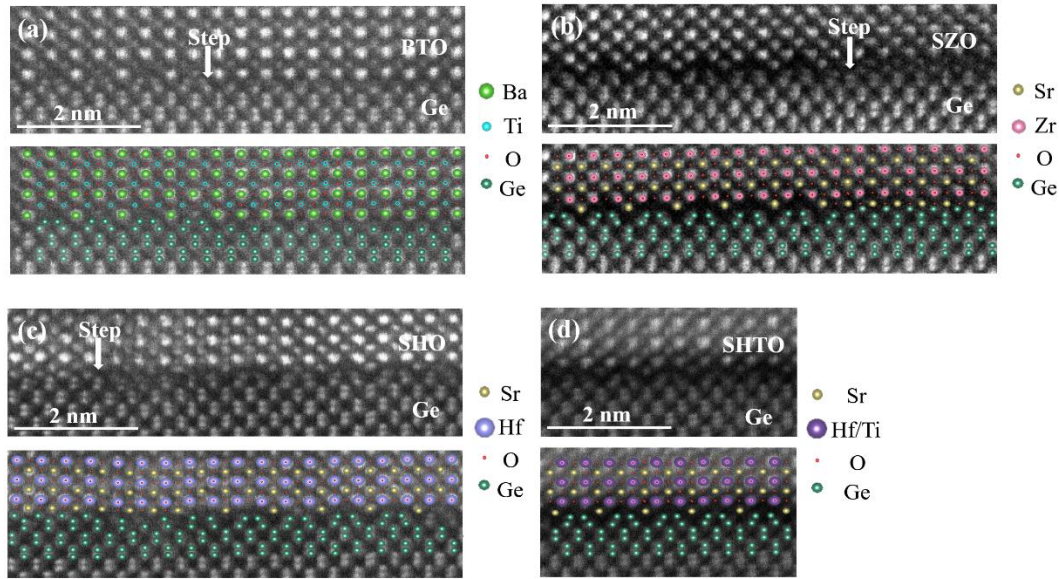


Figure 4.14. HAADF STEM images showing the interface of BaTiO<sub>3</sub> (BTO) (a), SrZrO<sub>3</sub> (SZO) (b), SrHfO<sub>3</sub> (SHO) (c) and SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> (SHTO) (d) films grown by ALD on Ge (001) substrates. White arrows mark the position of a single layer Ge surface step. Structural models below each image illustrate the interface structure in particular showing the change in periodicity of the Ge (001) substrate surface and the location of the Ba (or Sr) atomic columns between the Ge dimers. ((a) and (c) are reprinted with permissions from *J. Mater. Res.* **0**, 1 (2016).<sup>72</sup> (d) is reprinted with permission from *MRS Commun.* **6**, 125 (2016).<sup>42</sup> Copyright 2016 Cambridge University Press.)

The HAADF STEM studies were performed on four different crystalline perovskite oxides, namely BaTiO<sub>3</sub>, SrZrO<sub>3</sub>, SrHfO<sub>3</sub> and SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub>, that have been grown on Ge (001) by ALD.<sup>41,42</sup> The BaTiO<sub>3</sub> and SrZrO<sub>3</sub> films have not been described previously; however, all films shown in Figure 4.14 followed the same growth process.<sup>40-42</sup> The films were grown in the ALD system at 225 °C and each required post-deposition annealing in vacuum to achieve good crystallinity. In Figure 4.14.(a), (b) and (c), white arrows mark the one-layer Ge surface step position where the 2×1 periodicity changes to 1×1 periodicity when viewed along the [110] direction. As clearly shown in Figure 4.14, the 2×1

reconstructed Ge dimer is visible for each film with either Ba atoms or Sr atoms located at the trough between two dimer rows, which is the structure predicted for a Zintl template.

#### 4.5. DISCUSSION

Based on these observations we posit Ba and Sr form a Zintl layer at the perovskite/Ge interface during the initial stages of Ba- and Sr-based perovskite ALD. The similar Ge binding energy shifts, spectral features and valence band shifts with exposure to Ba or Sr by ALD and MBE indicate the same phenomena are occurring on the Ge surface at submonolayer AEM coverage. The first-principles DFT calculations for Ba and Sr adsorbed in a four-fold site between dimer rows on Ge (001) predict the binding energy shifts and the flattening of the tilted dimer (Figure 4.1 and Table 4.2) in qualitative agreement with experiment (Figures 4.5,4.11,4.12). Similar calculations of AEMs on Si (001) are the basis for Zintl layer formation in that system<sup>14,15,17</sup> and support a similar interpretation herein on Ge (001). A Zintl layer is reasoned to form the template for MBE growth of crystalline perovskites on Si and Ge.<sup>43,45,62</sup> The 2×1 periodicity at the perovskite–Ge interface in the HAADF-STEM images for BaTiO<sub>3</sub> grown by MBE on Ge (001)<sup>43</sup> and for the four perovskites grown by ALD on Ge (100) (Figure 4.14) with either Ba atoms or Sr atoms located at the trough between the dimer rows further demonstrate the formation of a Zintl layer during ALD.

McKee showed the interfacial layer formed from Sr or Ba could be as thin as 1 ML and was associated with a strontium silicide, SrSi<sub>2</sub>, for 0.5-ML Sr.<sup>1,2</sup> This interfacial layer is critical in protecting the Si surface from subsequent oxidation during MBE of perovskite.<sup>9,17,22–24</sup> Subsequent studies of this template layer formed from 0.5-ML Sr have shown the surface to be 2×1-reconstructed, with Sr in the four-fold sites between Si dimer

rows, that the binding energy and the valence band energy shift to higher energy when compared against a bare  $2\times 1$ -reconstructed Si (100) surface, and that the charge transfer between Sr and the Si-dimers leads to surface core level shifts to higher energies, and that the merging of the up- and down-atom Si  $3d_{5/2}$  components into one component is associated with a flattened dimer.<sup>14,15,17,21</sup> Demkov and Zhang interpreted the local  $\text{SrSi}_2$  surface stoichiometry, which is the stoichiometry of the bulk Zintl silicide,  $\text{SrSi}_2$ ,<sup>73,74</sup> as a Zintl layer.<sup>14</sup>

Except for the different surface reconstruction found for 0.5-ML-MBE Sr on Ge (Figure 4.3) the results reported herein for both Ba and Sr are qualitatively similar. Charge transfer from the AEM to Ge was found when using an ALD half-cycle dose at 225 °C or 0.5-ML MBE dose at 600 °C (Figure 4.4 and 4.5). This charge transfer is seen in the Ge valence band energy shift to higher energy when Ba was dosed on the surface (Figure 4.5). The  $2\times 1$ -reconstructed bare Ge surface consists of dimer rows and the dimers are tilted (asymmetric) giving rise to two surface states, with the up-dimer atom filled and the down-dimer atom empty. These states are resolved as the  $C_u$  and  $C_d$  components in the Ge  $3d$  spectral fitting (Figure 4.11). The asymmetric tilt of the dimer merges into one component feature,  $C_m$ , following Ba (Figure 4.11) or Sr (Figure 4.12) adsorption by ALD or MBE. On Si (001) the asymmetric tilt of the dimer was eliminated as a result of Sr atoms donating their 2 electrons to the Si (001) surface.<sup>17</sup> The same spectral features are found herein for Ge (001) indicating similar charge transfer and loss of asymmetry. By analogy to the interpretations offered for AEM on Si (001), the AEM atoms form a partial Zintl layer for 0.2-ML Ba or Sr derived from the ALD half-cycle, and they form a Zintl layer for the 0.5-ML-MBE Ba or Sr coverages on Ge (001).

The DFT results showed the surface dimers symmetrize (flatten) upon adsorption of 0.5-ML of Ba and Sr (Figure 4.1) and that the surface core levels for the up and down

dimers merge upon adsorption of the 0.5-ML of Ba and Sr (Table 4.1). As mentioned above, for most configurations of the clean Ge surface considered, the SCLSs (see Table 4.1) are in good agreement with previous theoretical and experimental work (see Table 4.2). However, the theoretical predictions do somewhat depend on the choice of the functional, slab thickness, and in-plane area of the simulation slab. Furthermore, screening depends on the band gap of the system. Since both LDA and GGA underestimate the band gap for germanium, the screening is exaggerated. This is especially important for the systems with 0.5-ML of barium and strontium, which are metallic in this theory. This is not a problem for similar studies done for silicon, where the band gap is maintained for with 0.5-ML of Sr adsorbed on the surface.<sup>14</sup> We find that the SCLSs for germanium (001) surface with 0.5-ML of barium and strontium are negative (towards lower binding energy), which matches the experimental results for both ALD and MBE growth. Furthermore, the theoretical SCLS is the same for both atoms in the Ge surface dimer, which indicates the dimer has flattened after the adsorption of the Ba and Sr, just as was observed experimentally. The magnitude of the SCLS is larger for barium than for strontium, also in qualitative agreement with experiment. However, the absolute value of the shift is overestimated for both metals, for the reasons discussed above.

During ALD exposures the Ge (001) surface gets contaminated with a carbonaceous material. An ALD half-cycle leads to 1.0 ML of carbon, *i.e.*, 5:1 C:Ba (:Sr), that does not increase with continued dosing, even as the Ba coverage increases (Figure 4.6.(b) and 4.8). The same 1-ML coverage is found upon exposure of a clean Ge (001) surface to the ALD chamber for 1 min (Fig. 7). Cycloaddition reactions of organic molecules containing C–C double bonds are common on Ge (001),<sup>75</sup> and it is conceivable that ligands either come directly from the precursor during adsorption of barium- or strontium-bis(triisopropylcyclopentadienyl) precursors or from

triisopropylcyclopentadienyl ligands that could desorb from the ALD chamber walls bind to the Ge (001) surface through a cycloaddition reaction. These ligands might be expected to persist on the Ge surface for a few ALD cycles that alternate between the precursor and water exposure.

It is possible the carbon associated with these bound ligands somehow contribute to the binding energy shift found for Ge (Figure 4.5). The AEM binding energy decreases with increasing coverage for MBE-dosed samples (Figure 4.2 and 4.4). The ALD half-cycle led to 0.2 ML of Ba or Sr and gave the same Ge binding energy shift as the corresponding 0.5-ML MBE dose (Figure 4.4). A GeC monolayer has been reported with a Ge 3d binding energy of 29.8 eV versus 29.4 eV for clean Ge,<sup>76</sup> providing precedence for carbon to contribute to the greater BE found for 0.2-ML AEM when it originates from an organometallic precursor than the BE that might be expected for this AEM coverage.

ALD of the ABO<sub>3</sub> perovskite films presented in Figure 4.14 led to amorphous films that required annealing between 500 and 650 °C to crystallize the deposited film into a heteroepitaxial layer on Ge (001).<sup>41,42</sup> The first complete ALD cycle produces 0.4-ML of Sr or Ba (Figure 4.9). Following this 0.4-ML Sr/Ba deposition the B element of the perovskite is introduced via a molecular precursor, and then A is deposited again. The RHEED and XPS results herein show the Sr/Ba in the first ALD half-cycle adsorb on the Ge (001) surface and form a partial Zintl layer. We propose the Zintl layer, SrGe<sub>x</sub> or BaGe<sub>x</sub>, is completed during the first few ALD cycles and this Zintl layer forms a template from which the epitaxial film crystallizes during the annealing step. The strongest evidence in support of the interfacial Zintl layer is found in the HAADF-STEM images (Figure 4.14) that show the Sr or Ba located at the trough between the dimer rows.



#### 4.6. SUMMARY

The organic ligands dissociate from the precursor after precursor adsorption on the Ge (001) surface, producing the same Zintl template critical for perovskite growth on Group IV semiconductors during molecular beam epitaxy. We find that the initial dosing of the barium- or strontium- bis(triisopropylcyclopentadienyl) precursors on a clean Ge surface produces a surface phase that has the same chemical and structural properties as the 0.5-monolayer Ba Zintl layer formed when depositing Ba by molecular beam epitaxy. Similar binding energy shifts are found for Ba, Sr and Ge when using either molecular or atomic metal sources. Germanium surface core level shifts reveal the tilted Ge surface dimers flatten for molecular and atomic metal sources. Similar binding energy shifts and changes in dimer tilting with alkaline earth metal adsorption are found with density functional theory calculations. High angle angular dark field scanning transmission microscopy images of BaTiO<sub>3</sub>, SrZrO<sub>3</sub>, SrHfO<sub>3</sub> and SrHf<sub>0.55</sub>Ti<sub>0.45</sub>O<sub>3</sub> reveal the location of the Ba (or Sr) atomic columns between the Ge dimers and reinforce the formation of a Zintl layer at the onset of ALD.

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## Chapter 5: Crystalline SrZrO<sub>3</sub> deposition on Ge (001) by atomic layer deposition for high-*k* dielectric application

### 5.1. INTRODUCTION

Crystalline perovskites have received increased attention due to their multiple properties, such as dielectric, piezoelectric, ferroelectric, ferromagnetic, etc.<sup>1,2</sup> McKee first reported epitaxial growth of SrTiO<sub>3</sub> (STO) on silicon in 1998.<sup>3</sup> The study of crystalline perovskites on semiconductors has been widely expanded subsequently for device applications,<sup>4-13</sup> including the use of crystalline perovskites as a high-*k* gate oxide for field-effect transistor device applications.

In the past decades, the goals of faster computing speed and lower power consumption have led to ever smaller transistor feature sizes and ever thinner gate oxides thicknesses. Unacceptable leakage current for SiO<sub>2</sub> on silicon<sup>14</sup> led to a search for other gate oxide materials and ultimately channel materials other than silicon. For high dielectric constant materials, the *k* value should be over 12, preferably 25 to 35.<sup>15</sup> Previous work in our group has studied the deposition of crystalline SrHfO<sub>3</sub> (SHO), STO and SrHf<sub>*x*</sub>Ti<sub>1-*x*</sub>O<sub>3</sub> (SHTO) on Ge (001) by atomic layer deposition (ALD). The titanium-based perovskites display a large leakage current because of the negligible conduction bandset offset with the Ti 3*d* states and Si and Ge.<sup>16-18</sup> While the STO/Ge heterojunction achieved *k*~90 and an equivalent oxide thickness (EOT) of 0.7 nm, the leakage current was around 10 A/cm<sup>2</sup> at 0.7 MV/cm.<sup>19</sup> Substituting Hf onto the Ti sites led to a lower dielectric constant, larger EOT and lower leakage current with values of *k*~30, an EOT of 1.8 nm, and a leakage current of 0.1 A/cm<sup>2</sup> at 1 MV/cm for SrHf<sub>0.55</sub>Hf<sub>0.45</sub>O<sub>3</sub>.<sup>9</sup> SHO displayed *k*~17, an EOT of 1.0 nm and a leakage current of ~6.3×10<sup>-6</sup> A/cm<sup>2</sup> at 1.0 MV/cm.<sup>10</sup> While the

leakage current and EOT for SHO/Ge were comparable to state-of-the-art Ge-based metal oxide semiconductor field effect transistors (MOSFETs), the interface trap density ( $D_{it}$ )  $\sim 5 \times 10^{13} \text{ cm}^{-2} \text{ eV}^{-1}$  of crystalline SrHfO<sub>3</sub> on Ge needed improvement.<sup>20</sup> Annealing temperatures above 650 °C were required to realize crystalline SHO films and this led to an amorphous interfacial layer of several Angstroms that may have contributed to the  $D_{it}$  values. Zirconium, with an atomic mass  $\sim 0.5$  that of Hf, is expected to reduce the crystallization temperature of thin SrZrO<sub>3</sub> (SZO) when compared to SHO.<sup>9</sup>

Strontium zirconate has a comparable conduction band offset (CBO) to SHO with Ge, with values of 1.41-1.77 eV and 2.17 eV for SZO/Ge and SHO/Ge, respectively, and SHO has a dielectric constant  $k$  around 30.<sup>10,13,21</sup> Several groups have studied growth of crystalline SZO deposition by molecular beam epitaxy (MBE) or pulse laser deposition (PLD).<sup>13,21,22</sup> Herein we report ALD growth of crystalline SZO, and approaches to lower the  $D_{it}$  through steps taken prior to ALD and by annealing in a flux of atomic deuterium. Comparison of various Column 4 B-site cations in ALD-grown crystalline ABO<sub>3</sub> perovskites shows Zr to perform the best among Ti, Zr and Hf in terms of leakage current and  $D_{it}$ .

The channel material also plays a significant role in MOSFET performance. Silicon is the most common substrate and further transistor scaling requires the channel material have higher mobility, which can lead to higher drive current.<sup>23-25</sup> Compared to Si, the electron mobility and hole mobility for Ge are 3900 vs 1400 cm<sup>2</sup>/Vs and 1900 vs 500 cm<sup>2</sup>/Vs, respectively, which makes Ge viable as a next generation channel material.<sup>25,26</sup> In addition, Ge has a more unstable oxide than Si and making it easier to remove the native oxide to achieve a clean surface.<sup>27-29</sup>

A high  $D_{it}$  can affect the channel mobility and negate the mobility advantages of Ge.<sup>25</sup> Defects at the interface of Ge-based heterojunctions are mainly formed by dangling

bonds.<sup>25,30</sup> Ge substrate roughness also increases  $D_{it}$ .<sup>9,31</sup> Several groups have employed different methods to lower the interface trap density;<sup>20,32–39</sup> one method uses  $\text{GeO}_x$  as a passivation layer to yield  $D_{it}$  values in the low  $10^{11} \text{ cm}^{-2} \text{ eV}^{-1}$  range.<sup>34,37–41</sup> However the passivation layer increases the EOT. The smoothness of a Ge surface can be improved with oxygen plasma treatment.<sup>28</sup> Zhang et al., used used post-deposition annealing to treat  $\text{Al}_2\text{O}_3/\text{GeO}_x/n\text{-Ge}$  in ambients of  $\text{N}_2$ , forming gas,  $\text{H}_2$ ,  $\text{D}_2$ , atomic H, and atomic D to heal dangling bonds; the the atomic D treatment led to the best effective electron mobility.<sup>42</sup> D desorption yields are 50 times lower than the H yields on Si;<sup>43</sup> and a lower yield should also be expected for D on Ge. Atomic deuterium treatment is reported herein for crystalline SZO/Ge interfaces that do not feature an interfacial oxide.

## 5.2. EXPERIMENT

### 5.2.1. GE SUBSTRATE PREPARATION

The growth facility has been previously described.<sup>44</sup> It includes an ALD chamber, a surface analysis chamber, a molecular beam epitaxy chamber, and a deuterium dosing chamber (described below) all connected with a transfer tube so the samples remain *in situ* between treatment, growth and analysis. The 4-inch Ge wafers (n-type, Sb-doped, 0.1-0.5  $\Omega\cdot\text{cm}$  resistivity) were purchased from MTI Corp. and diced into  $18\times 20 \text{ mm}^2$  pieces. After degreasing the Ge substrate with acetone, isopropyl alcohol and deionized water in an ultrasonic bath, two preparation methods were used to remove the residual carbon contamination and produce the  $2\times 1$ -reconstructed Ge (001) surface that is the necessary template for perovskite growth by ALD.<sup>19</sup> Method A is the same procedure as previous work,<sup>9,10,19,45</sup> in which a 30-min UV/ozone exposure is followed with 1 h thermal annealing at  $700^\circ\text{C}$  in vacuum ( $<2\times 10^{-9}$  torr) to remove any native oxide. Method B uses an oxygen



plasma to remove the residual carbon contamination instead of UV/ozone exposure and to lower the surface roughness. In Method B the sample is loaded into the vacuum system after degreasing and is transferred to the MBE chamber. An oxygen plasma with  $1.2 \times 10^{-5}$  torr background oxygen pressure and 300 W forward power is estimated to produce an atomic oxygen flux of  $\sim 5 \times 10^{13} \text{ cm}^{-2} \text{ s}^{-1}$ .<sup>28</sup> During the 30-min oxygen plasma exposure the substrate temperature is 100 °C. Following plasma treatment, 1 h thermal annealing at 700 °C is applied to remove the native GeO<sub>2</sub>.

*In situ* reflection high-energy electron diffraction (RHEED) (Staib Instruments operating at 21 keV) is used to verify the reconstructed surface structure for Methods A and B. The clean Ge (001) sample is transferred *in vacuo* to the surface analysis chamber fitted with an x-ray photoelectron spectrometer to verify the Ge surface composition. Following the thermal deoxidization process at 700 °C some samples were heated to 600 °C for Zintl template preparation.<sup>45</sup> The Zintl template consisting of 0.5 monolayer (ML) of Ba is prepared by depositing atomic Ba in the MBE chamber on the deoxidized Ge (001) surface. *In situ* RHEED confirmed the 2×1 reconstructed surface structure for the Zintl template and the Ba flux rate was calibrated to 1ML/min by a quartz crystal monitor in MBE.<sup>45</sup>

### 5.2.2. FILM GROWTH

The bare Ge and Zintl-templated Ge substrate were transferred *in situ* to the ALD chamber for SrZrO<sub>3</sub> (SZO) seed layer growth at 225 °C with 1 torr Ar flow as the carrier gas. Strontium bis(triisopropylcyclopentadienyl) from Air Liquid and tetrakis(dimethylamido) zirconium (IV) from Sigma-Aldrich were used as the Sr and Zr precursor

and were heated to 130 and 60 °C, respectively. H<sub>2</sub>O, maintained at room temperature, was used as the oxygen source. Each unit cycle includes 2-s precursor dosing, 1-s H<sub>2</sub>O dosing and 20-s Ar purging following each reactant exposure. When growing the slightly Sr rich film, the cycle ratio was Sr: Zr = 3:2 on bare Ge (001). On Zintl-templated Ge (001), a cycle ratio of Sr: Zr=2:1 was found to work best.

The first seed layer (20 ALD cycles and ~3 nm thick) always grew as an amorphous film, so post deposition annealing is required to obtain the single crystalline film. The sample was transferred back to the MBE chamber in vacuum and heated to the crystallization temperature with a 10°C min<sup>-1</sup> heating rate. The onset of crystalline is defined as the initial appearance of a diffraction spot in RHEED as described in previous work.<sup>9</sup> The sample temperature was then ramped up another 30 °C and maintained for 5 min to achieve better crystallinity. During the annealing process, RHEED was used to observe transformation from an amorphous to a crystalline film in real time. Attaining single crystalline SZO film above 3 nm thickness required a two-step growth process. After annealing the 3-nm amorphous SZO film to achieve single crystalline SZO, the sample was transferred back to ALD chamber for thicker film growths with a Sr:Zr=3:2 cycle ratio. The second SZO layer was crystalline as deposited and the crystallinity was improved with post deposition annealing to around 550 °C without pausing.

### **5.2.3. ATOMIC DEUTERIUM TREATMENT**

An atomic deuterium source chamber was home-built built. Atomic deuterium was generated by the tungsten filament of a 400-W Osram Xenophot bulb, which had part of the glass enclosure removed to expose the filament. A current of 5.2 A was supplied by

a DC power supply (KEPCO, MSK10-10M) to the tungsten filament and filament temperature was  $\sim 1800$  K, which was calibrated by a thermal pyrometer. At this temperature, the tungsten filament could crack deuterium molecule to generate atomic deuterium.<sup>46</sup> The pressure of deuterium gas (99.999%; Matheson) was controlled by a leak valve and maintained at  $5.0 \times 10^{-6}$  torr in the chamber. The flux of atomic deuterium was estimated around  $2 \times 10^{13}$  D/cm<sup>2</sup>·s based on the previous work.<sup>47</sup> The sample was positioned approximately 4 cm above, and faced toward the tungsten filament. A pyrolytic boron nitride heater from Momentive was fixed 2 cm above the sample and maintained the sample temperature at 350 °C during the treatment. The treatment time ranging from 1 to 3 h was adjusted for different sample thicknesses.

#### 5.2.4. CHARACTERIZATION

In addition to RHEED that was used to monitor the Ge surface reconstruction and the film surface order that is used to track the crystalline process, *in situ* x-ray photoelectron spectroscopy (XPS) was also applied to confirm the Ge surface composition and stoichiometry of SZO films. The XPS was equipped with a monochromatic Al K $\alpha$  source at 1486.6 eV and a VG Scienta R3000 electron energy analyzer, which is calibrated by a clean Ag foil. To obtain the spectra of Sr 3*d*, Zr 3*d*, Ge 3*d* and C 1*s* core levels, the basic settings were 100 eV pass energy with a 0.4 mm analyzer slit width and 50 meV steps with 157 ms/step dwell time. At this condition, the spectrometer yielded around 350 meV effective resolution. CasaXPS (Version 2.3.16 PR 1.6) was used to analyze the results. The atomic sensitivity factors for Sr 3*d* and Zr 3*d* were set as 1.843 and 2.576, respectively.<sup>48</sup>

For *ex situ* characterization, the film thickness was measured by x-ray reflectivity (XRR) and crystallinity was analyzed by x-ray diffraction (XRD) and rocking curve on a Rigaku Ultima IV system with a Cu K $\alpha$  source. The high-angle annular-dark-field scanning transmission electron microscopy (HAADF-STEM) was employed here to investigate the grown film and interface. The samples were prepared via standard cross-section method with Ar ion milling and STEM images were taken with a JEOL ARM 200F operated at 200 kV.

Metal oxide semiconductor capacitor (MOS capacitor) structures fabricated by a lift-off process to study the electrical properties, which include dielectric constant  $k$ , leakage current  $J$  and interface trap density  $D_{it}$ .<sup>23,25</sup> The 100  $\mu\text{m}\times 100 \mu\text{m}$  square top electrode contacts of Pt were applied by E-beam evaporation. The back side of the substrate was scratched and silver paste was applied to form the bottom electrode. The capacitance-voltage (C-V) and current-voltage (I-V) measurements were performed on an Agilent B1500A semiconductor device parameter analyzer with a Cascade Microtech probe station. A cryogenic probe stations from Lakeshore Cryotronics was used for low temperature  $D_{it}$  measurement at 250K.

### **5.3. RESULTS AND DISCUSSION**

#### **5.3.1. GE SUBSTRATE PRETREATMENT**

Figure 5.1 shows the RHEED images indicating the surface structure changes during the Ge substrate pretreatment process. The Ge surface structure is transformed from a 1 $\times$ 1- to 2 $\times$ 1-reconstructed structure after thermal annealing to remove oxygen, which demonstrates Ge surface dimer formation. The 2 $\times$ 1-reconstructed Ge substrate was found

to be necessary for ALD and MBE growth of crystalline perovskites.<sup>19,49</sup> The samples treated by UV/ozone and by the oxygen plasma have the same RHEED patterns after oxygen treatment Figure 5.1.(b) and after thermal annealing to remove oxygen Figure 5.1.(c) as illustrated for an oxygen plasma-treated sample. The weaker  $1\times 1$  pattern in Figure 5.1.(b) than Figure 5.1.(a) is a consequence the thicker oxide layer that forms during oxygen plasma treatment. Any native carbon contamination of Ge (001) is removed by either the UV/ozone or oxygen plasma process<sup>19,28</sup> and the native oxide is removed after thermal annealing.<sup>9,19</sup> Oxygen plasma treatment was used to achieve a flatter reconstructed Ge (001) surface with fewer steps than are found using a UV/ozone treatment.<sup>28</sup> Fewer surface steps can help to reduce the defects at the interface and lower the anti-phase boundary (APB) formation in films.<sup>9,50</sup>

Over time the ALD chamber accumulates  $^i\text{Pr}_3\text{Cp}$  ligands or  $^i\text{Pr}_3\text{Cp}$ -derived molecules on the chamber surfaces that likely adsorb on bare Ge (001) and produce about 1 ML-equivalent of carbon contamination on the Ge (001) surface.<sup>45</sup> A recent study found a Zintl template formed by adsorbing 0.5 ML Ba on Ge (001) by MBE, could prevent the carbon adsorption in the ALD chamber.<sup>51</sup> The Zintl templated-surface formed with Ba retains the reconstructed surface structure as shown in Figure 5.1.(d). The sharper  $2\times 1$  pattern following Zintl layer formation indicates the long range order and cleanliness of the Ge surface. *In situ* XPS was used to check the surface composition for both Zintl-templated and clean bare Ge.<sup>51</sup> XPS results also verify the Zintl template is free of carbon and oxygen.<sup>45</sup>

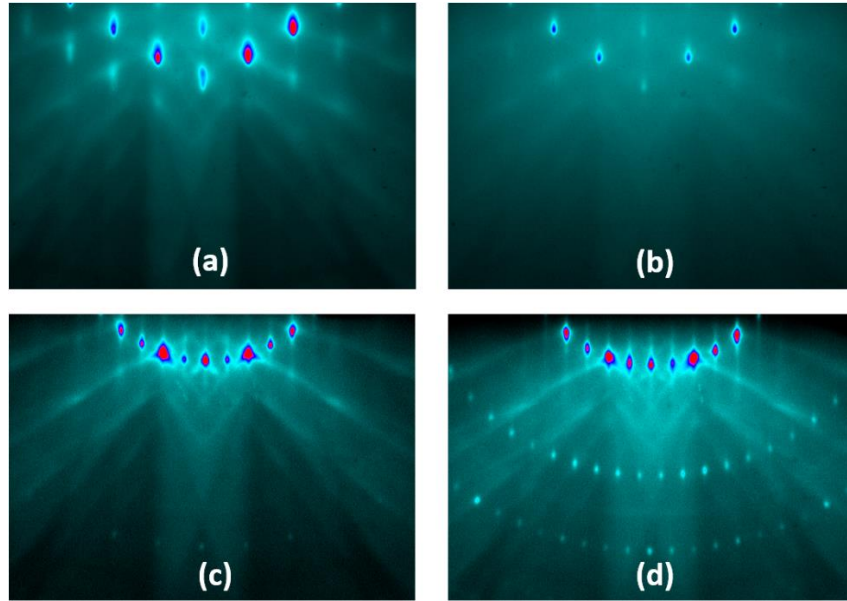


Figure 5.1. Ge (001) surface observation by RHEED along the [110] zone axis, (a) prior to oxygen plasma treatment (b) after oxygen plasma treatment for 30 min (c) after 1 h vacuum annealing at 700°C. (d) Zintl template formation by 0.5-ML Ba deposition on Ge. All images are taken at a 200 °C substrate temperature with the same filament intensity.

### 5.3.2. DEPOSITION AND CRYSTALLIZATION OF $\text{SrZrO}_3$

The SZO films above 3 nm thickness required a two-step growth procedure in which thicker films are deposited on a crystallized layer of SZO. As shown in Figure 5.2.(a), the first seed layer grows as an amorphous film on Zintl template. With vacuum annealing at 630 °C for 5 min, the first layer of SZO transforms from amorphous to crystalline. The second layer grows as a crystalline layer at 225 °C. Sharper RHEED patterns can be realized by annealing at 550 °C (Figure 5.2.(b)). Similar results were found with STO where a crystalline seed layer enabled subsequent STO growth as a crystalline film.<sup>52</sup> The SZO films grown on bare Ge (001) required the same two-step procedure in

which the 2-3 nm amorphous films were annealed to crystallize the film (RHEED images not shown). The crystallization temperature for the seed layer on bare Ge is around 590°C, which is lower than SrHfO<sub>3</sub> (at~650 °C) but higher than SrTiO<sub>3</sub> (at ~510 °C) on bare Ge (001).<sup>9</sup> This is consistent with the observation that the crystallization temperature in SrMO<sub>3</sub> amorphous thin films is be influenced by the atomic weight of atom M.<sup>9</sup>

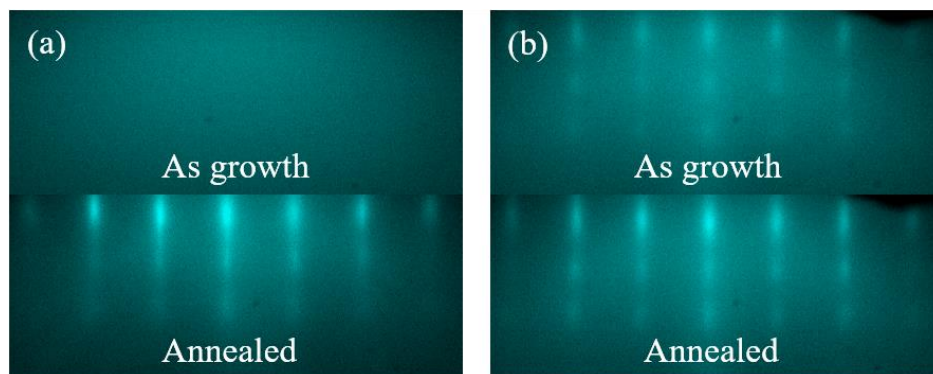


Figure 5.2. RHEED images of two step growth for a 6.2-nm SrZrO<sub>3</sub> film on Zintl template Ge before and after annealing, (a) the first layer is around 2 nm (b) the second layer is 4.2 nm. All images were taken by the beam was aligned along <110> directions.

On bare Ge (001), the SZO seed layer is grown with cycle ratio of Sr: Zr= 3:2 and 20 unit cycles in total (~3nm), which results in 49.3% Sr+Ba and 50.7% Zr (metals basis) by XPS. On Zintl-templated Ge, the same cycle ratio of Sr:Zr=3:2 will lead to SZO stoichiometry of 45.9% of Sr+Ba and 55.1% Zr, Films with this stoichiometry crystallize at 640°C. A higher Sr cycle is needed on Zintl-templated Ge. With a Sr:Zr=2:1 cycle ratio and total 20 unit cycles, the stoichiometry is 49.9% Sr and 50.1% Zr and the crystallization temperature is around 600°C.

The previous study showed that the bare 2×1 Ge (001) surface could decompose the Sr precursor to form adsorbed Sr atoms that feature many of the same binding energy

shifts as MBE-deposited Sr or Ba atoms.<sup>45</sup> However, higher-order reconstructions of Ge (001) were found with Sr than the 2×1 reconstruction with 0.5-ML Ba that creates the Zintl template. This Ba-based Zintl template protects the Ge (001) surface from C incorporation from the precursor ligands and oxide formation from the H<sub>2</sub>O ALD oxidant.<sup>51</sup> For reasons beyond the scope of this study strontium bis(triisopropylcyclopentadienyl) appears less reactive on a Zintl-templated surface than on a bare Ge (001) surface necessitating the higher Sr cycle of Sr:Zr=2:1 to obtain stoichiometric films. The amorphous SZO on the Zintl-templated surface starts to crystallize at 640 °C while the amorphous SZO on the bare surface starts to crystallize at 590 °C. The annealing temperature, which we adopt as 30 °C higher than crystallization temperature, is a significant factor to determine the interface quality. Higher annealing temperature will increase the possibility to form a blurry interface.<sup>9,10</sup> As shown in Figure 5.3.(a) the SZO seed layer annealed at 620 °C features sharp interface in HAADF-STEM with Sr atoms located between the dimer rows as expected of a Zintl layer.<sup>45,53</sup> The SZO seed layer annealed at 670°C does not feature a sharp at the interface interface in HAADF-STEM (Figure 5.3.(b)). The amorphous region at interface could indicate the Ge mixed or diffused into the SZO film at the higher annealing temperature. A similar interfacial reaction was observed when SrHfO<sub>3</sub> was annealed on Ge above 650 °C. Since the interfacial reaction might lead to higher interface trap density, the annealing temperature is controlled below 640°C in this work by using 2:1 of Sr to Zr ALD cycle ratio on Zintl template and 3:2 on bare Ge (001).



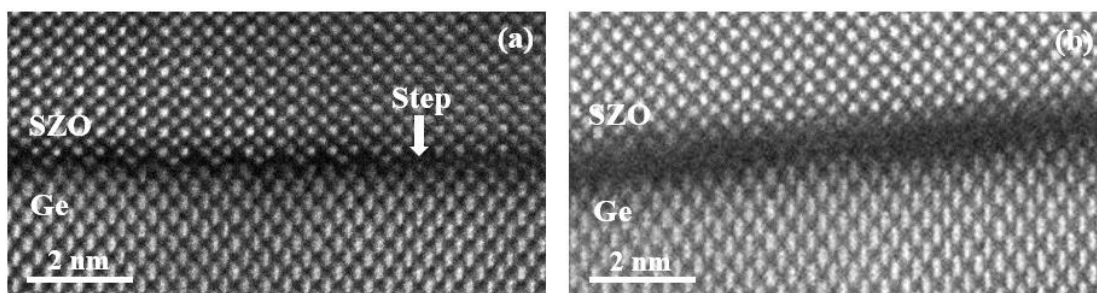


Figure 5.3. HAADF-STEM images of (a) SrZrO<sub>3</sub> on bare Ge (001) annealed at 620 °C, (b) SrZrO<sub>3</sub> on Zintl-templated Ge (001) annealed at 670 °C.

Regardless of bare Ge or a Zintl template, the second layer of SZO has the same growth cycle ratio of Sr:Zr=3:2 and crystallizes as it grows. Post deposition annealing at 550°C is applied for all second-layer SZO films to improve the crystal quality. Based on the XPS the composition for the SZO films is 53.0% of Sr and 47.0% of Zr. This result is consistent with previous work that showed Sr-rich composition helped the crystallization process.<sup>10,19</sup> Figure 5.4 presents the Sr 3*d* and Zr 3*d* X-ray photoelectron spectra. There are no obvious chemical shifts or low oxidization state for Sr<sup>2+</sup> and Zr<sup>4+</sup>, which indicates that the vacuum annealing does not lead to detectable oxygen vacancies in the SZO films.

Film thickness was confirmed by XRR and calculated by Bragg's law. The sample for X-ray measurement was grown with 80 unit cycles in total on bare Ge and eventually reached to 11.5 nm thickness. The growth rate for SZO on Ge was around 1.4 Å per cycle. The XRD results in Figure 5.5.(a) reveal the (002) peak is located at 43.83°, which indicates that the out-of-plane lattice constant for SZO is 4.13 Å and implies the film is compressively strained. The lattice constant for cubic SZO is 4.10 Å at room temperature.<sup>54</sup> Since the Ge-Ge distance is 3.992 Å<sup>55-57</sup> at the Ge (001) surface, a heteroepitaxial SZO film on Ge will lead to in-plane compressive strain of -2.6%. Figure 5.5.(b) shows the rocking curve for the (002) reflection of the same sample. The full width at half

maximum (FWHM) is  $1.4^\circ$ , which is a comparable crystalline SHO films grown by ALD on Ge (001).<sup>10</sup>

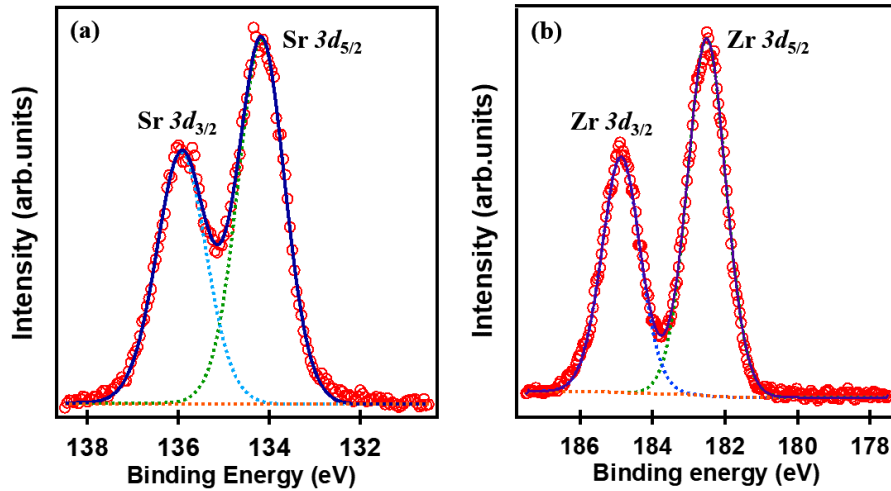


Figure 5.4. X-ray photoelectron spectra for Sr 3d (a) and Zr 3d (b) in 11.5 nm SrZrO<sub>3</sub> film after post deposition annealing at 550 °C.

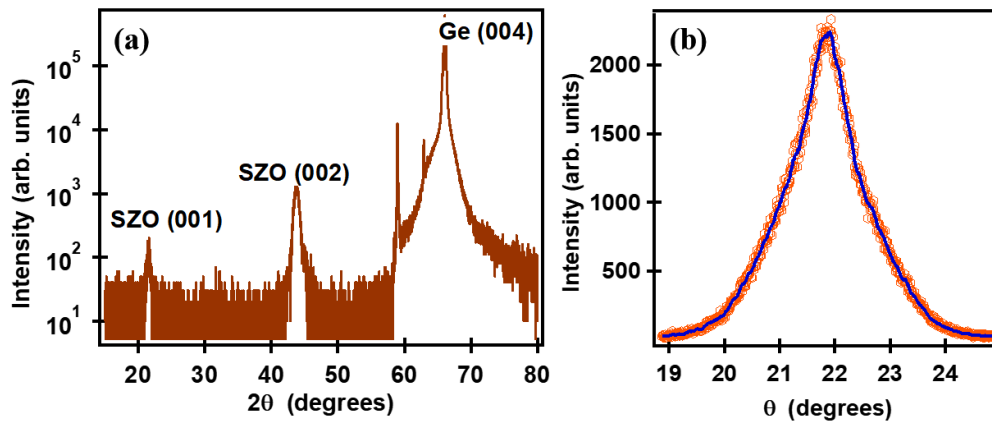


Figure 5.5. X-ray diffraction pattern (a), and rocking curve (b), for 11.5-nm SrZrO<sub>3</sub> film grown on bare Ge by ALD. The 3-nm thick seed layer was post-deposition annealed at 620 °C in vacuum and the final film was annealed at 550 °C. The peak of the SZO (002) reflection is at  $2\theta = 43.83 \pm 0.5^\circ$  and the rocking curve for the (002) reflection has a full width at half maximum (FWHM) of  $1.4^\circ$ .

### 5.3.3. ELECTRIC PROPERTIES

Experiments were conducted to compare the effects of different starting surfaces and post-deposition exposure to atomic D on  $D_{it}$ . Figure 5.6 presents a schematic of treatment protocols discussed herein. An amorphous SZO film of different thicknesses was used and that had no impact on the crystal quality. Device C used the thinnest first SZO layer to minimize the SZO film through which atomic D would need to diffuse to heal dangling bonds at the Ge-SZO interface. More functional and stable devices are detected on the Zintl template samples than bare Ge samples, which indicates the non-uniform carbon contamination at the interface might be the reason to cause some device failure on bare Ge samples. So to prepare better performance device, the Zintl template is still required. Since a thicker film would require longer gas diffusion time to reach the interface for treatment, the atomic D treatment was applied after obtaining the first crystalline 3nm-SZO layer instead of treating the 15 nm SZO film on Device B. Device A corresponds to an 11.5-nm film grown directly on bare Ge (001) that was treated with UV/ozone. Only Device C was subjected to the oxygen plasma pretreatment protocol.

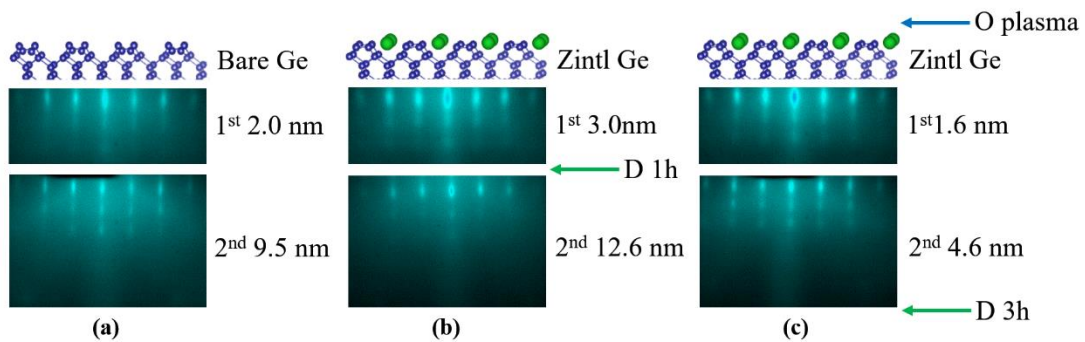


Figure 5.6. Schematic of the treatment procedures for Devices A, B and C. (a) Device A, two-step SZO deposition on bare Ge, (b) Device B, two-step SZO deposition on Zintl-templated Ge with 1 hr atomic deuterium post treatment, and (c) Device C, two-step SZO growth on Zintl-templated Ge with an oxygen plasma pretreatment and 3 hr atomic deuterium post treatment.

For all three devices, both C-V and I-V measurements were performed at room temperature and  $D_{it}$  was measured at 250K.<sup>58</sup> Figure 5.7 shows the C-V characteristics for Device C at frequencies from 1kHz to 1000kHz. The capacitance is normalized by the area of top 100  $\mu\text{m} \times 100 \mu\text{m}$  square Pt electrode. The saturated  $C_{ox}$  at accumulation was 3.92  $\mu\text{F}/\text{cm}^2$  with the 1kHz C-V scan, which results in a dielectric constant of 27.6. A more accurate  $C_{ox}$  should be determined by C-V scans at low frequency around 20Hz but noisy signals were obtained. A quasi-static method<sup>59</sup> was applied to correct the  $C_{ox}$  to estimate the dielectric constant. The final estimation for this device is around 30, which is consistent with the reports in the literature.<sup>22</sup> The same measurement and correction method were applied to Device A and B. Both devices yield  $k$  of 30. As shown in Figure 5.8, all devices behave very insulating at the applied voltages. The leakage current is normalized by the area of top electrode to get the leakage current density. Table 5.1 lists the leakage current density ( $J$ ) at applied electric field ( $E$ ) of 1 MV/cm along with the corresponding equivalent oxide thickness for the devices. The electrical properties in a previous study on STO/Ge, SHO/Ge and SHTO/Ge heterojunctions are also listed for comparison. The SrBO<sub>3</sub> indicates the Sr based perovskite, B indicates the elements of Ti, Zr or Hf in Titanium group.

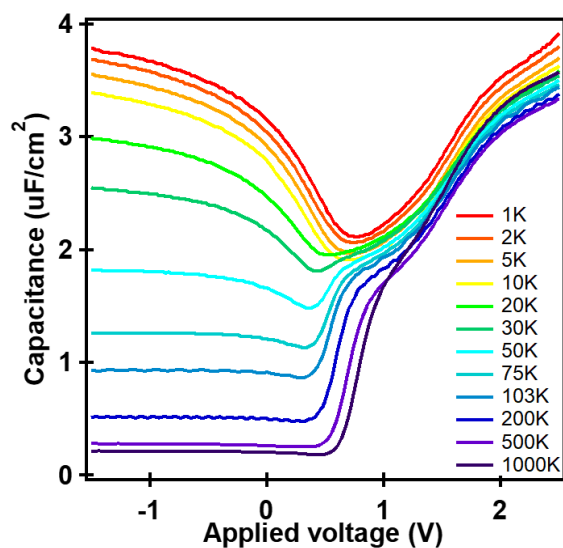


Figure 5.7. Capacitance-voltage (C-V) characteristics of 6.2 nm-SZO on Zintl template (Device C) for frequencies from 1 kHz to 1000kHz. The capacitance is normalized by the area of top  $100\ \mu\text{m} \times 100\ \mu\text{m}$  square Pt electrode.

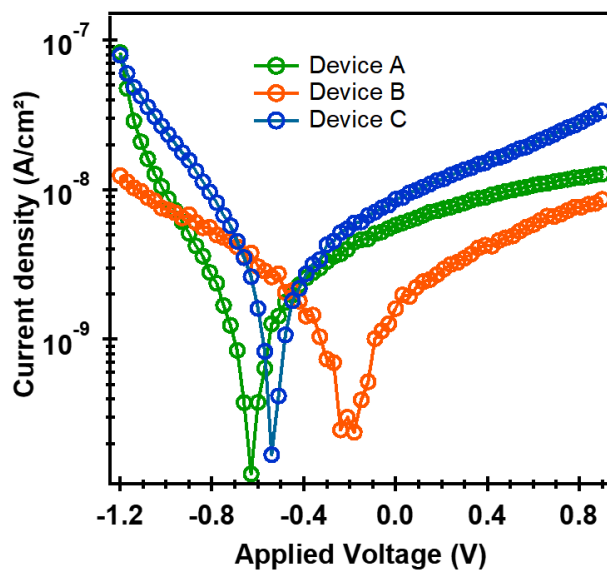


Figure 5.8. Leakage current-voltage (I-V) characteristics of the 11.5 nm-SZO on Ge (Device A), 15.6 nm-SZO on Zintl template (Device B) and 6.2 nm-SZO on Zintl template (Device C). All devices exhibit low leakage current density. The leakage current density is normalized by the area of top  $100\ \mu\text{m} \times 100\ \mu\text{m}$  square Pt electrode.

Table 5.1: Electrical properties for SrBO<sub>3</sub>/Ge heterojunctions

| <b>Material</b>   | <b><i>J</i> (A/cm<sup>2</sup>)</b> | <b>EOT (nm)</b>       | <b><i>k</i></b> | <b>E (MV/cm)</b> |     |
|---|------------------------------------|-----------------------|-----------------|------------------|-----|
| SrZrO <sub>3</sub>  | Device A                           | 1.45×10 <sup>-8</sup> | 1.5             | 30               | 1.0 |
|   | Device B                           | 1.52×10 <sup>-8</sup> | 2.0             |                  |     |
|   | Device C                           | 2.14×10 <sup>-8</sup> | 0.8             |                  |     |
| SrTiO <sub>3</sub> <sup>19</sup>                                    | ~10                                | 0.65                  | ~90             | 0.7              |     |
| SrHfO <sub>3</sub> <sup>10</sup>                                    | 6.3×10 <sup>-6</sup>               | 1.0                   | 17              | 1.0              |     |
| SrHf <sub>0.55</sub> Ti <sub>0.45</sub> O <sub>3</sub> <sup>9</sup> | 0.1                                | 1.8                   | 30              | 1.0              |     |

SZO has better performance on electrical properties than STO, SHO and SHTO (Hf=0.55). The leakage current of SZO/Ge is about nine orders of magnitude less than STO/Ge, which is consistent with the conduction band offset (CBO) for these heterojunctions. The CBO for STO/Ge, SZO/Ge and SHO/Ge heterojunctions are 0.12, 1.77 and 2.17 eV, respectively.<sup>10,19,22</sup> Even though SHO/Ge has a higher CBO than SZO/Ge, the leakage current for SZO/Ge is approximately two order of magnitudes less than SHO/Ge. The main reason may be that SZO is easier to crystalize and obtain better crystallinity than SHO. The annealing temperature for SZO/Ge is about 50 °C less than SHO/Ge. Higher temperature post deposition annealing could cause more oxygen vacancies in SHO films. When comparing the leakage current performance and the EOT with other gate oxides on Ge that re reported in literatures,<sup>20</sup> shows crystalline SZO has a lower leakage current without sacrificing the EOT. This suggests crystalline SZO could be a competitive high-*k* gate oxide material for field-effect transistor device applications.

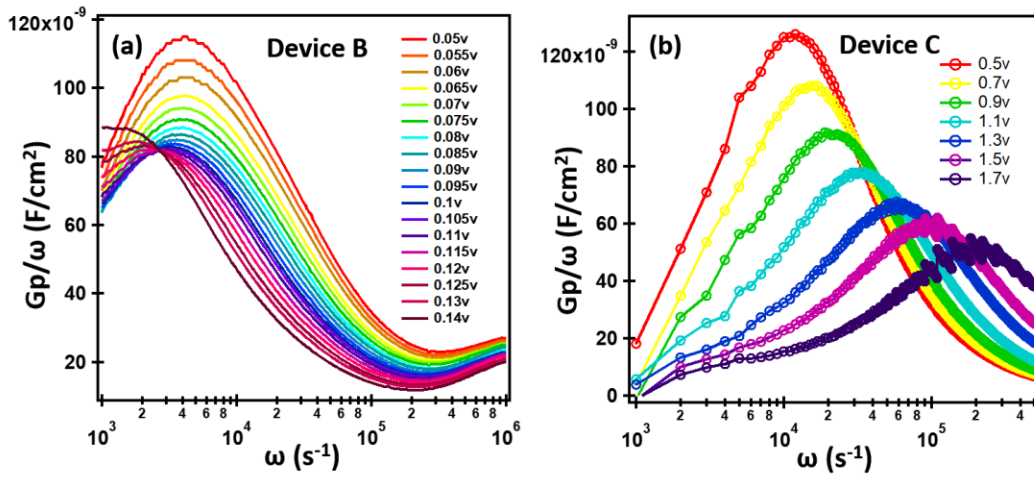


Figure 5.9. The conductance  $G_p$  is measured as a function of frequency and plotted as  $G_p/\omega$  versus  $\omega$ . (a) The  $G_p/\omega$  versus  $\omega$  plot for Device B, (b) The  $G_p/\omega$  versus  $\omega$  plot for Device C.  $G_p/\omega$  is normalized by the area of top  $100 \mu m \times 100 \mu m$  square Pt electrode.

The interface trap density is also an important consideration for high performance field-effect transistor applications. The previous study of crystalline SHO on Ge (001) revealed  $D_{it}$  was  $4 \times 10^{13} cm^{-2} eV^{-1}$ ,<sup>10</sup> which is too high a value for device applications. The  $D_{it}$  of SHO/Ge was measured at room temperature and was estimated by the conductance method.<sup>10,20</sup> The conductance model was set up based on Si, which assumes small minority carrier concentration of  $1.45 \times 10^{10}$ . Ge has a much higher minority carrier concentration of  $2.4 \times 10^{13}$ , contributing strong conductance loss in weak inversion. Therefore  $D_{it}$  measurements on oxide/Ge heterojunctions should be performed at lower temperature around 250K.<sup>58,60</sup> In conductance method for  $D_{it}$  measurement, the conductance  $G_p$  is measured as a function of frequency and plotted as  $G_p/\omega$  versus  $\omega$ . Figure 5.9 presents the results for Devices B and C in depletion region and weak inversion region. The plot of Device A is not shown here. Based on the conductance method, in the depletion and weak

inversion regions, one takes the minimal peak value among the  $G_p/\omega$  versus  $\omega$  curves and substitutes it into the following equation to estimate the  $D_{it}$  value.<sup>59</sup>

$$D_{it} \approx \frac{2.5}{q} \left( \frac{G_p}{\omega} \right)_{max}$$

The estimated  $D_{it}$  values for three devices are listed in Figure 5.10. Device A is expected to have the roughest starting surface and carbon contamination at the interface and it has the largest  $D_{it}$  value of the three devices. Crystalline SZO in Device A comes closest to crystalline SHO with respect to the starting interface and contamination levels, and SZO has a  $D_{it}$  value that is less than the  $\sim 4 \times 10^{13} \text{cm}^{-2} \text{eV}^{-1}$  for SHO.<sup>10</sup>  $D_{it}$ , improved with a Zintl-templated surface and some form of atomic D treatment. The lowest the  $D_{it}$  value of  $8.56 \times 10^{11} \text{cm}^{-2} \text{eV}^{-1}$  was observed when an oxygen plasma was used to generate a flatter Ge (001) surface and lower the the Ge surface steps.<sup>28</sup> The steps could cause the dangling bond at the interface and anti-phase boundary formation within the epitaxial films.<sup>9</sup> The  $D_{it}$  investigation illustrates steps can be taken to improve the electrical properties of crystalline perovskite-germanium interfaces and additional studies may find more optimal treatment conditions.

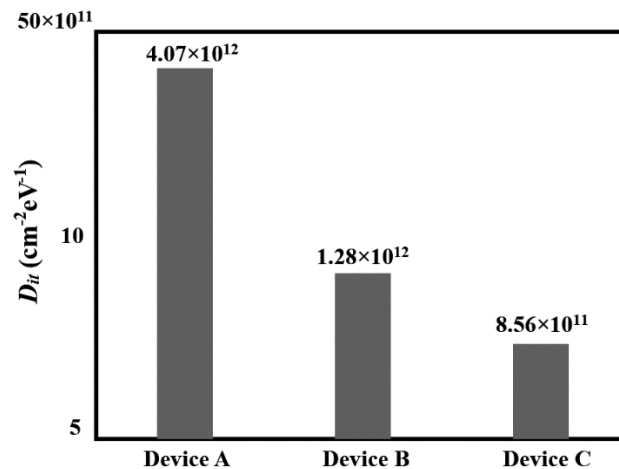


Figure 5.10.  $D_{it}$  values for the three devices



## 5.4. SUMMARY

Heteroepitaxial growth of crystalline SrZrO<sub>3</sub> (SZO) on Ge (001) by atomic layer deposition is reported in this work. Ge (001) surfaces are pretreated with 0.5 monolayers (ML) of Ba atoms and an amorphous 3-nm SZO layer is grown from strontium bis(triisopropylcyclopentadienyl), tetrakis (dimethylamido) zirconium and water at 225 °C. This 3-nm layer crystallizes at 590 °C and subsequent SZO growth at 225 °C leads to crystalline films that do not require further annealing. The film properties are measured using x-ray photoelectron spectroscopy, x-ray diffraction, transmission electron microscopy and metal-oxide semiconductor capacitor structures. The electrical properties for SrZrO<sub>3</sub>/Ge heterojunctions are a dielectric constant of 30 and leakage current of  $2.14 \times 10^{-8}$  A/cm<sup>2</sup> at 1 MV/cm with an EOT=0.8 nm. Ozone plasma pretreatment of Ge (001), Zintl layer formation with 0.5 ML Ba, and atomic deuterium post growth treatment were explored to lower interface traps. The lowest  $D_{it}$  was  $8.56 \times 10^{11}$  cm<sup>-2</sup>eV<sup>-1</sup>.

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## Chapter 6: Research Summary

### 6.1. SUMMARY AND CONCLUSIONS

This work reports the growth of crystalline  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  (SHTO) films on Ge (001) substrates by atomic layer deposition. Samples were prepared with different Hf content  $x$  to explore if strain, from tensile ( $x = 0$ ) to compressive ( $x = 1$ ), affected film crystallization temperature and how composition affected properties. Amorphous films grew at 225 °C and crystallized into epitaxial layers at annealing temperatures that varied monotonically with composition from ~530 °C ( $x = 0$ ) to ~660 °C ( $x = 1$ ). Transmission electron microscopy revealed abrupt interfaces. Electrical measurements revealed 0.1 A/cm<sup>2</sup> leakage current at 1 MV/cm for  $x = 0.55$ .

Using in situ X-ray photoelectron spectroscopy, reflection high-energy electron diffraction and density functional theory, we analyzed the surface core level shifts and surface structure during the initial growth of  $\text{ABO}_3$  perovskites on Ge (001) by atomic layer deposition, where A = Ba, Sr and B = Ti, Hf, Zr. We find that the initial dosing of the barium- or strontium- bis(triisopropylcyclopentadienyl) precursors on a clean Ge surface produces a surface phase that has the same chemical and structural properties as the 0.5-monolayer Ba Zintl layer formed when depositing Ba by molecular beam epitaxy. Similar binding energy shifts are found for Ba, Sr and Ge when using either chemical or elemental metal sources. The observed germanium surface core level shifts are consistent with the flattening of the initially-tilted Ge surface dimers using both molecular and atomic metal sources. Similar binding energy shifts and changes in dimer tilting with alkaline earth metal adsorption are found with density functional theory calculations. High angle angular dark field scanning transmission microscopy images of  $\text{BaTiO}_3$ ,  $\text{SrZrO}_3$ ,  $\text{SrHfO}_3$  and

$\text{SrHf}_{0.55}\text{Ti}_{0.45}\text{O}_3$  reveal the location of the Ba (or Sr) atomic columns between the Ge dimers. The results imply that the organic ligands dissociate from the precursor after precursor adsorption on the Ge surface, producing the same Zintl template critical for perovskite growth on Group IV semiconductors during molecular beam epitaxy.

Heteroepitaxial growth of crystalline  $\text{SrZrO}_3$  (SZO) on Ge (001) by atomic layer deposition is reported. Ge (001) surfaces are pretreated with 0.5 monolayers (ML) of Ba atoms and an amorphous 3-nm SZO layer is grown from strontium bis(triisopropylcyclopentadienyl), tetrakis (dimethylamido) zirconium and water at 225 °C. This 3-nm layer crystallizes at 590 °C and subsequent SZO growth at 225 °C leads to crystalline films that do not require further annealing. The film properties are measured using x-ray photoelectron spectroscopy, x-ray diffraction, transmission electron microscopy and metal-oxide semiconductor capacitor structures. The electrical properties for  $\text{SrZrO}_3/\text{Ge}$  heterojunctions are a dielectric constant of 30 and leakage current of  $2.14 \times 10^{-8} \text{ A/cm}^2$  at 1 MV/cm with an EOT=0.8 nm. Oxygen plasma pretreatment of Ge (001), Zintl layer formation with 0.5 ML Ba, and atomic deuterium post growth treatment were explored to lower interface traps. The lowest  $D_{it}$  was  $8.56 \times 10^{11} \text{ cm}^{-2} \text{ eV}^{-1}$ .

## 6.2. RECOMMENDATIONS FOR FUTURE WORK

This research implied that  $\text{SrZrO}_3$  has the best electrical properties in  $\text{SrBO}_3$  perovskite for high- $k$  gate oxides applications in transistors. An efficient treatment strategy, which combines Zintl template preparation, oxygen plasma pre-treatment and post atomic deuterium treatment, has been employed here to reduce the  $D_{it}$  value. The lowest  $D_{it}$  value ( $8.56 \times 10^{11} \text{ cm}^{-2} \text{ eV}^{-1}$ ) has met the requirement for device fabrication.<sup>1</sup> Since high quality interfaces can result in higher channel mobility and driving current  $I_{on}$ . Tagaki has reported

the high effective electron mobility of  $488 \text{ cm}^2/\text{Vs}$  for  $\text{Al}_2\text{O}_3/\text{GeO}_x/\text{Ge}$  nMOSFETs with atomic deuterium PDA at  $400 \text{ }^\circ\text{C}$ .<sup>2</sup> So the performance of MOSFET device based on the materials of this dissertation could be very promising. The MOSFET fabrication process could follow the procedure shown in Figure 6.1.<sup>3</sup> Following the same process and replacing the step 3) with the  $\text{SrZrO}_3$  deposition and treatment process as discussed in Chapter 5 can easily obtain the MOSFET for further electrical measurements.

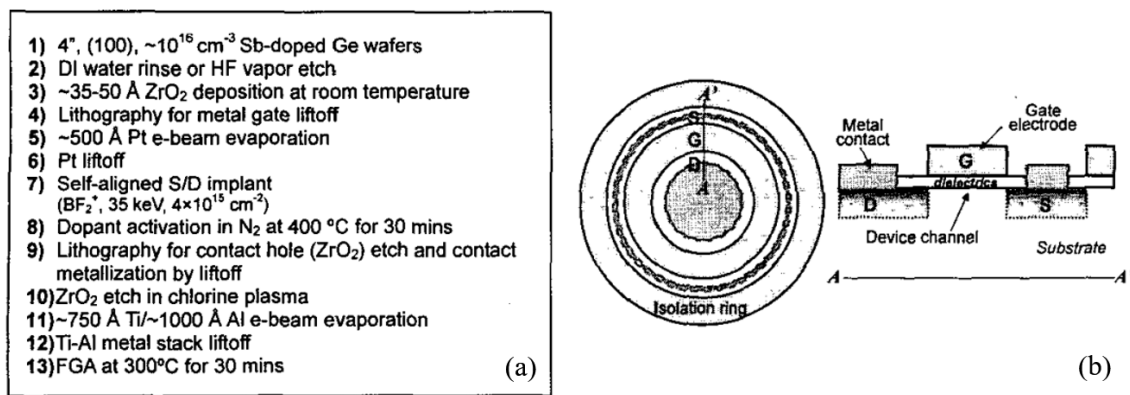


Figure 6.1. (a) The procedure for nMOSFET fabrication process based on  $\text{ZrO}_2/\text{Ge}$  gate stack, (b) the top-view of the ring MOSFET structure (left) and the cross-sectional side view of AA' (right). Taken from "A Sub- $400 \text{ }^\circ\text{C}$  Germanium MOSFET Technology with High- $k$  Dielectric and Metal Gate." *Electron Devices Meeting (IEDM)*, International. IEEE, 437-440, (2002).<sup>3</sup>

Though the  $\text{SrZrO}_3$  study obtained the desired electrical properties and acceptable  $D_{it}$  value. The influence from the relatively high compressive strain, which is around  $-2.6\%$  of  $\text{SrZrO}_3$  on Ge, has not been estimated. High strain could cause the bond length changes at the interface and possibly lead to dangling bonds. The work in Chapter 3 presents the strain of  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  and it can be adjusted by the Hf content  $x$ . But since the Ti content will eliminate the conduction band offset (CBO) between  $\text{SrHf}_x\text{Ti}_{1-x}\text{O}_3$  and Ge and can lead to higher leakage current, so the influence of strain on  $D_{it}$  still hasn't been explored

yet. There is another perovskite  $\text{CaHfO}_3$  gives an opportunity here. Since  $\text{CaHfO}_3$  has a simple cubic perovskite lattice parameter  $a_p=3.991 \text{ \AA}$ ,<sup>4</sup> with the  $3.992 \text{ \AA}$  Ge-Ge distance on (001) surface, the pseudocubic unit cell of  $\text{CaHfO}_3$  can perfectly match with Ge (001) substrate.

The large band gap of  $\text{CaHfO}_3=6.4 \text{ eV}$  also enables the  $\text{CaHfO}_3/\text{Ge}$  heterojunction to be insulating enough for the  $D_{it}$  measurement. In addition, the suitable precursors of Ca and Hf are available as bis(tri-isopropylcyclopentadienyl) calcium 1,2-dimethoxyethane and hafnium formamidinate [ $\text{Hf}(\text{fmd})_4$ ] (Hf-FAMD), respectively for ALD deposition. Also the deposition of  $\text{CaHfO}_3$  on Ge by ALD has never been reported before.

Last but not the least, this research offers a very insulating materials  $\text{SrZrO}_3$  on Ge (001) and a good strategy to achieve lower interface trap density. We can further apply this work for ferroelectric switching study of  $\text{BaTiO}_3$  by building up  $\text{BaTiO}_3/\text{SrZrO}_3/\text{Ge}$ .

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## Appendix

### First-Author Publications

- <sup>1</sup> S. Hu, A. B. Posadas, A. A. Demkov and J. G. Ekerdt, "The function of Zintl layer for atomic layer deposition on Ge (001)" **In preparation.**
- <sup>2</sup> S. Hu, L. Ji, P-Y Chen, H-L Chang, A. Posadas, H-W Wu, E. T. Yu, D. J. Smith, A. A. Demkov, and J. G. Ekerdt, "Crystalline SrZrO<sub>3</sub> deposition on Ge (001) by atomic layer deposition for high-k dielectric application" **In preparation.**
- <sup>3</sup> S. Hu, E. L. Lin, A. K. Hamze, A. Posadas, H-W Wu, D. J. Smith, A. A. Demkov, and J. G. Ekerdt, "Zintl layer formation during perovskite atomic layer deposition on Ge (001)" *J. Chem. Phys.* **146**, 052817 (2016).
- <sup>4</sup> S. Hu, M. D. McDaniel, A. Posadas, C. Hu, H. Wu, E. T. Yu, D. J. Smith, A. A. Demkov, and J.G. Ekerdt, "Monolithic Integration of Perovskites on Ge(001) by Atomic Layer Deposition: A Case Study with SrHf<sub>x</sub>Ti<sub>1-x</sub>O<sub>3</sub>" *MRS Commun.* **6**, 125 (2016).

## Co-Author Publications

- <sup>1</sup> F. Cheng, A. D. Johnson, Y. Tsai, P-H Su, S. Hu, J. G. Ekerdt, and C-K. Shih, “Enhanced Photoluminescence of Monolayer WS<sub>2</sub> on Ag Films and Nanowire–WS<sub>2</sub>–Film Hybrids” *ACS Photonics*, **4**, 1421 (2017).
- <sup>2</sup> E. L. Lin, S. Hu, and J. G. Ekerdt, “Monolithic integration of metal-ferroelectric-semiconductor heterostructure using atomic layer deposition” In *Proc. of SPIE* **10105**, 1010519 (2017).
- <sup>3</sup> E. L. Lin, B. I. Edmondson, S. Hu, and J. G. Ekerdt, “Epitaxial Growth of Perovskite Strontium Titanate on Germanium via Atomic Layer Deposition” *JoVE (Journal of Visualized Experiments)*, **113**, e54268-e54268 (2016).
- <sup>4</sup> M. D. McDaniel, T. Q. Ngo, S. Hu, A. Posadas, A. A. Demkov, and J. G. Ekerdt, “Atomic layer deposition of perovskite oxides and their epitaxial integration with Si, Ge, and other semiconductors” *Appl. Phys. Rev.* **2**, 041301 (2015).

## Technical Presentation

<sup>1</sup> (Presentation) *Zintl template formation and function during atomic layer deposition growth of crystalline perovskites on Ge (001)*, AVS 64th International Symposium & Exhibition 2017, Tampa, Florida, October 29-November 3, 2017.

<sup>2</sup> (Poster) *Structural and Electrical Characterization of Epitaxial Sr(Hf,Ti)O<sub>3</sub> grown on Ge (001) by Atomic Layer Deposition*, The 15th International Conference on Atomic Layer Deposition, Portland, Oregon June 28 – July 1, 2015.

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## Vita

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