

## Research Article

# **Control System Design of Shunt Active Power Filter Based on Active Disturbance Rejection and Repetitive Control Techniques**

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Received 13 December 2013; Accepted 18 February 2014; Published 23 March 2014

Academic Editor: Xiaojie Su

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To rely on joint active disturbance rejection control (ADRC) and repetitive control (RC), in this paper, a compound control law for active power filter (APF) current control system is proposed. According to the theory of ADRC, the uncertainties in the model and from the circumstance outside are considered as the unknown disturbance to the system. The extended state observer can evaluate the unknown disturbance. Next, RC is introduced into current loop to improve the steady characteristics. The ADRC is used to get a good static performance. A good simulation result is got through choosing and changing the parameters, and the feasibility, adaptability, and robustness of the control are testified by this result.

#### 1. Introduction

The proliferation of nonlinear loads caused by more and more modem electronic equipments results in deterioration of power quality in power transmission or distribution systems. Harmonic, reactive, negative sequence and flickers are the reasons of various undesirable phenomena in the operation of power system. In order to solve these problems, the concept of active power filter (APF) was presented. Active power filters, which compensate harmonic and reactive current component for the power supplies, can improve the power qualities and enhance the reliabilities and stabilities on power utility [1–3]. In recent 30 years from APF presented, the continual innovation of control strategies mainly impels the APF techniques to be developed rapidly [4–7].

Active disturbance rejection control (ADRC) is a robust control method that is based on extension of the system model with an additional and fictitious state variable, representing everything that the user does not include in the mathematical description of the plant [8–11]. Different from other disturbances and states estimation [12–15], this virtual state (sum of internal and external disturbances, usually denoted as a "total disturbance") is estimated online with a state observer and used in the control signal in order to decouple the system from the actual perturbation acting on the plant. This disturbance rejection feature allows user to treat the considered system with a simpler model, since the negative effects of modeling uncertainty are compensated in real time. As a result, the operator does not need a precise analytical description of the system, as one can assume the unknown parts of dynamics as the internal disturbance in the plant. Robustness and the adaptive ability of this method make it an interesting solution in scenarios where the full knowledge of the system is not available.

Repetitive control is a control method developed by a group of Japanese scholars in 1980s. It is based on the Internal Model Principle and used specifically in dealing with periodic signals, for example, tracking periodic reference or rejecting periodic disturbances. The repetitive control system has been proven to be a very effective and practical method dealing with periodic signals [15–18]. Repetitive control has some similarities with iterative learning control.

This paper addresses the electric current tracking control problem for shunt APF. The control law is joint ADRC and RC which can deal with the static and dynamic performance. The rest of this paper is organized as follows. In Section 2,



FIGURE 1: Block diagram of the ADRC.

a brief description of the ADRC is presented. In Section 3, main results of ADRC + RC control technique are developed. In Section 4, simulation results are presented to show the effectiveness of the proposed control technique. Finally, some conclusions are made in Section 5.

#### 2. Active Disturbance Rejection Control

In ADRC, the tracking differentiator (TD) is used to deal with the reference input and the extended state observer (ESO) is used to deal with the output of controlled system. Then the ADRC control law can be selected through the appropriate nonlinear combination of state errors. The general structure of ADRC is shown in Figure 1. In Figure 1 of ADRC, the transient profile generator is used to obtain each order derivative  $\dot{y}_*(t), \ddot{y}_*(t), \dots, y_*^n(t)$  of reference trajectory  $y_*(t)$ . Next, brief description of ADRC is given as follows.

Consider a class SISO nonlinear system as

$$y^{n} = f(y, \dot{y}, \dots, y^{(n-1)}, t) + bu(t) + d(t).$$
(1)

Equation (1) also can be described as

$$\dot{x}_{1} = x_{2}$$

$$\vdots$$

$$x_{n-1} = x_{n}$$

$$\dot{x}_{n} = f(x_{1}, x_{2}, \dots, x_{n-1}, t) + bu(t) + d(t)$$

$$y = x_{1},$$
(2)

where  $f(x_1, x_2, ..., x_{n-1}, t)$  is unknown function, d(t) is unknown disturbance, and u(t) is control input.

Construct the following ESO for nonlinear systems (2):

$$\dot{z}_{1} = z_{2} - g_{1} (z_{1} - y)$$

$$\vdots$$

$$\dot{z}_{n} = z_{n+1} - g_{n} (z_{1} - y) + bu (t)$$

$$\dot{z}_{n+1} = -g_{n+1} (z_{1} - y).$$
(3)

Let  $a(t) = f(x_1, x_2, ..., x_{n-1}, t) + d(t)$ , so we can obtain the following conclusion:

$$z_1 \longrightarrow x_1, z_2 \longrightarrow x_2, \dots, z_n \longrightarrow x_n, z_{n+1} \longrightarrow a$$
 (4)

through selecting appropriate nonlinear function  $g_1, g_2, ..., g_{n+1}$ . Defining that  $\hat{a}(t)$  is the estimation value of a(t), we can obtain  $z_{n+1} = \hat{a}(t)$ .

From the above brief description of ESO, it can be seen that ESO can be used to estimate the states and the sum of model uncertainty  $f(x_1, x_2, ..., x_{n-1}, t)$  and disturbance d(t). So, ESO is such a link, which uses the output y(t) of plant to get each order derivative signal  $z_1, z_2, ..., z_n$  and estimation value of disturbance.

Using  $z_1, z_2, ..., z_n$  from ESO and  $\dot{y}_*(t), \ddot{y}_*(t), ..., y_*^n(t)$  from TD, we get the state errors as

$$\varepsilon_i = y_*^i(t) - z_i, \quad i = 1, 2, \dots, n.$$
 (5)

So the following nonlinear combination can be gotten by state errors (5):

$$u_0(t) = k_1 \operatorname{fal}\left(\varepsilon_1, \alpha, \delta\right) + \dots + k_n \operatorname{fal}\left(\varepsilon_n, \alpha, \delta\right), \qquad (6)$$

where  $k_i$ ,  $\alpha$ , and  $\delta$  are adjustable parameters. And nonlinear function fal is defined as follows:

$$\operatorname{fal}\left(\varepsilon_{i}, \alpha, \delta\right) = \begin{cases} \left|\varepsilon_{i}\right|^{\alpha} \operatorname{sgn}\left(\varepsilon_{i}\right) & \left|\varepsilon_{i}\right| > \delta\\ \frac{\varepsilon_{i}}{\delta^{1-\alpha}} & \left|\varepsilon_{i}\right| \le \delta. \end{cases}$$
(7)

Using the nonlinear state errors feedback (6) and estimation value  $\hat{a}(t)$ , the ADRC law can be given by

$$u(t) = \frac{u_0(t) - \hat{a}(t)}{b}, \quad i = 1, 2, \dots, n.$$
(8)

#### 3. Main Results

Shunt APF circuit schematic is shown in Figure 2; the upper and lower arm of the shunt APF can be considered as ideal switch from the APF working principle. The equivalent circuit of APF is shown in Figure 3. Since the switching operation can control voltage size of the AC side. So shunt APF can be considered as a controllable voltage source and a parallel impedance in the circuit, and to compensate harmonic current and reactive current can be achieved.

So we can obtain the model of shunt APF as follows:

$$L\frac{di_c}{dt} = u_i - Ri_c - u_c.$$
<sup>(9)</sup>



FIGURE 2: Block diagram of shunt APF.



FIGURE 3: Equivalent circuit of shunt APF.

Define PWM as a proportional part, namely,  $u_c = uV_c$ , where *u* is modulation amount. Let *u* be the control input of system.  $V_c$  is voltage of DC side. For the supply current, we know

$$i_s = i_c + i_L. \tag{10}$$

Substituting (10) into (9), we have

$$(L+L_{s})\frac{di_{s}}{dt} = -(R+R_{s})i_{s} - uV_{c} + u_{s} + Ri_{L} + L\frac{di_{L}}{dt}.$$
 (11)

Designed system controller can be considered by a DC voltage outer-loop control and an inner-loop current control. Since the response speed of inner-loop is much faster than the DC voltage outer-loop, it can be considered that DC voltage is constant when the inner current controls. Ignore the impedance of the power line; we let  $\overline{d}(t) = u_s + Ri_L + L(di_L/dt)$ ; system (11) can be written as

$$\left(L+L_{s}\right)\frac{di_{s}}{dt}=-\left(R+R_{s}\right)i_{s}-uV_{c}+\overline{d}\left(t\right).$$
(12)

The APF is a first-order system. ADRC does not need to detect the load current and supply voltage and only uses them as unknown disturbances. A PI controller is used to control the outer-loop DC voltage, which is order to obtain a given current value  $i_s^*(t)$ .  $i_s^*(t)$  can be seen as the reference input  $y_*(t)$  of ADRC. The control objective is to make the supply current  $i_s$  able to track the given current value  $i_s^*(t)$  through controlling the modulation amount u of PWM. Set an order TD output as

$$\dot{z}_{1,1} = -k_0 \text{fal}\left(\left(z_{1,1} - i_s^*(t)\right), \alpha_0, \delta_0\right), \quad (13)$$

where  $k_0$ ,  $\alpha_0$ , and  $\delta_0$  are selected parameters. Construct of the following formula ESO:

$$\begin{aligned} \dot{z}_{1} &= z_{2} - k_{11} \text{fal} \left( \left( z_{1} - i_{s}^{*} \left( t \right) \right), \alpha_{1}, \delta_{1} \right) - V_{c} u \left( t \right), \\ \dot{z}_{2} &= -k_{12} \text{fal} \left( \left( z_{1} - i_{s}^{*} \left( t \right) \right), \alpha_{1}, \delta_{1} \right), \end{aligned}$$
(14)

where  $k_{11}$ ,  $k_{12}$ ,  $\alpha_1$ , and  $\delta_1$  are selected parameters. So we can obtain the ADRC law as

$$u_{0}(t) = k_{2} \operatorname{fal}\left(\left(i_{s}^{*}(t) - z_{1}\right), \alpha_{2}, \delta_{2}\right),$$

$$u(t) = \frac{u_{0}(t) - \dot{z}_{2}}{V_{c}},$$
(15)

where  $k_2$ ,  $\alpha_2$ , and  $\delta_2$  are also selected parameters. All selected parameters of ADRC controller must try to get in simulation.

RC is mainly used in continuous processes for tracking or rejecting periodic exogenous signals. In most cases, the period of the exogenous signal is known. The internal model principle is the theoretical foundation of RC. According to internal model principle, to track or reject a certain signal without steady-state error, the signal can be regarded as the output of an autonomous generator that is inside the control system.

Although RC system can still get a good static performance, it cannot get a good dynamic performance of the system. RC is usually used to meet up with other control strategies. Actually, RC is only used to restrain the tracking error. But ADRC can improve the rapid response of the system. After being coupled with the repetitive controller, controller can detect the tracking error and accumulate a correction on the basis of the original command to reduce the error. Repetitive controller can be seen as an embedded component, so this system is called embedded repetitive control system (ERCS). Figure 4 is a block diagram of a parallel ADRC with RC. Next, how to select the controller parameters of RC is shown as follows.

- *Cycle delay factor N: N* is sampled beat number of sinusoidal cycle and can be described as fundamental frequency f<sub>s</sub> and the switching frequency f<sub>c</sub>.
- (2) Compensation link Q(z): Q(z) characterizes the steady precision of repetitive controller. In general, Q(z) is a constant. When Q(z) = 1, the open-loop gain of system is infinite, and steady-state error is zero. But this may likely cause system instability. So we usually select a constant that is less than but close to 1. Q(z) is also preferably chosen zero phase low pass filter.
- (3) Compensation link S(z) of plant: S(z) is used to reform the controlled plant. After reformation, the amplitude-frequency characteristics of the plant has zero gain in the low frequency band. Generally, the series correction part  $S_1(z)$  is first selected to correct the low-frequency gain of controlled plant. Then, in order to improve system stability, the second-order low-pass filter is selected to attenuate high frequency gain.
- (4) Phase compensation factor k: the aim of phase compensation factor k is to compensate phase lag for reformed controlled plant in the low frequency.
- (5) Repetitive controller gain K<sub>r</sub>: K<sub>r</sub> is used to ensure the stability of the system in the high frequency band. The smaller K<sub>r</sub> can cause the better stability, but the speed of convergence will become slow and the steady-state



FIGURE 4: Block diagram of a parallel ADRC with RC for shunt APF.



FIGURE 5: Load electric current.

error will increase. In general,  $K_r$  is chosen to be close to 1 as possible under maintaining the well stability of the RC.

#### 4. Simulation Results

In this section, we use Matlab/Simulink for testing and verifying the proposed APF control method. The parameters of chosen APF are L = 1 H,  $R = 8 \Omega$ ,  $T = 100 \mu$ s, and  $f_c = 10$  kHz. The ADRC controller parameters are designed as  $\alpha_0 = 2$ ,  $\alpha_1 = 0.5$ ,  $\alpha_0 = 1$ ,  $\delta_0 = 0.00001$ ,  $\delta_1 = 0.001$ ,  $\delta_0 = 0.00001$ ,  $k_0 = 8000$ ,  $k_{11} = 10000$ ,  $k_{12} = 5000$ , and  $k_2 = 500$ . The RC controller parameters are designed as N = 200,  $S_1(z) = (z - 0995)/[0.1248(z - 06)]$ ,  $S_2(z) = (0.0675z^2 + 01349z + 00675)/(z^2 - 1143z + 0.4128)$ , Q(z) = 0.97, k = 3, and  $K_r = O.8$ . First, we consider the 150 Hz sine wave for load. Figures 5 and 6 show the load electric current which is third harmonic and the output current i(t) of controlled APF.



FIGURE 6: Reference current  $i^*(t)$  and output current i(t) of controlled APF.

Figure 7 shows the total harmonic distortion (THD) analysis for grid current. It can be seen that the proposed control method of the APF can better restrain harmonic of grid. The value of TDH can achieve 0.26%.

First, we consider the 200 Hz square wave signal for load. Figures 8 and 9 show the load electric current which is fourth harmonic and the output current i(t) of controlled APF. Figure 10 shows the total harmonic distortion (THD) analysis for grid current. It can be seen that the proposed control method of the APF can better restrain harmonic of grid. The value of TDH can achieve 3.88%.

In simulation process, we do not correct the controller parameters, just change the distortion form of load; the simulation results are provided to show that the proposed



FIGURE 7: THD analysis for grid current.



FIGURE 8: Load electric current.



FIGURE 9: Reference current  $i^*(t)$  and output current i(t) of controlled APF.



FIGURE 10: THD analysis for grid current.

control algorithm of APF has a very reliable robustness and adaptability for the different distortion forms.

#### 5. Conclusions

Since the switch voltage drops, the drive circuit delay differs and dead zones are affected, there are a large number of low-order harmonics in the output current of a single-phase grid-connected APF. The traditional PI controller exists the capacity deficiencies in the harmonic suppression, and unable realizes the static error tracking for the sine command current. It can effectively improve the grid current waveform through ADRC + RC controller. In this paper, we give the composite control law design method for single-phase grid connected APF current loop. Theory and simulation are provided to show that the proposed control algorithm has a very reliable tracking ability and satisfactory robustness to different harmonics of the load.

#### **Conflict of Interests**

The authors declare that there is no conflict of interests regarding the publication of this paper.

#### Acknowledgments

The authors wish to thank Professor Yongqiang Ye and Dr. Xu for their constructive comments and suggestions which have helped to improve the presentation of the paper. This work was partially supported by the Advanced Research Project of Jiangsu Electric Power Company Research Institute.

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