# Optimal and Heuristic Algorithms to Synthesize Lattices of Four-Terminal Switches<sup>☆</sup>

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## Abstract

In this work, we study implementation of Boolean functions with nano-crossbar arrays where each crosspoint behaves as a fourterminal switch controlled by a Boolean literal. These types of arrays are commonly called as *switching lattices*. We propose optimal and heuristic algorithms that minimize lattice sizes to implement a given Boolean function. The algorithms are mainly constructed on a technique that finds Boolean functions of lattices having independent inputs. This technique works recursively by using transition matrices representing columns and rows of the lattice. It performs symbolic manipulation of Boolean literals as opposed to using truth tables that allows us to successfully find Boolean functions having up to 81 variables corresponding to a  $9\times9$ -lattice. With a Boolean function of a certain sized lattice, we check if a given function can be implemented with this lattice size by defining the problem as a satisfiability problem. This process is repeated until a desired solution is found. Additionally, we fix the previously proposed algorithm that is claimed to be optimal. The fixed version guarantees optimal sizes. Finally, we perform synthesis trials on standard benchmark circuits to evaluate the proposed algorithms by considering lattice sizes and runtimes in comparison with the recently proposed three algorithms.

Keywords: Nano-crossbar Arrays, Switching Lattices, Logic Synthesis, Satisfiability.

#### 1. Introduction

Nano-crossbar arrays have emerged as area and power efficient structures with an aim of achieving high performance computing beyond the limits of current CMOS [1, 2, 3]. Computing is achieved with crosspoints behaving as switches, either two-terminal or four-terminal. This is illustrated in Figure 1. Depending on the used technology, a two-terminal switch behaves as a diode [4, 5], a resistive/memristive switch [6], or a field effect transistor (FET) [7]. Diode and resistive switches correspond to the crosspoint structure in Figure 1 a); here, the switch is controlled by the voltage difference between the terminals. Figure 1 b) shows a FET based switch; here, the red line represents the controlling input. A four-terminal switch is given in Figure 1 c). The controlling input, not shown in the figure, has a separate physical formation from the crossbar that is thoroughly explained for different technologies in [8].

Comparing the array sizes to implement a given Boolean function, we see that the four-terminal switch based arrays overwhelm the two-terminal based ones [9]. In these comparisons resistive/memristive arrays are not taken into account. However, it is not difficult to guess that their sizes are much worse than those of diode and FET based arrays. The reason is that resistive arrays use a minterm/maxterm representation of a given Boolean function such that each minterm/maxterm is implemented by a crossbar line [6, 10, 11]. On the other hand, diode

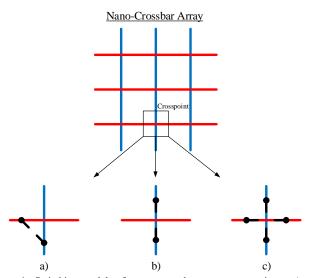


Figure 1: Switching models of a nano-crossbar array: crosspoint as a) twoterminal switch with terminals in the crossed lines, b) two-terminal switch with terminals in the same line, and c) four-terminal switch.

and FET based arrays do not have such restriction, so the minimal sum of product forms can be used with each product implemented by a line [7, 12, 9]. As a result, four-terminal switch based arrays offer an important size advantage. Indeed, this is not surprising since they use two dimensional paths to implement products of a given function as opposed to using one dimensional paths (crossbar lines). In this study, we further investigate four-terminal switch based arrays to synthesize Boolean functions. These types of arrays are commonly called as *switching lattices*; we use this naming throughout the paper.

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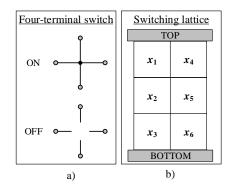


Figure 2: a) four-terminal switch, and b) switching lattice.

A four-terminal switch is shown in Figure 2 a). The four terminals of the switch are all either mutually connected (ON) or disconnected (OFF). A 3 ×2 switching lattice having 6 four-terminal switches is shown in Figure 2 b). Here, each switch is controlled by a Boolean literal. If the literal takes the value 1 (0) then the corresponding switch is ON (OFF). The Boolean function for the lattice evaluates to 1 iff there is a closed path between the top and bottom plates of the lattice. The function is obtained by taking the sum of the products of the literals along each path. These products are  $x_1x_2x_3$ ,  $x_1x_2x_5x_6$ ,  $x_4x_5x_2x_3$ , and  $x_4x_5x_6$ . We conclude that this lattice of four-terminal switches implements the Boolean function  $x_1x_2x_3+x_1x_2x_5x_6+x_2x_3x_4x_5+x_4x_5x_6$ .

The logic synthesis problem of switching lattices is first introduced in [8]. In this work, a systematic technique is proposed to implement a given Boolean function with an  $m \times n$ lattice where n and m are the number of products of the function and its dual, respectively. Although it is a general and a straightforward technique, the resulting lattices may become quite large, far from optimal lattice sizes. To achieve smaller sizes, a Boolean decomposition based technique is proposed [13]. However, it is only applicable for parity functions (XOR functions). More comprehensive decomposition based studies are proposed in [14] and [15] by exploiting P-circuits and dimension-reducibility, respectively. The results are satisfactory with affordable runtimes, but still no guarantee of being close to optimal results. Furthermore, dimension-reducibility can not be applicable to all Boolean functions; there are restrictions. Another decomposition based technique is proposed for a special class of "regular" Boolean functions, called autosymmetric functions [16]. In this work, the idea of connecting separate lattices, not necessarily using a single lattice, is also examined. Although, using separate lattices can significantly reduce the total lattice area, it certainly kills the main motivation of using nano-crossbar arrays that is "overcoming interconnection problems between separate blocks/gates/transistors of conventional circuits".

There are also studies aiming at optimal results. A simple, truth table based brute-force algorithm is presented in [13]. However, as expected it suffers from high runtimes that quickly grow beyond practical limits with an increase in lattice size. Another optimal algorithm is proposed in [17]. It is an anytime

algorithm that reduces the problem into a satisfiability problem with using dichotomic search. Although its runtimes are much better than those of the above mentioned one, speed of the algorithm is still an issue especially for relatively large benchmarks. Additionally, the algorithm is claimed to be optimal, but it is not for some cases. We fix this algorithm to guarantee optimal sizes.

Considering the mentioned shortcomings in the literature, we develop optimal and heuristic algorithms, based on a new technique that finds Boolean functions of lattices having independent inputs. For example, a Boolean function of a  $3\times3$  lattice has 9 variables each of which is assigned to each of the 9 switches. This technique works recursively by using transition matrices representing columns and rows of the lattice. It performs symbolic manipulation of Boolean literals as opposed to using truth tables that allows us to successfully find Boolean functions having up to 81 variables corresponding to a  $9\times9$  lattice. After having a Boolean function of a certain sized lattice, we check if a given target function can be implemented with this size by defining the problem as a Boolean satisfiability (SAT) problem, and using a SAT solver. This process is repeated until a desired solution is found.

Outline of the paper is as follows. In Section 2, we introduce preliminaries for switching lattices and their logic synthesis fundamentals. In Section 3, we present our optimal and heuristic algorithms. In Section 4, we first show how to fix the previously proposed optimal algorithm in [17], and then we give experimental results to evaluate the proposed algorithms by considering lattice sizes and runtimes in comparison with the recently proposed three algorithms. Section 5 concludes this study with insights and future directions.

## 2. Preliminaries

We first explain key concepts used in this study, and then define the logic synthesis problem with examples.

# 2.1. Definitions

**Definition 1.** Consider k independent **Boolean variables**,  $x_1$ ,  $x_2, \ldots, x_k$ . **Boolean literals** are Boolean variables and their complements, i.e.,  $x_1, \bar{x}_1, x_2, \bar{x}_2, \ldots, x_k, \bar{x}_k$ .

**Definition 2.** A product (P) is an AND of literals, e.g.,  $P = x_1 \bar{x}_3 x_4$ . A sum-of-products (SOP) expression is an OR of products.

**Definition 3.** A sum (S) is an OR of literals, e.g.,  $S = x_1 + \bar{x}_3 + x_4$ . A product-of-sums (POS) expression is an AND of sums.

**Definition 4.** A prime implicant (PI) of a Boolean function *f* is a product that implies *f* such that removing any literal from the product results in a new product that does not imply *f*.

**Definition 5.** An irredundant sum-of-products (ISOP) expression is an SOP expression, where each product is a PI and no PI can be deleted without changing the Boolean function f represented by the expression.

			TOP						
	$x_1$			<i>x</i> <sub>2</sub>		<i>x</i> <sub>3</sub>			
	<i>x</i> <sub>4</sub>			<i>x</i> 5		<i>x</i> <sub>6</sub>			
	<i>x</i> <sub>7</sub>			<i>x</i> 8		<i>x</i> 9			
BOTTOM									

Figure 3: A lattice with eight-connected paths.

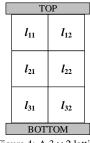


Figure 4: A  $3 \times 2$  lattice.

**Definition 6.** Given a Boolean function f in SOP form, let the **degree** of f, denoted by degree<sub>f</sub>, be the maximum number of literals in a product of f.

For example, if  $f = x_1x_2x_3 + \bar{x}_1x_4$  then  $degree_f = 3$ .

Definition 7. f and g are dual Boolean functions iff

$$f(x_1, x_2, \ldots, x_k) = \bar{g}(\bar{x}_1, \bar{x}_2, \ldots, \bar{x}_k).$$

Given an expression for a Boolean function in terms of AND, OR, NOT, 0, and 1, its dual can also be obtained by interchanging the AND and OR operations as well as interchanging the constants 0 and 1.

For example, if  $f(x_1, x_2, x_3) = x_1x_2 + \bar{x}_1x_3$  then  $f^D(x_1, x_2, x_3) = (x_1 + x_2)(\bar{x}_1 + x_3)$ . A trivial example is that for f = 1, the dual  $f^D$  is 0.

**Definition 8.** An eight-connected path in a lattice, consists of both directly and diagonally adjacent sites.

An example is shown in Figure 3. Here the paths  $x_1x_4x_8$  and  $x_3x_6x_5x_8$  shown by red and blue lines are both eight-connected paths; however only the blue one is four-connected. For simplicity, we generally use "path" to refer a four-connected path.

**Definition 9.** Consider an  $R \times C$  lattice. A **lattice input**  $l_{ij}$  is assigned to a switch/site in the ith row and the jth column of the lattice where  $1 \le i \le R$  and  $1 \le j \le C$ . A lattice input can be a Boolean literal, logic 0, or logic 1. The **lattice function**  $f_{R\times C}(l_{11}, l_{12}, ..., l_{RC})$  is defined as OR of all four-connected top-to-bottom paths.

As an example, consider a lattice in Figure 4. Here,  $f_{3\times 2} = l_{11}l_{21}l_{31} + l_{11}l_{21}l_{22}l_{32} + l_{12}l_{22}l_{21}l_{31} + l_{12}l_{22}l_{32}$ .

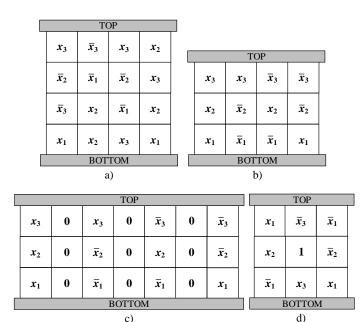


Figure 5: Implementation of  $f_T = XOR_3$  with a) 4×4 lattice (general method in [8]), b) 4×3 lattice (method for parity functions in [8]), and c) 3×7 lattice (separation of products with 0's), and d) 3×3 lattice (optimal solution).

#### 2.2. Synthesis Problem

Given a target Boolean function  $f_T$ , we aim to find a minimum size lattice with assigned literals, logic 0's, and logic 1's to its lattice inputs such that  $f_{R\times C} = f_T$  (OR of all top-to-bottom paths equals  $f_T$ ).

Suppose that  $f_T = XOR_3 = x_1x_2x_3 + x_1\overline{x_2} \ \overline{x_3} + \overline{x_1}x_2\overline{x_3} + \overline{x_1}x_2\overline{x_1} + \overline{x_1}x_2\overline{x_2} + \overline{x_1}x_2\overline{x_1} + \overline{x_1}x_2\overline{x_2} + \overline{x_1}x_2\overline{x_1} + \overline{x_$  $\overline{x_1}$   $\overline{x_2}x_3$ . Figure 5 shows different solutions to implement  $f_T$ . The first lattice in Figure 5 a) corresponds to a general method proposed in [8]. Here, R and C are found as the number of products in  $f_T^D$  and  $f_T$ , respectively, so R = 4, C = 4, and the lattice size is 16. The second one in Figure 5 b) corresponds to a specific method, only applicable for parity functions (XOR functions), again in [8]. Here, R and C are the number of variables and products in  $f_T$ , respectively, so R = 3, C = 4, and the lattice size is 12. The third one in Figure 5 c) corresponds to a general method based on separating products with 0's. Here, Ris the degree of  $f_T$  and C is two times the number of products in  $f_T$  minus one, so R = 3, C = 7, and the lattice size is 21. Finally, Figure 5 d) shows the optimal solution with a lattice size of 9 that is found by applying the proposed optimal algorithm in this study.

#### 3. Proposed Algorithms

We propose optimal and heuristic algorithms that minimize lattice sizes to implement a target Boolean function  $f_T$ . The general structure of the algorithms is presented in a flow chart in Figure 6. It has four steps; while Step 3 and Step 4 are the same for both of the algorithms, Step 1 and Step 2 have some differences. In Step 1 to determine the upper bound, we use three different formulas for the heuristic algorithm. On the other hand, we achieve a more strict upper bound for the optimal algorithm by first running the heuristic algorithm. In Step 2, the optimal

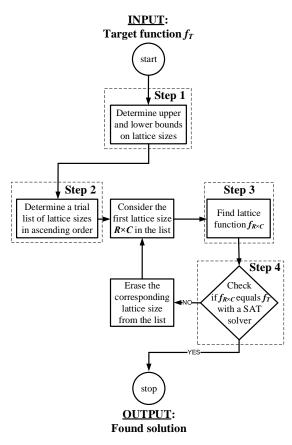


Figure 6: Flow chart of the proposed algorithms.

algorithm needs to include all probable lattice sizes into the trial list. However, only a few (or a limited number of) lattice sizes are considered for the heuristic algorithm.

In the following subsections, we elaborate on the steps followed by evaluation of the proposed algorithms.

## 3.1. Step 1: Upper and Lower Bounds

We directly use the lower bound (LB) values found in [8]. For the upper bound (UB), we use different approaches for the heuristic and the optimal algorithms. For the heuristic one, we consider three general implementation techniques. The first one from [8] gives lattice sizes as the number of products in  $f_T^D$ , denoted by  $N_{f_T^D}$ , times the number of products in  $f_T$ , denoted by  $N_{f_T^c}$ . As a result:

$$Lattice\_Size_1 = N_{f_T^D} \times N_{f_T}.$$
 (1)

The second one is based on separating products of  $f_T$  with columns of 0's. Therefore,

$$Lattice\_Size_2 = degree_{f_T} \times (2 \times N_{f_T} - 1).$$
(2)

Finally, the third one is achieved by separating products of  $f_T^D$  by rows of 1's. Note that each product of  $f_T^D$  is implemented with a row, that corresponds to a sum for  $f_T$ , and rows of 1's makes product operations, so a product-of-sum implementation of  $f_T$  is obtained. Here,

$$Lattice\_Size_3 = (2 \times N_{f_p^D} - 1) \times degree_{f_p^D}.$$
 (3)

We have the minimum of these three UB values:

$$UB = min(Lattice_Size_1, Lattice_Size_2, (4)$$

$$Lattice_Size_2)$$

As an example, for a given  $f_{T1}$  suppose that  $N_{T1} = 8$ ,  $N_{f_{T1}^D} = 5$ ,  $degree_{f_{T1}} = 4$ , and  $degree_{f_{T1}^D} = 6$ . Using (4), we find that UB = 40. Additionally, from [8] we find that LB = 15.

For the optimal algorithm, we use a more strict UB that is achieved by running the heuristic algorithm. The found solution with a certain lattice size becomes the UB.

#### 3.2. Step 2: Trial List

We have constructed the trial list according to UB and LB, sorted in ascending order. The reason of using an ascending order is that the algorithm stops when there is a solution. However, for a descending order the algorithm cannot stop when there is "no solution" for a certain sized lattice since a smaller lattice might give a solution. For example, a Boolean function can be implemented with a lattice size of 20, but cannot be implemented with a lattice size of 21. However, we can state that if both the number of rows and columns are smaller or equal to the previously used sizes, then "no solution" in the previous trial is directly applicable for the new one.

For the optimal algorithm, we consider all possible sizes between UB and LB. One thing to mention is that in forming the list we consider LB values not just for the lattice size, but also for the number of lattice columns and rows as given in [8]. Thus, we eliminate many trivial cases.

For the heuristic algorithm, we consider three *UB* values of  $degree_{f_T}, N_{f_T^D}$ , and  $2N_{f_T^D} - 1$  as well as two averages  $\lfloor \frac{degree_{f_T} + N_{f_T^D}}{2} \rfloor$  and  $\lfloor \frac{N_{f_T^D} + 2N_{f_T^D} - 1}{2} \rfloor$  for the number of rows. Similarly, we consider three *UB* values of  $degree_{f_T^D}, N_{f_T}$ , and  $2N_{f_T} - 1$  as well as two averages  $\lfloor \frac{degree_{f_T^D} + N_{f_T}}{2} \rfloor$  and  $\lfloor \frac{N_{f_T} + 2N_{f_T} - 1}{2} \rfloor$  for the number of columns. Figure 7 illustrates these levels. As a result, there are total of 25 different lattice sizes. Since 13 of them are larger than or equal to *UB* and 4 of them are almost equal to *UB*, we only consider the remaining 8. Note that a size close to *UB* is not worth to try while we already have an *UB* solution.

## 3.3. Step 3: Lattice Function

We aim to find lattice functions in ISOP form. For this purpose, we need to determine paths implementing products that are not covered by products of other paths. We call this type of paths *irredundant paths*. Indeed, in general number of redundant paths in a lattice is much higher than the number of irredundant ones. Therefore eliminating redundant paths is crucial for the sake of computational time.

We propose two techniques. The first one considers paths that cannot go up, so it might calculate wrong lattice functions. On the other hand, the second one deals with all types of paths and guarantees a correct lattice function. It is fundamentally constructed on the first technique. Figure 8 shows a path having an up movement, so it is neglected by the first technique,

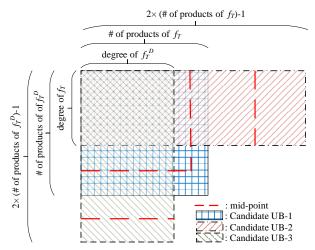


Figure 7: Illustration of the trial list of lattice sizes for the heuristic algorithm with UB candidates.

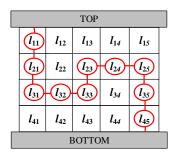


Figure 8: Path with an up movement in a  $4 \times 5$  lattice.

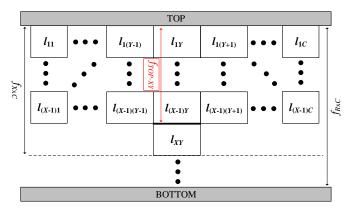


Figure 9: Illustration of  $f_{TOP-XY}$ ,  $f_{X\times C}$ , and  $f_{R\times C}$ .

but accounted by the second one. Although the first one is not fully correct - we call it semi-correct, one can efficiently use it since paths having up movements are not likely being used to implement products of target functions. In terms of the computational load, the first one is much better, especially for large lattices.

For both of our algorithms, we use the second correct one.

## 3.3.1. Semi-correct lattice function

We obtain the lattice function by considering paths having left, right, and/or down movements. We only use Boolean operators (not arithmetic) for all of the following expressions.

Consider an  $R \times C$  lattice. We define a dummy Boolean func-

$$\begin{bmatrix} f_{TOP-(X+1)1} \\ f_{TOP-(X+1)2} \\ \vdots \\ f_{TOP-(X+1)C} \end{bmatrix} = \begin{bmatrix} \prod_{j=1}^{1} l_{Xj} & \prod_{j=1}^{2} l_{Xj} & \cdots & \prod_{j=1}^{C} l_{Xj} \\ \prod_{j=1}^{2} l_{Xj} & \prod_{j=2}^{2} l_{Xj} & \cdots & \prod_{j=2}^{C} l_{Xj} \\ \vdots & \vdots & \ddots & \vdots \\ \prod_{j=1}^{C} l_{Xj} & \prod_{j=2}^{C} l_{Xj} & \cdots & \prod_{j=C}^{C} l_{Xj} \end{bmatrix}. \begin{bmatrix} f_{TOP-X1} \\ f_{TOP-X2} \\ \vdots \\ \vdots \\ f_{TOP-XC} \end{bmatrix}$$

Figure 10: Matrix representation of (7).<sup>1</sup>

tion  $f_{TOP-XY}$  as a sum of products of the paths between the "TOP plate" and the "upper part of the site  $l_{XY}$ ". This is illustrated in Figure 9 Therefore,

$$f_{X\times C} = \sum_{Y=1}^{C} l_{XY} f_{TOP-XY}.$$
(5)

Recursively, we can obtain

$$f_{(X+1)\times C} = \sum_{Y=1}^{C} l_{(X+1)Y} f_{TOP-(X+1)Y}$$
(6)

where  $f_{TOP-(X+1)Y}$  can be expresses in terms of  $l_{XY}$  and  $f_{TOP-XY}$ such that

$$\begin{split} f_{TOP-(X+1)1} = & l_{X1} f_{TOP-X1} + l_{X1} l_{X2} f_{TOP-X2} + \dots \\ & + l_{X1} l_{X2} \dots l_{XC} f_{TOP-XC} \\ f_{TOP-(X+1)2} = & l_{X1} l_{X2} f_{TOP-X1} + l_{X2} f_{TOP-X2} + \dots \\ & + l_{X2} l_{X3} \dots l_{XC} f_{TOP-XC} \\ \dots \\ \end{split}$$

$$f_{TOP-(X+1)C} = l_{X1}...l_{XC}f_{TOP-X1} + l_{X2}...l_{XC}f_{TOP-X2} + ... + l_{XC}f_{TOP-XC}.$$

As a result,

$$f_{TOP-(X+1)Y} = \sum_{i=1}^{C} f_{TOP-Xi} \prod_{j=min(Y,i)}^{max(Y,i)} l_{Xj}$$
(7)

where max(Y, i) and min(Y, i) are the largest and the smallest values among Y and i, respectively.

Matrix representation of (7) is shown in Figure  $10^1$ . In this representation, if we name the column matrices having C number of  $f_{TOP-(X+1)Y}$  and  $f_{TOP-XY}$  functions as  $F_{X+1}$  and  $F_X$ , respectively, and the transition matrix as  $T_{(X,X+1)}$  which relates  $F_{(X+1)}$  with  $F_X$  then

$$F_{X+1} = T_{(X,X+1)} \cdot F_X,$$
  

$$F_{X+1}^T = F_X^T \cdot T_{(X,X+1)}, \text{ and extensively}$$
(8)  

$$F_{X+1}^T = F_{X-1}^T \cdot T_{(X-1,X)} \cdot T_{(X,X+1)}$$

where  $T_{(X,X+1)} = T_{(X,X+1)}^T$ . It means that we can recursively calculate  $F_{X+1}^T$  using  $F_1^T$ and related transition matrices. Since  $F_1^T = \begin{bmatrix} 1 & 1 & \cdots & 1 \end{bmatrix}_{1 \times C}$ 

<sup>&</sup>lt;sup>1</sup>Matrix multiplication is denoted with ".".

•••	l <sub>(X-1)Y</sub>	•••
<i>l</i> <sub>X(Y-1)</sub>	l <sub>XY</sub>	<i>l</i> <sub>X(Y+1)</sub>
<i>l</i> <sub>(X+1)(Y-1)</sub>	<i>l</i> <sub>(X+1)Y</sub>	<i>l</i> <sub>(X+1)(Y+1)</sub>
• • •	<i>l</i> <sub>(X+2)Y</sub>	• • •

Figure 11: An example of a redundant path that needs to be eliminated.

and  $F_2^T = \begin{bmatrix} l_{11} & l_{12} & \cdots & l_{1C} \end{bmatrix}$  which represents the lattice inputs for the first row, called as  $L_1$ ,  $F_2^T = L_1$ . Similarly, we define a row matrix  $L_R$  having the lattice inputs for the last row. To conclude,

$$f_{R\times C} = \sum_{k=1}^{C} l_{Rk} f_{TOP-Rk},$$

$$f_{R\times C} = F_R^T \cdot L_R^T,$$

$$f_{R\times C} = F_{R-1}^T \cdot T_{(R-1,R)} \cdot L_R^T,$$

$$f_{R\times C} = F_{R-2}^T \cdot T_{(R-2,R-1)} \cdot T_{(R-1,R)} \cdot L_R^T,$$

$$\dots$$

$$f_{R\times C} = F_1^T \cdot T_{(1,2)} \cdot T_{(2,3)} \cdots T_{(R-1,R)} \cdot L_R^T,$$
 and finally
$$f_{R\times C} = L_1 \cdot T_{(2,R)} \cdot L_R^T.$$
(9)

In this calculation, it is not guaranteed that the final form of  $f_{R\times C}$  is in ISOP form, so a further simplification might be needed. An example of a redundant path is given in Figure 11. This type of paths occur iff there are opposite movements in adjacent rows. To completely eliminate them, we add extra products consisting of negated inputs into the elements of transition matrices. Thus, redundant paths have both an input and its negated form, so they evaluate to 0. Excluding the elements in the matrix diagonal, we add  $\overline{l_{(X-1)(i-1)}}$  for the elements in the lower triangle part, and  $\overline{l_{(X-1)(i+1)}}$  for the upper triangle part; *i* represents the row number. This is illustrated in Figure 12. Final version of a transition matrix is given in (10):

$$T_{(X,X+1)}(k,l) = \begin{cases} \overline{l_{(X-1)(k+1)}} \prod_{j=k}^{l} l_{Xj} & l > k \\ l_{Xk} & l = k \\ \overline{l_{(X-1)(k-1)}} \prod_{j=l}^{k} l_{Xj} & l < k \end{cases}$$
(10)

where *k* and *l* represent row and column numbers of the matrix, respectively.

We present a few examples to elucidate our technique of obtaining lattice functions in ISOP forms.

**Example 1.** Calculate  $f_{3\times3}$ ; R = 3 and C = 3.

$$L_{1} = \begin{bmatrix} l_{11} & l_{12} & l_{13} \end{bmatrix}$$
$$T_{(2,3)} = \begin{bmatrix} l_{21} & l_{21}l_{22} & l_{21}l_{22}l_{23} \\ l_{21}l_{22} & l_{22} & l_{22}l_{23} \\ l_{21}l_{22}l_{22} & l_{23}l_{23} & l_{23} \end{bmatrix}$$

$$L_{3} = \begin{bmatrix} l_{31} & l_{32} & l_{33} \end{bmatrix}$$

$$f_{3\times3} = L_{1} \cdot T_{(2,3)} \cdot L_{3}^{T}$$

$$f_{3\times3} = l_{11}l_{21}l_{31} + l_{12}l_{22}l_{32} + l_{13}l_{23}l_{33}$$

$$+ l_{11}l_{21}l_{22}l_{32} + l_{12}l_{22}l_{23}l_{33} + l_{13}l_{23}l_{22}l_{32}$$

$$+ l_{12}l_{22}l_{21}l_{31} + l_{11}l_{21}l_{22}l_{23}l_{33} + l_{13}l_{23}l_{22}l_{21}l_{31}$$

**Example 2.** Calculate  $f_{4\times 2}$ ; R = 4 and C = 2.

$$L_{1} = \begin{bmatrix} l_{11} & l_{12} \end{bmatrix}$$

$$T_{(2,3)} = \begin{bmatrix} l_{21} & l_{21}l_{22} \\ l_{21}l_{22} & l_{22} \end{bmatrix}$$

$$T_{(3,4)} = \begin{bmatrix} l_{31} & \overline{l_{22}}l_{31}l_{32} \\ \overline{l_{21}}l_{31}l_{32} & l_{32} \end{bmatrix}$$

$$L_{4} = \begin{bmatrix} l_{41} & l_{42} \end{bmatrix}$$

$$f_{4\times2} = L_{1} \cdot T_{(2,3)} \cdot T_{(3,4)} \cdot L_{4}^{T}$$

$$f_{4\times2} = L_{1} \cdot T_{(2,4)} \cdot L_{4}^{T}$$

$$f_{4\times2} = l_{11}l_{21}l_{31}l_{41} + l_{11}l_{21}l_{31}l_{32}l_{42} + l_{11}l_{21}l_{22}l_{32}l_{42} + l_{12}l_{22}l_{32}l_{42} + l_{12}l_{22}l_{32}l_{41} + l_{12}l_{22}l_{21}l_{31}l_{41}$$

#### 3.3.2. Correct lattice function

We obtain the lattice function by considering paths having left, right, up, and/or down movements. Therefore, all types of paths are considered including paths having up movements that are neglected in calculation of semi-correct lattice functions. For this purpose, we update transition matrix elements. Each element of a transition matrix  $T_{(X,X+1)}$  in (10) represents a path between two sites in the Xth row of the lattice that makes left or right movements, but no up movements. To consider up movements, we calculate a semi-correct function between the sites by transposing the related sub-matrix – later, we call this function  $f_{I_{Xk}-I_{Xl}}$ . Note that left and right movements in the transposed matrix correspond to down and up movements in the original matrix. The elements of the transition matrix become,

$$T_{(X,X+1)}(k,l) = \begin{cases} \overline{l_{(X-1)(k+1)}} f_{l_{Xk}-l_{Xl}} & l \ge k+4 \\ \overline{l_{(X-1)(k+1)}} \prod_{j=k}^{l} l_{Xj} & k+4 > l > k \\ l_{Xk} & l = k \\ \overline{l_{(X-1)(k-1)}} \prod_{j=l}^{k} l_{Xj} & k-4 < l < k \\ \overline{l_{(X-1)(k-1)}} f_{l_{Xk}-l_{Xl}} & l \le k-4 \end{cases}$$
(11)

where k and l represent row and column numbers of the matrix, respectively. Here, a dummy Boolean function  $f_{l_{xk}-l_{xl}}$  is used.

Note that the only difference between (10) and (11) is on the calculation of  $T_{(X,X+1)}(k, l)$  where  $l \ge k + 4$  and  $l \le k - 4$ . In (11), instead of calculating a single path between  $l_{Xk}$  and  $l_{Xl}$  $(\prod_{j=k}^{l} l_{Xj})$  without an up movement as in (10), we are calculating all probable paths having all types of movements. Calculation of  $f_{l_{Xk}-l_{Xl}}$  gives these paths between  $l_{Xk}$  and  $l_{Xl}$ . This function is obtained by calculating a semi-correct lattice function in Figure 13. Note that, it mainly consists of the transpose of the related part of the original lattice (Figure 9). In (11), inequalities to represent the cases are obtained by considering the

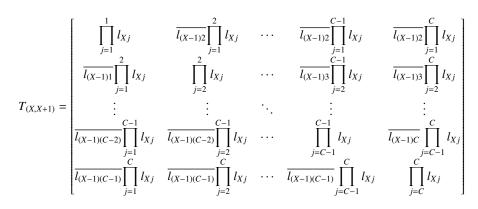


Figure 12: Transition matrix formation to eliminate redundant paths.

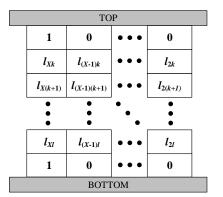


Figure 13: Lattice realizing a  $f_{l_{Xk}-l_{Xl}}$  that represents the connection between  $l_{Xk}$  and  $l_{Xl}$ .

TOP											
<i>l</i> <sub>11</sub>	0	0	0	0							
<i>l</i> <sub>21</sub>	0	<i>l</i> <sub>23</sub>	l <sub>24</sub>	l <sub>25</sub>							
<i>l</i> <sub>31</sub>	<i>l</i> <sub>32</sub>	l <sub>33</sub>	0	l <sub>35</sub>							
0	0	0	0	l <sub>45</sub>							
BOTTOM											

Figure 14: A  $4 \times 5$  lattice with a path having an up movement.

fact that the smallest lattice having up movements should have at least 4 rows and 5 columns as previously shown in Figure 8.

We present an example to elucidate our technique of obtaining correct lattice functions.

**Example 3.** *Calculate the correct function f of the* 4×5 *lattice* in Figure 14 (Note that if we calculated a semi-correct function of the lattice, it would be 0).

$$T_{(3,4)} = \begin{bmatrix} l_{31} & \overline{l_{22}} l_{31} l_{32} & \overline{l_{22}} l_{31} l_{32} l_{33} & 0 & \overline{l_{22}} f_{l_{31} - l_{35}} \\ \hline l_{21} l_{31} l_{32} & l_{32} & \overline{l_{23}} l_{32} l_{33} & 0 & 0 \\ \hline l_{22} l_{31} l_{32} l_{33} & \overline{l_{22}} l_{32} l_{32} & l_{33} & 0 & 0 \\ 0 & 0 & 0 & 0 & 0 & 0 \\ \hline l_{24} f_{l_{31} - l_{35}} & 0 & 0 & 0 & l_{35} \end{bmatrix}$$

$$f_{l_{31} - l_{35}} = l_{31} l_{32} l_{33} l_{23} l_{24} l_{25} l_{35}$$

$$L_R = \begin{bmatrix} 0 & 0 & 0 & 0 & l_{45} \end{bmatrix}$$

$$J = L_1 \cdot I_{(2,3)} \cdot I_{(3,4)} \cdot L_4^T$$
  

$$f = L_1 \cdot T_{(2,4)} \cdot L_4^T$$
  

$$f = l_{11} l_{21} l_{32} l_{32} l_{23} l_{24} l_{25} l_{35} l_{45}$$

Pseudo code of the algorithm is given below.

Algorithm Calculation of Correct or Semi-Correct Lattice Function  $f_{R \times C}$ 

- 1: **Input:** *R*: number of Rows, *C*: number of Columns, *Flag<sub>Correct</sub>*: Flag indicates Correct or Semi-Correct Calculation 2: **Output:**  $f_{R \times C}$  function in ISOP form 3: 4: function CREATE\_TRANS\_MAT(LM, X, Flag<sub>Correct</sub>) **for** *k* in 1 : *C* **do** 5:
- **for** *l* in 1 : *C* **do** 6:

  - if  $Flag_{Correct} == FALSE$  then  $T_{(X,X+1)}(k, l) \leftarrow$  elements of *LM* according to (10) end if
- 9:
- 10: if  $Flag_{Correct} == TRUE$  then  $T_{(X,X+1)}(k,l) \leftarrow$  elements of *LM* according to (11)  $\triangleright$ 11:
- calculate  $f_{l_{Xk}-l_{Xl}}$  recursively. end if
- 12:

7: 8:

- 13: end for
- end for 14:
- return  $T_{(X,X+1)}$ 15:
- 16: end function
- 17:
- 18: **create** *LM* Matrix with elements of  $l_{ij}$  with size of  $R \times C$ ⊳  $1 \le i \le R, 1 \le j \le C$
- 19:  $L_1 \leftarrow [l_{11} \cdots l_{l(C-1)} \quad l_{1C}]$  (first row of *LM*) 20:  $L_R \leftarrow [l_{R1} \cdots l_{R(C-1)} \quad l_{RC}]$  (last row of *LM*) 21:  $T2R \leftarrow$  Unit matrix

- 22: **for** X in [2,*R*-1] **do**
- 23:
- $T_{(X,X+1)} \leftarrow \text{Create_Trans_Mat}(LM, X, Flag_{Correct})$  $T2R = \text{Logic_Matrix_Multiplication}(T2R, T_{(X,X+1)}) \Rightarrow \text{Logic}$ 24: OR and AND operations used in matrix multiplications instead of conventional algebraic additions and multiplications.
- 25: end for
- 26:  $temp \leftarrow \text{Logic_Matrix_Multiplication}(L_1, T2R)$
- 27:  $f_{R \times C} \leftarrow \text{Logic_Matrix_Multiplication}(temp, L_R^T)$
- 28: Store  $f_{R \times C}$
- 29: return  $f_{R \times C}$

#### 3.4. Step 4: SAT Equivalence

We use a SAT solver to check if a given target function can be implemented with a certain lattice size. First, the problem needs to be turned into a satisfiability problem using a conjunctive disjoint form (CNF) or a POS form. Since we calculate lattice functions in SOP form, and the target function is given in a PLA form that is also a SOP form, we can easily combine them into one function  $f_{SAT}$  in CNF. If  $f_{SAT}$  is satisfiable, it means that the target function  $f_T$  can be implemented with an  $R \times C$  lattice. Core of this relation is that  $f_T$  is TRUE iff  $f_{R \times C}$  is TRUE:

$$f_T \iff f_{R \times C}$$
 (12)

that is also used in [17]. More explicitly,

a) 
$$f_{R\times C} \Rightarrow f_T \quad (f_{R\times C} \land f_T)$$
  
b)  $\overline{f_{R\times C}} \Rightarrow \overline{f_T} \quad (\overline{f_{R\times C}} \land f_T).$ 
(13)

We need both  $f_{R\times C}$  and  $\overline{f_{R\times C}}$  in CNF, preferably in irredundant CNF for the sake of computational time. Indeed,  $f_{R\times C}$  in ISOP form is same as  $\overline{f_{R\times C}}$  in irredundant CNF with negated inputs. Similarly,  $\overline{f_{R\times C}}$  in ISOP form is the same as  $f_{R\times C}$  in irredundant CNF with negated inputs. Therefore, along with  $f_{R\times C}$  in ISOP form, we also need  $\overline{f_{R\times C}}$  in ISOP form that can be computed using logic minimization tools such as Espresso [18].

Summary of how we use SAT equivalence in Step-4 of our algorithm is given in Figure 15. As an example, assume that check whether a target function  $f_T = x_1 x_2 + x_3$  is implementable with a 2 × 2 lattice. Here,  $f_{R\times C} = f_{2\times 2} = l_{11}l_{21} + l_{12}l_{22}$  and  $f_{R\times C} = f_{2\times 2} = l_{11}l_{12} + l_{11}l_{12} + l_{11}l_{22} + l_{21}l_{12} + l_{21}l_{22}$ . Since  $f_T$  has three variables, there are  $2^3 = 8$  truth table cases; 5 of them make  $f_T = 1$  (*TRUE*) and 3 of them make  $f_T = 0$  (*FALS E*). Thus, to calculate  $f_{SAT}$  in CNF form we use the relation in (13a) for the five cases and the relation in (13b) for the remaining three.

Our treatment does not fit the 3-SAT rule, since paths are directly used as SAT problem clauses. Although it is possible to turn these clauses into 3-termed clauses, this would extensively enlarge the number of clauses in the final form that causes dramatic runtime increases. In [17], they build their SAT problem with constraints considering the 3-SAT rule. However, at the end, the total number of variables and clauses are much lower for our case compared to those used in [17].

#### 3.5. Evaluation of Algorithms

Suppose that a given function  $f_T$  and its dual  $f_T^D$  have a total of *m* products in their SOP form. Also suppose that  $f_T$  has *n* variables. Total time needed for our algorithms can be represented as (time needed to determine *UB* and *LB* in Step 1) + (number of trials of lattice sizes in Step 2)×((time needed to obtain  $f_{R\times C}$  in Step 3)+(time needed for the SAT solver to check an equivalence in Step 4)).

The third and the fourth terms, corresponding to Step 3 and Step 4 of the algorithms respectively, are the same for both the

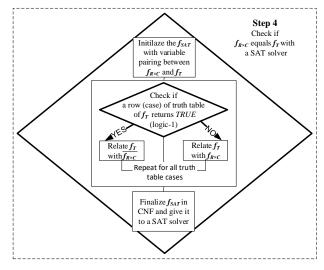


Figure 15: Flow chart of Step-4 of the algorithm.

heuristic and the optimal algorithms. For the third term, matrix multiplications are performed to calculate  $f_{R\times C}$  in ISOP form. For both the number of transition matrices and the matrix dimensions are upper bounded by m. There are m - 2 number of transition matrices with dimensions of  $m \times m$ . Therefore, the time complexity becomes  $O(m^m)$ . If a truth table based technique was used then the total number of truth table rows would be upper bounded by  $2^{(m^2)}$  and for each row to determine the output as logic 0 or 1, we would need  $m^2$  operations. As a result the complexity would be  $O(m^22^{(m^2)})$  (still not in ISOP form). This is much worse than what we have. Another important aspect is that the cost of obtaining  $f_{R\times C}$  is mostly independent of a given function. Therefore, we can share this cost among target functions by initially creating a library of all probable  $f_{R\times C}$ 's.

The fourth term corresponds to a SAT solver. Here, the total time is linearly dependent on the number of truth table rows that is upper bounded by  $2^n$ , the number of the clauses that is upper bounded by  $m^2$ , and the number of literals in a clause that is also upper bounded by  $m^2$ . As a result, the complexity is  $O(m^42^n)$ .

For the heuristic algorithm, the first term corresponding to Step 1 has a complexity of O(1) since a fixed number of calculations is done. The second term also has a O(1) complexity since the number of trials is upper bounded by 8. As a result, the time complexity for the whole algorithm becomes  $O(m^m)$  if no library of  $f_{R\times C}$ 's is constructed, and  $O(m^42^n)$  is the library is constructed.

For the optimal algorithm, the first term corresponding to Step 1 has the same complexity of the heuristic algorithm since we run the heuristic algorithm to obtain the *UB*. The second term corresponding to Step 2 has a  $O(m^2)$  complexity since *UB* is upper bounded by  $m^2$ . As a result, the time complexity of the optimal algorithm is  $m^2$  times larger than that of the heuristic algorithm.

Of course, all these analyses are based on the worst-case scenarios. Therefore, real runtimes given in the next section might be different, generally better, than what we expect.

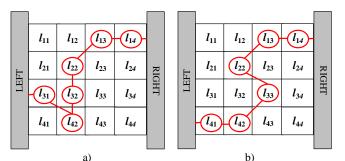


Figure 16: a) A redundant path needs to be eliminated, and b) a valid path needs to be accounted.

## 4. Experimental Results

Before presenting experimental results, we first show how we fix the optimal algorithm in [17].

# 4.1. Fixing the Optimal Algorithm

We fix the previously proposed algorithm in [17] that is claimed to be optimal, but it is not for some cases. The fixed version guarantees optimal sizes.

While constructing eight-connected paths between left and right plate, they put a constraint to eliminate redundant paths such as the one shown in Figure 16 a). However, this causes elimination of irredundant paths such as the one shown in Figure 16 b). The reason of this mis-elimination is their constraint definition:

• "A redundant path should have at most one element from the 2nd and the (C - 1)th columns."

A redundant path in Figure 16 b) is a counter example for this constraint. We have corrected it as:

• "A redundant path should have a single element from the first and the last (*C*th) column such that this element has a single neighbor element in the path. "

Thus, irredundant paths having more than one element in the 2nd and the (C - 1)th columns, such as the one in Figure 16 b), are considered.

In the following part, benchmark simulation results show some cases such that the fixed algorithm gives a correct result, but the previously proposed algorithm does not.

## 4.2. Comparing Optimization Algorithms

We compare six different algorithms by considering runtime and lattice sizes for different benchmarks<sup>2</sup>. We treat each output of a benchmark circuit as a separate target function. We limit the runtime with 10800 seconds (3 hours). Among these six algorithms, three of them are previously proposed algorithms in [14], [15], and [17]; one of the them is the fixed version of the optimal algorithm in [17]; and the remaining two are the proposed optimal and heuristic algorithms. Experiments run on a 3.20GHz Intel Core i7 CPU (only single core used) with 4GB memory. Used SAT solver is *glucose\_*3.0 [19]. Results are given in Table 1 and Table 2. In Table 2, we aim to compare non-optimal algorithms with the guidance of the fixed optimal algorithm by using relatively large benchmark functions. Examining the numbers in Table 1, we see that the proposed optimal algorithm outperforms the other optimal algorithms with the best runtime for most of the cases. Also note that for three cases corresponding to the benchmarks "b12\_01", "dc1\_02" and "ex5\_12", the optimal algorithm in [17] does not find the optimal solution, but both the fixed version of it and the proposed algorithm find the solution.

Considering the results for the non-optimal algorithms in Table 1 and Table 2, we see the superiority of the proposed heuristic algorithm offering small sizes and high speed. For 65 benchmarks out of 70, it results in optimal sizes. For example, consider "clpl\_03". Algorithm "Proposed (Optimal)" finds the optimal solution in 53 seconds; "Fixed Version of [17] (Optimal)" finds in 156 seconds; and "Proposed Heuristic (non-Optimal)" finds just in 18 seconds. For couple of relatively large functions, "Proposed (Optimal)" could not find the solution inside the time limit yet other optimal algorithms do. The reason is that the proposed optimal algorithm does not fit to the 3-SAT rule but "Fixed Version of [17] (Optimal)" fits.

For relatively small number of cases, decomposition based algorithms in [14] and [15] give the best runtime values, but their solutions are generally much larger than the optimal ones. When we compare our non-optimal heuristic algorithm with them, we observe that our algorithm offers an average of 23.07% and 20.51% lattice size improvements over the alorithms in [14] and [15], respectively. The compared algorithms even yield larger sizes than the upper bound used for the proposed optimal algorithm, for the benchmark "mp2d\_08". Additionally, applicability of the dimension-reducing based algorithm is quite limited.

## 5. Conclusion

In this study, we propose logic synthesis algorithms for switching lattices. We offer both optimal and heuristic algorithms to implement a given Boolean function with optimized lattice sizes. Our algorithms are fundamentally constructed on a technique that finds Boolean functions of lattices having independent inputs. This technique can also be used to find a Boolean function of a given lattice with assigned inputs. For our algorithms, we translate the problem of checking whether a given Boolean function can be implemented with a certain sized lattice, to the SAT problem. Our algorithms give considerably better results in terms of lattice size and runtime compared to previously proposed algorithms.

As a future work, we aim to construct multi-output lattices to implement multi-output Boolean functions. So far, the literature only considers single output lattices. Another direction is the investigation of reconfigurability in switching lattices.

#### References

- W. Lu, C. M. Lieber, Nanoelectronics from the bottom up, Nature materials 6 (11) (2007) 841–850.
- [2] A. Zhang, G. Zheng, C. M. Lieber, Nanoelectronics, circuits and nanoprocessors, in: Nanowires, Springer, 2016, pp. 103–142.

<sup>&</sup>lt;sup>2</sup>Source codes of all algorithms are available at http://www.ecc.itu. edu.tr/images/3/33/Algorithms\_for\_Switching\_Lattices.zip

- [3] D. Alexandrescu, M. Altun, L. Anghel, A. Bernasconi, V. Ciriani, L. Frontini, M. Tahoori, Synthesis and performance optimization of a switching nano-crossbar computer, in: Digital System Design (DSD), 2016 Euromicro Conference on, IEEE, 2016, pp. 334–341.
- [4] S. C. Goldstein, M. Budiu, Nanofabrics: Spatial computing using molecular electronics, ACM SIGARCH Computer Architecture News 29 (2) (2001) 178–191.
- [5] Y. Huang, X. Duan, Y. Cui, L. J. Lauhon, K.-H. Kim, C. M. Lieber, Logic gates and computation from assembled nanowire building blocks, Science 294 (5545) (2001) 1313–1317.
- [6] G. Snider, Computing with hysteretic resistor crossbars, Applied Physics A: Materials Science & Processing 80 (6) (2005) 1165–1172.
- [7] G. Snider, P. Kuekes, T. Hogg, R. S. Williams, Nanoelectronic architectures, Applied Physics A 80 (6) (2005) 1183–1195.
- [8] M. Altun, M. D. Riedel, Logic synthesis for switching lattices, IEEE Transactions on Computers 61 (11) (2012) 1588–1600.
- [9] M. C. Morgul, M. Altun, Synthesis and optimization of switching nanoarrays, in: Design and Diagnostics of Electronic Circuits & Systems (DDECS), 2015 IEEE 18th International Symposium on, IEEE, 2015, pp. 161–164.
- [10] P. Huang, J. Kang, Y. Zhao, S. Chen, R. Han, Z. Zhou, Z. Chen, W. Ma, M. Li, L. Liu, et al., Reconfigurable nonvolatile logic operations in resistance switching crossbar array for large-scale circuits, Advanced Materials 28 (44) (2016) 9758–9764.
- [11] L. Xie, H. A. Du Nguyen, M. Taouil, S. Hamdioui, K. Bertels, A mapping methodology of boolean logic circuits on memristor crossbar, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems.
- [12] T. Wang, P. Narayanan, M. Leuchtenburg, C. A. Moritz, Nasics: A nanoscale fabric for nanoscale microprocessors, in: Nanoelectronics Conference, 2008. INEC 2008. 2nd IEEE International, IEEE, 2008, pp. 989– 994.
- [13] M. C. Morgül, M. Altun, Anahtarlamalı nano dizinler ile lojik devre tasarımı ve boyut optimizasyonu logic circuit design with switching nano arrays and area optimization, in: ELECO, 2014.
- [14] A. Bernasconi, V. Ciriani, L. Frontini, V. Liberali, G. Trucco, T. Villa, Logic synthesis for switching lattices by decomposition with p-circuits, in: Digital System Design (DSD), 2016 Euromicro Conference on, IEEE, 2016, pp. 423–430.
- [15] A. Bernasconi, V. Ciriani, L. Frontini, G. Trucco, Synthesis on switching lattices of dimension-reducible boolean functions, in: Very Large Scale Integration (VLSI-SoC), 2016 IFIP/IEEE International Conference on, IEEE, 2016, pp. 1–6.
- [16] A. Bernasconi, Composition of switching lattices and autosymmetric boolean function synthesis, in: Digital System Design (DSD), 2016 Euromicro Conference on, IEEE, 2017.
- [17] G. Gange, H. Søndergaard, P. J. Stuckey, Synthesizing optimal switching lattices, ACM Transactions on Design Automation of Electronic Systems (TODAES) 20 (1) (2014) 6.
- [18] R. K. Brayton, G. D. Hachtel, C. McMullen, A. Sangiovanni-Vincentelli, Logic minimization algorithms for VLSI synthesis, Vol. 2, Springer Science & Business Media, 1984.
- [19] G. Katsirelos, A. Sabharwal, H. Samulowitz, L. Simon, et al., Resolution and parallelizability: Barriers to the efficient parallelization of sat solvers., in: AAAI, Citeseer, 2013.

	Table 1: Comparison of Optimization Algorithms												
Benchmark         Elapsed Imme (s)         Size         Time (s)         Size         Siz												D-Reducing	
Name         Time (s)         502         <			(imal)		nal)		stimal)		imal)		mal) [14]		mai) [15]
$ \begin{array}{c c c c c c c c c c c c c c c c c c c $			Size		Size		Size		Size		Size		Size
ah 1.41         0.002         3.32         0.004         3.32         0.004         3.32         0.004         3.32         0.001         3.32         0.001         3.32         0.001         3.32         0.001         3.32         0.001         3.33         0.015         4.43         0.016         4.43         0.017         4.33         0.021         5.33           b12.01         0.060         3.42         0.002         2.44         0.002         2.44         0.015         5.44         0.017         5.35         N/A         -           b12.06         7.237         5.44         0.003         2.44         0.003         3.54         9.0035         5.54         9.0035         5.54         9.0035         5.54         9.002         2.21         N/A         -           c17.01         0.060         3.32         0.0019         3.32         0.0017         3.34         0.002         3.34         0.012         3.34         0.012         3.34         0.012         3.34         0.012         3.34         0.012         3.34         0.012         3.34         0.013         3.35         0.013         3.35         0.014         3.35         N/A         -         cp .010         0.003 <td></td>													
$      b12.01^{**} = 6.066 + 4 + 4 + 16.102 + 5 + 3 + 3.218 + 5 + 3 + 0.065 + 4 + 4 + 0.963 + 4 + 4 + 0.961 + 0.71 + 6 + 3 + 12.03 + 0.062 + 3 + 4 + 15.99 + 3 + 4 + 0.065 + 4 + 4 + 2.118 + 5 + 8 + N/A + - 5 + 12.03 + 0.063 + 3 + 2 + 0.997 + 3 + 0.023 + 3 + 2 + 0.997 + 3 + 0.023 + 3 + 2 + 0.997 + 3 + 0.023 + 3 + 6 + 0.35 + 12.1 + 8 + 3 + 12.07 + 2.130 + 3 + 6 + 0.35 + 0.032 + 3 + 0.023 + 3 + 6 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 3 + 0 + 0.032 + 2 + 0.044 + 2 + 0.042 + 3 + 0 + 0.032 + 2 + 0.044 + 2 + 0.042 + 3 + 0 + 0.022 + 3 + 0 + 0.022 + 3 + 0 + 0.022 + 3 + 0 + 0.022 + 3 + 0 + 0.022 + 3 + 0 + 0.023 + 3 + 0 + 0.02 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 3 + 0 + 0.01 + 0 + 0 + 0 + 0 + 0 + 0 + 0 + 0 + 0 +$													
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	$b12_02$		4X4						484			IN/A N/A	
$      b12.06  \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$													
	$b12_04$ b12_06												
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ex5.06       2.777       3x6       5.814       3x6       3.575       3x6       0.955       3x6       0.477       3x10       N/A          ex5.08       11.254       3x7       325.598       3x7       50.687       3x7       0.004       3x7       1.389       3x9       N/A          ex5.09       9.757       4x6       **       **       226.1837       4x6       6.642       4x6       5.2424       3x11       N/A          ex5.10       1.463       3x6       3.994       3x6       1.828       3x6       0.302       2x8       0.977       2x10       N/A       -         ex5.11       0.002       2x8       **       **       0.004       2x8       0.907       2x10       N/A       -         ex5.13       41.045       4x6       **       **       231.295       4x6       1.494       2x8       0.208       3x5       0.209       3x13       N/A       -         ex5.14       3.75       0.003       2x5       0.004       2x5       0.108       2x7       N/A       -         ex5.16       0.002       2x6       0.003       2x6       0.003       2x6 </td <td></td>													
$ \begin{array}{c} ex5.07 & 167.767 & 4x6 & ** & ** & 578.060 & 4x6 & 26.843 & 4x6 & 0.288 & 3x13 & N/A & - ex5.09 & 9.757 & 4x6 & ** & ** & 261.837 & 4x6 & 6.642 & 4x6 & 5.424 & 3x11 & N/A & - ex5.11 & 0.003 & 2x8 & ** & ** & 261.837 & 4x6 & 0.49 & 3x6 & 0.178 & 3x9 & N/A & - ex5.12 & 6.026 & 3x6 & 1.450 & 3x5 & 8.097 & 3x5 & 0.208 & 3x5 & 2.969 & 5x9 & N/A & - ex5.13 & 41.045 & 4x6 & ** & ** & 231.295 & 4x6 & 11.494 & 3x8 & 0.720 & 3x13 & N/A & - ex5.14 & 3.751 & 2x8 & 211.941 & 2x8 & 4.091 & 2x8 & 0.368 & 2x8 & 0.231 & 3x11 & N/A & - ex5.15 & ** & ** & ** & ** & ** & ** & ** & $								0.002					
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$            ex5.09  9.757  4x6  **  **  **  **  **  201.837  4x6  6.642  4x6  5.424  3x11  N/A  -* \\            ex5.11  0.003  2x8  **  **  **  0.004  2x8  0.002  2x8  0.097  2x10  N/A  -* \\            ex5.12  6.026  3x6  1.450  3x5  8.097  3x5  0.208  3x5  2.969  5x9  N/A  -* \\            ex5.13  41.045  4x6  **  **  **  **  231.295  4x6  11.494  3x8  0.720  3x13  N/A  -* \\            ex5.14  3.751  2x8  211.941  2x8  4.091  2x8  0.368  2x8  0.231  3x11  N/A  -* \\            ex5.15  **  **  **  **  **  **  **  $									4x6				
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		1.463						0.39	3X0				-
ex5.1341.0454x6**231.2954x611.4943x80.7203x13N/A-ex5.143.7512x8211.9412x84x912x80.3682x80.2133x11N/A-ex5.15************59.7804x71.6584x13N/A-ex5.160.0022x50.0032x50.0032x50.0042x50.1082x7N/A-ex5.17****************1.13744x10N/A-ex5.190.0032x70.0022x70.0022x70.0032x60.2863x61.2385x7N/A-ex5.200.0032x60.0032x60.0032x60.0313x8N/A-ex5.21287.7663x7185.5923x712.4783x70.9563x71.3684x11N/A-ex5.23**********10598.2774x80.1164x11N/A-ex5.2514.1723x7513.4783x756.5083x70.3863x70.7403x8N/A-ex5.26*108.2753x7****1348.1504x621.0924x61.1304x10N/A-ex5.27*1779.2613x8****134.1604x621.0924x61.130 </td <td></td> <td>0.003</td> <td></td> <td></td> <td></td> <td></td> <td></td> <td>0.002</td> <td>2x8</td> <td></td> <td></td> <td></td> <td>-</td>		0.003						0.002	2x8				-
ex5.143.7512.88211.9412.884.0912.880.03682.880.2313.111N/A-ex5.15************\$9.7804x71.6584x13N/A-ex5.17************\$*\$9.7804x71.6584x13N/A-ex5.180.0022x70.0022x70.0242x70.0032x70.1082x7N/A-ex5.193.9623x68.3373x64.1093x60.2863x61.2385x7N/A-ex5.200.0032x60.0032x60.0033x60.1463x60.3223x8N/A-ex5.223.0023x69.8513x64.3033x60.1463x60.0164x11N/A-ex5.23**********10598.2770.7403x8N/A-ex5.24**********10698.2770.7403x8N/A-ex5.2514.1723x7513.4783x750.5083x71.52573x71.3684x11N/A-ex5.27*1779.2613x8****1348.1504x621.0924x61.1304x10N/A-ex5.28*2.5664x6****13.296x41.3743x50.04014x3								0.208	3x5				-
ex5.15**************57.804x71.6584x13N/A-ex5.160.0032x70.0032x50.0032x50.0042x71.0182x7N/A-ex5.180.0032x70.0022x70.0242x70.0052x70.2142x9N/A-ex5.193.9623x66.3373x64.1093x60.2863x61.2385x7N/A-ex5.200.0032x60.0022x60.0032x60.0323x8N/A-ex5.21287.7663x71185.5923x712.4783x70.9563x713.684x9N/A-ex5.23************698.0925x60.1635x14N/A-ex5.2514.1723x7513.4783x756.5083x715.2873x71.3684x11N/A-ex5.26108.2753x7******167.063x715.2873x71.3684x11N/A-ex5.27*179.2613x8****1134.1504x621.0924x61.1304x10N/A-ex5.28*27.564x6****134.1504x621.0924x61.1304x10N/A-ex5.26*108.2753x7****152.964x43.00.8								11.494					-
ex5.160.0022x50.0032x50.0042x50.1082x7N/A-ex5.17**************8483.3154x7121.3744x10N/A-ex5.180.0032x70.0022x70.0022x70.0052x70.2142x9N/A-ex5.193.9623x68.3373x64.1093x60.0263x61.2385x7N/A-ex5.200.0032x60.0032x60.0032x60.0333x8N/A-ex5.21287.7663x7185.5923x712.4783x70.9563x71.3684x9N/A-ex5.23************10598.2774x80.01164x11N/A-ex5.24************10598.2774x80.1164x11N/A-ex5.2514.1723x7513.4783x756.5083x71.52573x71.3684x11N/A-ex5.26108.2753x7****167.7063x715.2573x71.3684x10N/A-ex5.27*1779.2613x8****1348.1504x20.0244x20.4044x30.082x4miser1.000.0874x20.0384x20.0244x40.1304x60.130 <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>0.368</td><td></td><td>0.231</td><td></td><td></td><td>-</td></t<>								0.368		0.231			-
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newapla2_00         0.003         6x1         0.002         6x1         0.003         6x1         0.003         6x1         0.023         6x1         N/A         -           newbyte_00         0.003         5x1         0.002         5x1         0.004         5x1         0.003         5x1         0.064         5x1         N/A         -           newtag_00         6.103         3x6         10.921         3x6         7.719         3x6         0.262         3x6         0.842         3x8         N/A         -													
newbyte_00         0.003         5x1         0.002         5x1         0.004         5x1         0.003         5x1         0.064         5x1         N/A         -           newtag_00         6.103         3x6         10.921         3x6         7.719         3x6         0.262         3x6         0.842         3x8         N/A         -													
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				10.002									

Table 1: Comparison of Optimization Algorithms

+ Bold values represent the best results; "\*" indicates the non-optimal solutions of Algorithm in [17] that are different than the fixed version; "\*\*" indicates time-out; "N/A" is used for non-D-reducible functions.

Table 2: Comparison of Optimization Algorithms										
	Fixed Ve		Proposed Heuristic		P-Decom		D-Reducing			
	of [17] (Optimal)		(non-Optimal)		(non-Opti	mal) [14]	(non-Optimal) [15]			
Benchmark	Elapsed	Size	Elapsed	Size	Elapsed	Size	Elapsed	Size		
Name	Time (s)		Time (s)	SIZC	Time (s)	SIZC	Time (s)	SIZC		
5xp1_00	21.867	5x4	2.305	5x5	3.599	5x8	N/A	-		
5xp1_01	**	**	1405.815	5x5	1.697	5x10	N/A	-		
5xp1_03	**	**	1062.801	4x15	10.716	4x11	N/A	-		
5xp1_04	15.812	4x3	0.020	4x3	0.609	5x10	N/A	-		
5xp1_05	0.285	3x4	0.209	3x4	0.065	4x6	N/A	-		
5xp1_06	0.003	3x3	0.002	3x3	0.031	3x3	Ň/A	-		
5xp1_07	0.002	2x2	0.002	2x2	0.015	2x2	0.03	2x2		
5xp1_08	0.002	1x1	0.002	1x1	0.017	1x1	N/A	-		
5xp1_09	1.179	4x3	0.039	4x3	0.237	4x4	0.06	5x3		
bw_00	1.147	5x3	0.341	5x3	0.069	4x6	N/A	-		
bw_01	0.002	3x3	0.003	3x3	0.04	3x4	0.05	5x2		
bw_02	0.33	4x3	0.046	4x3	0.062	3x5	0.06	5x3		
bw_03	0.248	3x4	0.020	3x4	0.084	3x6	N/A	-		
bw_04	0.412	4x3	0.049	4x3	0.212	5x5	N/A	_		
bw_05	0.513	5x3	0.04	5x3	0.069	3x7	N/A	_		
bw_06	0.657	4x3	0.165	4x3	0.283	5x6	N/A N/A	_		
			0.071	4x3	0.285		0.06	- 5x4		
bw_07	0.134	4x3		-		4x5		384		
bw_08	0.766	3x4	0.319	5x4	0.121	3x6	N/A	-		
bw_09	0.002	3x3	0.007	3x3	0.056	3x4	N/A	- 1		
bw_10	0.036	$4x^2$	0.003	4x2	0.143	$5x^2$	0.07	2x4		
bw_11	0.358	4x3	0.04	4x3	0.058	3x5	0.15	5x3		
bw_12	0.002	3x3	0.005	3x3	0.051	3x4	N/A	-		
bw_13	0.42	4x3	0.087	4x3	0.068	4x5	N/A			
bw_14	0.357	5x2	0.064	5x2	0.066	3x4	0.16	5x5		
bw_15	0.236	4x3	0.059	4x4	0.279	4x4	0.13	5x4		
bw_16	0.002	3x3	0.002	3x3	0.056	3x4	N/A	-		
bw_17	1.463	3x5	0.215	4x4	0.38	4x7	0.16	6x3		
bw_18	0.368	3x4	0.066	4x4	0.078	4x5	0.17	6x3		
bw_19	1.005	3x5	0.112	3x5	0.104	3x6	N/A	-		
bw_20	0.223	3x4	0.015	3x4	0.231	4x5	N/A	-		
bw_21	0.002	5x1	0.003	5x1	0.028	5x1	N/A	-		
bw_22	1.392	3x5	0.072	4x4	0.364	4x6	N/A	-		
bw_23	1.099	5x3	0.163	4x4	0.504	5x6	N/A	-		
bw_24	0.261	2x5	0.094	2x5	0.112	2x6	N/A	-		
bw_25	1.258	3x5	0.082	4x4	0.172	4x7	N/A	-		
bw_26	0.898	3x5	0.135	5x4	0.078	3x6	N/A	-		
bw_27	0.002	5x1	0.003	5x1	0.071	5x1	N/A	-		
inc_00	8.420	4x4	1.557	5x4	0.451	5x7	N/A	-		
inc_01	17.425	5x4	12.512	5x5	0.506	5x7	N/A	-		
inc_02	**	**	6020.116	4x11	5.227	5x10	N/A	-		
inc_04	7.395	4x4	0.833	5x4	0.033	6x8	N/A	-		
inc_05	0.567	5x2	0.107	5x2	0.03	4x3	0.04	5x3		
inc_06	1.277	4x3	0.003	4x3	0.02	4x3	N/A	-		
inc_07	0.994	4x3	0.072	5x3	0.18	5x4	0.16	5x3		
inc_08	0.003	3x2	0.003	3x2	0.021	3x2	0.03	4x2		
misex3c_00	1.145	3x5	0.073	3x5	**	**	N/A	-		
misex3c_01	3.908	3x5	0.072	4x5	**	**	N/A	-		
misex3c_02	0.065	4x3	0.024	4x3	36.467	5x10	N/A	-		
misex3c_02	165.451	4x5	16.155	3x8	259.354	4x7	N/A	-		
misex3c_04	262.09	3x7	9.294	4x6	164.132	5x5	N/A	-		
misex3c_06	**	**	1563.699	5x6	3.996	4x5	N/A	_		
rd73_02	**	**	81.927	35x4	4.195	4x5 5x16	N/A N/A	-		
t481	**	**	91.927 91.894	9x8	4.195	5X10 **	N/A N/A			
vg2_00	**	**	68.74	9xo 9x4	**	**	N/A N/A	-		
	**	**	3.509	9x4 9x4	**	**	N/A N/A			
vg2_02								-		
vg2_05 vg2_07	22.738 16.067	<b>4x4</b> 4x4	1.857 <b>1.679</b>	4x5 4x5	<b>0.5</b> 15.782	4x5 <b>4x4</b>	N/A N/A	-		
1 1/17/11/										

Table 2: Comparison of Optimization Algorithms

 $\frac{10.007}{4.4} = \frac{10.007}{4.4} = \frac{10.077}{4.4} = \frac{10.77}{4.4} = \frac{10.77}{4.4} = \frac{10.74}{4.4} = \frac{10.74}{$