

# GPU-based data processing for ultrafast X-ray imaging — KSETA Plenary Workshop 2014

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## Motivation



### Synchrotron radiation from ANKA

- X-ray source producing beam with broad spectrum and high brilliance
- X-ray scattering and diffraction, high-resolution and high-speed radiography, tomography and laminography
- New IMAGE beamline used to investigate fast processes in life and material sciences



Figure: Floor plan of ANKA electron ring with 16 tangential *beamlines* 



Beamline







50 to 150 motors

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Convert X-ray photons into visible light, magnify and detect them with CCD or CMOS.









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## Challenges

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### Fast, high volume acquisition

- Up to hundreds of samples per experiment
- Many thousand frames per second per sample
- Up to eight mega pixels at 16 bit per frame

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### Fast, high volume acquisition

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### Efficient, fast data processing

- On-site data processing for visual quality assurance
- Use reconstructed data for early feedback
- Use existing computing infrastructure

## Tomographic reconstruction



Problem

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### From a series of projections ...





## **Tomographic reconstruction**



### Problem

4

### From a series of projections ... reconstruct unknown slice information



## **Tomographic reconstruction**



### Problem

### From a series of projections ... reconstruct *unknown* slice information



### Solutions

- Solve analytically using the Fourier-slice theorem (DFI)
- Filter projections and smear back into empty volume (FBP)
- Model detection as a linear system and solve algebraically (ART)

## Improving reconstruction



There is no "one-size-fits-all" solution

- Quality: DFI < FBP < ART</p>
- Speed: DFI > FBP > ART<sup>1</sup>

### CPUs are still too slow

- Up to hours for very large data sets
- On-line and on-site inspection assessment inconvenient
- Fast feeback impossible

 $^{1}O(n \log n) \subset O(n^{3}) \subseteq O(cn^{3})$ 

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### A superficial comparison

Processor	Purpose	Cores	Performance	Bandwidth	Price
CPU	General	12	0.5 TFLOPs	60 GB/s	>€2200
GPU	Graphics	2880	5.0 TFLOPs	336 GB/s	€430

But ...

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- Work differently than a CPU
- Cover restricted problem domains



Heterogeneous computing

 Systems with different processors and topologies (CPUs, GPUs, FPGAs)



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Heterogeneous computing

- Systems with different processors and topologies (CPUs, GPUs, FPGAs)
- Needs precise scheduling and resource allocation as well as architecture-specific code
- Avoid unnecessary copies between devices





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- Avoid unnecessary copies between devices

### Data streaming

- Common in trigger systems
- Low latency and high throughput desirable
- Must consider clusters



## **Basic approach**



 Define algorithmic computation and data flow as a directed graph



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- Define algorithmic computation and data flow as a directed graph
- Determine local system of CPUs and GPUs





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### Define algorithmic computation and data flow as a directed graph

**Basic approach** 

- Determine local system of CPUs and GPUs
- Transform graph to accomodate for additional processors









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**Basic approach** 

- Define algorithmic computation and data flow as a directed graph
- Determine local system of CPUs and GPUs
- Transform graph to accomodate for additional processors
- Assign tasks to GPUs





## **Basic approach**



- Define algorithmic computation and data flow as a directed graph
- Determine local system of CPUs and GPUs
- Transform graph to accomodate for additional processors
- Assign tasks to GPUs and CPUs



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## **Basic approach**



- Define algorithmic computation and data flow as a directed graph
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## **Scaling to clusters**

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## Scaling to clusters

- Use algorithmic description
- Replicate sub-graph







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### **Scaling to clusters**

- Use algorithmic description
- Replicate sub-graph
- Instatiate tasks on remote nodes





t<sub>3</sub>

 $t_4$ 

Local master



Remote slave



Scaling to clusters



- Replicate sub-graph
- Instatiate tasks on remote nodes
- Forward data and receive results



Local master

Remote slave





## Improvements

## Single node multi-GPU





Good scalability with near linear speed-up for up to 6 NVIDIA GTX 580's.

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Network Compute Runtime (s) Runtime (s) High network overhead Almost linear speedup

FBP



FBP + NLM



## Let's do better

## Improving user experience



Performance problems but ...

- Yet another compute language
- Hardware knowledge required for best performance
  - Work group layout,
  - Memory access patterns
  - Cache hierarchies etc. pp. ...

## Improving user experience



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- Yet another compute language
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GPU programming is hard!

## Improving user experience



Performance problems but ...

- Yet another compute language
- Hardware knowledge required for best performance
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  - Cache hierarchies etc. pp. ...

## GPU programming is hard!

Use Python instead

- Straightforward: Use NumPy's vectorized expressions
- Clever: Run the same code on GPU/CPU/FPGA



 Python interprets statements on an abstract virtual CPU





- Python interprets statements on an abstract virtual CPU
- Parse Python code into syntax tree





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- Python interprets statements on an abstract virtual CPU
- Parse Python code into syntax tree
- Optimize tree according to environment
- Generate OpenCL C code
- Run generated code on GPU(s)



Benefits



Non-invasive changes

```
def calc(x, y):
    return np.cos(x) + np.sin(y)
```

becomes

```
@jit
def calc(x, y):
    return np.cos(x) + np.sin(y)
```

Additional optimization opportunities

## Speedup compared to NumPy





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Heterogeneous systems can be utilized for streamed data



- Heterogeneous systems can be utilized for streamed data
- Scaling out to clusters works well

## Conclusion



- Heterogeneous systems can be utilized for streamed data
- Scaling out to clusters works well
- No one needs to write GPU kernels



## Thank you. Any questions?