

Work Package 2: Real-time Data Processing

Andreas Kopmann

HDRI Spring Meeting 13.-14.4.2015, DESY



Real-time Data Processing – Concepts in POF2

1 DAQ hardware

PCIe
Acceptable
data rates

2 PC-based processing



Technologies

Programmable hardware

FPGA, DSP, embedded GPUs

„Parallel Computing“:

PC + Coprocessing / GPU

Tasks

Selected commercial hardware
Standardized custom hardware

Hardware independant programming (e.g. OpenCL)
Library of common algorithms

Real-time Data Processing – Milestones in POF2

- **Data processing with dedicated hardware** ✓
 - programmable hardware platform
- **Real-time data assessment with parallel computing:** ✓
 - Development of computation system based on GPU co-processors
 - ✗ Prototype adoption of a complete PX data flow
 - Implementation of an online tomographic reconstruction
- **General environment for parallel image processing** ✓
 - a) Independent from available hardware (OpenCL)
 - b) Library of standard algorithms
 - c) Easy adaption to new problems

Dedicated Hardware – Selected Results

MTCA.4 (MTCA for Physics)

- RTMs for large channel numbers
- HGF AMC = Multipurpose readout board for several XFEL Machine controls
 - FPGA-based
 - High speed serial links (10G Ethernet)
- Last workshop in Karlsruhe, Dec 14
 - DESY, GSI, FZJ, HZDR, KIT
 - Development of common HGF-AMC Board support package
- Also part of Detector Program (DTS)

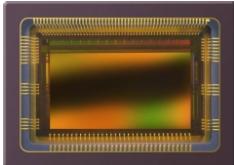


Smart scientific cameras

- Detectors for high speed imaging
 - What is commercially available?

Scientific sensor developments?

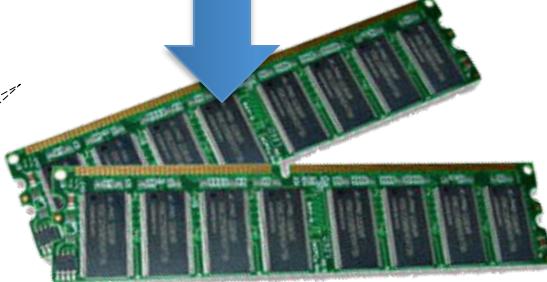
CMOS sensor



pco.dimax 4MP@1300fps
(7500MB/s)



Custom embedded logic is missing



Readout

Camera Link (250 / 850 MB/s)

USB3 (new: 500 MB/s)

GigE Vision (125 MB/s)

Internal Memory

e.g.: 36GB → 5s

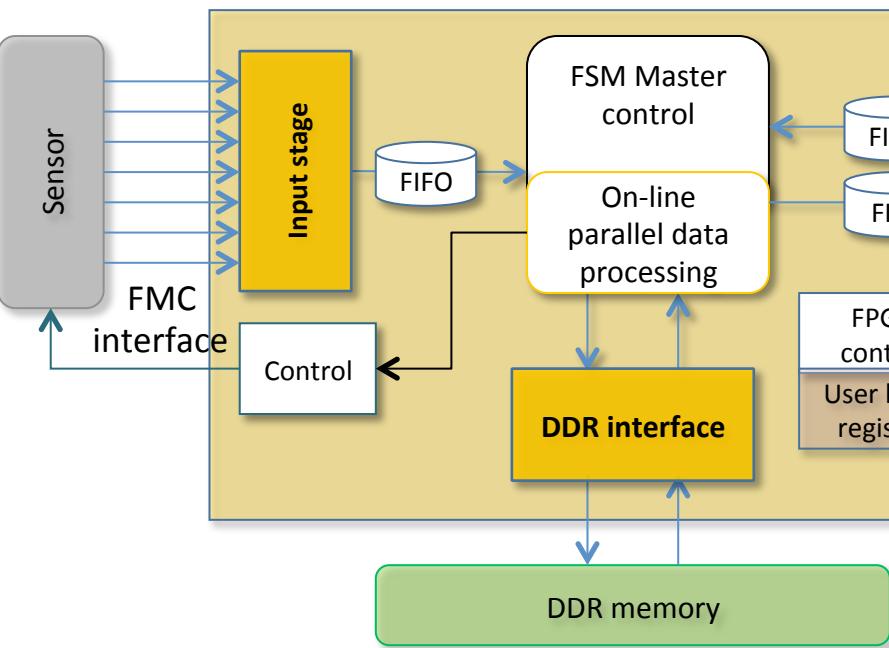
No online data analysis

UFO DAQ Framework

Goal:

- Rapid development of DAQ systems for high-speed sensors

1- Pluggable sensor interface



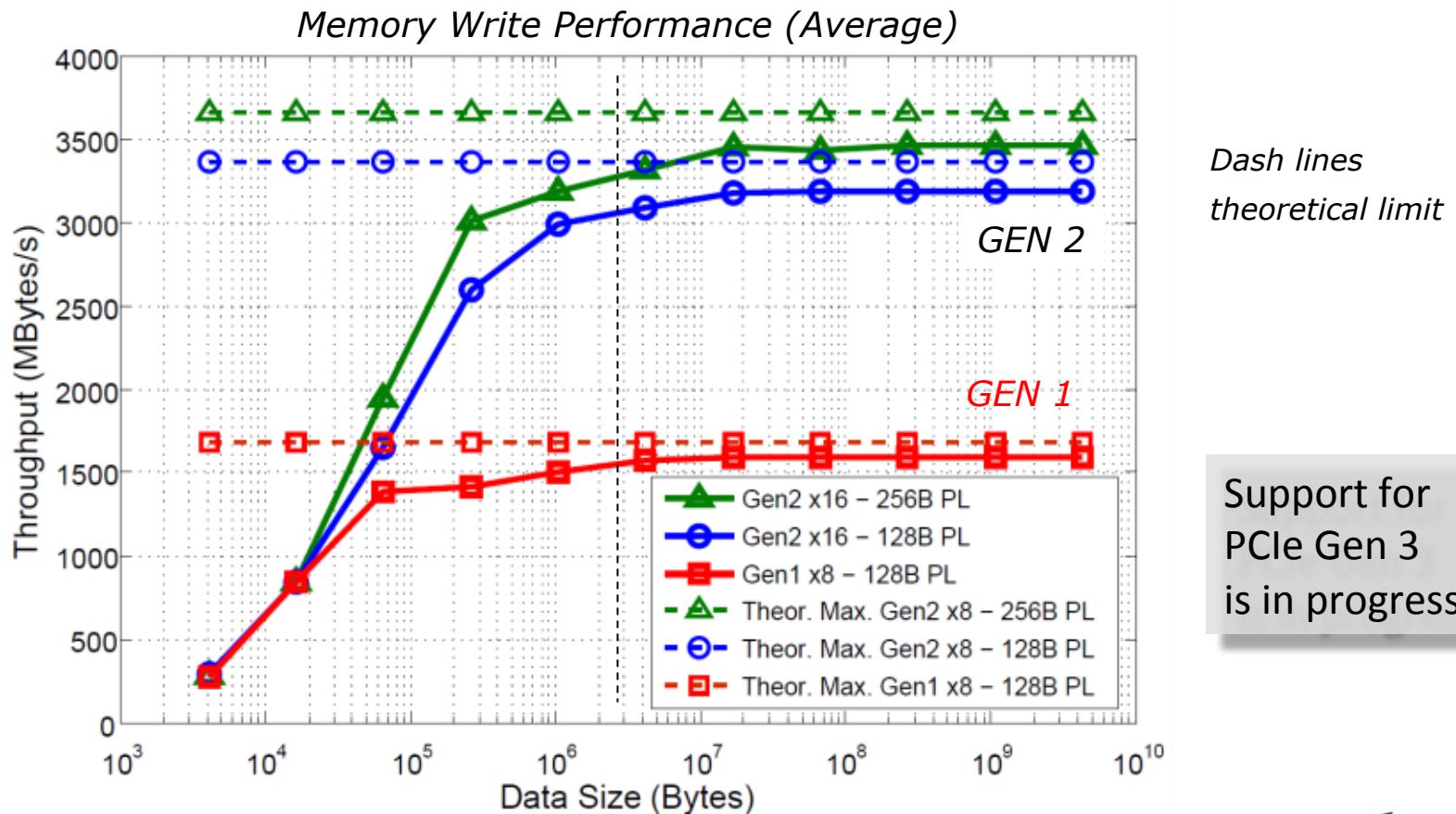
2- Modular FPGA architecture

3- High-speed links

4- Parallel Computing

PCIe-DMA Engine – Performance

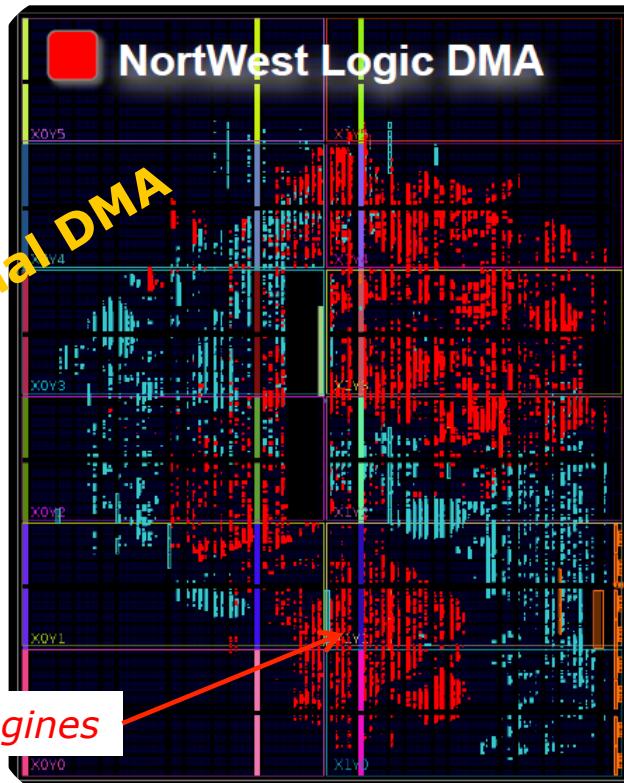
- Average data throughput of 3.5 GB/s (DMA max data transfer @ 4 GB/s)
- No bit errors observed (tested up to several TB of data exchanged)



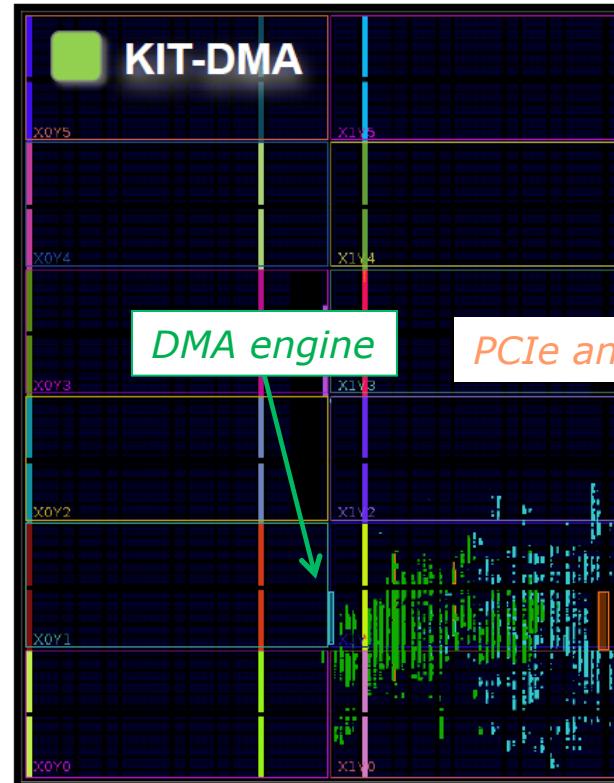
L. Rota et al. IEEE Real Time Conference 2014

PCIe-DMA Engine – Floor map

Commercial DMA



NW DMA engines



DMA engine

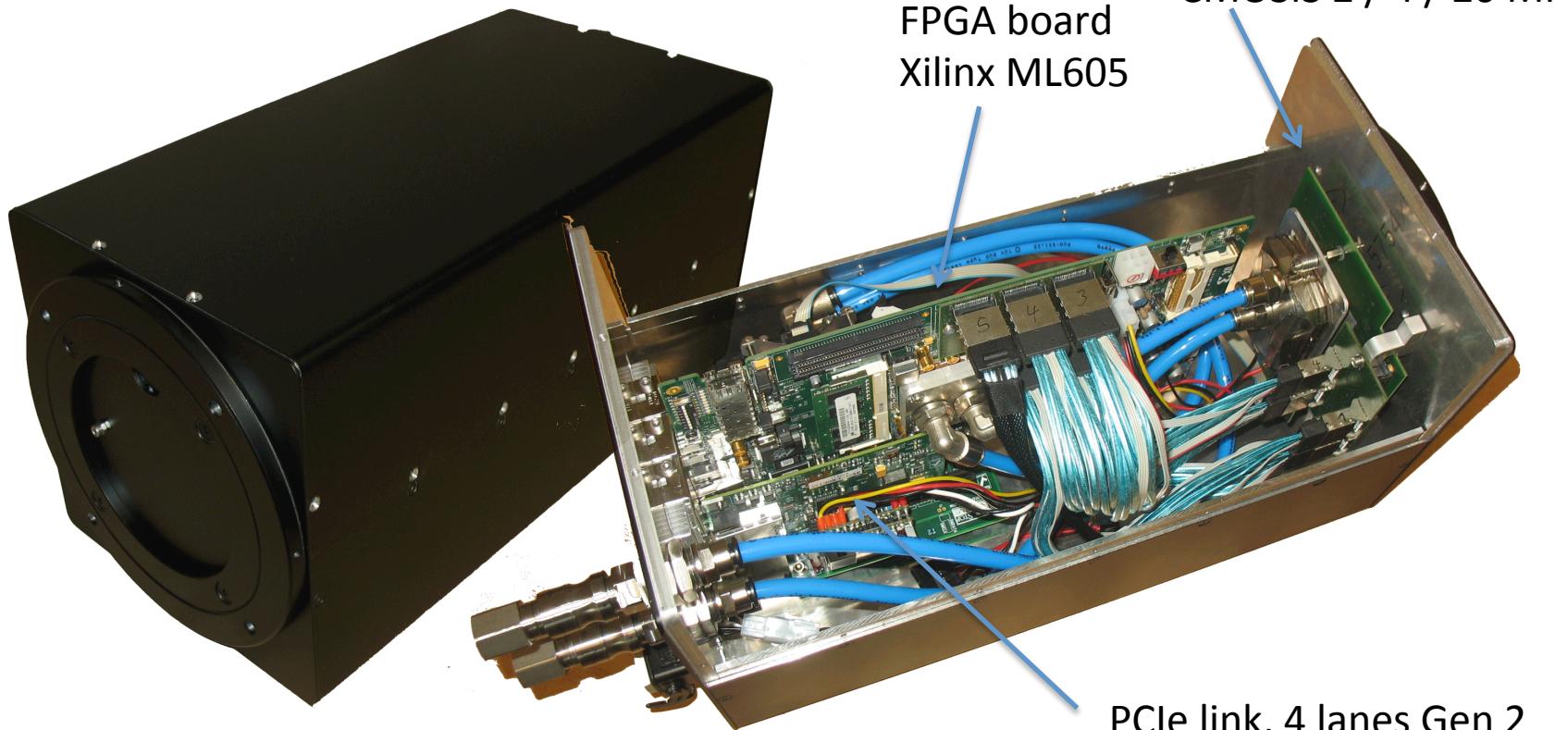
PCIe and GTX HW-core

Same logic functions

FPGA: Virtex 6 -> XC6VLX240-2 FF1759

Application 1: Smart phase contrast camera

- **Goals:**
 - Automatic grating control
 - Online reconstruction (with GPU)

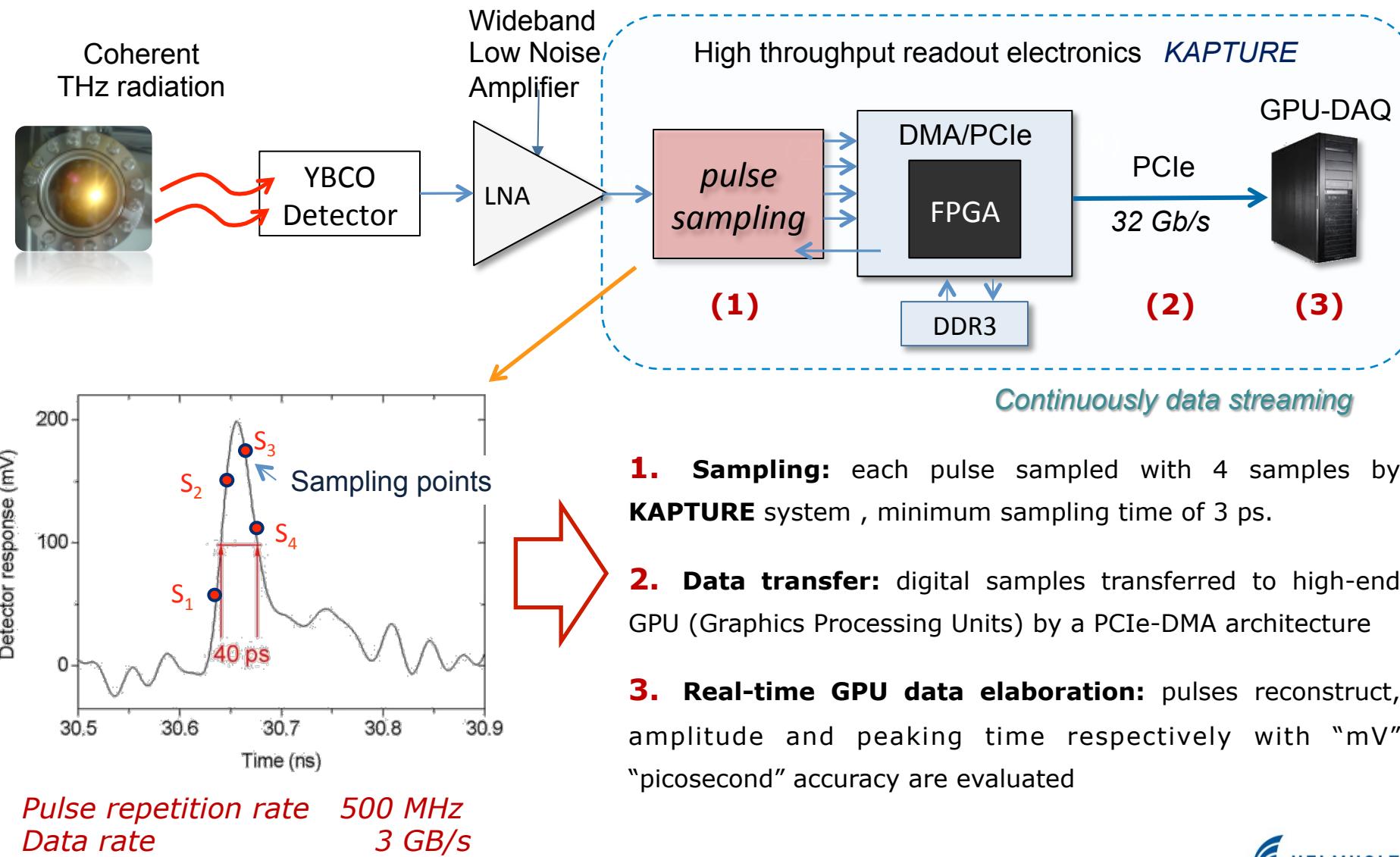


Helmholtz-Zentrum
Geesthacht

Zentrum für Material- und Küstenforschung

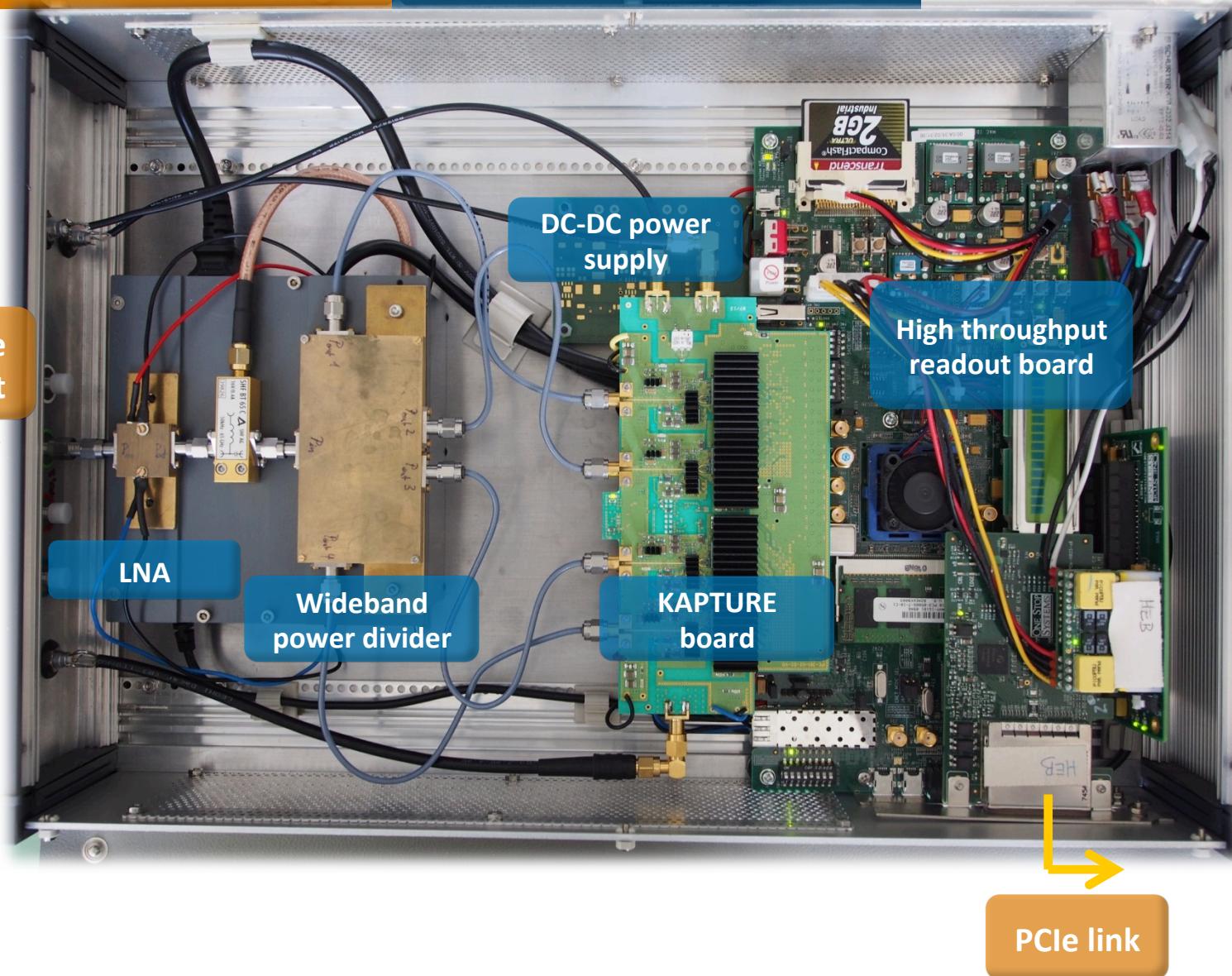
Image sensor board
CMOSIS 2 / 4 / 20 MP

Application 2: Picosecond Pulse Sampling



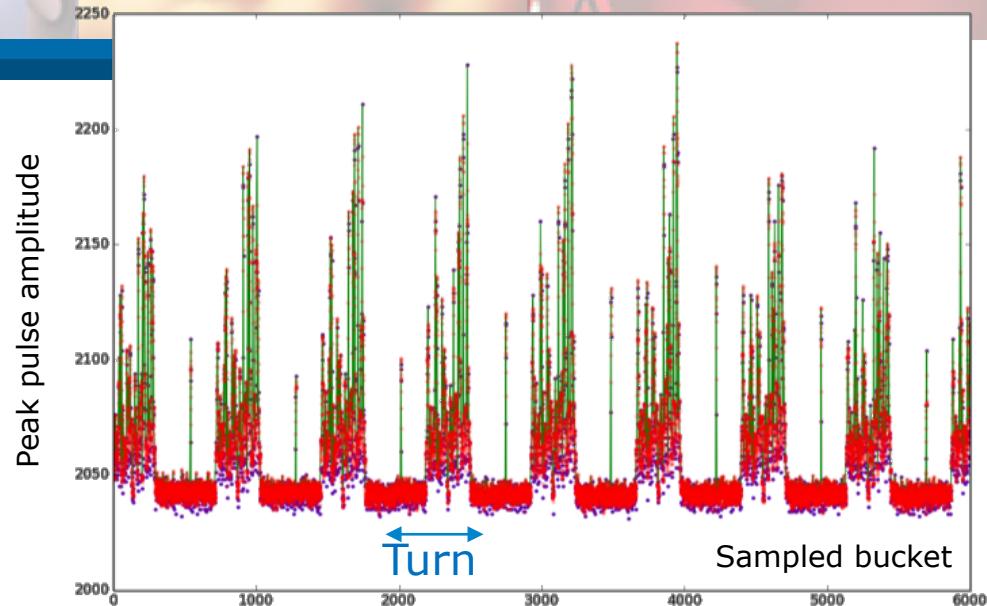
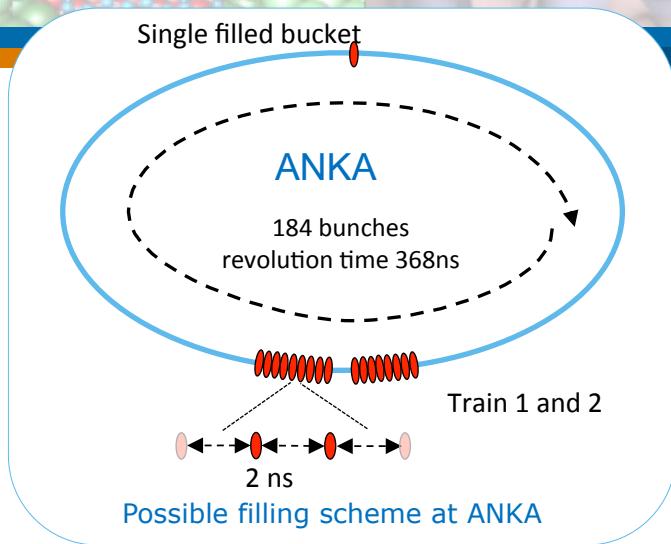
- 1. Sampling:** each pulse sampled with 4 samples by **KAPTURE** system , minimum sampling time of 3 ps.
- 2. Data transfer:** digital samples transferred to high-end GPU (Graphics Processing Units) by a PCIe-DMA architecture
- 3. Real-time GPU data elaboration:** pulses reconstruct, amplitude and peaking time respectively with "mV" "picosecond" accuracy are evaluated

KAPTURE System

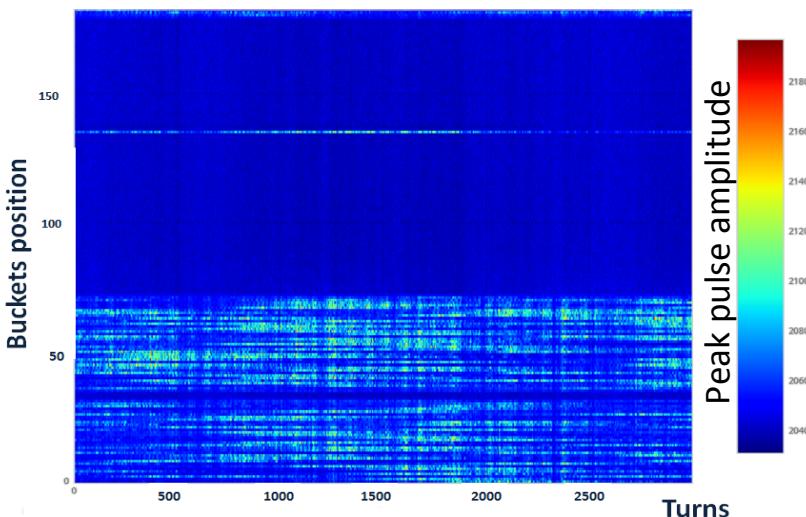


KAPTURE Measurements

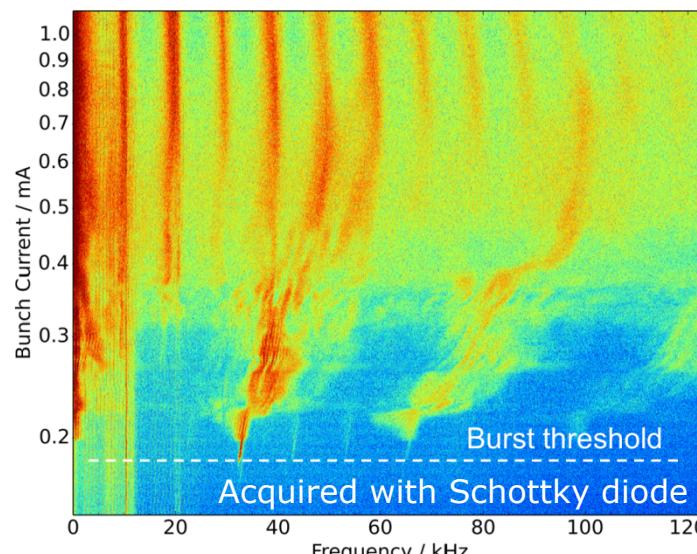
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CSR fluctuation in time domain
with YBCO detector



CSR frequency behaviour vs. different bunch current



General environment for parallel image processing

How to support code development for GPUs?

Requirements:

- Processes data streams (usually 1 to 4 dimensional floating point data)
- Detect and use all hardware resources

Developer:

- Hides parallelization and concurrency details
- Management of memory transfers
- Multiple implementations (e.g. for CPU + GPU)
- Automatic scheduling

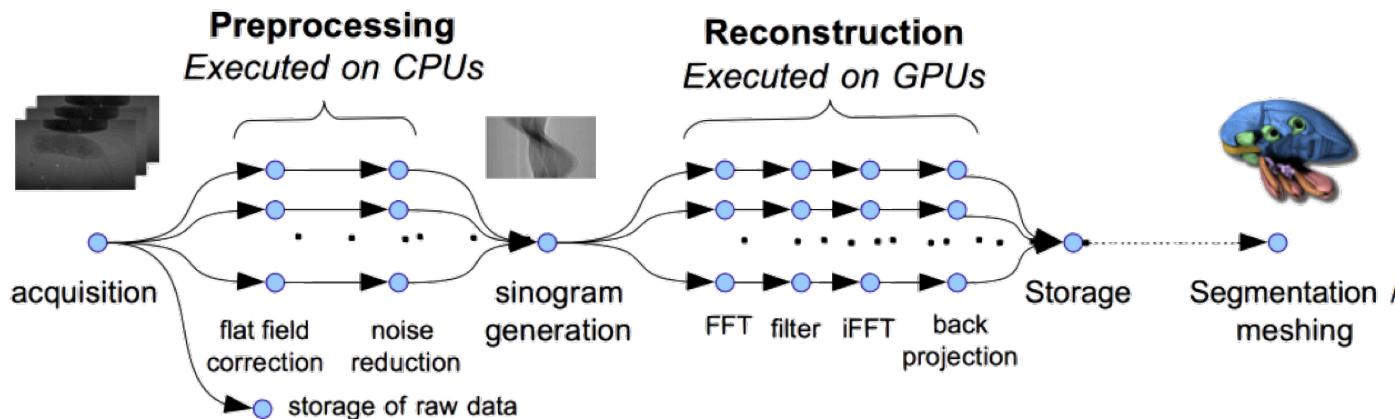
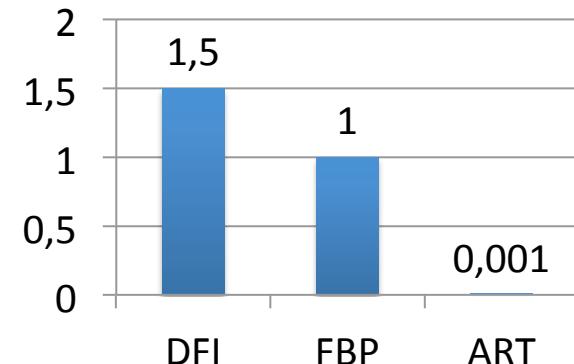
User:

- Simple end-user interface
 - GUI + Scripting
- Modular algorithm design

UFO Parallel Processing Framework

- Free and open-source
- Supports OpenCL parallel hardware
- Hardware-specific optimizations
- Fast pipelined architecture
- Scheduling across multiple devices and nodes
- Introspection interface to Python and other scripting languages
- Integrated with Tango control system

**Reconstruction w
single GPU (GB/s)**



Used at:
• ANKA
• ESRF
• PETRA 3
• HZDR

HDRI in POF3 (2015-2019)

Volume I



POF3 proposal

*“Common to all large-scale facilities is [...] how to cope with the huge amounts of data [...] in terms of **real-time analysis**, evaluation, storage and archiving.*

To meet this challenges [...] MML continues [...] HDRI [...].”

Review: Strengthening of this initiative is of fundamental importance

Research Field Matter
Proposal for a Helmholtz Research Programme
From Matter to Materials and Life

2015 – 2019
Participating Centres
Helmholtz-Zentrum Geesthacht Zentrum für Material- und Küstenforschung
Deutsches Elektronen-Synchrotron DESY
Forschungszentrum Jülich
GSI Helmholtzzentrum für Schwerionenforschung
Helmholtz-Zentrum Berlin für Materialien und Energie
Helmholtz-Zentrum Dresden-Rossendorf
Karlsruher Institut für Technologie

New work package structure

1. Standard Data Format and Offline Data Processing Frameworks
2. Fast Online Data Processing Frameworks
 - Scalable Parallel Computing
 - Configurable Data Acquisition Systems
 - Online Data Processing Frameworks
3. Scientific Computing

WP2: Fast Online Data Processing Frameworks

Milestones:

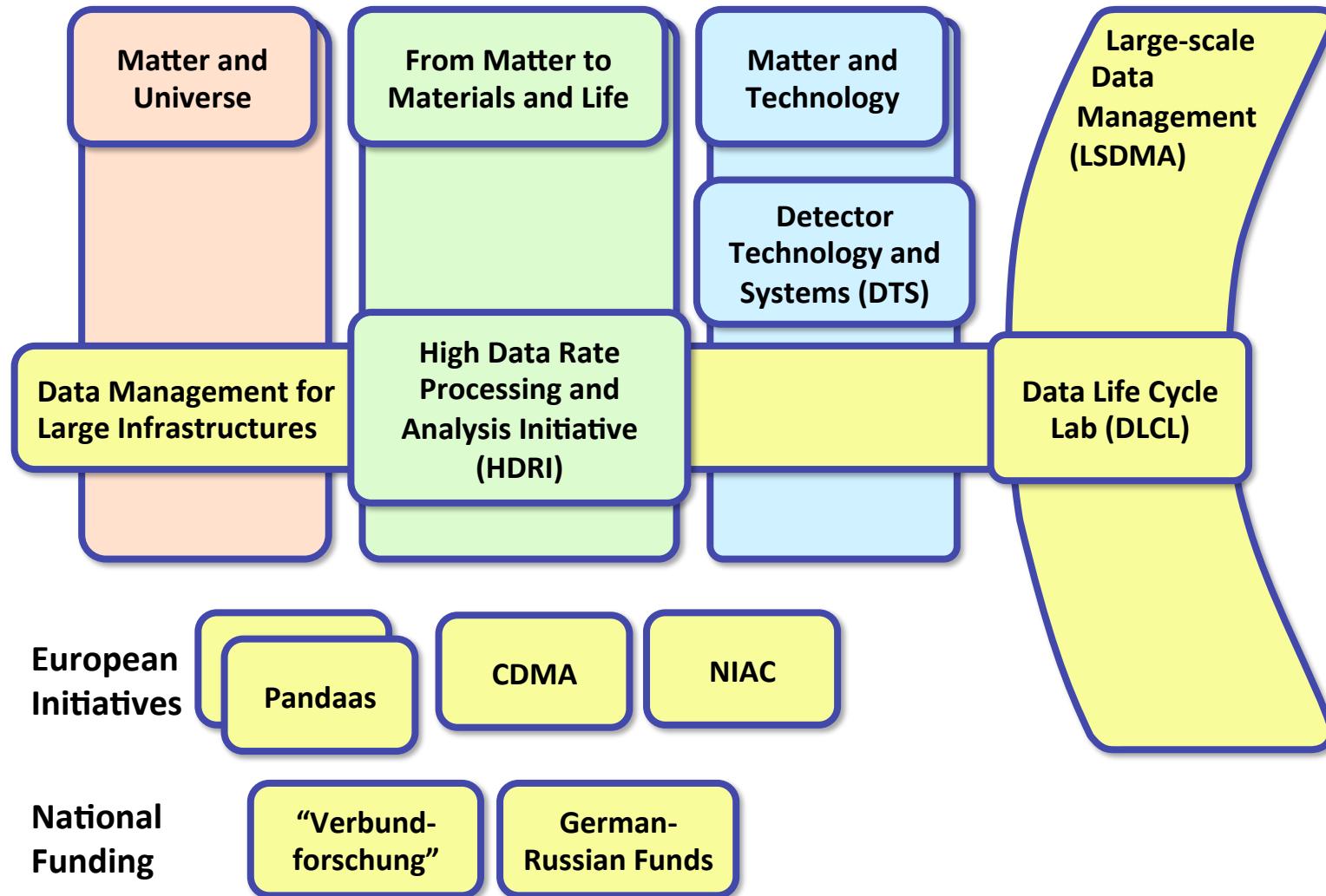
- **Standard platforms for online monitoring**
 - High-performance acquisition and computing hardware
 - Fast control system for computationally intensive applications
- **Applications based on DAQ hardware and computing platforms**
- **Near-time analysis frameworks**
 - Early data quality checks
 - Based on DPDAK, DAWN and UFO-Framework
- **Application of real-time data processing + data quality checks**
- **Web portals for data access and remote computing**
 - Integration of online tools

Resources

Helmholtz Centre	POF (FTE)	Additional funding (FTE)
DESY	8	1.5
FZJ	1	4
HZB	2	0
HZDR	0.5	0
HZG	1.5	1
GSI	0.5	1
KIT	2	0
Total	15.5	7.5

Source: POF3, Volume 3

HDRI in the Research Programme “Matter”



Detector Technology and Systems (DTS)

Sensors, ASICs and Interconnects

Data Transmission and Processing

Detector Systems

Sensors

Terabit/s Optical Data Transmission

HGF-Cube

Readout Electronics

Intelligent
Programmable
Electronics

Compact Gaseous
Detectors

Interconnect, Packaging
and Innovative
Materials

Parallel Processing

Fast Photon and X-ray
Detectors

Fast Timing Detector
Systems

CMOS Sensor Systems

Networking, Outreach and Applications beyond “Matter”

Selected Milestones:

- **Common technology platform based on Helmholtz AMC**
- **Scalability of CPU/GPU clusters for DAQ systems**
- **Helmholtz-Cube / Lambda**
- **Multi-channel bolometric detector systems**

Highlighted projects (Common fund):

- **Ultrafast 1D line detector for optical and near IR radiation**
 - Caselle, Hiller, Müller, Rota, Weber (KIT), Correa, Dariusz, Gerth, Graafsma, Steffen (DESY)
- **New DAQ-Electronics demonstrator system for X-Ray tomography**
 - Kaever (HZDR), Zimmer (DESY), Balzer (KIT)

Arthropod Structure revealed by ultra-fast Tomography and Online Reconstruction

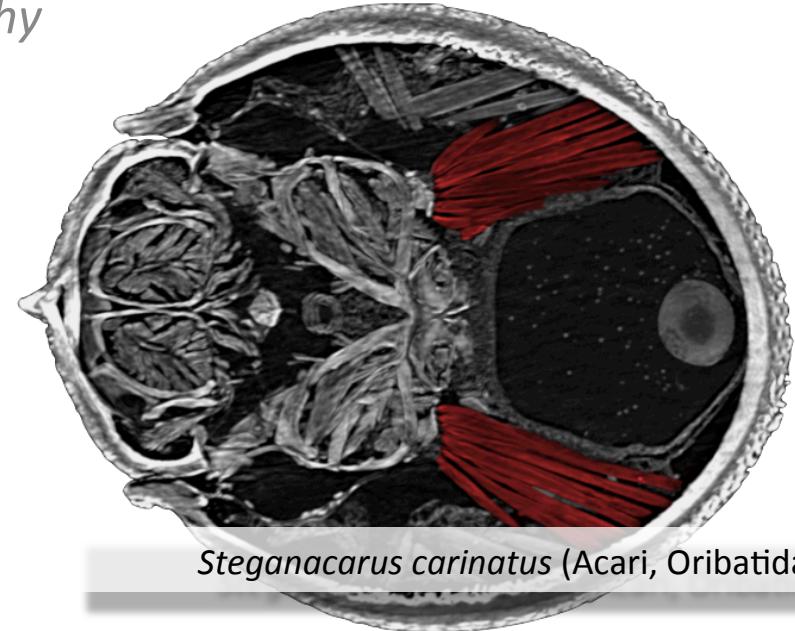
A new method for ultra-fast X-ray tomography

Science-driven development of

- Management of large datasets
- Cloud-based analysis environment
- Advanced segmentation
of 4D X-ray images



*Arthropod Structure revealed
by ultra-fast Tomography
and Online Reconstruction*



Steganacarus carinatus (Acari, Oribatida)

Network for functional morphology and systematics:

Virtual Analysis Infrastructure

Cloud technologies enable remote analysis without data download

- 2
- Access via DFN + DSL
- Remote 3D applications

ASTOR web-portal

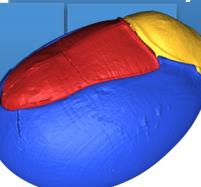
Virtual analysis infrastructure

- 4
- Interactive 4D visualization

Long-term archive

ASTOR processing cache

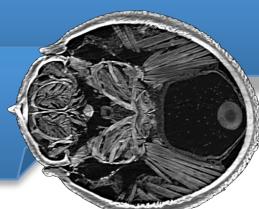
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- 3 - Managing large-scale datasets

ANKA imaging beamline

1



Tasks:

- 1 - Science-driven
- 2 - Analysis infrastructure
- 3 - Work flow
- 4 - Web portal
- 5 - 4D analysis
e.g. segmentation

astor

HELMHOLTZ
ASSOCIATION

Summary

- **HDRI in POF2**

- Established technology platform of beamline scientists, engineers, computer scientists, IT departments, ...
- Introduced parallel computing (w GPUs)
- Common hardware platforms are existing

- **HDRI in POF3**

- Technologies (cont.)
- New focus: Data access + analysis of “big data”
- Resume:
 - “The task are not getting smaller”
 - More resources required
 - Search collaboration with DTS, LSDMA, Universities, ...
 - HDRI should be link between beamline scientists and technology experts