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Multichannel sequential pulse accumulator with a two-group memory addressing

Sinyavskij V., Manapov R.

Kazan Federal University, 420008, Kremlevskaya 18, Kazan, Russia

Abstract

Based on the two-group RAM IC addressing principle, a multichannel (256 channels) sequential data accumulator applying KP537RU14A RAM IC with one-bit memory (4k×1) is proposed. The channel capacity is 2¹⁶-1, and the input pulse repetition frequency is up to 62 kHz.
