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Abstraction Refinement for Non-Zeno Fairness Verification of Linear Hybrid Automata

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I. INTRODUCTION

Linear hybrid automaton is a specification language for hybrid systems. For verification of hybrid systems, it is important to check fairness assumptions. For example, an embedded system keeps running forever when it starts to move by turning on the switch. Such a system has to be checked not only system safety but also fairness and non-Zenoness.

The state space explosion is a fundamental problem in model checking, since it is a method that performs an exhaustive search of states. To avoid state space explosion problem in model checking, CEGAR (CounterExample Guided Abstraction Refinement) is an effective technique. In this paper, we propose transition predicate abstraction and CEGAR verification algorithm for linear hybrid automata.

II. RELATED WORK

Hybrid automaton is a specification language proposed by R. Alur, et al [1], and linear hybrid automaton is a subset of the language. R. Alur proposed predicate abstraction for reachability analysis and CEGAR verification algorithm [2], [3]. F. Wang specified non-Zeno fairness assumption by using extended temporal logic TECTL^f and proposed verification method for timed automaton [4]. R. Kurki-Suonio formalized a hybrid system as a discrete time system that has distributed clocks, and he verified non-Zeno fairness for the system [5]. B. Cook, A. Podelski and A. Rybalchenko extended predicate abstraction to *transition predicate abstraction* and proposed an algorithm for checking fair termination of discrete systems [6], [7].

In our work, we propose verification method by CEGAR using transition predicate abstraction for linear hybrid automaton. Our proposed CEGAR based on transition predicate is quite different from existing CEGARs based on state predicate.

III. NON-ZENO FAIRNESS

Definition 1 (Non-Zeno fairness). Wang defined non-Zeno fairness by using event predicate and $TECTL^{f}$ formula[4] as

$$\exists^{[a_1,\ldots,a_m]}_{\langle b_1,\ldots,b_n\rangle}\Box^{\varepsilon}\varphi_1,$$

where $a_1, \ldots, a_m, b_1, \ldots, b_n, \varepsilon$ are either event predicates or $TECTL^f$ formulas, and φ_1 is either null (not specified) or an event predicate. $[a_1, \ldots, a_m]$ denotes the strong-fairness and $\langle b_1, \ldots, b_n \rangle$ denotes the weak-fairness, and $\exists_{\langle b_1, \ldots, b_n \rangle}^{[a_1, \ldots, a_m]}$ means that there exists a path satisfying them.

- The strong-fairness is a property that a transition satisfying an event predicate occurs at least once if the transition can be taken infinitely often.
- The weak-fairness is a property that a transition satisfying an event predicate occurs at least one if the transition can always occur.

 $\Box^{\varepsilon}\phi_1$ means that the path always satisfies ϕ_1 after a transition satisfying ε . If ε is not assigned, it is equal to a "globally" formula of typical CTL. In addition, the path must satisfy that the sum of time for each transitions diverges to infinity.

IV. TRANSITION PREDICATE ABSTRACTION FOR LINEAR HYBRID AUTOMATON

We utilise Podelski's predicate abstraction method and extend it for linear hybrid automata.

A. Transition Predicate

Let a linear hybrid automaton be $H = (L, X, F, I, A, T, \theta)$, a transition predicate p is a binary relation over states, and it is described as an assertion over X and their primed variables.

1) Predicates of Transition Relations: A time transition in a location l of a linear hybrid automaton $H = (L, X, F, I, A, T, \theta)$ corresponds to the following set \mathcal{P}_l^{\uparrow} of predicates:

$$\mathcal{P}_{l}^{\top} = \{at_l, at'_l\} \cup \operatorname{Preds}(I(l))$$
$$\cup \operatorname{Preds}(\exists d \in \mathbb{R}_{\geq 0}. \bigwedge \{x'_{i} = x_{i} + F(l)(x_{i}) \cdot d \mid x_{i} \in X\})$$
$$\cup \operatorname{Preds}(I(l)[X'/X]), \tag{1}$$

where Preds() is a function that symbolically evaluates a formula and divides it into the set of atomic formulas with conjunctions (e. g. $\operatorname{Preds}(p_1 \wedge p_2) = \{p_1, p_2\}$), and [X'/X] denotes a formula computed by replacing $x_i \in X$ with the primed variable x'_i .

A discrete transition $\tau = (l, \phi, a, \lambda, l') \in T$ of H is also represented by the following set \mathcal{P}_{τ} of predicates:

$$\mathcal{P}_{\tau} = \{at_l, at'_l'\} \cup \operatorname{Preds}(I(l) \land \phi \land \lambda \land I(l')[X'/X]).$$
(2)

2) Composition of transition predicates: Given two sets \mathcal{P}_1 and \mathcal{P}_2 of transition predicates, the composition $\mathcal{P}_1 \circ \mathcal{P}_2$ is computed by the following rules:

- If $\bigwedge \mathcal{P}_1 = false, \mathcal{P}_1 \circ \mathcal{P}_2 = \{false\}.$
- If $\bigwedge \mathcal{P}_2 = false, \mathcal{P}_1 \circ \mathcal{P}_2 = \{false\}.$
- Let $\mathcal{P}_1 = \{at_l_1, at'_l'_1\} \cup \mathcal{P}'_1 \text{ and } \mathcal{P}_2 = \{at_l_2, at'_l'_2\} \cup$ \mathcal{P}'_2 , If $l'_1 = l_2$,

$$\mathcal{P}_1 \circ \mathcal{P}_2 = \{at_l_1, at'_l_2'\} \cup \mathsf{Preds}((\exists X'$$

$$(\rho_2[X''/X'])[X'/X] \wedge \rho_1)[X'/X'']),$$

where $\rho_1 = \bigwedge \mathcal{P}'_1$ and $\rho_2 = \bigwedge \mathcal{P}'_2$. Otherwise, $\mathcal{P}_1 \circ \mathcal{P}_2 = \{ false \}.$

Let $\mathcal{P}_{\tau}^{\uparrow}$ be a set of transition predicates when a discrete transition τ occurs after time transition from a location l, it can be computed as $\mathcal{P}_l^{\uparrow} \circ \mathcal{P}_{\tau}$, where \mathcal{P}_l^{\uparrow} is the predication set of a time transition and \mathcal{P}_{τ} is one of a discrete transition.

Given a sequence $\pi = \tau_0 \tau_1 \dots \tau_k$ of discrete transitions, the predicate set $\mathcal{P}^{\uparrow}_{\pi}$ considering time transitions is computed as

$$\mathcal{P}_{\pi}^{\uparrow} = \mathcal{P}_{\tau_0}^{\uparrow} \circ \mathcal{P}_{\tau_1}^{\uparrow} \circ \cdots \circ \mathcal{P}_{\tau_k}^{\uparrow}.$$

3) Transition Abstraction for a Linear Hybrid Automaton: Given a linear hybrid automaton $H = (L, X, F, I, A, T, \theta)$ and a set \mathcal{P} of transition predicates, the predicate abstraction for a predicate set \mathcal{P}_{τ} with \mathcal{P} is defined as

$$\alpha_{\mathcal{P}}(\mathcal{P}_{\tau}) = \{ p \in \mathcal{P} \mid \rho_{\tau} \subseteq p \},\$$

where predicates at_l and at'_l for each location are implicitly contained by \mathcal{P} , and ρ_{τ} is an assertion of τ , that is, $\rho_{\tau} = \bigwedge \mathcal{P}_{\tau}^{\uparrow}$.

A predicate abstraction $\hat{\alpha}_{\mathcal{P}}(\pi)$ for a finite path π = $\delta_{d_0} \tau_0 \delta_{d_0} \tau_1 \dots \delta_{d_k} \tau_k$ is recursively computed:

$$\hat{\alpha}_{\mathcal{P}}(\pi) = \alpha_{\mathcal{P}}(\mathcal{P}_{\tau_0}^{\uparrow} \circ \hat{\alpha}_{\mathcal{P}}(\delta_{d_1}\tau_1 \dots \delta_{d_k}\tau_k))$$
$$\hat{\alpha}_{\mathcal{P}}(\delta_k \tau_k) = \alpha_{\mathcal{P}}(\mathcal{P}_{\tau_k}^{\uparrow}).$$

For a sequence $\pi = \tau_0 \tau_1 \dots \tau_k$ of discrete transitions, the abstraction $\hat{\alpha}_{\mathcal{P}}^{\uparrow}(\pi)$ considering time transitions is computed as

$$\hat{\alpha}_{\mathcal{P}}^{\uparrow}(\pi) = \alpha_{\mathcal{P}}(\mathcal{P}_{\tau_0}^{\uparrow} \circ \hat{\alpha}_{\mathcal{P}}^{\uparrow}(\tau_1 \dots \tau_k))$$
$$\hat{\alpha}_{\mathcal{P}}^{\uparrow}(\tau_k) = \alpha_{\mathcal{P}}(\mathcal{P}_{\tau_k}^{\uparrow}).$$

V. CEGAR FOR NON-ZENO FAIRNESS

We propose the approach of non-Zeno fairness verification with CEGAR for a linear hybrid automaton based on Cook's method[7].

For proving the non-Zeno fairness $\exists_{\langle b_1,...,b_n \rangle}^{[a_1,...,a_m]} \Box^{\varepsilon} \phi_1$, we have to find the path satisfying the assumption. However, the search will not terminate if the number of paths is infinite. Avoiding this problem, we compress paths to check by using the transition abstraction method.

The algorithm of non-Zeno fairness verification with CE-GAR is shown in Fig. 1.

1: $\mathcal{R} := \emptyset / *$ A set of ranking relations */ 2: $\mathcal{P} := \varnothing$ /* A set of transition predicates */ 3: while exists $\pi_2 = \tau_k \dots \tau_l$ s.t. $\hat{\alpha}_{\mathcal{P}}^{\uparrow}(\pi_2) \not\subseteq R$ for any $R \in \mathcal{R}$ do

if exists $R \in \mathcal{R}$ s.t. $\mathcal{P}_{\pi_2}^{\uparrow} \subseteq R$ then 4: /* Refinement */ 5: $\mathcal{P}_{path} := \bigcup_{i \in \{k, \dots, l\}} \mathcal{P}^{\uparrow}_{\rho_{\tau_i} \dots \tau_l}$ 6: $\begin{aligned} \mathcal{P}_{loop} &:= R \cup \bigcup_{i \in \{k, \dots, l\}} \mathcal{P}_{\tau_i \dots \tau_l}^{\uparrow} \circ R \\ \mathcal{P} &:= \mathcal{P} \cup \mathcal{P}_{path} \cup \mathcal{P}_{loop} \ /* \ \text{Update} \ \mathcal{P} \ */ \end{aligned}$ 7: 8: 9: else if π_2 is well-founded by the ranking relation R then 10: 11: $\mathcal{R} := \mathcal{R} \cup \{R\}$ 12: else 13: if exists $\pi_1 = \tau_1 \dots \tau_{k-1}$ s.t. $\hat{\alpha}_{\mathcal{P}}(\pi_1 \pi_2) \neq false$ then if $\mathcal{P}_{\pi_{1}\pi_{2}}^{\uparrow} \neq false$ then if A cycle time of π_{2} diverges, $\forall i \in \{1, \dots, m\} \exists j \in \{k, \dots, l\}.$ $(\exists X. \mathcal{P}_{\pi_{1}\tau_{k}...\tau_{j}}^{\uparrow})[X/X'] \land a_{i} \neq false,$ 14: 15: 16: $\forall i \in \{1, \dots, m\}. \forall j \in \{k, \dots, l\}. \\ (\exists X. \mathcal{P}_{\pi_1 \tau_k \dots \tau_j}^{\uparrow}) [X/X'] \land b_i \neq \textit{false, and}$ 17: $\begin{array}{c} \forall i \in \{1, \dots, l\}. \ (\exists X. \mathcal{P}_{\tau_1 \dots \tau_i}^{\uparrow})[X/X'] \land \\ \varepsilon \neq \textit{false} \implies (\exists X. \mathcal{P}_{\tau_1 \dots \tau_i}^{\uparrow})[X/X'] \land \phi_1 \neq \textit{false. then} \end{array}$ 18: 19: **return** SAT and the path $\pi_1\pi_2$ else

21: /* Refinement */
22:
$$\mathcal{P} := \mathcal{P} \cup \bigcup_{i \in \mathcal{U}}$$

:
$$\mathcal{P} := \mathcal{P} \cup \bigcup_{i \in \{k, \dots, l\}} \mathcal{P}_{\tau_i}^{\uparrow}$$

23: return UNSAT

Fig. 1. Algorithm of CEGAR for Non-Zeno Fairness $\exists_{(b_1,...,b_n)}^{[a_1,...,a_m]} \Box^{\varepsilon} \phi_1$

VI. CONCLUSION

In this paper, we presented transition predicate abstraction and CEGAR verification algorithm for a linear hybrid automaton. We are working on implementation of the algorithm. Future work will focus on further practical experiments and evaluation.

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