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Control, Operation and Power Sharing among Parallel Converter–Interfaced DERs in a Microgrid in the Presence of Unbalanced and Harmonic Loads

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*Abstract—***This paper demonstrates power management and control of DERs in an autonomous MG. The paper focuses on the control and performance of converter–interfaced DERs in voltage controlled mode. Several case studies are considered for a MG based on the different types of loads supplied by the MG (i.e. balanced three–phase, unbalanced, single–phase and harmonic loads). DERs are controlled by adjusting the voltage magnitude and angle in their converter output through droop control, in a decentralized concept. Based on this control method, DERs can successfully share the total demand of the MG in the presence of any type of loads. This includes proper total power sharing, unbalanced power sharing as well as harmonic power sharing, depending on the load types. The efficacy of the proposed power control, sharing and management among DERs in a microgrid is** validated through extensive simulation **PSCAD/EMTDC.**

*Index Terms***––Microgrid, DER, Voltage Control, Power Sharing.**

I. INTRODUCTION

The ever increasing energy demand, along with the necessity of cost reduction and higher reliability requirements, are driving the modern power systems towards distributed generation (DG) as an alternative to the expansion of the current electricity distribution networks [1–2].

Microgrids (MG) are systems with clusters of DGs and loads. To deliver high quality and reliable power, the MG should appear as a single controllable unit that responds to changes in the system [3]. In MGs, parallel DGs are controlled to deliver the desired active and reactive power to the system while local signals are used as feedback to control the converters. The power sharing among the DGs can be achieved by controlling two independent quantities, frequency and fundamental voltage magnitude, at the converter output [4–7].

General introduction on MG basics, including the architecture, protection and power management is given in [8–9]. A review of ongoing research projects on MG in US, Canada, Europe and Japan is presented in [9–10]. Different power management strategies and controlling algorithms for a MG is proposed in [11–14]. Reference [15] has evaluated the feasibility of MGs operation during islanding and synchronisation periods.

One of the main issues still to be investigated in MGs is the effect of single–phase, unbalanced and harmonic loads on the control, operation and power sharing among parallel converter interfaced DERs. In the presence of balanced and linear loads, the converters can be operated in voltage control mode [16–19] or current control mode [20–23]. Majority of the researches for DER converter control in MGs utilise current control mode

[20–23]. For this, the output current reference for each converter should be calculated properly based on the load demand and the desired power sharing ratio among existing DERs. Later, using a proper switching mechanism in the converter, it can supply the MG by the desired reference current. This can be relatively complex when the MG is supplying single–phase, unbalanced or harmonic loads. In this case, the DERs not only should share the active and reactive power but also should share harmonic, negative and zero sequence currents. This needs a fast and accurate negative and zero sequence and harmonic current extraction mechanism as discussed in [24–25]. If the extracted sequence and harmonic currents mismatch the actual load requirement, the operation and power sharing of DERs can be significantly affected. However, by using a voltage control mode, there will be no necessity to measure and analyse the load current and to extract the sequence and harmonic current components.

In this paper, the operation, control and power sharing among parallel converter–interfaced micro sources, operating in voltage control mode, are investigated for grid–connected and autonomous modes. Through the extensive simulation results carried out by PSCAD/EMTDC, it is demonstrated that the DERs can successfully supply the MG loads with the desired power sharing ratios in the presence of single–phase, unbalanced and harmonic loads.

II. MICROGRID STRUCTURE AND CONTROL

Let us consider the MG system as shown in Fig. 1. The considered MG system consists of three converter–interfaced DERs. DERs such as photovoltaic cells (PV), fuel cells and batteries are usually connected to the MG through voltage source converters (VSC) and a properly tuned second order filter to the MG. The DERs are then controlled to supply the loads within the MG. It is to be noted that the considered DERs in this paper are working in voltage control mode and their output power, required by power sharing, are within their rated capabilities. Distribution Static Compensator (DSTATCOM) is installed at the secondary side of the distribution transformer to regulate the voltage at its Point of Common Coupling (PCC). The microgrid is maintained and operated using two control loops. The outer control loop satisfies proper power sharing among the DERs while the inner control loop is responsible of proper switching of DER converters in order to generate the desired voltage at their output. The DER and DSTATCOM converter structure in addition to outer and inner control mechanisms are discussed in detail below.

2.1 DER Converter Structure

Let us assume that the DERs are connected to the MG through voltage source converters (VSC). The VSC structure consists of three single–phase H–bridges, using IGBTs, as shown in Fig. 2(a). Each IGBT has proper parallel reverse diode and snubber circuits. The outputs of each H–bridge are connected to a single–phase transformer, with 1 : *a* ratio, and three transformers are star–connected. In this figure, the resistance R_f represents the switching and transformer losses, while the inductance L_f represents the leakage reactance of the transformers and the filter capacitor C_f is connected to the output of the transformers to bypass the switching harmonics.

2.2 Outer Control Loop

For a converter–interfaced DER, with the structure shown in Fig. 2(a), the instantaneous active and reactive power flows from converter output to its PCC are [16]

$$
p = \frac{|V_T| \times |V_{cf}|}{\omega L_T} \sin(\delta_{cf} - \delta_T)
$$

\n
$$
q = \frac{|V_T|}{\omega L_T} (V_{cf} |\cos(\delta_{cf} - \delta_T) - |V_T|)
$$
\n(1)

where $V_T = |V_T| \angle \delta_T$ and $V_{cf} = |V_{cf}| \angle \delta_{cf}$ are respectively the RMS value of PCC voltage and AC filter capacitor voltage of DER. *L^T* is the coupling inductance between PCC and inverter output which controls DER output power flow and ω is the angular frequency of the system. The switching frequency components in the instantaneous active power (*p*) and reactive power (*q*) can be filtered out to yield the average active power (*P*) and reactive power (*Q*) by passing them through a low pass filter. In [17], it was proposed that decentralized power sharing among the DERs in an autonomous MG can be achieved, similar to conventional droop control, by changing the voltage magnitude and angle of DERs as

$$
\delta_{cf} = \delta_{rated} - m(P_{rated} - P)
$$

\n
$$
V_{cf} = V_{rated} - n(Q_{rated} - Q)
$$
\n(2)

where V_{rated} and δ_{rated} are respectively the rated voltage magnitude and angle of the DER when supplying the load with rated active and reactive power of *Prated* and *Qrated* while *m* and *n* are the droop coefficients.

The principle of decentralized power sharing in the MG is based on keeping the power output of DERs proportional to their ratings while the sum of the generated power from DERs is equal to the total required demand in the MG. This was mathematically simplified in [18] as

$$
P_{Load} = \sum_{1}^{DER number} P_i, \qquad Q_{Load} = \sum_{1}^{DER number} Q_i
$$
\n
$$
\frac{P_i}{P_j} \approx \frac{m_j}{m_i} = \frac{P_{i,rated}}{P_{j, rated}} \qquad , \qquad \frac{Q_i}{Q_j} \approx \frac{n_j}{n_i} = \frac{Q_{i, rated}}{Q_{j, rated}}
$$
\n(3)

where *i* and *j* show the number of each DER.

Fig. 1. Schematic diagram of the network under consideration.

(b) Single–phase equivalent circuit of VSC.

2.3 Inner Control Loop

In this paper, the main concept of DER converter control is based on fixing the voltage magnitude and angle across C_f as defined by the droop control from (2) – (3) . This is achieved by appropriate switching of IGBTs in the converter. For this, let us consider the single–phase equivalent circuit of VSC as shown in Fig. 2(b). In this figure, $u \cdot a \cdot V_{dc}$ represents the converter output voltage, where u is the switching function that can take on ± 1 value depending on which pair of the IGBTs is turned on. The VSC is utilizing a closed–loop optimal robust controller based on state feedback to generate *u*. For this, two different state vectors can be assumed.

First, let us assume the state vector be defined as

$$
x = [v_{cf} \quad \tilde{i}_f]^T
$$
 (4)

where v_{cf} represent the instantaneous voltage and \tilde{t}_f is the high frequencies of i_f . Now, the reference vector, x_{ref} is to be calculated for each state variable, for each DER as

$$
v_{cf, ref, A} = V \angle \delta \qquad , \qquad \tilde{i}_{f, ref, A} = 0
$$

\n
$$
v_{cf, ref, B} = V \angle \delta - 120^{\circ} \qquad , \qquad \tilde{i}_{f, ref, B} = 0
$$

\n
$$
v_{cf, ref, C} = V \angle \delta + 120^{\circ} \qquad , \qquad \tilde{i}_{f, ref, C} = 0
$$
 (5)

For DER control in this paper, *vcf,ref* is calculated from (1) when the MG is in grid–connected mode and from (2) when in autonomous mode. From the circuit of Fig. 2(b), system state space description can be given as

$$
\dot{x} = Ax + B_1 u_c + B_2 v_T \tag{6}
$$

where u_c is the continuous time version of switching function u and v_T is assumed as load disturbance induced to the converter and neglected here. Discrete–time equivalent of (6) is

$$
x(k+1) = Fx(k) + G_1 u_c(k) + G_2 v_T(k)
$$
\n(7)

In (7), $u_c(k)$ is computed using a suitable state feedback control law. For this, switching control laws are given by

$$
u_c(k) = -K[x(k) - x_{ref}(k)]
$$
\n(8)

where K is a gain matrix. The gain matrix is obtained from Linear Quadratic Regulator (LQR) method which ensures the desired results for the system while the variations of system load and source parameters are within acceptable limits of reality. From $u_c(k)$, the switching function is then generated based on the error level as

If
$$
u_c > +h
$$
 then $u = +1$
\n*elseif* $u_c < -h$ then $u = -1$ (9)

where *h* shows the error level and has a very small positive value. More detail on converter control is given in [26].

2.4 Voltage Control in Autonomous Mode

Based on the DER converter control described above, there is no direct voltage control in the MG in autonomous mode. This can be achieved if one of the DERs in MG regulates the network voltage. However, the DERs in residential networks can be owned by customers and are not responsible for network voltage support. Utilizing the converter of a DER to generate reactive power to support the network voltage profile will reduce the active power generation capacity of the converter. This will not be desired by their owners.

Alternatively, voltage regulation in a MG can be achieved using a DSTATCOM. This was presented and discussed in [27] and is not described here.

III. STUDY CASES AND SIMULATION RESULTS

For investigating the performance of the MG with several converter–interfaced DERs, different simulation cases are considered. The simulation cases are built to demonstrate the efficacy of the proposed voltage control mode for all DERs in proper power sharing among them in the network. The MG performance and power sharing among DERs are studied for different types of loads in the network such as balanced, unbalanced and harmonic. The DER, converter, filter, network and load parameters are given in the Appendix B.

3.1. MG with balanced loads

Let us consider the simple structure of Fig. 1 to investigate the MG operation during grid–connected and autonomous modes. In grid–connected mode, each DER will generate its rated power and the extra load demand will be supplied by the grid or the extra generation will flow back into the grid. In autonomous mode, total power demand is shared among the DERs proportional to their rating.

First let us assume in the system of Fig. 1, CB_G , CB_{M1} and CB_{S1} are closed. The system is assumed to be in steady state condition at $t = 0$ s and all the DERs are running in their rated condition.

- (a) Active power dispatch of grid and 3 DERs,
- (b) Single–phase active power output of DER–1,
-
- (c) Active power output of all DERs in their phase–A, (d) Network voltage profile in secondary side of transformer,
- (e) Three–phase instantaneous current output of a DER–1.

At $t = 0.5$ s, CB_G opens and the grid is disconnected hence MG will work in autonomous mode. Therefore, the DERs have to increase their output power to satisfy the load demand within the MG. At $t = 1.5$ s one of the existing loads in the network is increased by 3 kW. It can be seen that in both cases all DERs are sharing the load change proportional to their ratings.

Fig. 3(a) shows the active power dispatch of grid and 3 existing DERs in the MG between 0 and 2.5 seconds in the above–mentioned network. The single–phase active power output of one of the DERs (e.g. DER–1) is shown in Fig. 3(b). This is the same for all phases of A, B and C. From this figure, it can be seen that the network loads are three–phase balanced. The active power output in phase–A of all three DERs are also shown in Fig. 3(c). From this figure, it can be seen that all DERs are sharing the loads according to their ratios and they have an equal amount of power generated in each phase.

The voltage profile of the network is also shown in Fig. 3(d). As it can be seen in this figure, the network voltage is successfully regulated to 1 pu at all times.

The three–phase instantaneous current output of a sample DER (e.g. DER–1), shown in Fig. 3(e), confirms the DERs are generating balanced currents in their output.

3.2. MG with unbalanced loads

In this case, let us assume in the system of Fig. 1, at $t = 0$ s CB_G , CB_{M1} and CB_{S1} are closed and the system is in steady state condition. At $t = 0.5$ s, CB_G is opened to disconnect the grid hence MG will work in autonomous mode. It is to be noted that distribution transformer is still connected to the LV side.

- (b) Active power output of DER–1,
(c) Active power output of all DER Active power output of all DERs in their phase–A,
- (d) Active power supplied by the distribution transformer,
- (e) Three–phase instantaneous current output of a DER–1.

At $t = 1.5$ s a new single–phase 2 kW load is connected to phase–A. Later, at $t = 2.5$ s another single–phase 2 kW load is connected to phase–C.

Fig. 4(a) shows the active power dispatch of grid and 3 existing DERs in the MG between 0 and 3.5 seconds in the above–mentioned network. From this figure, it can be seen that, the total amount of active power generation among DERs are kept based on the desired power sharing ratio among them. The active power output of one of the DERs (e.g. DER–1) is shown in Fig. 4(b) for each phase individually. From this figure, it can be seen that for $t < 1.5$ s, all three phases of DER–1 have an equivalent amount of generated active power. However, at $t = 1.5$ s when a single–phase load is connected to phase– A, the active power output of all phases of the DER increases. This increase is slightly higher for phase–A. This slight difference is further discussed in Appendix A. In a similar way, at t $= 2.5$ s when another single–phase load is connected to phase– C, the output active power of all phases of the DER increases but this increase is more for phase–C. Similar results are monitored in all the DERs of the MG. From this figure it can be seen that single phase load power demand is shared among the three phases of the DERs.

Fig. 4(c) shows the active power output of all DERs in their phase–A for the studied case. From this figure, it can be seen that for a load change in the network, the contribution level of each phase of the DERs in power generation is also based on the desired sharing ratio among them.

It is to be noted that, all phases of the DERs contribute to a single–phase (or unbalanced) load change in the network since there is a possibility of power circulation from one phase of MG to the other two phases through the windings of the distri-

(a) Active power output of DER–1,

- (b) Active power output of all DERs in their phase–A,
-
- (c) Active power supplied by the distribution transformer,
- (d) Three–phase instantaneous current output of a DER–1.

bution transformer. Fig. 4(d) shows the active power supplied by the distribution transformer in the studied case. From this figure, it can be seen that the distribution transformer output power was zero when the MG was working in autonomous mode with balanced loads (i.e $0.5 < t < 1.5$ s). However, it can be seen that for $1.5 < t < 2.5$ s there is a negative active power flow into the distribution transformer in phase–B and C which is circulated and retuned to phase–A where the single phase load is connected to. In a similar case, for $2.5 < t < 3.5$ s there is a negative active power flow into the distribution transformer in phase–B which is circulated and retuned to phase–A and C where the single phase loads are connected to.

The three–phase instantaneous current output of a sample DER (e.g. DER–1) is shown in Fig. 4(e) which shows the DERs are generating unbalanced current in their output.

3.3. MG with unbalanced loads and isolated transformer

Now, let us consider the network of case 4.2 where the distribution transformer is isolated from the LV side when the grid is disconnected. Therefore, let us assume that at $t = 0.5$ s, CB_G and CB_{M1} open and MG works in autonomous mode.

As the distribution transformer is isolated from the LV side, no power circulation can happen among the three phases in the autonomous mode. Therefore, it is expected that by applying a single–phase (unbalanced) load change in the network, the DERs will contribute only in the phase in which the single– phase load is applied.

The active power output of one of the DERs (e.g. DER–1) is shown in Fig. 5(a) for each phase individually. From this figure, it can be seen that for $t < 1.5$ s, all three phases of DER–1 have an equivalent amount of generated active power. However, at $t = 1.5$ s when a single–phase load is connected to phase– A, only the active power output of phase–A increases. In a similar way, at $t = 2.5$ s when another single–phase load is connected to phase–C, only the output active power of phase–C increases. Similar results are monitored in all the DERs of the MG. From this figure, it can be seen that single phase load power demand is only supplied by the relevant single–phase of the DERs.

Fig. 5(b) shows the active power output of all DERs in their phase–A for the studied case. From this figure, it can be seen that for a load change in the network, the contribution level of each phase of the DERs in power generation is based on the desired sharing ratio among them.

Fig. 5(c) shows the active power supplied by the distribution transformer in the studied case. As the transformer is isolated at $t = 0.5$ s, then its output power is zero.

The three–phase instantaneous current output of a sample DER (e.g. DER–1) is shown in Fig. 5(d) which shows the DERs are generating unbalanced current in their output.

3.4. MG with harmonic loads

In this section, let us consider the MG of Fig. 1. Let us assume that the system is in steady state condition at $t = 0$ s and at $t = 0.5$ s, the grid is disconnected (i.e. CB_G is opened) and MG will work in autonomous mode.

At $t = 1.5$ s a new three–phase harmonic load of 3 kW is connected to the network. At $t = 2.5$ s, its demand is increased to 6 kW. The harmonic load has a Total Harmonic Distortion (THD) of 25.5% with the Fast Fourier Transform (FFT) Spectrum as shown in Fig. 6(a).

Fig. 6(b) shows the active power dispatch of grid and 3 existing DERs in the MG between 0 and 3.5 seconds in the above–mentioned network. The single–phase active power

output of one of the DERs (e.g. DER–1) is shown in Fig. 6(c). This is the same for all phases of A, B and C. The active power output in phase–A of all three DERs are also shown in Fig. 6(d). From these figures, it can be seen that as the load is supplied from the three–phase network, its demand is shared equally among the three phases of each DER. However, the DERs share the extra demand based on their desired power sharing ratio.

The three–phase instantaneous current output of a sample DER (e.g. DER–1) is shown in Fig. 6(e) which shows the output current of the DERs are distorted as required by the network harmonic load.

IV. CONCLUSION

The power management and control of converter–interfaced DERs were discussed in this paper, for autonomous operation of a MG with balanced, unbalanced and harmonic loads. In many MG related researches, the converter–interfaced micro sources are current controlled. In such a case, if the MG supplies unbalanced loads, detailed calculations are required to extract the required positive, negative and zero sequences of the reference current components for the DERs. Similarly, if the MG supplies nonlinear and harmonic loads, detailed calculations are required to extract the required different harmonic components for the current reference for the DERs. These calculations are later utilized for proper total power sharing, unbalanced power sharing and harmonic power sharing among DERs in the MG. However, if the converters are voltage controlled, there is no necessity for these complicated current reference extractions and calculations. In this paper, through several case studies, it was verified that proper total power sharing, harmonic power sharing and unbalance power sharing among DERs can be achieved through voltage controlled converters in the MG without any complicated current reference extractions for DERs.

Fig. 6. Simulation results of MG in case 4.4:

- (a) FFT Spectrum of harmonic load current,
- (b) Active power dispatch of grid and 3 DERs,
- (c) Single–phase active power output of DER–1,

(d) Active power output of all DERs in their phase–A,

(e) Three–phase instantaneous current output of a DER–1.

APPENDIX A

Power Circulation through Transformer

Let us consider the simple network illustrated in Fig. 7. A three–phase converter–interfaced DER in voltage control mode is connected to Bus–3 to supply a single–phase load in Bus–2 connected to phase–A. A three–phase DSTATCOM is installed in Bus–1 to regulate its PCC to a three–phase balanced voltage of 1 pu. Only the downstream of Δ /Y–Grounded transformer is connected while its upstream side is open–circuited. The Δ /Y– Grounded transformer can circulate the reverse fed current and power into one of its Y–grounded windings into its two other Y–Grounded windings due to its primary delta configuration.

First, let us assume that CB–1 is open and the DSTATCOM is not connected. In this case, the single–phase load will be supplied by i_{a1} and i_{a2} as

$$
i_{a1} = i_{ga} = \frac{v_{ga} - v_{load}}{\alpha Z}
$$

\n
$$
i_{a2} = i_{gb} + i_{gc} = \frac{v_{gb} - v_{load}}{Z + Z_{tr_eq} + (1 - \alpha)Z} + \frac{v_{gc} - v_{load}}{Z + Z_{tr_eq} + (1 - \alpha)Z}
$$
(10)

where i_{a1} is the phase–A current supplied from the generator side, i_{a2} is the phase–A current supplied from the transformer side, v_{ga} , v_{gb} and v_{gc} are the three–phase output voltage of the DER, i_{ga} , i_{gb} and i_{gc} are the three–phase current output of the DER, *vload* is the voltage at load PCC, *Z* is the feeder impedance between the buses, $0 < \alpha < 1$ and $Z_{tr,eq}$ is the equivalent impedance between two terminals of the transformer. From Fig. (7), we have

$$
\alpha Z < \left(Z + Z_{tr_eq} + (1 - \alpha)Z \right) \tag{11}
$$

Fig. 7. Simplified network under consideration.

Therefore, it is expected that $i_{a1} > i_{a2}$. Hence,

$$
i_{ga} > i_{gb} = i_{gc} \quad or \quad P_{ga} > P_{gb} = P_{gc} \tag{12}
$$

From (10), it can be seen that the amount of current (or power) supplied from DER in its phase–B and C are highly dependent on the equivalent impedance of the transformer and the line.

Now, let us assume that CB–1 is closed and the DSTAT-COM is regulating its PCC to 1 pu balanced voltage. In this case, i_{a1} will be same as in (10) while i_{a2} will be

$$
i_{a2} = \frac{1\angle\delta - v_{load}}{(1-\alpha)Z}
$$
 (13)

where δ is the angle of phase–A in Bus–1. Therefore, from (10) and (13), it can be seen that there will be always a small difference between i_{a1} and i_{a2} . The same difference will be reflected in i_{ga} , i_{gb} and i_{gc} in the output of the DER. Hence, it is expected that there will be a slight difference between P_{ga} compared to P_{gb} and P_{gc} . This difference is dependent on the distance between load and DER and transformer as well as the load demand.

APPENDIX B

Table I. Technical data of the network parameters of Fig. 1.

MV Network	11 kV L-L RMS, 50 Hz
MV Line Impedance	$R = 0.2 \Omega$, $L = 10 \text{ mH}$
LV Feeder	415 V L-L RMS, 50 Hz
LV Line Impedance	$R = 0.02 \Omega$, $L = 1$ mH
Transformer	30 kVA, 11 kV/ 415 V, Three–Phase, 50 Hz,
	\triangle Y-Grounded, $Zt = 5\%$
Balanced Three Phase Loads	$P = 2.7$ kW, $PF = 0.95$
Single Phase Load	$P = 2$ kW, $PF = 0.95$ in Phase A
Single Phase Load	$P = 4$ kW, $PF = 0.95$ in Phase B
DER VSCs and Filters	$R_f = 0.1 \Omega$, $L_f = 0.36 \text{ mH}$, $C_f = 50 \mu \text{F}$,
	$V_{dc} = 150$ V, $a = 3.33$, $h = 10^{-5}$
DSTATCOM VSC and	$R_f = 1$ m Ω , $L_f = 4$ mH, $C_f = 25$ μ F, $L_T = 10$
Filter	mH, $V_{dc} = 1$ kV, $C_{dc} = 2000$ µF, $a = 1$, $h = 10^{-5}$

Table II. Technical data of DERs and droop control coefficients in Fig. 1.

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