

Research Article

High Dynamic Optimized Carrier Loop Improvement for Tracking Doppler Rates

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Mathematical analysis and optimization of a carrier tracking loop are presented. Due to fast changing of the carrier frequency in some satellite systems, such as Low Earth Orbit (LEO) or Global Positioning System (GPS), or some planes like Unmanned Aerial Vehicles (UAVs), high dynamic tracking loops play a very important role. In this paper an optimized tracking loop consisting of a third-order Phase Locked Loop (PLL) assisted by a second-order Frequency Locked Loop (FLL) for UAVs is proposed and discussed. Based on this structure an optimal loop has been designed. The main advantages of this approach are the reduction of the computation complexity and smaller phase error. The paper shows the simulation results, comparing them with a previous work.

1. Introduction

PLL is used in many communication systems for synchronization and tracking of carrier. Use of PLL assisted FLL is very common in a lot of receivers for reducing locking time and avoiding false locks. The typical trade-off in tracking loop design is bandwidth versus dynamic performance. The effect of noise increase is increment of the loop bandwidth, while dynamic tracking error increment decreases the loop bandwidth [1].

Because speed of the UAVs is limited (against GPS and LEO that have high speed), therefore shift of the carrier frequency is almost small; hence the bandwidth of our tracking loop is narrow so it has two advantages, tracking ability of high dynamic carrier and the noise decreasing.

It is obvious that limit of the Doppler shift tracking has direct relationship with the loop bandwidth and bandwidth of the loop corresponds to the carrier bandwidth with attention to Doppler shift that should take it into account for the loop design.

Most of the above phenomena are related to the presence of the Doppler effects that shall be compensated at the receiver side. This puts forward higher requirements for the carrier tracking structure due to the high changing in carrier

frequency. The goal of this paper is the improvement of the tracking loop dynamic behavior proposing a new topology based on the modified third-order PLL assisted second-order FLL, which has a smaller phase error. This new structure has been simulated and the results are compared to the previous works. Reducing the phase error allows obtaining a faster tracking of the input signal. For designing the tracking loop, there are some methods such as two-term quadratic functional with weighing [2] or optimization process that poses trade-off between noise and bandwidth [3]. In this paper a new method based on the mathematical modification of the poles inside and outside the unitary circle is proposed.

2. Loop Structure

A typical communication front-end stage extracts the in-phase and quadrature components from the received signal. The NCO generates the local in-phase and quadrature reference signals at the sampling rate f_{samp} for coherent correlating. The output of the loop filter modifies the phase and frequency of the NCO every NT_{samp} .

The phase and the frequency of the NCO are modified every NT_{samp} period, with the same pace of the loop filter

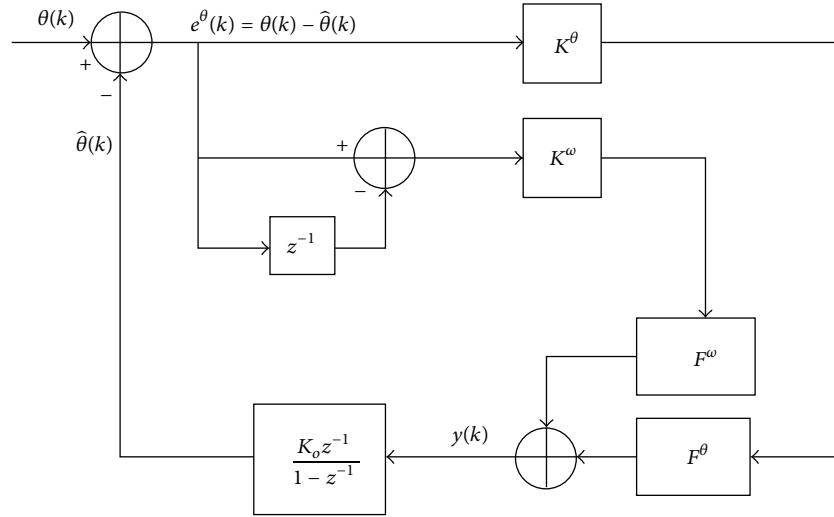


FIGURE 1: Structure of the third-order PLL assisted second-order FLL.

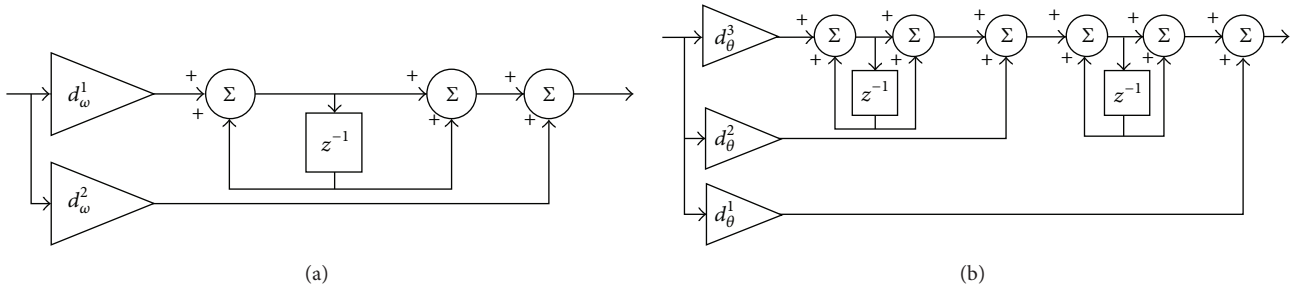


FIGURE 2: Block diagrams of (a) second- and (b) third-order digital loop filters excluding last integrator (the NCO).

output. The in-phase and quadrature components are then sent to the inputs of the tracking loops. The base-band linear model of the third-order PLL assisted second-order FLL tracking loop used in our work is shown in Figure 1. In this model, we introduced a differential block in the frequency error path for evaluating the frequency error. In the following sections, we will use the methods developed in [4, 5] for analyzing this structure. The analysis results will be used for the design of an optimized tracking loop. The in-phase and in-quadrature correlation of the received signal with the locally generated replicas are the inputs of the tracking loops [4]. In our analysis, the signals obtained from the front-end section can be expressed, for the k th sample, as

$$\begin{aligned} I_k &= A_k \cos(\varphi_k) + n_i[k], \\ Q_k &= A_k \sin(\varphi_k) + n_q[k], \end{aligned} \quad (1)$$

where $A_k = \sqrt{P} d_k R(\tau_k) \text{sinc}(\delta\omega_k T_L/2)$ is the results of correlation between the input signal and the NCO output, $\varphi_k = \delta\theta_k = \theta_k - \hat{\theta}_k$ is the phase estimation error, and $\delta\omega_k$ is the frequency estimation error. T_L is the integrations time and $R(\cdot)$ is the code correlation function. The phase discriminator

and the linear frequency discriminator have the following outputs:

$$\begin{aligned} e^\theta(k) &= \arctan\left(\frac{Q(k)}{I(k)}\right), \\ e^\omega(k) &= \arctan\left[2 \cdot \left(\frac{\text{Dot}(k)}{\text{Cross}(k)}\right)\right], \end{aligned} \quad (2)$$

where $\text{Dot}(k)$ and $\text{Cross}(k)$ can be written as [4]

$$\begin{aligned} \text{Dot}(k) &= I(k-1)I(k) + Q(k-1)Q(k), \\ \text{Cross}(k) &= I(k-1)Q(k) - I(k)Q(k-1). \end{aligned} \quad (3)$$

A mathematical model of the FLL and the PLL is shown in Figure 2.

In this paper combining both of the filters (Figure 2), applying some modifications (for finding suitable factors) [2] and optimizations (described in Section 4), we achieve the final tracking loop of Figure 3.

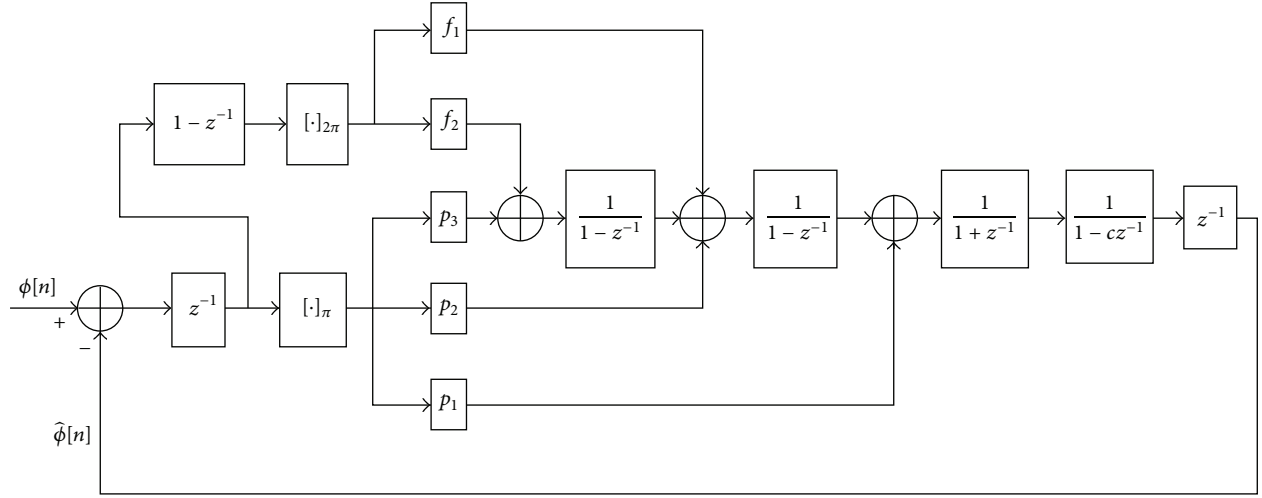


FIGURE 3: The structure of the optimum PLL for tracking Doppler rates.

Considering the transfer functions of the NCO, $N(z) = z^{-1}/(1 - z^{-1})$, and the loop filters, $F^\theta(z)$ and $F^\omega(z)$, the loop equations become [5, 6]

$$\begin{aligned} e^\omega(k) &= g^\omega[\varphi(k)], \\ e^\theta(k) &= g^\theta[\varphi(k)], \\ \hat{\theta}(k) &= N(z)y(k), \\ y(k) &= F^\theta(z)e^\theta(k) + D(z)F^\omega(z)e^\omega(k), \end{aligned} \quad (4)$$

where $y(k)$ is the error of the loop which also represents the NCO input.

The expressions for $D(z)$, $N(z)$, $F^\omega(z)$, and $F^\theta(z)$ are

$$\begin{aligned} D(z) &= 1 - z^{-1}, \\ N(z) &= \frac{K_o z^{-1}}{1 - z^{-1}}, \\ F^\omega(z) &= \left(d_\omega^1 + \frac{d_\omega^2}{1 - z^{-1}} \right) \cdot \frac{1}{1 - z^{-1}}, \\ F^\theta(z) &= d_\theta^1 + \frac{d_\theta^2}{1 - z^{-1}} + \frac{d_\theta^3}{(1 - z^{-1})^2}. \end{aligned} \quad (5)$$

3. Loop Performance

In this second-order FLL, the overall frequency transfer function, $H^\omega(z)$, can be expressed in terms of the loop filter, $F^\omega(z)$, the numerically controlled oscillator, $N(z)$, and the discriminator function, $D(z)$, obtaining [7, 8]

$$H^\omega(z) = \frac{K^\omega D(z) F^\omega(z) N(z)}{1 + K^\omega D(z) F^\omega(z) N(z)}. \quad (6)$$

Also, we have

$$\Phi(z) = \frac{\Theta(z)}{1 + K^\omega D(z) F^\omega(z) N(z)}, \quad (7)$$

where $D(z) = 1 - z^{-1}$ is the phase difference function. $\Theta(z)$ and $\Phi(z)$ are the z transformations of $\theta(k)$ and $\varphi(k)$. For the third-order PLL, the loop transfer function is [2, 8]

$$\Phi(z) = \frac{\Theta(z)}{1 + K^\theta F^\theta(z) N(z)}. \quad (8)$$

By expanding the function, we have

$$\begin{aligned} \Theta(z) (1 - 3z^{-1} + 3z^{-2} - z^{-3}) &= \Phi(z) [1 \\ &+ (G_1^\theta + G_2^\theta + G_3^\theta - 3)z^{-1} - (2G_1^\theta + G_2^\theta - 3)z^{-2} \\ &+ (G_1^\theta - 1)z^{-3}]. \end{aligned} \quad (9)$$

4. Loop Optimization

In this section, the optimization of the PLL assisted by FLL loop will be considered. The goal of this section is to find an optimal filter for the loop, able to optimize the dynamic behavior and the noise bandwidth.

Let us assume the following performance function [2, 6]:

$$Q = E[n_0^2(k)] + \alpha \sum_k e^2(k), \quad (10)$$

where $e(k) = \theta(k) - \hat{\theta}(k)$ is the deterministic part of the phase difference between the incoming and the generated phase. The parameter α is determined on the basis of noise bandwidth considerations:

$$E[n_0^2(k)] = \frac{1}{2\pi j} \oint_{|z|=1} \Phi_{\text{noise}}(z) \frac{1}{z} dz. \quad (11)$$

The noise part of (10) can be expressed in terms of the closed loop transfer function, $H(z)$. Φ_{noise} is the noise spectral density of noise n_0 and is related to the input noise spectral density by

$$\Phi_{\text{noise}}(z) = H(z) H(z^{-1}) \Phi_{\text{input}}(z). \quad (12)$$

The closed loop transfer function is

$$H(z) = \frac{F(z)N(z)}{1 + F(z)N(z)}. \quad (13)$$

Applying Parseval's equation in (10), we have

$$\sum_k [e^\theta(k)]^2 = \frac{1}{2\pi j} \oint_{|z|=1} [1 - H(z)][1 - H(z^{-1})] \cdot \Theta(z)\Theta(z^{-1}) \frac{1}{z} dz. \quad (14)$$

In this paper we rewrite the function that should be minimized $H(z)$ as the product of two new functions, $W(z)$ and $N(z)$,

$$H(z) = W(z)N(z). \quad (15)$$

For minimizing $H(z)$ we can minimize $W(z)$. To do this minimization, we used the method proposed in [9]. Applying some factorization to the equation derived by [9] and taking into account (11) and (14), (10) will be expressed:

$$Q = \frac{1}{2\pi j} \oint_{|z|=1} [\alpha\Phi_\theta(z) + P(z)W(z)W(z^{-1}) - \alpha W(z)N(z)\Phi_\theta(z) - \alpha W(z^{-1})N(z^{-1})\Phi_\theta(z)] \frac{1}{z} dz, \quad (16)$$

$$P(z) = [\Phi_{\text{input}}(z) + \alpha\Phi_\theta(z)]N(z)N(z^{-1}), \quad (17)$$

in which $\Phi_\theta(z) = \Theta(z)\Theta(z^{-1})$.

Let $W(z) = W_0(z) + \varepsilon\rho(z)$, where $W_0(z)$ is the optimal loop transfer function we are searching and $\varepsilon\rho(z)$ is the variation that should be minimized on the optimized $W_0(z)$. Substituting this equation into (16) and setting the variation of Q and ε to zero, we complete the standard vibrational procedure [6, 8]:

$$\begin{aligned} \frac{\partial Q [W_0(z) + \varepsilon\rho(z)]}{\partial \varepsilon} \Big|_{\varepsilon=0} &= \frac{1}{2\pi j} \\ &\cdot \oint_{|z|=1} [P(z)W(z)\rho(z^{-1}) + P(z)W(z^{-1})\rho(z) \\ &- \alpha\eta(z)N(z)\Phi_\theta(z) - \alpha\eta(z^{-1})N(z^{-1})\Phi_\theta(z)] \\ &\cdot \frac{1}{z} dz. \end{aligned} \quad (18)$$

The notation $[\cdot]_\pi$ indicates that the value $e(k) = \theta(k) - \hat{\theta}(k)$ is kept within the interval $(-\pi/2, \pi/2]$.

With the method proposed in [2], the transfer functions of the loop blocks are computed. However, in this paper we use a new method based on the separation of the poles to find the function of the loop blocks. In this way we reduce the amount of computation in comparison to the former works.

With assumption,

$$P(z) = P_{\text{in}}(z)P_{\text{out}}(z), \quad (19)$$

where all the poles in $P_{\text{in}}(z)$ are inside the unit circle while $P_{\text{out}}(z)$ has all the poles outside the unit circle.

So, we have from (18) that [4]

$$\begin{aligned} \frac{\partial Q [W_0(z) + \varepsilon\rho(z)]}{\partial \varepsilon} \Big|_{\varepsilon=0} &= \frac{1}{2\pi j} \oint_{|z|=1} \left\{ \rho(z^{-1}) \right. \\ &\cdot P_{\text{out}}(z) \left[P_{\text{in}}(z)W_0(z) - \lambda \frac{N(z^{-1})\Phi_\theta(z)}{P_{\text{out}}(z)} \right] \rho(z) \\ &\cdot P_{\text{in}}(z) \left[P_{\text{out}}(z)W_0(z^{-1}) - \lambda \frac{N(z)\Phi_\theta(z)}{P_{\text{in}}(z)} \right] \left. \right\} \\ &\cdot \frac{1}{z} dz. \end{aligned} \quad (20)$$

From (20), we can find that the poles in the first part are z_i and those in the second part are z_i^{-1} , and we have [6]

$$\oint f(z^{-1}) \frac{dz}{z} = \oint f(z) \frac{1}{z} dz. \quad (21)$$

Therefore we have

$$\begin{aligned} \frac{\partial Q [W_0(z) + \varepsilon\rho(z)]}{\partial \varepsilon} \Big|_{\varepsilon=0} &= \frac{1}{\pi j} \oint_{|z|=1} \left\{ \rho(z^{-1})P_{\text{out}}(z) \right. \\ &\cdot \left[z^{-1}P_{\text{in}}(z)W_0(z) - \alpha \frac{N(z^{-1})\Phi_\theta(z)}{zP_{\text{out}}(z)} \right] \left. \right\}. \end{aligned} \quad (22)$$

By putting (22) to zero, we obtain

$$W_0(z) = \frac{z \cdot [\alpha N(z^{-1})\Phi_\theta(z) / (z \cdot P_{\text{out}}(z))]_{\text{out}}}{P_{\text{in}}(z)}, \quad (23)$$

where $[\cdot]_{\text{out}}$ is defined such that all the poles are outside the unit circle. The corresponding optimal loop filter is

$$F(z) = \frac{W_0(z)}{1 - W_0(z)N(z)}. \quad (24)$$

Therefore

$$F(z) = \frac{a - bz^{-1} + cz^{-2}}{(1 - z^{-1})^3(1 + cz^{-1})}. \quad (25)$$

According to [2], loop factors can be computed. In our paper these factors are $a = 0.413$, $b = 1.354$, and $c = 0.612$.

5. Simulation Results

In this section the simulation results are shown. Phase error is simulated considering a step acceleration of 10 g (against [2] works with acceleration steps up to 40 g, the required computational burden is large since several simultaneous correlation calculations and FFT computations are needed [3]). Because our measurement time is very short (about 100 msec), in this time range it is possible to assume the motion path of the vehicle is a line.

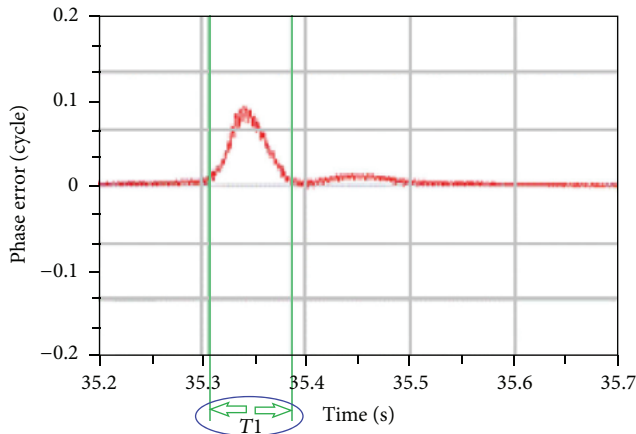


FIGURE 4: Phase error in proposed design during the 10 g steps.

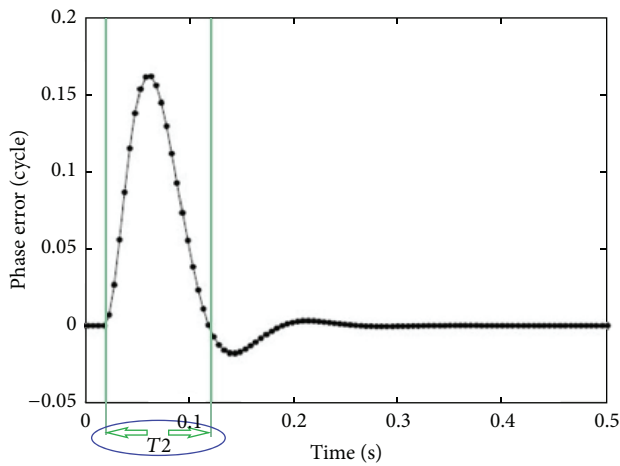


FIGURE 5: Phase error in [2].

The designed loop has been simulated by MATLAB. Figure 4 shows phase error simulation result for the designed loop. In our simulation, carrier frequency is 1580 MHz, its power is -110 dBm, $C/N_0 = 55$ dB/Hz, equivalent noise bandwidth is 60 Hz, and the UAV speed is up to 500 km/h. Comparing times T_1 of Figure 4 with T_2 of Figure 5 and T_3 of Figure 6 (that show the simulation results of [2] and [3], resp., for an identical experiment), we observe that our phase error width is almost 30–35% narrower (this corresponds to a shorter locking time) and its amplitude is about 35–45% smaller (this causes phase error that has less fluctuations after reaching the zero level).

6. Conclusion

The performance of a tracking loop for high dynamic Doppler rates consisting of a third-order PLL assisted second-order FLL is considered. An optimal loop filter design method with reduced calculation complexity was also proposed. In this work as first step the authors analyzed the structure of the modified third-order PLL assisted second-order FLL; then the analysis results are used for deriving an optimal tracking

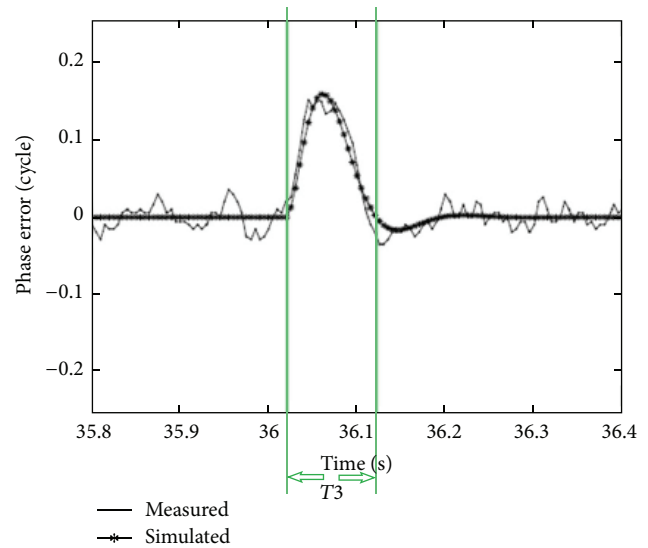


FIGURE 6: Phase error in [3].

loop. Simulation result confirms that a careful design of the loop has tracking capacity up to acceleration steps of 10 g, so as to reduce the computation complexity.

The main advantages of the proposed loop are (1) its simplicity for a digital (FPGA) implementation and (2) a better performance with respect to similar solution proposed previously.

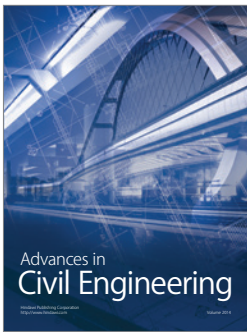
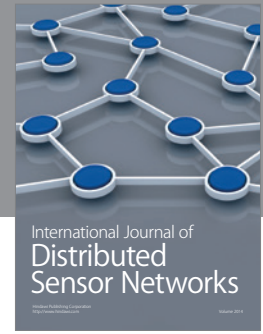
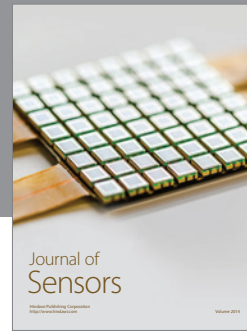
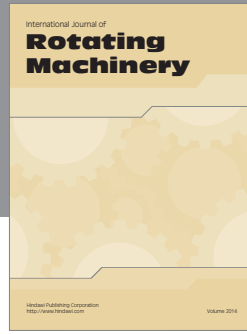
Conflict of Interests

The authors declare that there is no conflict of interests regarding the publication of this paper.

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