

## *Achieving of intensified conductive interconnections for Flex-on-Flex by using metal passivated Copper – Copper Thermocompression bonding*

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**Abstract**— There is a gradual increase in demand for flexible electronics due to the way it is going to empower the end user - to bent, roll/fold and arrange randomly in 3-D space, the devices without sacrificing the performance and reliability of devices, in a trend focused towards ever shrinking device footprint area. One of the prime mover towards realizing flexible electronics is interconnect scaling, which is motivating us to move towards three-dimensional interconnect integration. In this paper, we propose the interconnection of two flexible-flexible substrates by Thermocompression bonding between copper-to-copper, with their surface passivated with palladium, at low temperature and low pressure, to overcome the limitations imposed by conventional anisotropic conductive film (ACF) and nonconductive paste (NCP) approaches. The enhancement in interdiffusion of atomic species between the two surfaces, at low temperature and pressure, is possible only with unoxidized surface which has low RMS roughness or with surface which has varied film density. We have systematically optimized the thickness of palladium passivation layer for high quality Cu-Cu bonding, at low temperature of 140 °C and at low pressure of 5 bar. 2.06 nm RMS surface roughness was observed for Palladium passivation layer when its thickness was optimized at 5 nm, which made bonded Cu-Cu junction interface free from any copper oxide, as it was confirmed by X-ray Diffraction (XRD) and Energy Dispersive Spectroscopy (EDS) analysis. To confirm the quality of bonded interface, the samples were subjected to stress by folding just after Thermocompression bonding. Absence of any voids between the interfaces during Scanning Acoustic Microscopy (SAM) imaging confirms the superior quality of Cu-Cu bonding. The bonded interface image from cross-sectional Scanning Electron Microscopy (X-SEM) further reaffirms the quality of interface. Besides, very low contact resistance of  $\sim 2 \text{ E-7 } \Omega \cdot \text{cm}^2$  obtained for a fabricated daisy chain pattern with 70  $\mu\text{m}$  pitch and 100  $\mu\text{m} \times 100 \mu\text{m}$  bonding contact, in addition, confirms the good quality of bonding interface. The demonstrated bonding approach with metal passivated interconnect technique will be one of the prime contestant for future high bandwidth applications.

**Keywords**—Thermocompression bonding, surface passivation, RMS surface roughness, atomic inter diffusion.

### I. INTRODUCTION

There will a greater need of flexible and bendable electronics in the future, for wearable and Internet-of-Things

(IoT) applications, for RFIDs, sensors, displays and memory devices, to interconnect every object in the surrounding in a single network in a most cost effective manner [1].

These fascinating services would be possible only with very high circuit functionality, when they are being integrated on flexible substrate by incorporating diverse electronic circuits with devices. Traditionally, due to the absence of advanced technology to make direct electronic devices on flexible substrate, it has evolved from heterogeneous package integration of dice with flexible substrate, called Chip-on-Flex (COF) or die embedded in flex, known as Chip-in-Flex (CIF), by using either anisotropic conductive film (ACF) [2] or non-conductive paste (NCP) approaches. In conventional ACF technique, polymer resins with conductive particles are used in between the interconnections, which provides the electrical paths in the respective package [3]. Whereas in NCP approach, a non-conductive epoxy resins without conductive particles are used for die interconnections on flexible substrate, for high mechanical strength and stable contact resistance, besides providing high peel strength over the greater temperature range. The main limitations of these conventional approaches are the need for critical curing processes, or else, there will be a severe reliability issues, and the difficulty to scale-down interconnect pitches, which acts as a major bottleneck towards attaining a high density interconnects between substrates.

As ACF uses resins with conductive particles, thus special attention has to be given in selecting conductive particle density. If the particle density is higher, the interlayer short circuits the interconnects, or else interlayer resistance will be higher. One more crucial factor for higher interconnect density with ACF is size of conductive particles. In the NCP approach, there is a need of making the large contact area for higher reliability. Because of all these limitations in the conventional approaches, flexible interconnection packaging seems to be the most viable alternative. Flex-On-Flex (FOF) interconnection approach is one of the most cost-effective alternative towards realizing the next generation interconnect technology. FOF interconnection by three-dimensional integration will be an excellent choice for achieving high interconnect density with flexible hybrid assemblies [4].

To make these interconnections between the substrates, the conventional mechanism is metallic bonding, whereas in

case of ACF and NCP approaches, the mounting of dies or substrates having metallic bump in accordance with another substrate with a flexible interlayer resin is needed, the limitations of which has already been discussed earlier.

One of the appreciable solution for this is direct metallic bonding at low temperature. K.N. Chen and his group has developed eutectic based flex-on-flex bonding [5], in which there is a possibility of formation of intermetallic compound at the junction interface. These intermetallic compounds are very well known for their brittle nature, which may degrade the conductivity of inter-connections. Therefore, there is an overwhelming necessity to look for some suitable alternatives that will provide excellent interconnect with very high interconnect conductivity and with greater reliability, realizing which is a great challenge in itself.

Best economical solution towards this will be, to use copper for direct metallic interconnections [6]. There is a need for effective interdiffusion of copper atoms to make higher grains. Proper interdiffusion of copper atoms at the surface is a major challenge, which is limited by two factor, first is the formation of oxide compounds on the copper surface during bonding and second is the requirement for nanometer of surface roughness at the top surface [8].

In this work, we proposed an approach, by which it is possible to make direct metal-to-metal (Cu-Cu) bonding interconnections by inter-diffusion of metallic atom in between the flexible substrates using another metal as a passivation layer, thickness of which is optimized for achieving the low temperature and low pressure Thermocompression bonding.

## II. THE BONDING MECHANISM AND BONDING PROCESS

### A. Optimization of Passivation layer thickness of Pd layer

In the case of thermocompression bonding, as mentioned above, one of the major bottle neck is surface roughness. If the surface roughness is more, higher bonding pressure will be required due to uneven troughs/crests on the Cu surface. Thus, passivation material have dual role to play, firstly, it has to protect the Cu surface from getting oxidized as informed previously apart from providing [9] smooth surface with very minimal RMS roughness. Hence, thickness of palladium will play a key role in achieving good surface passivation in protecting copper surface from getting oxidized towards achieving better Cu-Cu bonding interface and lower interface contact resistance. Since the resistivity of Pd is one order higher than Cu, therefore, thicker Pd layer, may affect the contact resistance at the bonding interface. Hence, there is a need for optimizing the thickness of palladium passivation layer for high quality and reliable Cu-Cu bonding interface, for multilayer integration.

To evaluate surface roughness of the passivated Cu film, we deposited Pd passivation film multiple times, each time varying the Pd thickness from 1 nm to 7 nm, over Cu surface (200 nm). The surface roughness of the prepared samples were then determined using Atomic Force

Microscope (AFM) under tapping mode with sample scan are of 5  $\mu\text{m}$ .

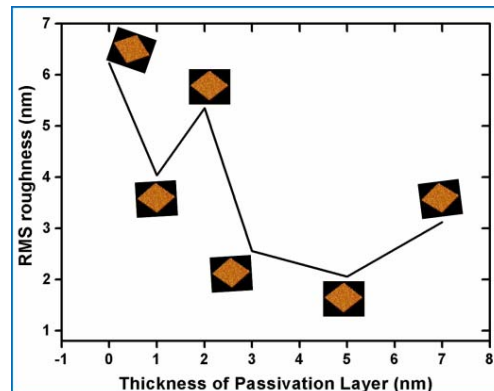


Figure 1. Deviation of RMS roughness on Cu surface with varying Palladium (Pd) passivation layer.

Figure 1, shows variation of RMS roughness of Pd passivated Cu substrates with varying Pd film thicknesses. Passivated Cu surface with 5 nm Pd ultrathin film has the lowest RMS roughness of 2.06 nm, which is comparatively much lower than other passivated Cu samples.

After finding out minimal surface roughness with 5 nm passivation layer on Cu surface, the effectiveness of optimized layer to be tested in such a way that can it will be able to compensate the surface oxide prevention or not, to observe this issue, has been explained in preponing analysis of copper surface.

### B. Bonding Mechanism and Pre Bonding analysis of copper surface

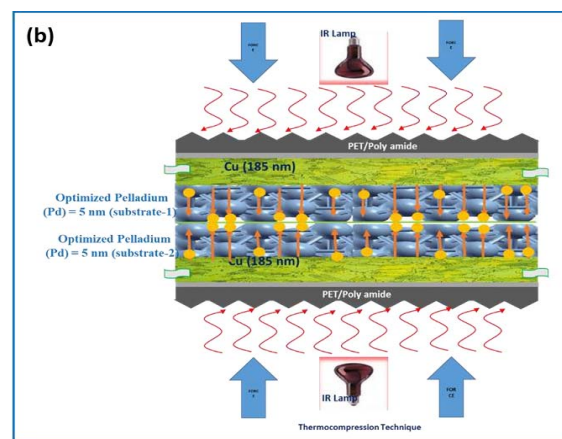


Figure 2. The bonding process in passivated thermocompression cycle.

Palladium has been used in this method as an optimized thick passivation material due to its high temperature oxidation resistive properties [10], relatively good electrical conductivity at room temperature. Apart from these, palladium has comparably low density than Cu and it has

higher atomic radius of  $1.37 \text{ \AA}$  respectively than atomic radius of pure Cu ( $1.28 \text{ \AA}$ ). This is one of the key aspect that tells us about the surface compressive stress by the higher atomic radius atoms, on the copper surface makes easier for inter diffusion. In addition, Cu is having lower activation energy at the surface [11]. Hence, Cu can easily diffuse through palladium by applying of temperature and pressure while thermocompression bonding and interdiffusion and surface stresses with themocompersion boding process as shown in Figure 2. From this due to surface stresses and having the path for the interdiffusion of copper atoms, by applying the inadequate pressure and temperature makes the surface atoms causes the moving towards each other and make the sophisticated grains at the interface caused the high strength interconnects with the high reliable and effective conductive paths.

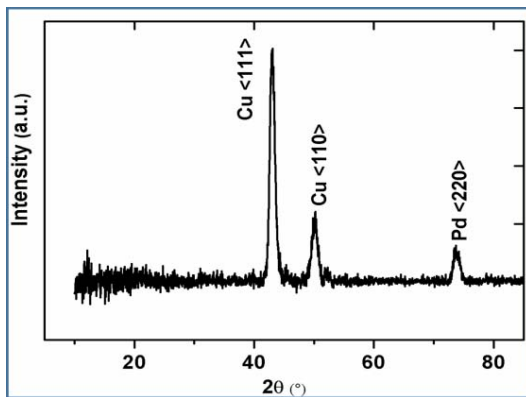
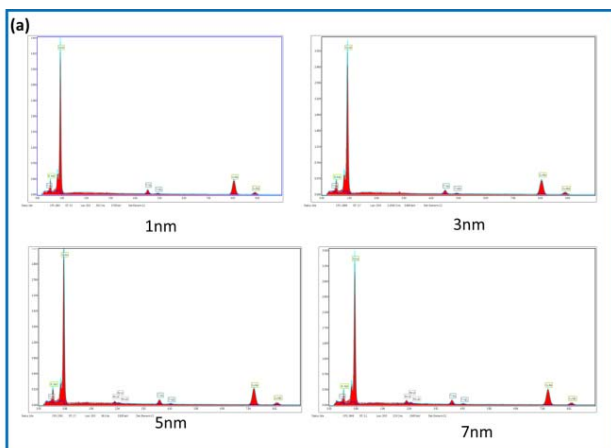


Figure 3. Diffraction pattern of the palladium passivated Cu surface. Reflection of pure Cu  $\langle 111 \rangle$  orientation plane is an indicative of diffusion enhancement.

Further, surface diffusion coefficient at the bonding interface plays vital role in enhancement of interdiffusion. Surface diffusion coefficient of  $\langle 111 \rangle$  oriented Cu plane is 3 to 4 orders of higher magnitude compared to other oriented Cu plane at the bonding interface [12]. In order to evaluate the preferable Cu plane orientation at the surface of an optimized layer of passivated Cu surface was subjected to same thermal stress as that of bonding conditions and subsequent Glancing angle X-Ray Diffraction (GI-XRD) was performed. Figure 3, confirms the reflections of pure Cu  $\langle 111 \rangle$  plane along with frail Cu  $\langle 110 \rangle$  and Pd  $\langle 220 \rangle$ .



From the observation of XRD, we got the conformation that for an optimized thickness of Pd layer of 5 nm acted as a surface oxide barrier for copper. This enhanced diffusion caused by surface stress and high surface diffusion coefficient enables low temperature and low pressure high quality bonding.

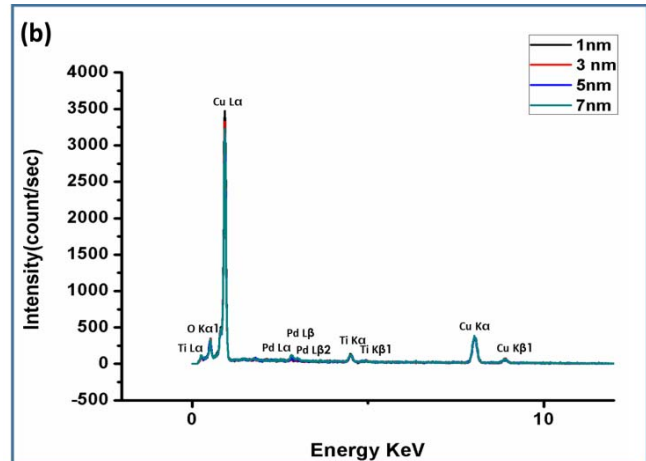


Figure 4. (a) EDS analysis of passivated samples (b) EDS comparison plot

To conform the surface oxide on the copper surface we have been performed the energy dispersive X-Ray spectroscopy (EDS) by using the scanning electron microscopy (SEM) for the samples with ultrathin Pd passivation layer of 1, 3, 5 and 7 nm as shown in Figure 4(a). We observed the reduced oxygen on the sample on the 5 nm ultrathin Pd passivated copper film, we observed the common oxide peak on each sample because of chamber ambient oxide in instrument. The comparison plot of EDS profile of the samples are shown in the Figure 4(b).

### C. Experimental

Here we have taken the clean  $5.5 \text{ cm} \times 5.5 \text{ cm}$  square samples of flexible substrates (PET/ Kapton Polyamide) for the face to face Cu/Pd-Pd/Cu blanket bonding. Initially flex films were cleaned with acetone, via ultrasonicator for 5 mins, followed by isopropanol alcohol (IPA), followed by bake, up to 2 mins at  $100^\circ\text{C}$  in order to retain the plane surface and complete removal of wet content present on the substrates. Then on the cleaned flex surfaces, thin film deposition was carried by 20 nm of Ti as an adhesive layer for copper on flex, 200 nm of Cu layer deposition followed by optimized palladium passivation layer of 5 nm. Then the samples carried for the bonding.

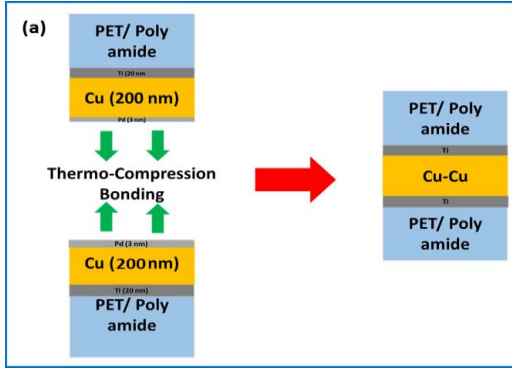


Figure 5. The bonding mechanism

Bonding was performed inside the alignment Wafer Bonder (AML-AWB, UK). The thin film deposited flexible substrates kept face-to-face and closed the chamber, before the creation of vacuum inside the bonding chamber. Once the vacuum created up to  $5 \times 10^{-5}$  torr, then the heater was turned on. Once the temperature reached to  $140^\circ\text{C}$ , we applied a contact force of 4kN was applied for 50 mins. Then the bonded flexible films are kept steady inside the bonding chamber in order to cool down to room temperature. Figure 5, briefly describes the schematic of bonding process using thermocompression cycle. Then various post bonding characterizations were performed in order to evaluate the bonding quality which discussed briefly in the further results and discussion section.

### III. RESULTS AND DISCUSSION

#### A. Bonding interface by bending angle analysis.

The bonded flexible substrates, can be expected for the bending angles, to test the quality of bond we have tried manually to bend up to  $\sim 45^\circ$  bending of bonded samples. We observed the good bonding quality and we used razor blade test at the inter face to peel-off the bonded samples, but we observed the good bonding at the interface.

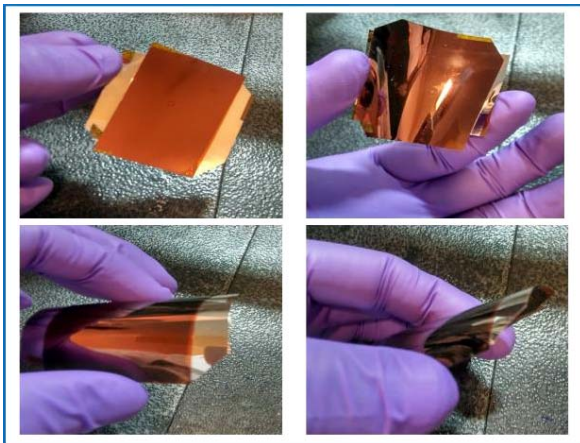


Figure 6. Testing of bonding quality of flexible substrates by bending.

#### B. Bonding interface with Scanning acoustic microscopy observation

The copper bonded samples on flexible substrates with 5 nm passivated samples are subjected to scanning acoustic microscopy (SAM) as shown in the Figure 7. To perform this, we have taken  $5.5\text{cm} \times 5.5\text{cm}$  bonded samples in Figure 7(a), has attached to stage in Figure 7(b) with surrounded tape to protect the damaging of sample under water and we kept it in water tub having acoustic transceiver shown in Figure 7(c) from D9600 C-SAM, and we went for imaging using appropriate peak. We observed the interface is a void free and high efficient bonding quality as shown in Figure 7(d)

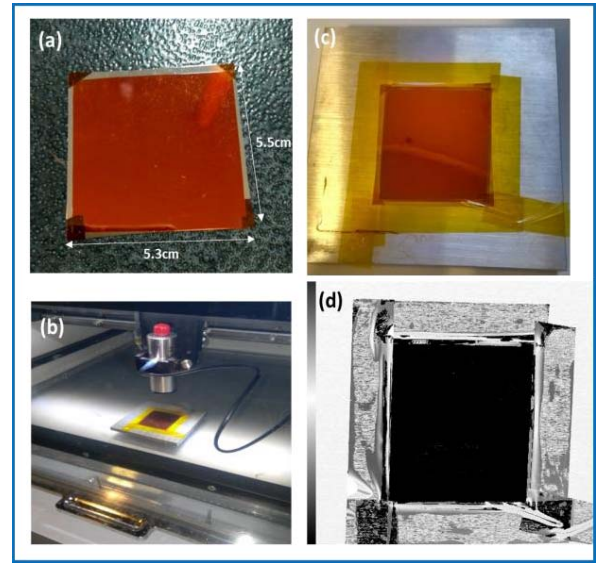


Figure 7. SAM analysis of bonded samples

#### C. Microstructure imaging of the bonded substrates

Cross-sectional imaging analysis basically useful for the assessment of bonded intermediate interface layer excellence. Undesirable intermediate layer or uneven voids at the interface reduce the bonding quality. In order to identify the presence of intermediate layers and voids, Field Emission-Single Electron Microscopy (FE-SEM, ZEISS) was carried out. Figure 8, shows the cross section FE-SEM image of the bonded layer. The picture as shown is confirms the success of Cu-Cu bonding with optimized palladium passivation. The presence of fine bonding between two Cu films with palladium passivation is a proof for high quality bonding.

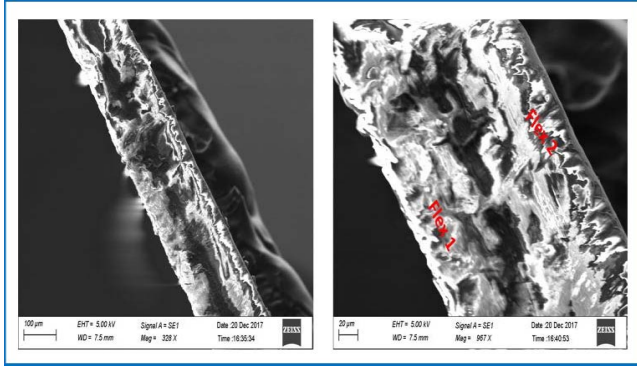


Figure 8. FE-SEM analysis of bonded interface.

#### D. Electrical analysis.

The electrical characterization has been performed using modified kelvin structure as shown in Figure 9.

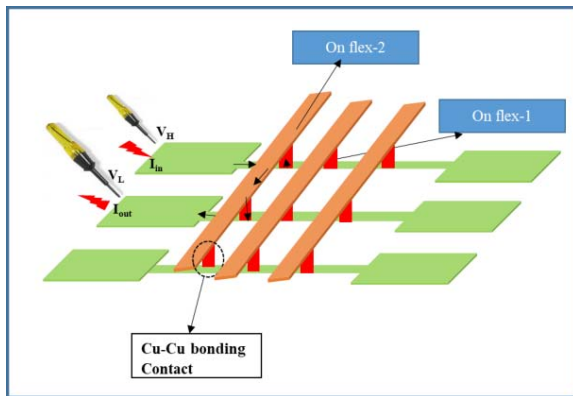


Figure 9. Modified kelvin structure.

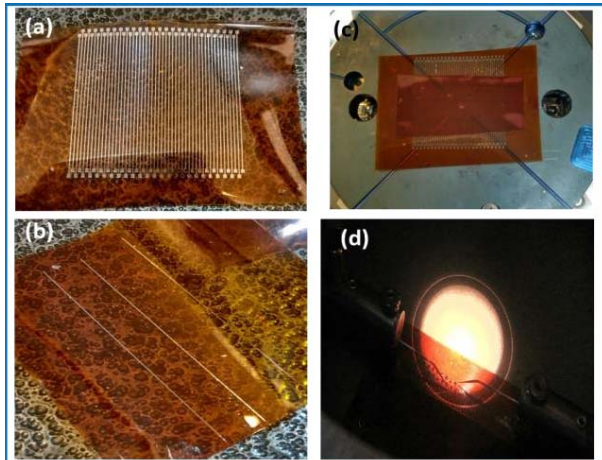


Figure 10. (a) Contact lines with pads on flex1 (b) contact lines on flex 2 (c) Bonded sample (d) bonded sample under test.

Here we have fabricated 70  $\mu\text{m}$  pitch of contact lines with bond pads on one flexible substrate with 100  $\mu\text{m}$  width shown in Figure 10(a), and 100  $\mu\text{m}$  width of metallic lines were fabricated as shown in Figure 10(b) on one more flexible substrate by using the lithographic process, after ultrasonic cleaning in acetone followed by drying and followed by spin coating of S1813 photoresist, followed by writing with laser beam with laser power of 812  $\text{mJ}/\text{cm}^2$ .

After writing the patterns we kept the sample in acetone for 5 hours, for lift off. Then we have done ultrasonication for 10 sec. we find the good patterns on flexible substrate. After sample preparation, we went for bonding we have done it for 50 min at 3.5 kN and take out the sample for electrical characterization using Keithley 4200A-SCS Parameter Analyzer, and we applied the voltage and respective current readings the specific contact resistivity has been found at plane condition and 30° bending is  $\sim 2\text{E}-7 \Omega\text{-cm}^2$ . This high conductive inter connects are novel mothed for motivating future flex on flex interconnections.

#### IV. CONCLUSION

Flex-on-Flex Cu-Cu thermocompression bonding was successfully demonstrated at 140°C with nominal contact force of 4 kN, using optimally chosen ultra-thin Palladium passivation layer of 5 nm thickness. The proposed bonding technology is suitable for Flex-on-Chip (FOC) and Flex-in-Chip (FIP) applications as-well-as for high bandwidth applications like IoT built on Flex-on-Flex technology in a cost effective manner. Furthermore, SAM imaging of Flex-on-Flex bonded sample just after bending test, shows a void free interface, which confirms good quality interface, as also confirmed by X-SEM analysis. In addition, modified Kelvin daisy-chain analysis of passivated bonded structure provides specific contact resistance of  $\sim 2.0\text{E}-7 \Omega\text{-cm}^2$ , for normal and bending conditions. This suggests excellent contact stability without any electrical performance degradation. It is hoped that the presented result will pave the way for future flexible high conductive interconnections.

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