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Research Article Analytical Model of Random Variation in Drain Current of FGMOSFET

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The analytical model of random variation in drain current of the Floating Gate MOSFET (FGMOSFET) has been proposed in this research. The model is composed of two parts for triode and saturation region of operation where the process induced device level random variations of each region and their statistical correlations have been taken into account. The nonlinearity of floating gate voltage and dependency on drain voltage of the coupling factors of FGMOSFET have also been considered. The model has been found to be very accurate since it can accurately fit the SPICE BSIM3v3 based reference obtained by using Monte-Carlo SPICE simulation and FGMOSFET simulation technique with SPICE. It can fit the BSIM4 based reference if desired by using the optimally extracted parameters. By using the proposed model, the variability analysis of FGMOSFET and the analytical modeling of the variation in the circuit level parameter of any FGMOSFET based circuit can be performed. So, this model has been found to be an efficient tool for the variability aware analysis and design of FGMOSFET based circuit.

1. Introduction

FGMOSFETs have been extensively utilized in various analog/digital circuits such as [1–9]. Similarly to the MOSFET based circuits, the performances of the FGMOSFET based circuits have been deteriorated by process induced device level random variations [10–12]. This is because these device level variations yield random variations in circuit level parameters, for example, drain current (I_D) and transconductance. These variations yield variations in parameters of FGMOSFET based circuit such as transconductance of the FGMOSFET based voltage to current converter [1] and equivalent resistance of the FGMOSFET based voltage controlled resistor [2, 3]. For handling this issue, variability aware analysis/design concept has been applied in the designing of many FGMOSFET based circuits, for example, [4–6].

Similarly to MOSFET, I_D has been found to be the key circuit level parameter of FGMOSFET as it is directly measurable and can be the basis for determining the others. According to its importance, the analytical models of process induced random variation and mismatch in I_D of MOSFET have been proposed without regard to certain circuit in many previous researches, for example, [13–15]. So, their

results are applicable to any MOSFET based circuit. For the FGMOSFET, most of the previous work has been oriented to certain FGMOSFET circuits, for example, [16–19]. Some previous studies have not been devoted to any circuit, for example, [20, 21]. However, the results of these works are incomplete since the statistical correlations between device level random variations have been neglected. Moreover, nonlinearity of floating gate voltage and dependency on drain voltage of the coupling factors, which are the important features of FGMOSFET [22], have not been considered.

So, the analytical model of process induced random variation in I_D (ΔI_D) of the FGMOSFET has been proposed in this research. The proposed model is composed of two parts for triode and saturation regions of operation where the process induced device level random variations of each region and their statistical correlations have been taken into account. The nonlinearity of floating gate voltage and dependency on drain voltage of the coupling factors of FGMOSFET have also been considered. This model has been formulated without regard to any circuit. So, it is applicable to all FGMOSFET based circuits. It has been found to be very accurate since it can fit the SPICE BSIM3v3 based reference obtained by using FGMOSFET simulation technique with SPICE [23] and

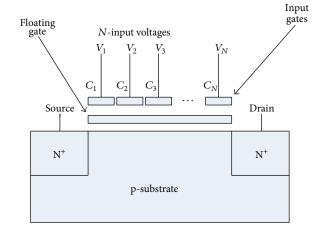


FIGURE 1: A cross-sectional view of N-type N inputs FGMOSFET [2].

Monte-Carlo SPICE simulation with very high accuracy. If desired, it can fit the BSIM4 based reference by applying the optimum parameters extracted by using the optimization algorithm [24]. The variability analysis of FGMOSFET and the analytical modeling of process induced random variation in circuit level parameter of any FGMOSFET based circuit can be performed by using the proposed model as the basis. Hence, this model has been found to be an efficient tool for the variability aware analysis/design of any circuit involving FGMOSFET.

2. The Overview of FGMOSFET

FGMOSFET is a special type of MOSFET with an additional gate isolated within the oxide, namely, the floating gate [2]. A cross-sectional view of an N-type FGMOSFET with N inputs implemented as N discrete input gates where N > 1 can be shown in Figure 1.

It should be mentioned here that the dimension (width and length) of any input gate determines the magnitude of its corresponding input capacitance. The symbol and equivalent circuit of N inputs FGMOSFET are shown in Figure 2. Such equivalent circuit is composed of a MOSFET, N input capacitances $(C_1, C_2, C_3, ..., C_N)$, overlap capacitance between floating gate and drain $(C_{\rm fd})$, overlap capacitance between floating gate and source $(C_{\rm fs})$, and parasitic capacitance between floating gate and substrate $(C_{\rm fb})$ [2].

Let $\{i\} = \{1, 2, 3, ..., N\}$ and let any *i*th input capacitance be denoted by C_i ; the floating gate voltage, V_{FG} , can be given by [2]

$$V_{\rm FG} = \frac{\sum_{i=1}^{N} \left[C_i V_i \right] + C_{\rm fd} V_D + C_{\rm fs} V_S + C_{\rm fb} V_B}{C_T}, \qquad (1)$$

where V_i is the input voltage at any *i*th input gate, V_D is the drain voltage, V_S is the source voltage, and V_B is the bulk

voltage. Moreover, C_T denotes the total capacitance of the floating gate which can be defined as [2]

$$C_T = \sum_{i=1}^{N} \left[C_i \right] + C_{\rm fd} + C_{\rm fs} + C_{\rm fb}.$$
 (2)

Let k_i , k_{fd} , k_{fs} , and k_{fb} denote the coupling factor of any *i*th input gate, drain, source, and bulk and let them be defined as $k_i = C_i/C_T$, $k_{fd} = C_{fd}/C_T$, $k_{fs} = C_{fs}/C_T$, and $k_{fb} = C_{fb}/C_T$, respectively; V_{FG} can be alternatively given as follows:

$$V_{\rm FG} = \sum_{i=1}^{N} \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fs} V_S + k_{\rm fb} V_B.$$
(3)

From either (1) or (3), it can be seen that V_{FG} depends on V_i , V_D , V_S , and V_B . According to [22], V_{FG} of FGMOSFET in triode is a nonlinear function of V_D and C_T depends on V_D as C_{fb} does. So, k_i , k_{fd} , k_{fs} , and k_{fb} are dependent on V_D since they are functions of C_T . For FGMOSFET in saturation region, C_{fb} is constant and so are C_T , k_i , k_{fd} , k_{fs} , and k_{fb} . As a result, V_{FG} become a linear function of V_D .

3. Formulation of the Proposed Model

3.1. Triode Region Part. Firstly, I_D of FGMOSFET in triode region will be formulated by letting the gate to source voltage $(V_{\rm GS})$ in the analytical model of I_D of MOSFET in such region be replaced by $V_{\rm FG} - V_S$. The second order effects such as mobility degradation and short channel effect have been taken into account for making the model applicable to deeply scaled technology. In order to do so, the linear model of mobility degradation [15] has been adopted. Hence, I_D of the MOSFET in triode can be given by

$$I_{D} = \beta \left[1 - \theta \left(V_{GS} - V_{TH} \right) \right] \left[\left(V_{GS} - V_{TH} \right) V_{DS} - \frac{1}{2} V_{DS}^{2} \right],$$
(4)

where V_{DS} , V_{TH} , β , and θ denote drain to source voltage, threshold voltage, current factor, and mobility degradation coefficient, respectively.

Since $V_{\rm FG}$ of FGMOSFET in this region is a nonlinear function of V_D , $V_{\rm FG}$ can be given by

$$V_{\rm FG} = \sum_{i=1}^{N} \left[k_i \left(V_D \right) V_i \right] + k_{\rm fd} \left(V_D \right) V_D + k_{\rm fs} \left(V_D \right) V_S + k_{\rm fb} \left(V_D \right) V_B,$$
(5)

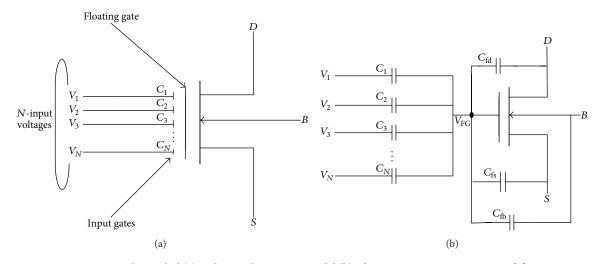


FIGURE 2: The symbol (a) and equivalent circuit model (b) of N-type N inputs FGMOSFET [2].

where $k_i(V_D)$, $k_{fd}(V_D)$, $k_{fs}(V_D)$, and $k_{fb}(V_D)$, which are, respectively, V_D dependent k_i , k_{fd} , k_{fs} , and k_{fb} , can be modeled as power series of V_D as follows:

$$k_i \left(V_D \right) = \sum_{m=0}^{\infty} \left[\alpha_{im} V_D^m \right], \tag{6}$$

$$k_{\rm fd}\left(V_D\right) = \sum_{m=0}^{\infty} \left[\alpha_{dm} V_D^m\right],\tag{7}$$

$$k_{\rm fs}\left(V_D\right) = \sum_{m=0}^{\infty} \left[\alpha_{sm} V_D^m\right],\tag{8}$$

$$k_{\rm fb}\left(V_D\right) = \sum_{m=0}^{\infty} \left[\alpha_{bm} V_D^m\right],\tag{9}$$

where α_{im} , α_{dm} , α_{sm} , and α_{bm} are coefficients of the power series representation of $k_i(V_D)$, $k_{fd}(V_D)$, $k_{fs}(V_D)$, and $k_{fb}(V_D)$, respectively.

As V_D can be very low in recent CMOS technology, high order terms of $k_i(V_D)$, $k_{\rm fd}(V_D)$, $k_{\rm fs}(V_D)$, and $k_{\rm fb}(V_D)$ can be neglected. So, (6)–(9) become (11)–(13) where α_{i0} , α_{d0} , α_{s0} , and α_{b0} and α_{i1} , α_{d1} , α_{s1} , and α_{b1} are α_{im} , α_{dm} , α_{sm} , and α_{bm} with m = 0 and m = 1:

$$k_i \left(V_D \right) = \alpha_{i0} + \alpha_{i1} V_D, \tag{10}$$

$$k_{\rm fd} \left(V_D \right) = \alpha_{d0} + \alpha_{d1} V_D, \tag{11}$$

$$k_{\rm fs}\left(V_D\right) = \alpha_{s0} + \alpha_{s1}V_D,\tag{12}$$

$$k_{\rm fb}\left(V_D\right) = \alpha_{b0} + \alpha_{b1}V_D. \tag{13}$$

By using (4), (10)–(13), and the methodology stated above, I_D of the triode region operated FGMOSFET is given by (14) as $\alpha_{s0} + \alpha_{s1}V_D \ll 1$:

$$\begin{split} I_{D} &= \beta \left[1 - \theta \left(\sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right. \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \left(\alpha_{b0} + \alpha_{b1} V_{D} \right) V_{D} - V_{S} \\ &- V_{\text{TH}} \right) \right] \left[\left(\sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right. \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \left(\alpha_{b0} + \alpha_{b1} V_{D} \right) V_{D} - V_{S} \\ &- V_{\text{TH}} \right) V_{\text{DS}} - \frac{1}{2} V_{\text{DS}}^{2} \right]. \end{split}$$
(14)

It can be seen from (14) that the process induced device level random variations of FGMOSFET in triode are random variations in V_{TH} , α_{i0} , α_{i1} , α_{d0} , α_{d1} , α_{b0} , α_{b1} , β , and θ denoted by ΔV_{TH} , $\Delta \alpha_{i0}$, $\Delta \alpha_{i1}$, $\Delta \alpha_{d0}$, $\Delta \alpha_{d1}$, $\Delta \alpha_{b0}$, $\Delta \alpha_{b1}$, $\Delta \beta$, and $\Delta \theta$, respectively. So, ΔI_D of FGMOSET in triode region can be given by (15) where all derivatives given by (16)–(24) can be found by using (14):

$$\Delta I_{D} = \left(\frac{\partial I_{D}}{\partial V_{\text{TH}}}\right) \Delta V_{\text{TH}} + \left(\frac{\partial I_{D}}{\partial \beta}\right) \Delta \beta + \left(\frac{\partial I_{D}}{\partial \theta}\right) \Delta \theta$$
$$+ \left(\frac{\partial I_{D}}{\partial \alpha_{d0}}\right) \Delta \alpha_{d0} + \left(\frac{\partial I_{D}}{\partial \alpha_{d1}}\right) \Delta \alpha_{d1} + \left(\frac{\partial I_{D}}{\partial \alpha_{b0}}\right) \Delta \alpha_{b0}$$
$$+ \left(\frac{\partial I_{D}}{\partial \alpha_{b1}}\right) \Delta \alpha_{b1} + \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{i0}}\right) \Delta \alpha_{i0} \right]$$
$$+ \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{i1}}\right) \Delta \alpha_{i1} \right],$$
(15)

$$\begin{aligned} \frac{\partial I_{D}}{\partial V_{\text{TH}}} &= \frac{\beta}{2} V_{\text{DS}} \left[4\theta \left(\alpha_{b1} V_{B} V_{D} + \alpha_{b0} V_{B} \right. \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) \end{aligned} (16) \\ &- \theta \left(4 \left(V_{S} + V_{\text{TH}} \right) + V_{\text{DS}} \right) - 2 \right], \end{aligned} \\ \begin{aligned} \frac{\partial I_{D}}{\partial \beta} &= \left[1 - \theta \left(\sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right. \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \left(\alpha_{b0} + \alpha_{b1} V_{D} \right) V_{D} - V_{S} \right. \\ &- V_{\text{TH}} \right) \right] \left[\left(\left(\sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right. \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \left(\alpha_{b0} + \alpha_{b1} V_{D} \right) V_{D} - V_{S} \right. \\ &- V_{\text{TH}} \right) V_{\text{DS}} - \frac{1}{2} V_{\text{DS}}^{2} \right], \end{aligned} (17) \\ &+ \left(\alpha_{d0} + \alpha_{d1} V_{D} \right) V_{D} + \left(\alpha_{b0} + \alpha_{b1} V_{D} \right) V_{D} - V_{D} \left(\alpha_{d0} \right) \\ &+ \alpha_{d1} V_{D} \right) - \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right] - \frac{1}{2} V_{\text{DS}} \left(V_{\text{DS}} \right) \\ &+ 2 \left(V_{S} + V_{\text{TH}} \right) + V_{\text{DS}} \left(\alpha_{b1} V_{B} V_{D} + \alpha_{b0} V_{B} + \left(\alpha_{d0} \right) \\ &+ \alpha_{d1} V_{D} \right) V_{D} + \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right), \end{aligned} (18) \\ &+ \alpha_{d1} V_{D} \right) V_{D} + \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) , \end{aligned} (19) \\ &+ \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) \right], \end{aligned} (19) \\ &+ \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) \right], \end{aligned} (20) \\ &+ \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) \right], \end{aligned} (20) \\ &+ \sum_{i=1}^{N} \left[\left(\alpha_{i0} + \alpha_{i1} V_{D} \right) V_{i} \right] \right) \right], \end{aligned} (21)$$

$$\begin{aligned} \frac{\partial I_D}{\partial \alpha_{d1}} &= \frac{\beta}{2} V_{\rm DS} V_D^2 \left[2 + \theta V_{\rm DS} + 4\theta \left(V_{\rm S} + V_{\rm TH} \right) \right. \\ &- 4\theta \left(\alpha_{b1} V_B V_D + \alpha_{b0} V_B + \left(\alpha_{d0} + \alpha_{d1} V_D \right) V_D \right. \end{aligned} \tag{21} \\ &+ \sum_{i=1}^N \left[\left(\alpha_{i0} + \alpha_{i1} V_D \right) V_i \right] \right) \right], \end{aligned} \\ &\frac{\partial I_D}{\partial \alpha_{b1}} &= \frac{\beta}{2} V_B V_D V_{\rm DS} \left[2 + \theta V_{\rm DS} + 4\theta \left(V_S + V_{\rm TH} \right) \right. \\ &- 4\theta \left(\alpha_{b1} V_B V_D + \alpha_{b0} V_B + \left(\alpha_{d0} + \alpha_{d1} V_D \right) V_D \right. \end{aligned} \tag{22} \\ &+ \sum_{i=1}^N \left[\left(\alpha_{i0} + \alpha_{i1} V_D \right) V_i \right] \right) \right], \end{aligned} \\ &\frac{\partial I_D}{\partial \alpha_{i0}} &= \frac{\beta}{2} V_{\rm DS} \sum_{i=1}^N \left[V_i \right] \left[2 + \theta V_{\rm DS} + 4\theta \left(V_S + V_{\rm TH} \right) \right. \\ &- 4\theta \left(\alpha_{b1} V_B V_D + \alpha_{b0} V_B + \left(\alpha_{d0} + \alpha_{d1} V_D \right) V_D \right. \end{aligned} \tag{23} \\ &+ \sum_{i=1}^N \left[\left(\alpha_{i0} + \alpha_{i1} V_D \right) V_i \right] \right) \right], \end{aligned}$$

Since ΔV_{TH} , $\Delta \alpha_{i0}$, $\Delta \alpha_{i1}$, $\Delta \alpha_{d0}$, $\Delta \alpha_{d1}$, $\Delta \alpha_{b0}$, $\Delta \alpha_{b1}$, $\Delta \beta$, and $\Delta \theta$ are random variables, so are ΔI_D and the statistical behavior of ΔI_D must be analytically modeled for its complete modeling. In order to do so, its average ($\Delta I_{D,\text{avr}}$) and variance ($\sigma^2_{\Delta I_D}$) must be formulated. As a result, $\Delta I_{D,\text{avr}} = 0$ similar to those of ΔV_{TH} , $\Delta \alpha_{i0}$, $\Delta \alpha_{i1}$, $\Delta \alpha_{d0}$, $\Delta \alpha_{d1}$, $\Delta \alpha_{b0}$, $\Delta \alpha_{b1}$, $\Delta \beta$, and $\Delta \theta$ and $\sigma^2_{\Delta I_D}$ of FGMOSFET in triode can be given by taking the statistical correlations of ΔV_{TH} , $\Delta \alpha_{i0}$, $\Delta \alpha_{i1}$, $\Delta \alpha_{d0}$, $\Delta \alpha_{d1}$, $\Delta \alpha_{b0}$, $\Delta \alpha_{b1}$, $\Delta \beta$, and $\Delta \theta$ into account as follows:

$$\begin{split} \sigma_{\Delta I_D}^2 &= \left(\frac{\partial I_D}{\partial V_{\rm TH}}\right)^2 \sigma_{\Delta V_{\rm TH}}^2 + \left(\frac{\partial I_D}{\partial \beta}\right)^2 \sigma_{\Delta\beta}^2 + \left(\frac{\partial I_D}{\partial \theta}\right)^2 \sigma_{\Delta\theta}^2 \\ &+ \left(\frac{\partial I_D}{\partial \alpha_{d0}}\right)^2 \sigma_{\Delta\alpha_{d0}}^2 + \left(\frac{\partial I_D}{\partial \alpha_{d1}}\right)^2 \sigma_{\Delta\alpha_{d1}}^2 + \left(\frac{\partial I_D}{\partial \alpha_{b0}}\right)^2 \\ &\cdot \sigma_{\Delta\alpha_{b0}}^2 + \left(\frac{\partial I_D}{\partial \alpha_{b1}}\right)^2 \sigma_{\Delta\alpha_{b1}}^2 + \sum_{i=1}^N \left[\left(\frac{\partial I_D}{\partial \alpha_{i0}}\right)^2 \sigma_{\Delta\alpha_{i0}}^2 \right] \end{split}$$

$$\begin{split} &+ \sum_{i=1}^{N} \left[\left(\frac{\partial I_D}{\partial \alpha_{i1}} \right)^2 \sigma_{\Delta \alpha_{i1}}^2 \right] + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \beta} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\beta} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\beta}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \theta} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d1}}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{d1}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d1}}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{b1}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d1}}^2} + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{b1}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d1}}^2} + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta V_{TH},\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \rho_{\Delta \alpha_{d0}}} \sqrt{\sigma_{\Delta \rho_{\Delta}}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \beta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \beta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \beta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \beta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d1}} \right) \\ &\cdot \rho_{\Delta \beta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \theta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \theta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \theta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \theta,\Delta\alpha_{d0}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\alpha_{d0}}^2} + 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial \alpha_{d0}} \right) \\ &\cdot \rho_{\Delta \theta,\Delta\alpha_{d0}} \sqrt$$

$$\begin{split} & \cdot \rho_{\Delta \theta_{c} \Delta \alpha_{i0}} \sqrt{\sigma_{\Delta \theta}^{2}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \theta} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \theta_{c} \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \theta}^{2}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} + 2 \left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d0}, \Delta \alpha_{d1}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} + 2 \left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d0}, \Delta \alpha_{b1}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{b1}}^{2}} + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d0}, \Delta \alpha_{b1}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d0}, \Delta \alpha_{i0}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \left(\frac{\partial I_{D}}{\partial \alpha_{d1}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d1}, \Delta \alpha_{b1}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \left(\frac{\partial I_{D}}{\partial \alpha_{d1}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d1}, \Delta \alpha_{b1}} \sqrt{\sigma_{\Delta \alpha_{d1}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{d1}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i0}} \right) \\ & \cdot \rho_{\Delta \alpha_{d1}, \Delta \alpha_{b1}} \sqrt{\sigma_{\Delta \alpha_{d1}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{d1}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{d1}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{d1}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{d0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{d1}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{d0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{b0}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{b0}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{b0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{b0}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha_{b0}} \right) \left(\frac{\partial I_{D}}{\partial \alpha_{i1}} \right) \\ & \cdot \rho_{\Delta \alpha_{b0}, \Delta \alpha_{i1}} \sqrt{\sigma_{\Delta \alpha_{i0}}^{2}} \sqrt{\sigma_{\Delta \alpha_{i1}}^{2}} \right] + 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial \alpha$$

$$+\sum_{j=1}^{N}\sum_{i=1}^{N}\left[\left(\frac{\partial I_{D}}{\partial \alpha_{i0}}\right)\left(\frac{\partial I_{D}}{\partial \alpha_{j1}}\right)\rho_{\Delta\alpha_{i0},\Delta\alpha_{j1}}\sqrt{\sigma_{\Delta\alpha_{i0}}^{2}}\sqrt{\sigma_{\Delta\alpha_{j1}}^{2}}\right]$$
$$+\sum_{j=1}^{N}\sum_{i=1}^{N}\left[\left(\frac{\partial I_{D}}{\partial \alpha_{i1}}\right)\left(\frac{\partial I_{D}}{\partial \alpha_{j0}}\right)\rho_{\Delta\alpha_{i1},\Delta\alpha_{j0}}\sqrt{\sigma_{\Delta\alpha_{i1}}^{2}}\sqrt{\sigma_{\Delta\alpha_{j0}}^{2}}\right]$$
$$+\sum_{j\neq i}^{N}\sum_{i=1}^{N}\left[\left(\frac{\partial I_{D}}{\partial \alpha_{i1}}\right)\left(\frac{\partial I_{D}}{\partial \alpha_{j1}}\right)\right]$$
$$\cdot\rho_{\Delta\alpha_{i1},\Delta\alpha_{j1}}\sqrt{\sigma_{\Delta\alpha_{i1}}^{2}}\sqrt{\sigma_{\Delta\alpha_{i1}}^{2}}\right],$$
(25)

where $\rho_{\Delta x,\Delta y}$, for example, $\rho_{\Delta V_{\text{TH}},\Delta\beta}$, $\rho_{\Delta\beta,\Delta\theta}$, and $\rho_{\Delta V_{\text{TH}},\Delta\theta}$, denotes the correlation coefficient of Δx and Δy and displays their degree of statistical correlation. Moreover, $\sigma^2_{\Delta V_{\text{TH}}}$, $\sigma^2_{\Delta\beta}$, $\sigma^2_{\Delta\alpha_{i0}}$, $\sigma^2_{\Delta\alpha_{a1}}$, $\sigma^2_{\Delta\alpha_{d0}}$, $\sigma^2_{\Delta\alpha_{b0}}$, $\sigma^2_{\Delta\alpha_{b1}}$, and $\sigma^2_{\Delta\theta}$ are nonzero and, respectively, denote the variances of ΔV_{TH} , $\Delta\alpha_{i0}$, $\Delta\alpha_{i1}$, $\Delta\alpha_{d0}$, $\Delta\alpha_{d1}$, $\Delta\alpha_{b0}$, $\Delta\alpha_{b1}$, $\Delta\beta$, and $\Delta\theta$. Finally, it can be seen that the 1st up to 9th terms of (25) have been contributed by $\sigma^2_{\Delta V_{\text{TH}}}$, $\sigma^2_{\Delta\beta}$, $\sigma^2_{\Delta\alpha_{i0}}$, $\sigma^2_{\Delta\alpha_{a1}}$, $\sigma^2_{\Delta\alpha_{a1}}$, $\sigma^2_{\Delta\alpha_{b0}}$, $\sigma^2_{\Delta\alpha_{b1}}$, and $\sigma^2_{\Delta\theta}$ where the others have been caused by the statistical correlations.

3.2. Saturation Region Part. Firstly, I_D of FGMOSFET in this region will be formulated in a similar manner to that of FGMOSFET in triode where the second order effects have been taken into account as well. With the linear model of mobility degradation, I_D of MOSFET in saturation can be given by

$$I_{D} = \frac{\beta}{2} \left[1 - \theta \left(V_{\text{GS}} - V_{\text{TH}} \right) \right] \left(1 + \lambda V_{\text{DS}} \right) \left(V_{\text{GS}} - V_{\text{TH}} \right)^{2},$$
⁽²⁶⁾

where λ stands for the channel length modulation coefficient which must be taken into account for MOSFET in saturation if the short channel effect has been considered.

For FGMOSFET in saturation region, the model formulation is simpler than that of triode FGMOSFET since k_i , k_{fd} , k_{fs} , and k_{fb} are constant. So, V_{FG} can be given by (3) and I_D can be determined in a similar manner to that of FGMOSFET in triode by using (3) and (26) as given in (27) since $k_{fs} \ll 1$:

$$I_{D} = \frac{\beta}{2} \left(1 + \lambda V_{DS} \right) \left[1 - \theta \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{fd} V_{D} + k_{fb} V_{B} - V_{S} - V_{TH} \right) \right]$$

$$\cdot \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{fd} V_{D} + k_{fb} V_{B} - V_{S} - V_{TH} \right)^{2}.$$

$$(27)$$

As the process induced random variations in $\lambda(\Delta\lambda)$ must also be considered for FGMOSET in saturation region, ΔI_D is given by (28). By using (27), all derivatives can be found as (29)–(35):

$$\begin{split} \Delta I_{D} &= \left(\frac{\partial I_{D}}{\partial V_{\text{TH}}}\right) \Delta V_{\text{TH}} + \left(\frac{\partial I_{D}}{\partial \beta}\right) \Delta \beta + \left(\frac{\partial I_{D}}{\partial \theta}\right) \Delta \theta \\ &+ \left(\frac{\partial I_{D}}{\partial \lambda}\right) \Delta \lambda + \left(\frac{\partial I_{D}}{\partial k_{\text{fd}}}\right) \Delta k_{\text{fd}} + \left(\frac{\partial I_{D}}{\partial k_{\text{fb}}}\right) \Delta k_{\text{fb}} \qquad (28) \\ &+ \sum_{i=1}^{N} \left[\left(\frac{\partial I_{D}}{\partial k_{i}}\right) \Delta k_{i} \right], \\ \frac{\partial I_{D}}{\partial \beta} &= \frac{1 + \lambda V_{\text{DS}}}{2} \left[1 \\ &- \theta \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right) \right] \\ &\cdot \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} \right] \\ &- V_{\text{TH}} \right)^{2}, \end{split} \tag{29} \\ &- V_{\text{TH}} \right)^{2}, \end{split} \tag{30} \\ &- V_{\text{TH}} \right)^{3}, \end{cases} \tag{30} \\ &- V_{\text{TH}} \right)^{3}, \end{cases} \tag{30} \\ &- 2 \left] \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right) \\ &- 2 \left] \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right), \\ &\frac{\partial I_{D}}{\partial \lambda} = \frac{\beta}{2} V_{\text{DS}} \left[1 \\ &- \theta \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right) \right] \\ &\cdot \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right) \right) \end{aligned} \tag{32} \\ &\cdot \left(\sum_{i=1}^{N} \left[k_{i} V_{i} \right] + k_{\text{fd}} V_{D} + k_{\text{fb}} V_{B} - V_{S} - V_{\text{TH}} \right) \right) \end{aligned}$$

$$\frac{\partial I_D}{\partial k_{\rm fd}} = -\frac{\beta}{2} V_D \left(1 + \lambda V_{\rm DS}\right) \\
\cdot \left[3\theta \left(\sum_{i=1}^N \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fb} V_B - V_S - V_{\rm TH} \right) \right] \quad (33)$$

$$-2 \left[\left(\sum_{i=1}^N \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fb} V_B - V_S - V_{\rm TH} \right) \right] , \quad (33)$$

$$\frac{\partial I_D}{\partial k_{\rm fb}} = -\frac{\beta}{2} V_B \left(1 + \lambda V_{\rm DS} \right) \\
\cdot \left[3\theta \left(\sum_{i=1}^N \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fb} V_B - V_S - V_{\rm TH} \right) \right] , \quad (34)$$

$$-2 \left[\left(\sum_{i=1}^N \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fb} V_B - V_S - V_{\rm TH} \right) \right] , \quad (34)$$

$$\frac{\partial I_D}{\partial k_i} = -\frac{\beta}{2} \left(\sum_{i=1}^N \left[k_i V_i \right] \right) \left(1 + \lambda V_{\rm DS} \right) \\
\cdot \left[3\theta \left(\sum_{i=1}^N \left[k_i V_i \right] + k_{\rm fd} V_D + k_{\rm fb} V_B - V_S - V_{\rm TH} \right) \right] , \quad (35)$$

Similarly to ΔI_D of the FGMOSFET in triode, ΔI_D of the FGMOSFET in saturation is also a random variable. So, its statistical behavior must be analytically modeled as well. Since $\Delta V_{\rm TH}$, $\Delta\beta$, Δk_i , $\Delta k_{\rm fd}$, $\Delta k_{\rm fb}$, $\Delta\theta$, and $\Delta\lambda$ have zero means, it has also been found that $\Delta I_{D,\rm avr} = 0$. Moreover, $\sigma^2_{\Delta I_D}$ can be given by taking the statistical correlations of $\Delta V_{\rm TH}$, $\Delta\beta$, Δk_i , $\Delta k_{\rm fd}$, $\Delta k_{\rm fb}$, $\Delta\theta$, and $\Delta\lambda$ into account as given in (36) where the first seven terms have been, respectively, contributed by $\sigma^2_{\Delta V_{\rm TH}}$, $\sigma^2_{\Delta\beta}$, $\sigma^2_{\Delta k_i}$, $\sigma^2_{\Delta k_{\rm fb}}$, $\sigma^2_{\Delta \theta}$, and $\sigma^2_{\Delta\lambda}$. On the other hand, the other terms have been contributed by the statistical correlations.

Before proceeding to the model verification, it should be mentioned here that $\rho_{\Delta x,\Delta y}$ can be obtained either by using the scatter plot [25, 26] or by calculation as given in (37) which can be obtained based on the prior knowledge that the averages of Δx and Δy are 0 as they are process induced device level random variations, that is, ΔV_{TH} , $\Delta \beta$, and $\Delta \theta$. Moreover, E[] stands for the expectation operator:

$$\begin{split} \sigma_{\Delta I_D}^2 \\ &= \left(\frac{\partial I_D}{\partial V_{\text{TH}}}\right)^2 \sigma_{\Delta V_{\text{TH}}}^2 + \left(\frac{\partial I_D}{\partial \beta}\right)^2 \sigma_{\Delta \beta}^2 + \left(\frac{\partial I_D}{\partial \theta}\right)^2 \sigma_{\Delta \theta}^2 \\ &+ \left(\frac{\partial I_D}{\partial \lambda}\right)^2 \sigma_{\Delta \lambda}^2 + \left(\frac{\partial I_D}{\partial k_{\text{fb}}}\right)^2 \sigma_{\Delta k_{\text{fb}}}^2 + \left(\frac{\partial I_D}{\partial k_{\text{fd}}}\right)^2 \sigma_{\Delta k_{\text{fd}}}^2 \\ &+ \sum_{i=1}^N \left[\left(\frac{\partial I_D}{\partial k_i}\right)^2 \sigma_{\Delta k_i}^2 \right] \end{split}$$

$$\begin{split} &+ 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial V_{TH}} \right) \rho_{\Delta\beta,\Delta V_{TH}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta V_{TH}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial \theta} \right) \rho_{\Delta\beta,\Delta\theta} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\lambda}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta\beta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta\lambda}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta\beta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \beta} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta\beta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta\beta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial V_{TH}} \right) \rho_{\Delta\theta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial V_{TH}} \right) \rho_{\Delta\theta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta\theta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta\theta,\Delta k_{fd}} \sqrt{\sigma_{\Delta\beta}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \theta} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta V_{TH},\Delta k_{fd}} \sqrt{\sigma_{\Delta k_{fd}}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta V_{TH},\Delta k_{fd}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta V_{TH},\Delta k_{fd}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta V_{TH},\Delta k_{fb}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial V_{TH}} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta V_{TH},\Delta k_{fb}} \sqrt{\sigma_{\Delta V_{TH}}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right) \left(\frac{\partial I_D}{\partial k_{fd}} \right) \rho_{\Delta \lambda,\Delta k_{fb}} \sqrt{\sigma_{\Delta \lambda}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta \lambda,\Delta k_{fb}} \sqrt{\sigma_{\Delta \lambda}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta \lambda,\Delta k_{fb}} \sqrt{\sigma_{\Delta \lambda}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta k_{fd},\Delta k_{fb}} \sqrt{\sigma_{\Delta \lambda}^2} \sqrt{\sigma_{\Delta k_{fd}}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right) \left(\frac{\partial I_D}{\partial k_{fb}} \right) \rho_{\Delta k_{fd},\Delta k_{fb}} \sqrt{\sigma_{\Delta \lambda}^2} \\ &+ 2 \left(\frac{\partial I_D}{\partial \lambda} \right$$

$$+ 2 \sum_{i=1}^{N} \left[\left(\frac{\partial I_D}{\partial k_{\rm fb}} \right) \left(\frac{\partial I_D}{\partial k_i} \right) \rho_{\Delta k_{\rm fb},\Delta k_i} \sqrt{\sigma_{\Delta k_{\rm fb}}^2} \sqrt{\sigma_{\Delta k_i}^2} \right] \\ + \sum_{\substack{j=1\\j\neq i}}^{N} \sum_{i=1}^{N} \left[\left(\frac{\partial I_D}{\partial k_i} \right) \left(\frac{\partial I_D}{\partial k_j} \right) \rho_{\Delta k_i,\Delta k_j} \sqrt{\sigma_{\Delta k_i}^2} \sqrt{\sigma_{\Delta k_j}^2} \right],$$
(36)

$$\rho_{\Delta x, \Delta y} = \frac{E\left[\Delta x \Delta y\right]}{\sigma_{\Delta x} \sigma_{\Delta y}}.$$
(37)

4. Model Verification

Before proceeding further, it should be mentioned here that the model verification has been performed by assuming that $C_i \gg C_{\rm fd}$, $C_{\rm fs}$, $C_{\rm fb}$ as has been assumed in many previous studies on FGMOSFET, for example, [2, 7–9, 20]. As a result, $k_{\rm fd}$, $k_{\rm fs}$, and $k_{\rm fb}$ can be neglected as $k_i \gg k_{\rm fd}$, $k_{\rm fs}$, $k_{\rm fb}$ and C_T is now independent of V_D because $C_{\rm fb}$ which causes the dependency of C_T on V_D can be neglected. So, k_i , which depends on C_T , is now constant for both triode and saturation regions as also assumed in [22].

Moreover, the model verification has been performed based on FGMOSFET of both N-type and P-type with N = 2and $k_1 = k_2 = 0.5$ for both triode and saturation regions, *W*/*L* = 20/0.25, SPICE BSIM3v3, and 0.25 μ m level CMOS process technology of TSMC where all necessary parameters have been provided by MOSIS. For performing the verification, the root mean square (rms.) value of ΔI_D calculated by using the model $(\Delta I_{D,\mathrm{rms},M})$ has been graphically compared to its SPICE BSIM3v3 based reference ($\Delta I_{D,rms,SPICE}$) obtained by using the Monte-Carlo SPICE simulation with 1000 runs. For convenience, ΔV_{TH} , $\Delta \beta$, Δk_1 , Δk_2 , $\Delta \theta$, and $\Delta \lambda$ have been assumed to be normally distributed and all $\rho_{\Delta x, \Delta y}$'s have been assumed to be 0.5 which is a reasonable estimation [26]. Since correlations must be taken into account in the simulations, each of ΔV_{TH} , $\Delta \beta$, Δk_1 , Δk_2 , $\Delta \theta$, and $\Delta \lambda$ has been expressed as a weighted sum of its correlated and uncorrelated components which are both normally distributed [26] and which have equal weights given by $\sqrt{0.5}$ due to the assumed $\rho_{\Delta x.\Delta y}$'s.

Finally, the SPICE BSIM3v3 based modelling of FGMÓS-FET with N = 2 can be performed by using the two inputs' version of the equivalent circuit of FGMOSFET in Figure 2 where the core MOSFET has been modelled by using the SPICE BSIM3v3 and the simulation methodology proposed in [23] has been adopted for solving the convergence problem of the simulator. Both $\Delta I_{D,rms,M}$ and $\Delta I_{D,rms,SPICE}$ have been expressed as percentage of deterministic I_D and comparatively plotted against the magnitude of the voltage of the 1st and 2nd inputs of FGMOSFET which are ranged from 3 to 8 V and denoted by $|V_1|$ and $|V_2|$, respectively. It should be mentioned here that $|V_2| = 0$ V in the comparative plots against $|V_1|$ and vice versa. At this point, verification of the proposed model will be presented.

4.1. Verification of Triode Region Part. Since λ and of course $\Delta\lambda$ have not been considered in triode region, only ΔV_{TH} ,

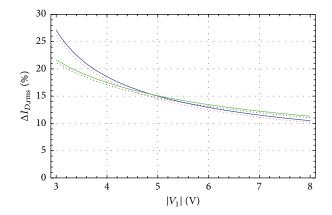


FIGURE 3: Triode and saturation regions N-type FGMOSFET based comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ against $|V_1|$ where $|V_2| = 0$.

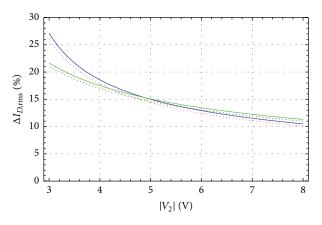


FIGURE 4: Triode and saturation regions N-type FGMOSFET based comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ against $|V_2|$ where $|V_1| = 0$.

 $\Delta\beta$, $\Delta\theta$, Δk_1 , and Δk_2 have been taken into account. We let $\sigma_{\Delta V_{\text{TH}}}/V_{\text{TH}} = \sigma_{\Delta\beta}/\beta = \sigma_{\Delta\theta}/\theta = \sigma_{\Delta k_1}/k_1 = \sigma_{\Delta k_2}/k_2$ for making the device level variations be bias-free and let these quantities be 0.01 as it has been assumed that V_{TH} , β , θ , k_1 , and k_2 have 1% variation. For N-type FGMOSFET, the comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ against $|V_1|$ where $|V_2|$ = 0 and vice versa can be, respectively, shown in Figures 3 and 4 whereas those of P-type FGMOSFET are shown in Figures 5 and 6. In these figures, highly strong agreements between $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ which are, respectively, drawn as blue normal curves and red dotted curves can be observed and the average deviations of $\Delta I_{D,\mathrm{rms},M}$ from $\Delta I_{D,\mathrm{rms},\mathrm{SPICE}}$ have been found as 3.74725% and 3.70765% for N-type and P-type FGMOSFET, respectively. These deviations which can be determined from triode region N-type and P-type FGMOSFET based comparative plots shown in Figures 3 and 4 and in Figures 5 and 6, respectively, are notably very small. So, it can be seen that triode region part of the proposed model has been found to be very accurate.

4.2. Verification of Saturation Region Part. In saturation region, λ and $\Delta\lambda$ must be considered. For making the device

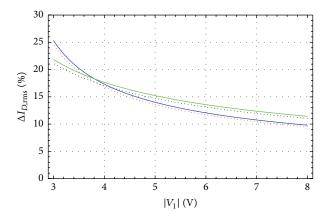


FIGURE 5: Triode and saturation regions P-type FGMOSFET based comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ against $|V_1|$ where $|V_2| = 0$.

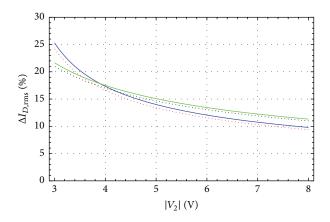


FIGURE 6: Triode and saturation regions P-type FGMOSFET based comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ against $|V_2|$ where $|V_1| = 0$.

level variations be bias-free and following the assumption that $V_{\rm TH},$ $\beta,$ $\theta,$ $k_1,$ $k_2,$ and λ have 1% variation, we let $\sigma_{\Delta V_{\rm TH}}/V_{\rm TH}$ = $\sigma_{\Delta\beta}/\beta = \sigma_{\Delta\lambda}/\lambda = \sigma_{\Delta\theta}/\theta = \sigma_{\Delta k_1}/k_1 = \sigma_{\Delta k_2}/k_2 = 0.01.$ The N-type FGMOSFET based comparative plots of $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms,SPICE}}$ against $|V_1|$ where $|V_2| = 0$ and vice versa can also be, respectively, depicted in Figures 3 and 4 where those based on P-type device are shown in Figures 5 and 6 as well. However, $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ have been, respectively, drawn as green normal curves and black dotted curves instead in this case. From Figures 3-6, highly strong agreements between $\Delta I_{D,\mathrm{rms},M}$ and $\Delta I_{D,\mathrm{rms},\mathrm{SPICE}}$ can also be seen in this case. The average deviations between $\Delta I_{D,\mathrm{rms},M}$ and $\Delta I_{D,\text{rms,SPICE}}$ have been, respectively, found as 2.6901% and 3.6681% for N-type and P-type FGMOSFET. These deviations which can be determined from the saturation region FGMOSFET based comparative plots are also considerably very small. As a result, the accuracy of saturation region part of the model has been verified.

From Figures 3–6, it has been found that both $\Delta I_{D,\text{rms},M}$ and $\Delta I_{D,\text{rms},\text{SPICE}}$ are increased with decreasing $|V_1|$ and $|V_2|$ because they have been expressed as percentage of their corresponding deterministic I_D 's which are increased with increasing $|V_1|$ and $|V_2|$ but with larger degree than the gross values of both rms. values do. Such increasing of $\Delta I_{D,\mathrm{rms},M}$ and $\Delta I_{D,\mathrm{rms},\mathrm{SPICE}}$ with decreasing $|V_1|$ and $|V_2|$ means that ΔI_D become more critical in low voltage/low power condition.

By considering the magnitude of both $\Delta I_{D,\mathrm{rms},M}$ and $\Delta I_{D,\mathrm{rms},\mathrm{SPICE}}$, it has also been found that the FGMOSFET in triode is more robust than that in saturation region when $|V_1|$ and $|V_2|$ become sufficiently large. This can be obviously seen in P-type device. On the other hand, the device in saturation is more robust when $|V_1|$ and $|V_2|$ become adequately low. This is more obvious in the N-type FGMOSFET. If desired, $\Delta I_{D,\mathrm{rms},M}$ can fit $\Delta I_{D,\mathrm{rms},\mathrm{SPICE}}$ simulated by using BSIM4. In order to do so, the optimum parameters of MOSFET's I_D equations, that is, (6) and (14), extracted from BSIM4's I_D model by using the optimization algorithm [24] must be used.

5. Discussions

Firstly, insight into device level variability of FGMOSFET will be discussed. By using the proposed model, the rate of change of per unit ΔI_D ($\Delta I_D/I_D$) of FGMOSFET in triode region with respect to per unit ΔV_{TH} , $\Delta \alpha_{i0}$, $\Delta \alpha_{i1}$, $\Delta \alpha_{d0}$, $\Delta \alpha_{d1}$, $\Delta \alpha_{b0}$, $\Delta \alpha_{b1}$, $\Delta \beta$, and $\Delta \theta$ denoted by $\Delta V_{\text{TH}}/V_{\text{TH}}$, $\Delta \alpha_{i0}/\alpha_{i0}$, $\Delta \alpha_{i1}/\alpha_{i1}, \Delta \alpha_{d0}/\alpha_{d0}, \Delta \alpha_{d1}/\alpha_{d1}, \Delta \alpha_{b0}/\alpha_{b0}, \Delta \alpha_{b1}/\alpha_{b1}, \Delta \beta/\beta$, and $\Delta \theta/\theta$, respectively, can be approximately given under low voltage/low power condition as follows:

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta V_{\rm TH} / V_{\rm TH}} = \frac{1 + 2\theta V_{\rm TH}}{1 + \theta V_{\rm TH}},\tag{38}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \beta / \beta} = 1, \tag{39}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \theta / \theta} = \frac{\theta V_{\rm TH}}{1 + \theta V_{\rm TH}},\tag{40}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{d0} / \alpha_{d0}} = 0, \tag{41}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{d_1} / \alpha_{d_1}} = 0, \tag{42}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{b0} / \alpha_{b0}} = 0, \tag{43}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{b1} / \alpha_{b1}} = 0, \tag{44}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{i0} / \alpha_{i0}} = 0, \tag{45}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \alpha_{i1} / \alpha_{i1}} = 0. \tag{46}$$

For the saturation region, a similar rate of changes can be approximately found under a similar condition as

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \beta / \beta} = 1, \tag{47}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \theta / \theta} = \frac{\theta V_{\rm TH}}{1 + \theta V_{\rm TH}},\tag{48}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta V_{\rm TH} / V_{\rm TH}} = \frac{2 + 3\theta V_{\rm TH}}{1 + \theta V_{\rm TH}},\tag{49}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta \lambda / \lambda} = 0, \tag{50}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta k_{\rm fd} / k_{\rm fd}} = 0, \tag{51}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta k_{\rm fb} / k_{\rm fb}} = 0, \tag{52}$$

$$\frac{\partial \Delta I_D / I_D}{\partial \Delta k_i / k_i} = 0.$$
(53)

From these equations, it can be seen that all process induced device level random variations are relatively insignificant compared to $\Delta\beta$, $\Delta\theta$, and ΔV_{TH} under low voltage/low power condition. So, $\Delta\beta$, $\Delta\theta$, and ΔV_{TH} must be mostly considered in the variability aware design of low voltage/low power FGMOSFET based circuit. Moreover, the effects of $\Delta\theta$ and ΔV_{TH} can be reduced by lowering either θ or V_{TH} or both as can be seen from (38), (40), (48), and (49).

Secondly, application of the proposed model will be discussed. This model can be used as the basis for the analytical modeling of process induced random variation in the circuit level parameter of any FGMOSFET based circuit. Let the circuit level parameter of interest of any FGMOSFET based circuit which is composed of *M* FGMOSFETs be *X*. The process induced random variation in *X* (ΔX) caused by the combined effect of ΔI_D of each FGMOSFET can be given by

$$\Delta X = \sum_{m=1}^{M} \left[\frac{\partial X}{\partial I_{Dm}} \Delta I_{Dm} \right], \tag{54}$$

where I_{Dm} and ΔI_{Dm} denote the I_D and ΔI_D of any *m*th FGMOSFET. Since ΔI_{Dm} can be analytically determined by using the proposed model due to its definition, so does ΔX via (54).

Since the average of ΔX is zero as those of ΔI_{Dm} 's are, it is convenient to analytically model the statistical behavior of ΔX by using its variance $(\sigma_{\Delta X}^2)$ as those of ΔI_{Dm} 's are nonzero. According to the above definition of ΔI_{Dm} , the variance of ΔI_{Dm} $(\sigma_{\Delta I_{Dm}}^2)$ can be determined by using the proposed model. So, $\sigma_{\Delta X}^2$ can be determined by using the proposed model as well as with the aid of the following equation:

$$\sigma_{\Delta X}^{2} = \sum_{m=1}^{M} \left[\left(\frac{\partial X}{\partial I_{Dm}} \right)^{2} \sigma_{\Delta I_{Dm}}^{2} \right] + \sum_{\substack{m=1\\m\neq k}}^{M} \sum_{k=1}^{M} \left[\left(\frac{\partial X}{\partial I_{Dm}} \right) \right] \\ \cdot \left(\frac{\partial X}{\partial I_{Dk}} \right) \rho_{I_{Dm}, I_{Dk}} \sqrt{\sigma_{\Delta I_{Dm}}^{2} \sigma_{\Delta I_{Dk}}^{2}} \right].$$
(55)

As a practical illustrative example, let X be the transconductance (G_m) of the FGMOSFET based voltage to current converter (VIC) [1]. The equivalent circuit of its core circuit

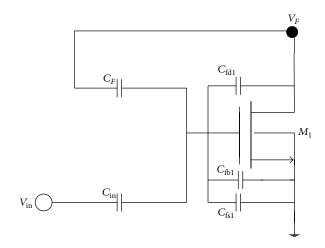


FIGURE 7: Equivalent circuit of FGMOSFET based transconductor which is the core circuit of FGMOSFET based VIC [1].

which is the FGMOSFET based transconductor can be depicted as in Figure 7 where C_F and $C_{\rm in}$ are the feedback and input capacitance of the transconductor [1]. Moreover, $C_{\rm fd1}$, $C_{\rm fs1}$, and $C_{\rm fb1}$ are, respectively, $C_{\rm fd}$, $C_{\rm fs}$, and $C_{\rm fb}$ of M_1 .

Obviously, G_m can be given by

$$G_m = \frac{\sqrt{2\beta_1 C_{\rm in}}}{C_{\rm in} + C_F + C_{\rm fd1} + C_{\rm fs1} + C_{\rm fb1}} \sqrt{I_{D1}}, \qquad (56)$$

where I_{D1} and β_1 denote I_D and β of M_1 .

Since M_1 is in saturation region and has N = 2, I_{D1} can be given as follows:

$$\begin{split} H_{D1} &= \frac{\beta_1}{2} \left(1 + \lambda_1 V_f \right) \\ &\cdot \left[1 - \theta_1 \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1} \right) V_F - V_{\rm TH1} \right) \right] \qquad (57) \\ &\cdot \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1} \right) V_F - V_{\rm TH1} \right)^2, \end{split}$$

where V_{TH1} , θ_1 , and λ_1 denote V_{TH} , θ , and λ of M_1 . Moreover, k_{in} , k_F , and k_{fd1} can be defined as

$$k_{\rm in} = \frac{C_{\rm in}}{C_{\rm in} + C_F + C_{\rm fd1} + C_{\rm fs1} + C_{\rm fb1}},$$

$$k_F = \frac{C_F}{C_{\rm in} + C_F + C_{\rm fd1} + C_{\rm fs1} + C_{\rm fb1}},$$

$$k_{\rm fd1} = \frac{C_{\rm fd1}}{C_{\rm in} + C_F + C_{\rm fd1} + C_{\rm fs1} + C_{\rm fb1}}.$$
(58)

At this point, the analytical model of process induced random variation in G_m (ΔG_m) can be given by using (54) and (56) as follows:

$$\Delta G_m$$

$$=\frac{k_{\rm in} \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1}\right) V_F - V_{\rm TH1}\right)^{-1}}{\sqrt{\left(1 + \lambda_1 V_F\right) \left[1 - \theta_1 \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1}\right) V_F - V_{\rm TH1}\right)\right]}}$$
(59)
$$\cdot \Delta I_{\rm D1},$$

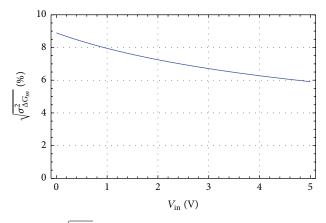


FIGURE 8: $\sqrt{\sigma_{\Delta G_m}^2}$ (%) of the FGMOSFET based transconductor against $|V_{in}|$.

where ΔI_{D1} denotes the process induced random variation in I_{D1} and can be determined by using (28)–(35) which belong to the saturation region part of the proposed model, with N = 2.

Moreover, it is necessary to model the statistical behavior of ΔG_m by using its variance $(\sigma^2_{\Delta G_m})$ because its mean is equal to zero similarly to that of ΔI_{D1} . By using (55) and (56), $\sigma^2_{\Delta G_m}$ can be formulated as

$$\sigma_{\Delta G_m}^2 = \left[\frac{k_{\rm in}^2 \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1} \right) V_F - V_{\rm TH1} \right)^{-2}}{\left(1 + \lambda_1 V_F \right) \left[1 - \theta_1 \left(k_{\rm in} V_{\rm in} + \left(k_F + k_{\rm fd1} \right) V_F - V_{\rm TH1} \right) \right]} \right]^2$$
(60)
$$\cdot \sigma_{\Delta I_{\rm DI}}^2,$$

where $\sigma_{\Delta I_{D1}}^2$ denotes the variance of ΔI_{D1} and can be analytically determined by using (29)–(36) which belong to the saturation region part proposed model, with N = 2. It should be mentioned here that there is no correlation related term in (60) since G_m is solely contributed by I_{D1} . Finally, we can plot $\sqrt{\sigma_{\Delta G_m}^2}$ against $|V_{in}|$ as shown in Figure 8 where $\sqrt{\sigma_{\Delta G_m}^2}$ has been expressed as percentage of the nominal G_m .

6. Conclusion

In this research, the analytical model of ΔI_D of FGMOSFET has been proposed. The model is composed of two parts for triode and saturation regions of operation. The process induced device level random variations of each region, their statistical correlations, nonlinearity of $V_{\rm FG}$, and dependency on V_D of the coupling factors have been taken into account. The proposed model has been found to be very accurate since it can fit the SPICE BSIM3v3 based reference obtained by using Monte-Carlo simulation and FGMOSFET simulation technique with SPICE with very high accuracy. If desired, it can also fit the BSIM4 based reference if the optimally extracted parameters obtained by using optimization algorithm have been applied. By using the proposed model, the insight into the device level variability of FGMOSFET can be provided and the analytical model of process induced random variation in circuit level parameter of any FGMOSFET based circuit can be obtained. It has been shown that ΔI_D become more critical in low voltage/low power operating condition where the influences of $\Delta\beta$, $\Delta\theta$, and $\Delta V_{\rm TH}$ become relatively large compared to those of other variations. Furthermore, the effects of $\Delta\theta$ and $\Delta V_{\rm TH}$ can be reduced by lowering either θ or $V_{\rm TH}$ or both. So, this model has been found to be an efficient tool for the variability aware analysis/designing of any FGMOSFET based circuit.

Conflict of Interests

The author declares that there is no conflict of interests regarding the publication of this paper.

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References

- A. S. Medina-Vazquez, J. de la Cruz-Alejo, F. Gomez-Castañeda, and J. A. Moreno-Cadenas, "Low-voltage linear transconductor and a memory current using the MIFGMOS Transistor," *International Journal of Electronics*, vol. 96, no. 9, pp. 895–914, 2009.
- [2] R. Pandey and M. Gupta, "FGMOS based voltage-controlled grounded resistor," *Radioengineering*, vol. 19, no. 3, pp. 455–459, 2010.
- [3] M. Gupta and R. Pandey, "Low-voltage FGMOS based analog building blocks," *Microelectronics Journal*, vol. 42, no. 6, pp. 903–912, 2011.
- [4] J. M. Miguel, A. J. Lopez-Martin, L. Acosta, J. Ramírez-Angulo, and R. G. Carvajal, "Using floating gate and quasi-floating gate techniques for rail-to-rail tunable CMOS transconductor design," *IEEE Transactions on Circuits and Systems. I. Regular Papers*, vol. 58, no. 7, pp. 1604–1614, 2011.
- [5] A. J. Lopez-Martin, J. M. Algueta, C. Garcia-Alberdi, L. Acosta, R. G. Carvajal, and J. Ramirez-Angulo, "Design of micropower class AB transconductors: a systematic approach," *Microelectronics Journal*, vol. 44, no. 10, pp. 920–929, 2013.
- [6] G.-C. Lizeth, T. Asai, and M. Motomura, "Application of nonlinear systems for designing low-power logic gates based on stochastic resonance," *Nonlinear Theory and Its Applications, IEICE*, vol. 5, no. 4, pp. 445–455, 2014.
- [7] E. Rodriguez-Villegas, Low Power and Low Voltage Circuit Design with the FGMOS Transistor, vol. 20 of IET Circuits, Devices & Systems Series, The Institution of Engineering and Technology, London, UK, 2006.
- [8] V. S. Babu, R. S. Devi, A. Sekhar, and M. R. Baiju, "FGMOSFET circuit for neuron activation function and its derivative," in *Proceedings of the 4th IEEE Conference on Industrial Electronics* and Applications (ICIEA '09), pp. 739–744, IEEE, Xi'an, China, May 2009.
- [9] M. Gupta, R. Srivastava, and U. Singh, "Low voltage floating gate MOS transistor based differential voltage squarer," *ISRN Electronics*, vol. 2014, Article ID 357184, 6 pages, 2014.

- [10] M. J. M. Pelgrom, A. C. J. Duinmaijer, and A. P. G. Welbers, "Matching properties of MOS transistors," *IEEE Journal of Solid-State Circuits*, vol. 24, no. 5, pp. 1433–1440, 1989.
- S. K. Saha, "Modeling process variability in scaled CMOS technology," *IEEE Design & Test of Computers*, vol. 27, no. 2, pp. 8–16, 2010.
- [12] K. Kuhn, "Variability in nanoscale CMOS technology," Science China Information Sciences, vol. 54, no. 5, pp. 936–945, 2011.
- [13] P. R. Kinget, "Device mismatch and tradeoffs in the design of analog circuits," *IEEE Journal of Solid-State Circuits*, vol. 40, no. 6, pp. 1212–1224, 2005.
- [14] H. Masuda, T. Kida, and S.-I. Ohkawa, "Comprehensive matching characterization of analog CMOS circuits," *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences*, vol. 92, no. 4, pp. 966–975, 2009.
- [15] F. Hong, B. Cheng, S. Roy, and D. Cumming, "An analytical mismatch model of nano-CMOS device under impact of intrinsic device variability," in *Proceedings of the IEEE International Symposium of Circuits and Systems (ISCAS '11)*, pp. 2257–2260, May 2011.
- [16] S. Vlassis and S. Siskos, "Design of voltage-mode and currentmode computational circuits using floating-gate MOS transistors," *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 51, no. 2, pp. 329–341, 2004.
- [17] A. E. Mourabit, P. Pittet, and G. N. Lu, "A low voltage, highly linear CMOS OTA," in *Proceedings of the 16th International Conference on Microelectronics (ICM '04)*, pp. 700–703, tun, December 2004.
- [18] Y. Zhai and P. A. Abshire, "Adaptive log domain filters for system identification using floating gate transistors," *Analog Integrated Circuits and Signal Processing*, vol. 56, no. 1-2, pp. 23–36, 2008.
- [19] V. S. Babu, A. Sekhar, R. S. Devi, and M. R. Baiju, "Floating gate MOSFET based operational transconductance amplifier and study of mismatch," in *Proceedings of the 4th IEEE Conference on Industrial Electronics and Applications (ICIEA '09)*, pp. 127– 132, IEEE, Xi'an, China, May 2009.
- [20] A. E. Mourabit, G.-N. Lu, and P. Pittet, "Wide-linear-range subthreshold OTA for low-power, low-voltage, and low-frequency applications," *IEEE Transactions on Circuits and Systems I: Regular Papers*, vol. 52, no. 8, pp. 1481–1488, 2005.
- [21] L.-L. Xiao, K. Liu, and D.-P. Han, "CMOS low data rate imaging method based on compressed sensing," *Optics and Laser Technology*, vol. 44, no. 5, pp. 1338–1345, 2012.
- [22] S. K. Saha, "Non-linear coupling voltage of split-gate flash memory cells with additional top coupling gate," *IET Circuits, Devices & Systems*, vol. 6, no. 3, pp. 204–210, 2012.
- [23] J. Ramirez-Angulo, G. Gonzlrlez-Altamirano, and S. C. Choi, "Modeling multiple-input floating-gate transistors for analog signal processing," in *Proceedings of the IEEE International Symposium on Circuits and Systems (ISCAS '97)*, vol. 3, pp. 2020– 2023, IEEE, Hong Kong, June 1997.
- [24] P. E. Allen and D. R. Holdberg, CMOS Analog Circuit Design, The Oxford Series in Electrical and Computer Engineering, Oxford University Press, Oxford, UK, 2011.
- [25] W. Lü, G. Wang, and L. Sun, "Analytical modeling of cutoff frequency variability reserving correlations due to random dopant fluctuation in nanometer MOSFETs," *Solid-State Electronics*, vol. 105, pp. 63–69, 2015.
- [26] K. Khu, "Statistical modeling for Monte Carlo simulation using Hspice," in *Proceedings of the Synopsys Users Group Conference*, pp. 1–10, 2006.

