

UNIVERSITY OF BIRMINGHAM

Research at Birmingham

Ka-Band LTCC Stacked Substrate Integrated Waveguide Bandpass Filter

Zheng, Peng; Liu, Zhifu; Ma, Mingsheng; Wang, Yi; Liu, Feng; Li, Yongxiang

DOI:

[10.1155/2018/9737219](https://doi.org/10.1155/2018/9737219)

License:

Creative Commons: Attribution (CC BY)

Document Version

Publisher's PDF, also known as Version of record

Citation for published version (Harvard):

Zheng, P, Liu, Z, Ma, M, Wang, Y, Liu, F & Li, Y 2018, 'Ka-Band LTCC Stacked Substrate Integrated Waveguide Bandpass Filter', *Wireless Communications and Mobile Computing*, vol. 2018, 9737219.
<https://doi.org/10.1155/2018/9737219>

[Link to publication on Research at Birmingham portal](#)

Publisher Rights Statement:

Checked for eligibility: 26/07/2018

General rights

Unless a licence is specified above, all rights (including copyright and moral rights) in this document are retained by the authors and/or the copyright holders. The express permission of the copyright holder must be obtained for any use of this material other than for purposes permitted by law.

- Users may freely distribute the URL that is used to identify this publication.
- Users may download and/or print one copy of the publication from the University of Birmingham research portal for the purpose of private study or non-commercial research.
- User may use extracts from the document in line with the concept of 'fair dealing' under the Copyright, Designs and Patents Act 1988 (?)
- Users may not further distribute the material nor use it for the purposes of commercial gain.

Where a licence is displayed above, please note the terms and conditions of the licence govern your use of this document.

When citing, please reference the published version.



Take down policy

While the University of Birmingham exercises care and attention in making items available there are rare occasions when an item has been uploaded in error or has been deemed to be commercially or otherwise sensitive.

If you believe that this is the case for this document, please contact UBIRA@lists.bham.ac.uk providing details and we will remove access to the work immediately and investigate.

Research Article

Ka-Band LTCC Stacked Substrate Integrated Waveguide Bandpass Filter

Peng Zheng,^{1,2} Zhifu Liu ,¹ Mingsheng Ma,¹ Yi Wang ,³ Feng Liu,¹ and Yongxiang Li^{1,4}

¹CAS Key Laboratory of Inorganic Functional Materials and Devices, Shanghai Institute of Ceramics, Chinese Academy of Sciences, Shanghai 200050, China

²College of Materials and Opto-Electronic Technology, The University of Chinese Academy of Sciences, Beijing 100049, China

³Department of Electronic, Electrical and Systems Engineering, University of Birmingham, B15 2TT, UK

⁴School of Engineering, RMIT University, Melbourne, VIC 3001, Australia

Correspondence should be addressed to Zhifu Liu; liuzf@mail.sic.ac.cn and Yi Wang; y.wang.1@bham.ac.uk

Received 24 February 2018; Accepted 29 May 2018; Published 3 July 2018

Academic Editor: Eva Antonino-Daviu

Copyright © 2018 Peng Zheng et al. This is an open access article distributed under the Creative Commons Attribution License, which permits unrestricted use, distribution, and reproduction in any medium, provided the original work is properly cited.

A Ka-band substrate integrated waveguide bandpass filter has been designed and fabricated using low temperature co-fired ceramic (LTCC) technology. The in-house developed SICCAS-K5F3 material with a permittivity of 6.2 and a loss tangent of 0.002 was used. The size and surface area of the proposed bandpass filter are reduced by exploiting vertical coupling in vertically laminated three-dimensional structures. The coupling between adjacent cavities is realized by a narrow slot. A vertical transition structure between the coplanar-waveguide feed line and the substrate integrated waveguide is adopted to facilitate the internal signal connection. The demonstrated third-order filter has a compact size of 6.79 mm×4.13 mm×1.34 mm ($0.63\lambda_0 \times 0.38\lambda_0 \times 0.12\lambda_0$) and exhibits good performance with a low insertion loss of 1.74 dB at 27.73 GHz and a 3 dB fractional bandwidth of 10 %.

1. Introduction

Substrate integrated waveguide (SIW) has received enormous attention due to its low loss and excellent compatibility with planar circuits [1, 2]. It preserves the advantage of rectangular waveguide and also has merits such as low profile, ease of packaging, being lightweight, and low fabrication cost. Therefore, SIW is a promising candidate structure for high performance microwave and millimeter-wave circuits. SIW is normally composed of waveguide structures defined by two rows of metallic via arrays in a dielectric substrate and sandwiched between two parallel conductor layers [3]. Apart from traditional print circuit board technology, SIW components could also be fabricated by low temperature co-fired ceramic (LTCC) process. Owing to the excellent dielectric, mechanical, and thermal properties, LTCC has been widely used in high density integrated circuits package [4–6]. Moreover, LTCC technology is very suitable for forming stacked structures to reduce the area of the circuit.

High performance and miniaturized bandpass filter (BPF) is an essential component in modern wireless communication system. With a BPF, the required channel spectrum is selected while the unwanted spectrum is rejected. Various BPFs are implemented by diverse transmission line technologies such as microstrip [7] and SIW [8]. Direct-coupled or cross-coupled SIW cavities are common in millimeter-wave filter designs [9, 10]. Compared with in-plane arranged SIW cavities, multilayer stacked SIW cavities are more compact.

In the design of high frequency transmission circuits, the internal signal connection and transition are vitally important, especially in multilayer LTCC package. Generally, the transition takes either horizontal or vertical form. Coplanar-waveguide- (CPW)- to-stripline vertical transitions have been researched extensively in recent years. In previous report, a CPW-to-stripline vertical transition was utilized for a V-band LTCC system-on-package applications [11]. A broadband CPW-to-stripline transition which is capable of operation up to 100 GHz was also presented for LTCC



FIGURE 1: Coupling topology of the proposed filter.

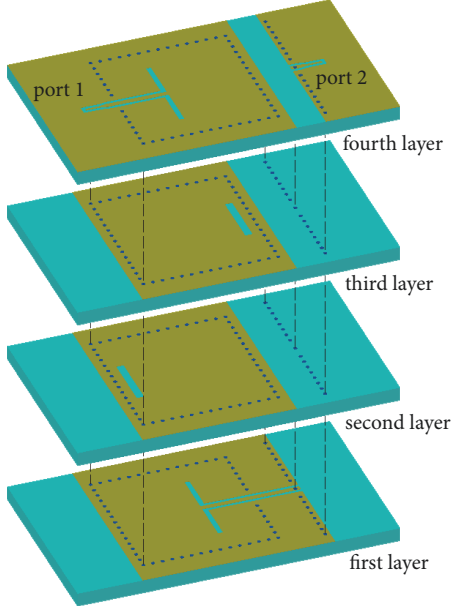


FIGURE 2: Exploded view of the proposed LTCC BPF.

module [12]. However, few works were reported on the CPW-to-CPW vertical transition for LTCC based SIW BPF.

In this paper, a compact Ka-band LTCC SIW BPF with a via-based CPW-to-CPW vertical transition is presented. The SIW cavities are vertically stacked to allow miniaturization. As a key element of the circuit integration, the CPW transition is adopted and analyzed. To validate the proposed scheme, a third-order SIW BPF in Ka-band was designed and fabricated using LTCC technology. The simulation result and measurement result are in good agreement. Competitive performance has been shown as compared with other multilayer SIW filters. It should also be noted that an in-house developed LTCC material was used in this work.

2. BPF Design

2.1. Overview of Structure. The coupling topology of the proposed third-order SIW BPF is depicted in Figure 1. S represents the source excitation, L represents the Load, and $R1$, $R2$, and $R3$ represent three SIW resonant cavities. Q_e is the external quality factor. k_{12} and k_{23} are the coupling coefficients.

The architecture of the proposed LTCC BPF is illustrated in Figure 2. The third-order BPF is built in four layers of LTCC substrates and four layers of metallization. The three resonators are vertically coupled to each other through slots which are made in the metallization layers and close to the wall. The coupling between adjacent SIW resonant cavities is magnetic in nature. A CPW structure is adopted

as the excitation ports for measurements using the common ground-signal-ground (GSG) probe. In order to bring the two CPW ports to the same circuit level, a CPW-to-CPW vertical transition structure at port 2 is used to facilitate the measurement.

The dielectric substrates are based on the SICCAS-K5F3 LTCC material with a permittivity of 6.2 and a loss tangent of 0.002. This material has been in-house developed at Shanghai Institute of Ceramics, Chinese Academy of Sciences. The thickness of each layer in this design is 0.3 mm, formed by 10 sheets of green tapes. 10 μm silver layer is screen-printed on the LTCC substrates acting as the top and bottom enclosure planes for the cavities.

2.2. SIW Resonant Cavity. The resonant cavity is composed of laminated dielectric substrate, parallel conductor layers, and metal via arrays. To design a SIW cavity operating at a given frequency, the following formula is adopted.

$$f_{res} = \frac{c}{2\pi\sqrt{\epsilon_r}} \sqrt{\left(\frac{m\pi}{L_{eff}}\right)^2 + \left(\frac{n\pi}{W_{eff}}\right)^2}$$

$$L_{eff} = L - \frac{d^2}{0.95p},$$

$$W_{eff} = W - \frac{d^2}{0.95p}$$
(1)

where c is the speed of light in vacuum and ϵ_r is the dielectric constant of the LTCC substrate. L and W are the length and width of the SIW resonant cavity and d and p represent the diameter of the metal via and the periodic length of the metal via array, respectively. Based on these formulas, the initial dimensions of the SIW cavity are determined.

2.3. Coupling Slots and Feeding Structure. According to the general Chebyshev BPF theory, the normalized coupling coefficients and the external quality factors can be obtained by following formula.

$$Q_{e1} = \frac{g_0 g_1}{FBW},$$

$$Q_{en} = \frac{g_n g_{n+1}}{FBW},$$

$$M_{i,i+1} = \frac{1}{\sqrt{g_i g_{i+1}}}, \quad \text{for } i = 1 \text{ to } n-1$$
(2)

The presented third-order Chebyshev BPF is designed with following specifications: passband return loss, 20 dB; fractional bandwidth (FBW), 6%. The low-pass prototype values can be determined as $g_0=g_4=1$, $g_1=g_3=0.8535$, and $g_2=1.1039$. The external quality factors are calculated as follows: $Q_{e1}=Q_{en}=14.225$. The coupling coefficients are $k_{12}=k_{23}=FBW \times M_{12}=0.0618$.

To minimize the occupation area, the resonant cavities are vertically stacked rather than cascaded laterally. The coupling between adjacent SIW cavities is realized by a narrow slot

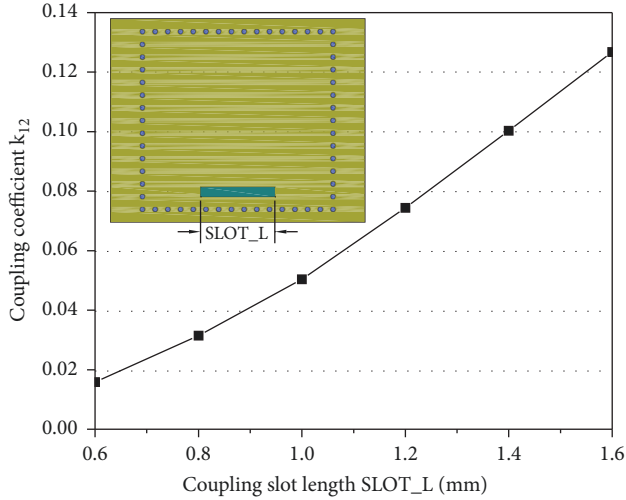


FIGURE 3: Relationship between the coupling coefficients k_{12} and the coupling slot length SLOT_L.

close to the metal via array. Since the SIW cavities are coupled together via the magnetic slot, the central frequency peak of the SIW cavities splits into two discrete peaks with frequencies f_1 and f_2 , respectively. The lower resonant frequency point f_1 and the higher frequency point f_2 are obtained from the simulation by ANSYS HFSS. The coupling coefficient k_{12} between the adjacent cavities is extracted using the following formula:

$$k_{12} = \frac{f_2^2 - f_1^2}{f_2^2 + f_1^2} \quad (3)$$

With the combination of the formula and ANSYS HFSS simulation, the coupling coefficient related to the slot dimension is achieved. The relationship between the extracted coupling coefficient and coupling slot length SLOT_L is depicted in Figure 3. When the coupling slot length is increased, the coupling coefficients become larger. Accordingly, the coupling coefficients can be tuned by the length, width, and position of the slot. The final values have been achieved by optimization using the full-wave simulator.

The external quality factor Q_e depends on the input and output excitation structure. This structure is composed of a section of 50 ohm CPW line and a slot dipole for coupling to the SIW cavity as shown in the inset of Figure 4. The external quality factor Q_e can be extracted using following formula:

$$Q_e = \frac{f_0}{\Delta f_{\pm 90^\circ}} \quad (4)$$

where f_0 is the resonant frequency of the structure shown in Figure 4 and $\Delta f_{\pm 90^\circ}$ is the frequency bandwidth corresponding to the phase shift of $\pm 90^\circ$ with respect to the absolute phase at f_0 . Using ANSYS HFSS simulation, the external quality factor Q_e can be derived. The external quality factor of the feeding structure is determined by the slot dipole length, width, and offset position. The relationship between the external quality factor and the slot dipole offset S.OFFSET is

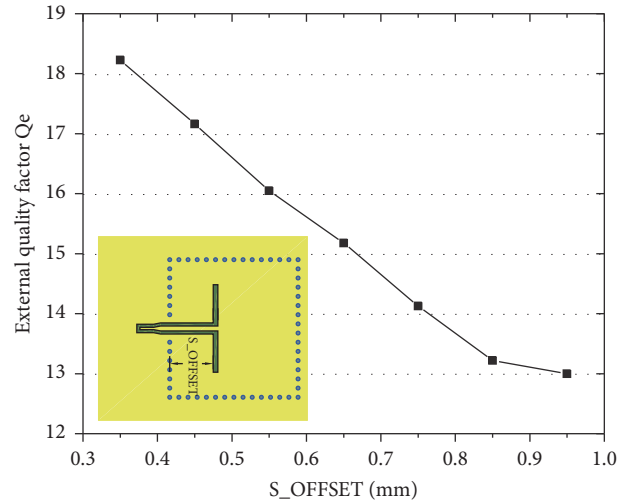


FIGURE 4: Relationship between the external quality factor Q_e and the offset value S.OFFSET.

depicted in Figure 4. When the S.OFFSET value is increased, the external quality factor becomes smaller. The final values have been achieved by optimization in ANSYS HFSS.

2.4. CPW-to-CPW Vertical Transitions. Microwave and millimeter-wave component measurement are often accomplished using GSG on-wafer probes. The back-to-back CPW structure is commonly utilized. As illustrated in Figure 5, the proposed CPW-to-CPW vertical transition structure is composed of two sets of metal vias and three sections of CPW transmission lines using a two-layer LTCC substrate. The thickness of the top layer is 0.1 mm; the thickness of the bottom layer is 0.9 mm. The diameter of the via hole is 0.09mm and the metal used is silver.

To maintain a good transmission and reflection characteristics, the CPW line is designed to be 50 ohm in Ka band. The width of the signal line is 0.25 mm, and the gap is 0.066 mm. The structure is simulated with ANSYS HFSS software. As depicted in Figure 6, the CPW vertical transition structure has a return loss better than 15 dB from 20 to 36 GHz. The simulated insertion loss is less than 0.61 dB.

Based on the above analysis, the CPW-to-CPW vertical transition structure is adopted at port 2 and optimized to miniaturize the multilayer SIW filter. Figure 7 shows the simulated performance of the whole SIW filter. It is observed that the length of the CPW line has a noticeable impact on the reflection coefficients. This is mainly caused by the imperfect impedance matching due to the introduction of the vertical transition structure. When the CPW line length is 0.6 mm, the return loss is over 20 dB. After full-wave simulation and optimization, the final dimensional parameters of the proposed SIW BPF are presented in Figure 8 and Table 1.

3. Fabrication and Measurement

3.1. LTCC Fabrication Process. The dielectric material used during the LTCC fabrication process was SICCAS-K5F3 developed by Shanghai Institute of Ceramics, Chinese

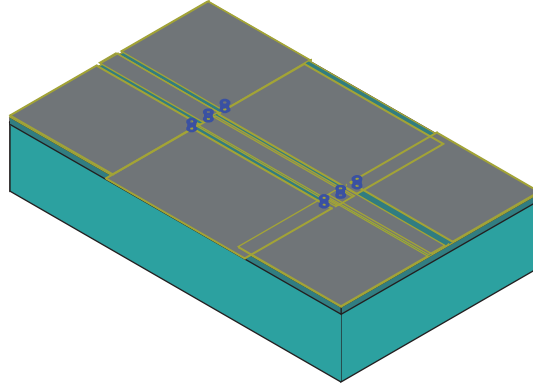


FIGURE 5: The model of the proposed CPW vertical transition structure.

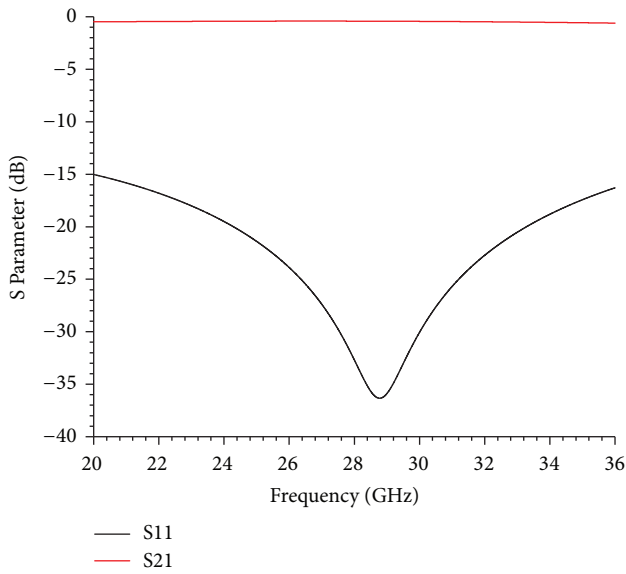


FIGURE 6: Simulated S-parameters of the CPW vertical transition.

TABLE 1: Dimensions of the SIW filter (mm).

S_L	S_W	SIW_L	SIW_W	VIA_R
6.7	4.0	2.8	3.0	0.04
VIA_P	H	SLOT_L	SLOT_W	SLOT_OFFSET
0.2	1.2	1.17	0.15	0.3
CP_L	CP_W	CPW_W	CPW_C	CPW_G
0.9	0.1	0.1	0.6	0.067

Academy of Sciences. The SICCAS-K5F3 green tape has a thickness of 50 μm . Holes with 0.1 mm diameter were punched by a KEKO, PAM-200 equipment. Silver paste (Dupont LL612) was deposited on the LTCC green tape by screen printing technique (KEKO, P-200 equipment). The vias were filled with silver paste. The green tapes were staked in proper order and laminated at 7000 psi and 70°C for 25 min in an isostatic pressing chamber (LT08001, PTC). Small filter blocks were obtained by cutting the pressed green block. The

TABLE 2: SIW bandpass filters comparison.

Ref	Order	f_0 (GHz)	Insertion Loss (dB)	FBW	Area(λ_0^2)
[4]	4	27.95	2.8	9%	0.57×0.57
[13]	2	34.5	1.7	2.5%	0.5×0.5
[14]	4	35	1.25	3.6%	1.28×1.28
[15]	4	29.87	3.3	13%	0.37×0.28
[16]	2	27.6	1.8	2.5%	0.78×0.77
This work	3	27.7	1.74	10%	0.63×0.38

filters were obtained after debinding at 450°C for 2 h and sintered at 900°C for 25 min. Owing to the shrinkage in the process, the designed value and fabricated dimension had a slight discrepancy. The photograph of the fabricated filter is exhibited in Figure 9(a), while the microscopic image of the CPW port is presented in Figure 9(b). The fabricated filter is 6.79 mm×4.13 mm×1.34 mm ($0.63\lambda_0 \times 0.38\lambda_0 \times 0.12\lambda_0$) in size.

3.2. Measurement. The LTCC SIW filter sample was measured using a Keysight N5245A Vector Network Analyzer (VNA) and a Cascade M150 probe station with two GSG probes after a SOLT calibration. The measured and simulated S-parameters of the SIW filter are plotted in Figure 10. The measurement results are in good agreement with the simulation results. The measured insertion loss is 1.74 dB at the center frequency of 27.73 GHz. The 3 dB bandwidth of the filter is 10% from 26.30 GHz to 29.08 GHz. Some discrepancies were observed in the return loss and transmission at the upper band edge. These are mainly due to the fabrication tolerance. The performance of the SIW BPF in this work, as compared with a few other reported results, is presented in Table 2. It is demonstrated that the SIW filter has a compact size, low insertion loss, and good comparable bandwidth.

4. Conclusions

In this paper, a Ka-band multilayer SIW BPF with CPW-to-CPW vertical transition is designed and fabricated using an in-house developed LTCC material and process. The coupling is realized by inductive slots between different layers. The CPW-to-CPW vertical transition structure is analyzed and

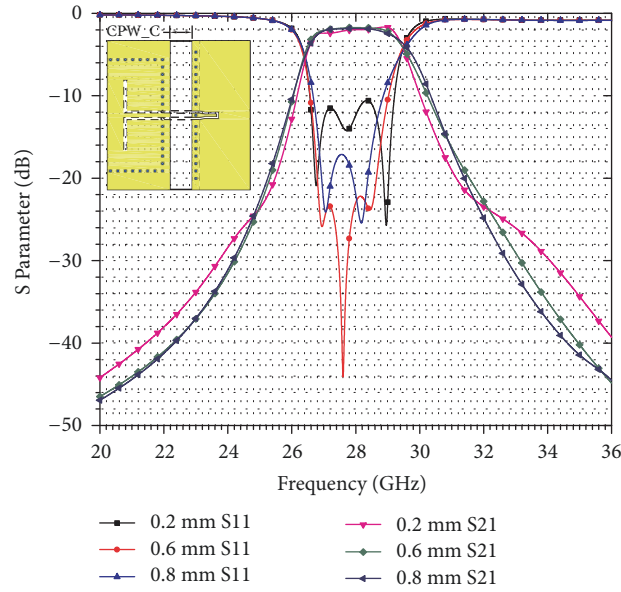


FIGURE 7: Effect of the length of the CPW line on the S-parameters of the SIW filter.

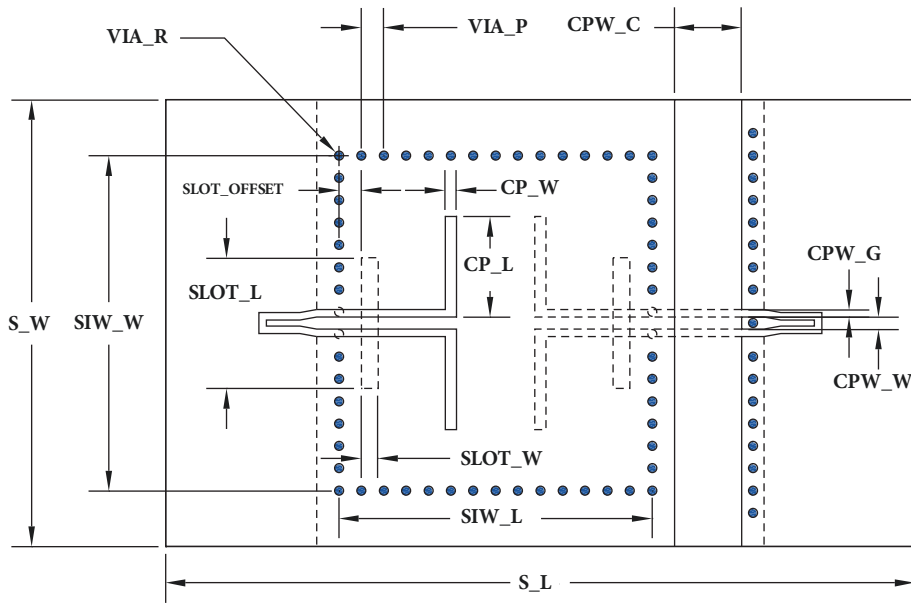


FIGURE 8: Geometric parameters of the proposed filter.

implemented in the multilayer filter. The simulated and measured BPF results exhibit good performance and agree reasonably well. The fabricated SIW filter has a center frequency of 27.73 GHz, low insertion loss of 1.74 dB, and a 3 dB FBW of about 10%. The competitive performance of the SIW filter also validates the LTCC materials used in this work.

Data Availability

The data used to support the findings of this study are available from the corresponding author upon request.

Conflicts of Interest

The authors declare that they have no conflicts of interest.

Acknowledgments

The authors would like to acknowledge the financial support from the National Key Research and Development Program of China (2017YFB0406303), the National Natural Science Foundation of China (61501438), and the Youth Innovation Promotion Association of CAS.

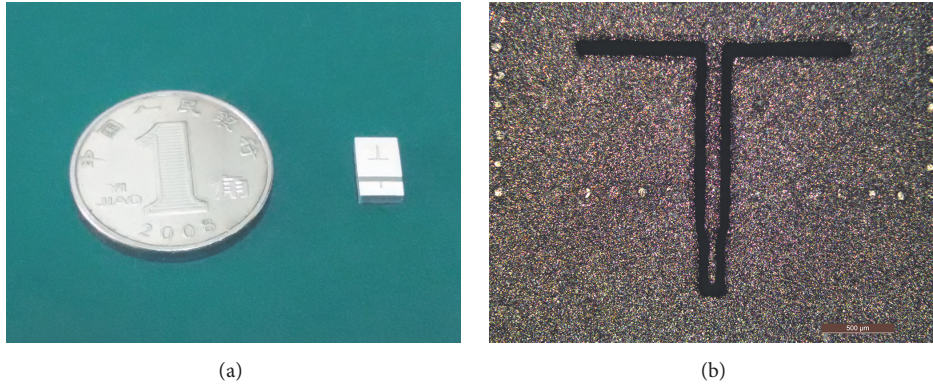


FIGURE 9: Photograph of (a) the filter sample and (b) the CPW transmission line at the input port.

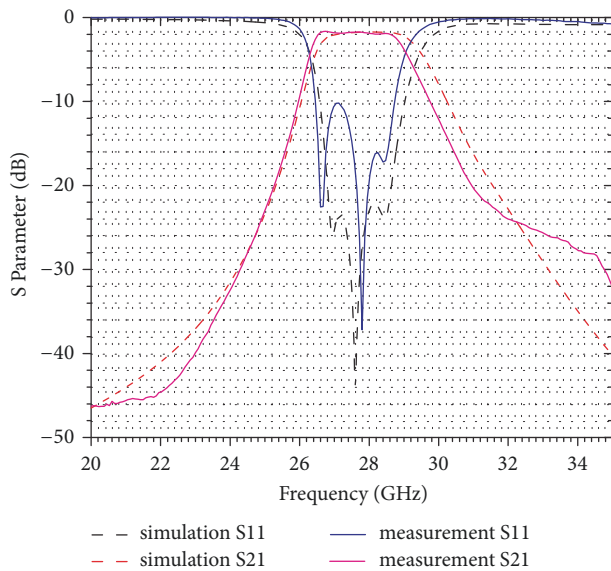


FIGURE 10: Simulation and measurement results of the proposed filter.

References

- [1] X.-P. Chen and K. Wu, "Substrate integrated waveguide filters: design techniques and structure innovations," *IEEE Microwave Magazine*, vol. 15, no. 6, pp. 121–133, 2014.
- [2] C. Yu and W. Hong, "37–38 GHz substrate integrated filtenna for wireless communication application," *Microwave and Optical Technology Letters*, vol. 54, no. 2, pp. 346–351, 2011.
- [3] D. Deslandes and K. Wu, "Accurate Modeling, Wave Mechanisms, and Design Considerations of a Substrate Integrated Waveguide," *IEEE Transactions on Microwave Theory and Techniques*, vol. 54, no. 6, pp. 2516–2526, 2006.
- [4] K. S. Chin, C. C. Chang, C. H. Chen, Z. Guo, D. Wang, and W. Che, "LTCC Multilayered Substrate-Integrated Waveguide Filter With Enhanced Frequency Selectivity for System-in-Package Applications," *IEEE Transactions on Components Packaging & Manufacturing Technology*, vol. 4, no. 4, pp. 664–672, 2014.
- [5] S. W. Wong, K. Wang, Z.-N. Chen, and Q.-X. Chu, "Electric coupling structure of substrate integrated waveguide (SIW) for the application of 140-GHz bandpass filter on LTCC," *IEEE Transactions on Components, Packaging, and Manufacturing Technology*, vol. 4, no. 2, pp. 316–322, 2014.
- [6] I. Llamas-Garro, F. Mira, P. Zheng, Z. Liu, L.-S. Wu, and Y. Wang, "All-resonator based LTCC diplexer using substrate-integrated-waveguides," *IEEE Electronics Letters*, vol. 53, no. 21, pp. 1410–1412, 2017.
- [7] S. Zhang and L. Zhu, "Compact and high-selectivity microstrip bandpass filters using triple-/quad-Mode stub-loaded resonators," *IEEE Microwave and Wireless Components Letters*, vol. 21, no. 10, pp. 522–524, 2011.
- [8] X.-P. Chen and K. Wu, "Substrate integrated waveguide cross-coupled filter with negative coupling structure," *IEEE Transactions on Microwave Theory and Techniques*, vol. 56, no. 1, pp. 142–149, 2008.
- [9] Q.-F. Wei, Z.-F. Li, L. Li, W.-J. Zhang, and J.-F. Mao, "Three-pole cross-coupled substrate-integrated waveguide bandpass filters based on PCB process and multilayer LTCC technology," *Microwave and Optical Technology Letters*, vol. 51, no. 1, pp. 71–73, 2009.
- [10] S. Sirci, M. Á. Sánchez-Soriano, J. D. MartíNez et al., "Design and Multiphysics Analysis of Direct and Cross-Coupled SIW Combine Filters Using Electric and Magnetic Couplings," *IEEE Transactions on Microwave Theory & Techniques*, vol. 63, no. 12, pp. 4341–4354, 2015.
- [11] Y. C. Lee, "CPW-to-stripline vertical via transitions for 60GHz LTCC SOP applications," *Progress in Electromagnetics Research Letters*, vol. 2, pp. 37–44, 2008.
- [12] D. M. Nair, W. E. McKinzie III, B. A. Thrasher, M. A. Smith, E. D. Hughes, and J. M. Parisi, "A 10 MHz to 100 GHz LTCC CPW-to-stripline vertical transition," in *Proceedings of the 2013 IEEE MTT-S International Microwave Symposium Digest, MTT 2013*, June 2013.
- [13] M. Shen, Z. Shao, X. Du, Z. He, and X. Li, "Ka-band multilayered substrate integrated waveguide narrowband filter for system-in-package applications," *Microwave and Optical Technology Letters*, vol. 58, no. 6, pp. 1395–1398, 2016.
- [14] X.-P. Chen and K. Wu, "Self-packaged millimeter-wave substrate integrated waveguide filter with asymmetric frequency response," *IEEE Transactions on Components, Packaging, and Manufacturing Technology*, vol. 2, no. 5, pp. 775–782, 2012.
- [15] T.-M. Shen, T.-Y. Lin, T.-Y. Huang, and R.-B. Wu, "A vertically stacked quasi-elliptic waveguide filter with crossly coupling

vias,” in *Proceedings of the Asia-Pacific Microwave Conference, APMC 2007*, December 2007.

- [16] D. Deslandes and K. Wu, “Millimeter-Wave Substrate Integrated Waveguide Filters,” in *Proceedings of the CCECE 2003 Canadian Conference on Electrical and Computer Engineering: Toward a Caring and Humane Technology*, pp. 1917–1920, May 2003.

